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(12) **United States Design Patent**
Kajiwara

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(54) **ARM FOR WAFER TRANSPORTATION FOR MANUFACTURING SEMICONDUCTOR**

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(73) Assignee: **Tokyo Electron Limited, Minato-Ku (JP)**

(**) Term: **14 Years**

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(30) **Foreign Application Priority Data**

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(51) **LOC (9) Cl.** **13-03**

(52) **U.S. Cl.** **D13/182**

(58) **Field of Classification Search** D13/182;
118/500, 725, 728, 729; 294/103.1; 414/217,
414/222.01, 416.03, 941

See application file for complete search history.

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(57) **CLAIM**

The ornamental design for arm for wafer transportation for manufacturing semiconductor, as shown and described.

DESCRIPTION

FIG. 1 is front perspective view of an arm for wafer transportation for manufacturing semiconductor illustrating my new design;

FIG. 2 is a top plan view thereof;

FIG. 3 is a bottom view thereof;

FIG. 4 is a front view thereof;

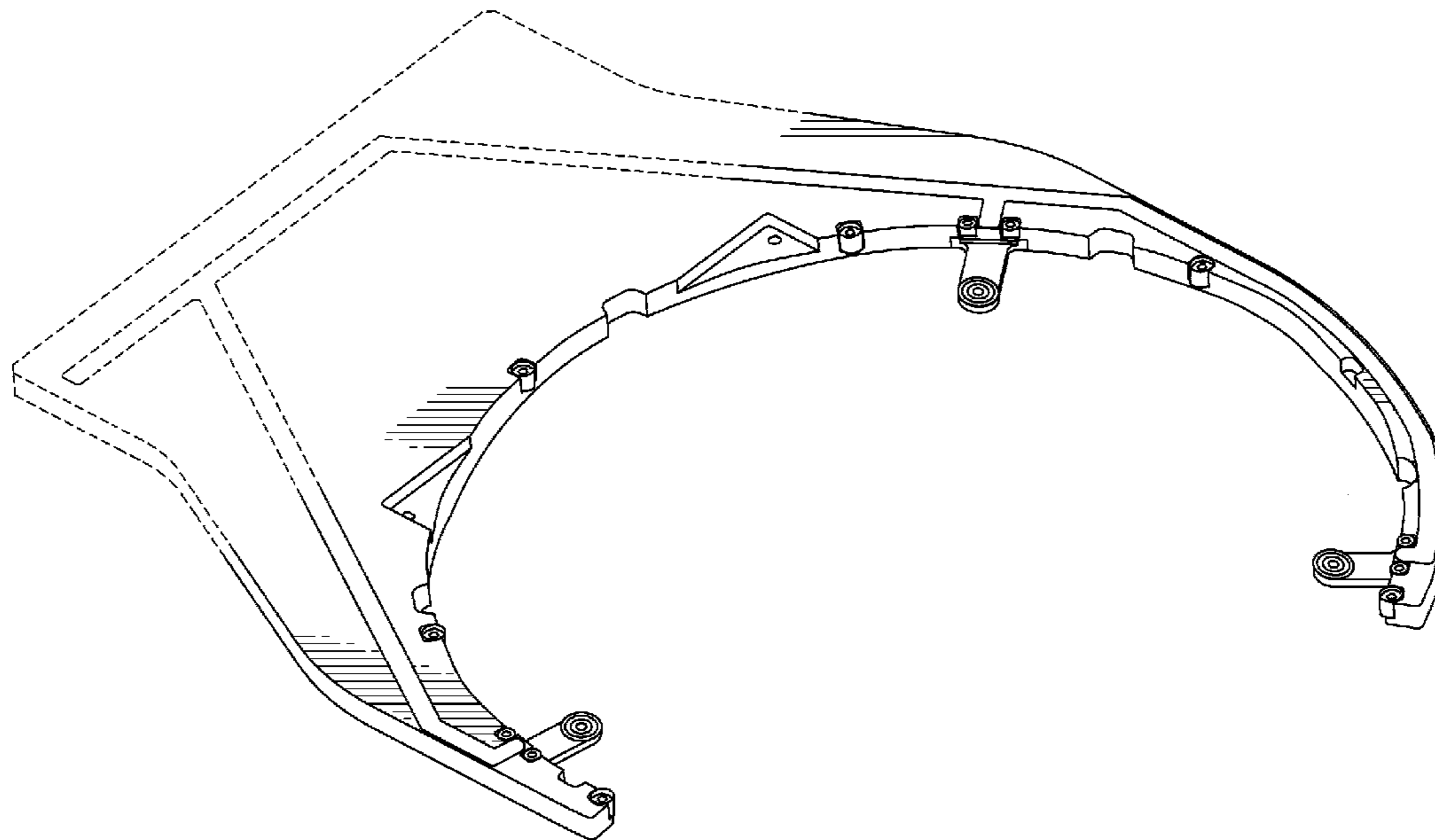
FIG. 5 is a rear view thereof;

FIG. 6 is a left side view thereof; and,

FIG. 7 is a right side view thereof.

The broken lines in the drawings form no part of the claimed design.

1 Claim, 7 Drawing Sheets



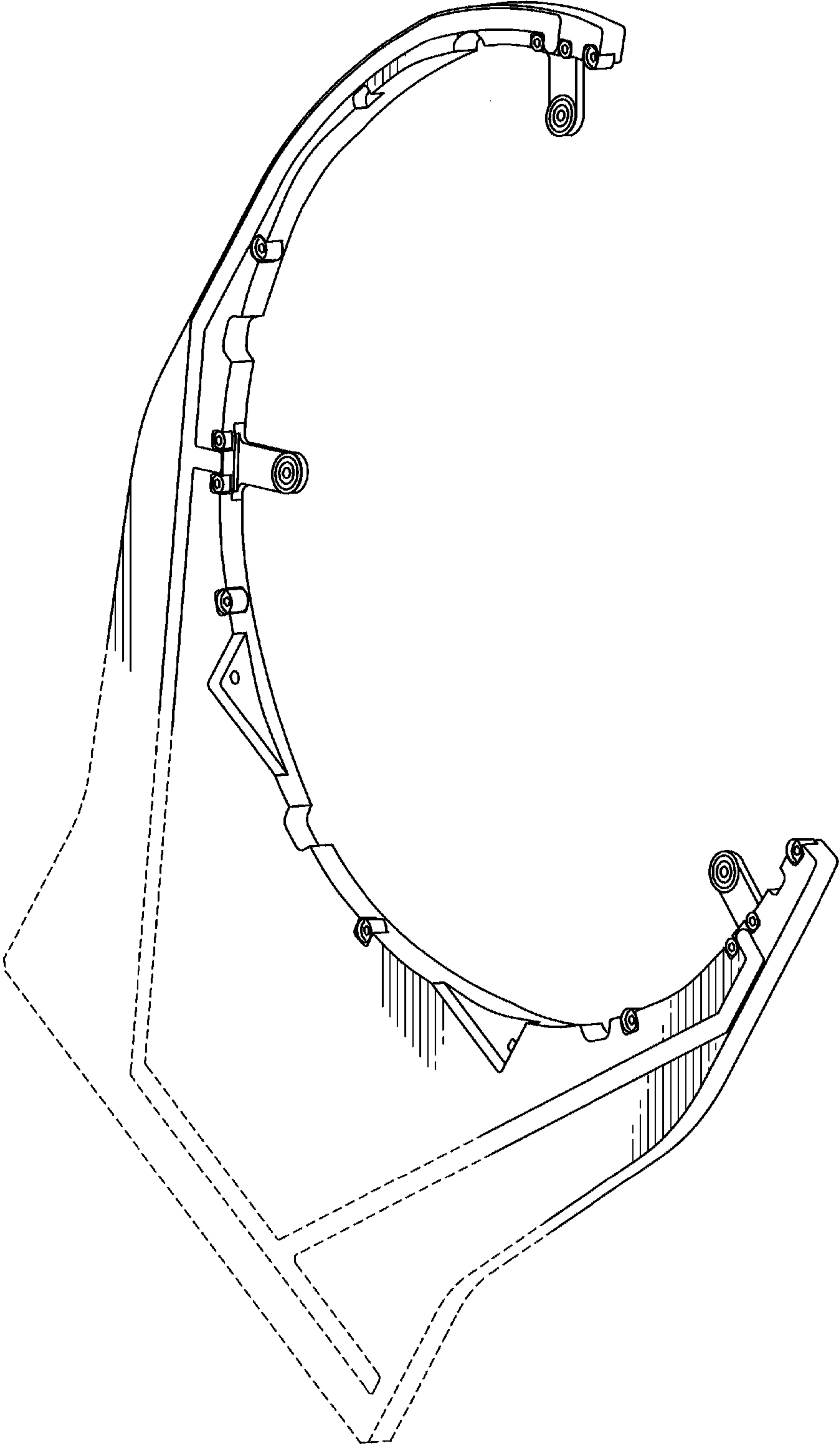


FIG. 1

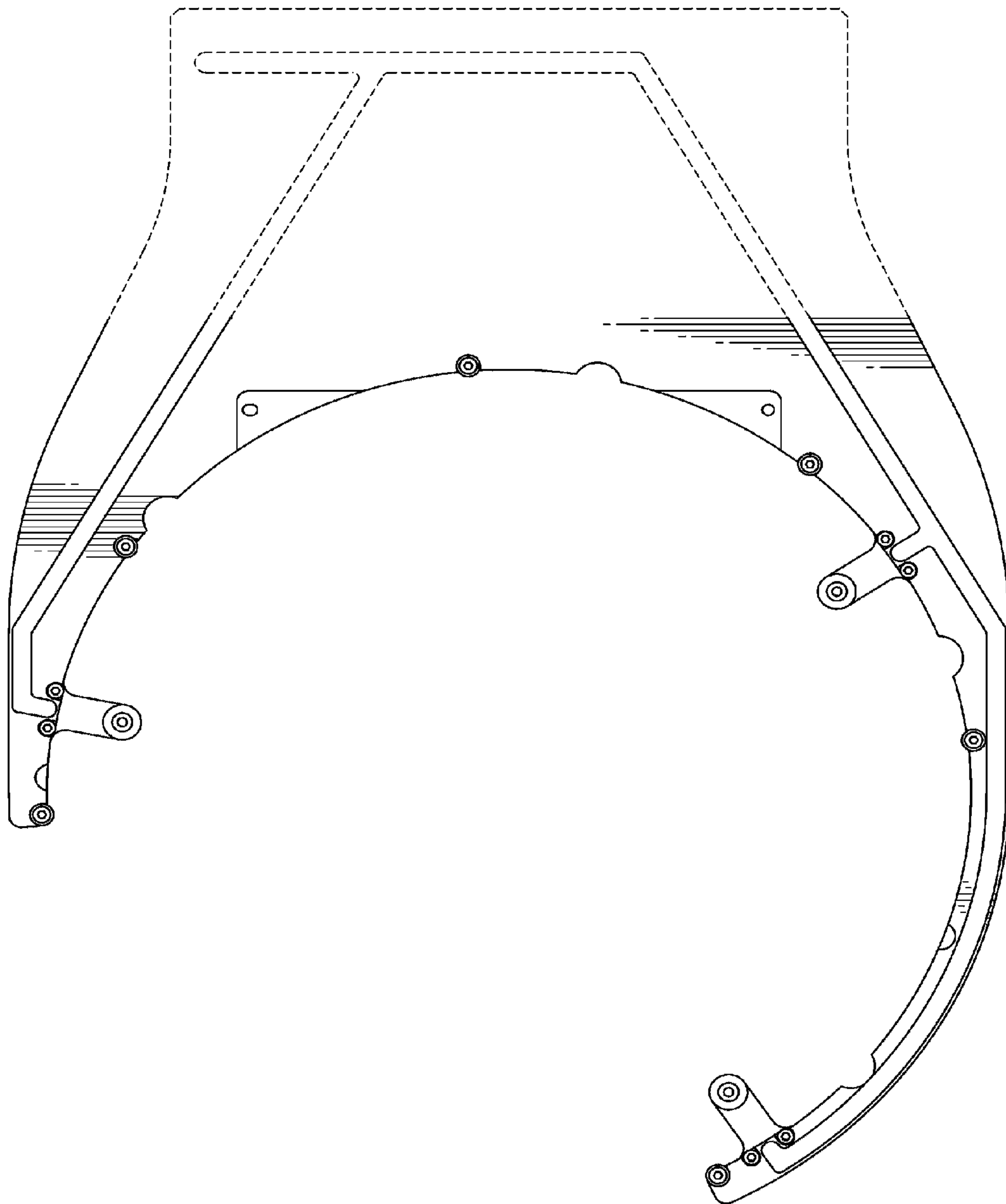


FIG. 2

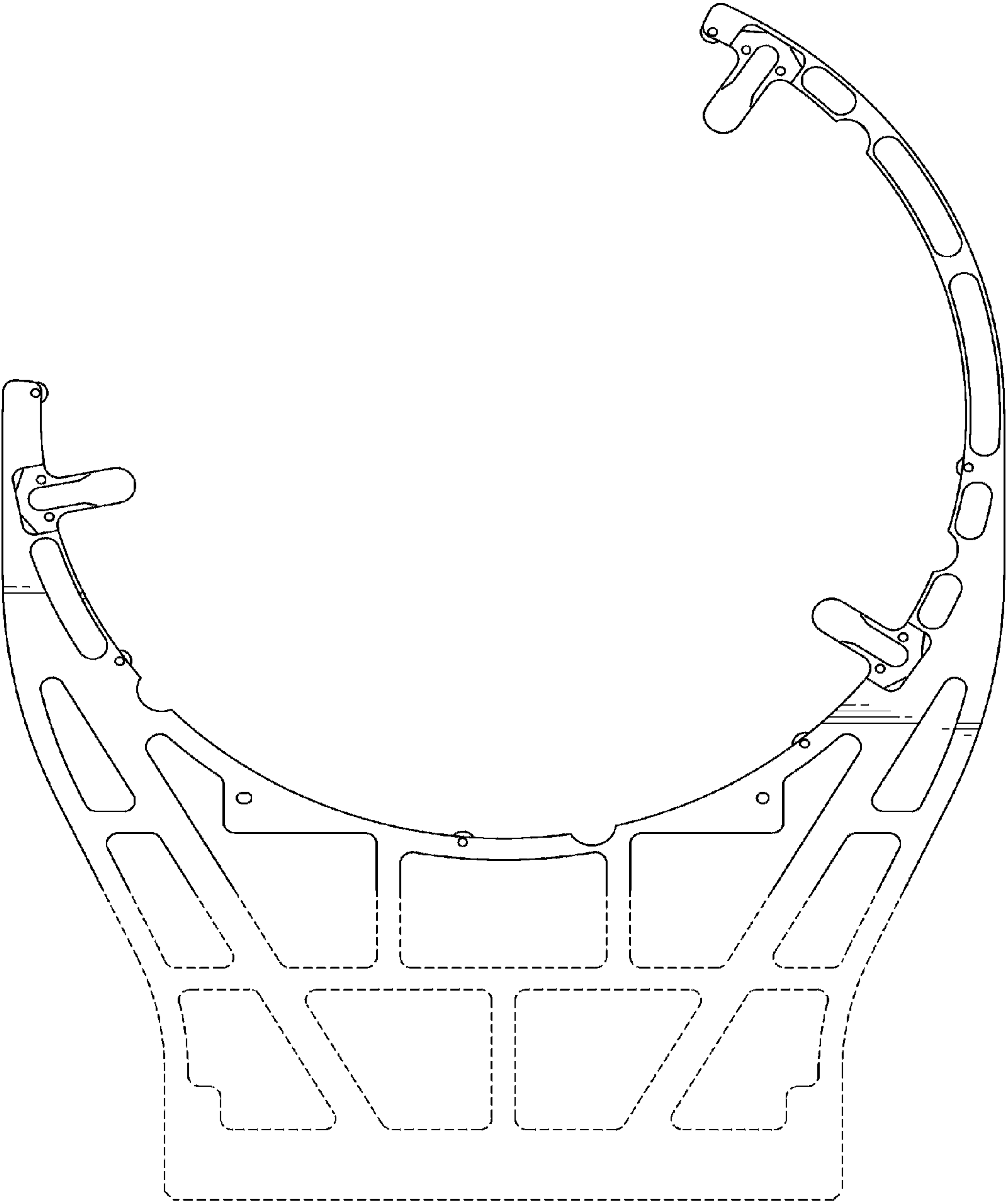


FIG. 3

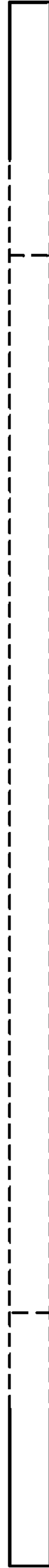


FIG. 5



FIG. 6

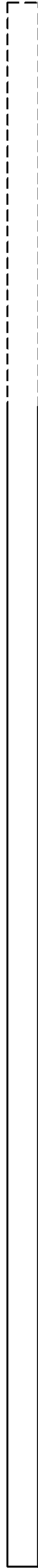


FIG. 7