

US00D672780S

(12) **United States Design Patent**
Chen et al.

(10) **Patent No.:** **US D672,780 S**
(45) **Date of Patent:** **** Dec. 18, 2012**

(54) **MICRO UNIVERSAL SERIAL BUS ADAPTER**

(75) Inventors: **Henry Chen**, Arcadia, CA (US); **Ka Hung Au**, Hong Kong (CN)

(73) Assignee: **Ever Win International Corporation**,
City of Industry, CA (US)

(**) Term: **14 Years**

(21) Appl. No.: **29/409,953**

(22) Filed: **Dec. 30, 2011**

(51) **LOC (9) Cl.** **14-02**

(52) **U.S. Cl.** **D14/433; D13/147**

(58) **Field of Classification Search** D14/356–358,
D14/383–385, 420, 426, 427, 432–439, 447,
D14/453, 454, 217, 253, 299; D13/107,
D13/108, 133, 146, 147, 152, 154, 156, 158,
D13/162, 162.1, 168, 173, 177; 439/131,
439/172–174, 373, 378; 361/679.08, 679.56,
361/679.21, 679.4

See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D366,242 S *	1/1996	Goto		D13/147
D437,290 S *	2/2001	Goto		D13/147
D478,689 S *	8/2003	Thorpe		D28/83
D531,127 S *	10/2006	Zhong et al.		D13/147
D560,165 S *	1/2008	Matityahu et al.		D13/133
D576,555 S *	9/2008	Bruseloff		D13/147
D578,070 S *	10/2008	Shen et al.		D13/147
D580,932 S *	11/2008	Tzou et al.		D14/356
D585,060 S *	1/2009	Han		D14/358
D585,061 S *	1/2009	Han		D14/358
D587,208 S *	2/2009	Tsai et al.		D13/147

D587,209 S *	2/2009	Tsai et al.		D13/147
D604,725 S *	11/2009	Chen		D14/240
D618,224 S *	6/2010	Tong et al.		D14/240
D618,225 S *	6/2010	Tong et al.		D14/240
D618,226 S *	6/2010	Tong et al.		D14/240
D618,240 S *	6/2010	Larmour et al.		D14/358
D630,632 S *	1/2011	Puls et al.		D14/356
D631,883 S *	2/2011	Maier		D14/435.1
D641,023 S *	7/2011	Chen et al.		
D642,516 S *	8/2011	Ueda et al.		D13/103
2007/0293079 A1 *	12/2007	Chao et al.		439/352

FOREIGN PATENT DOCUMENTS

CN ZL201130017868.X 9/2011

* cited by examiner

Primary Examiner — Austin Murphy

(74) *Attorney, Agent, or Firm* — Joe Nieh

(57) **CLAIM**

The ornamental design for a micro universal serial bus adapter, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a micro universal serial bus adapter showing our new design;

FIG. 2 is a front view thereof;

FIG. 3 is a bottom view thereof;

FIG. 4 is a top view thereof;

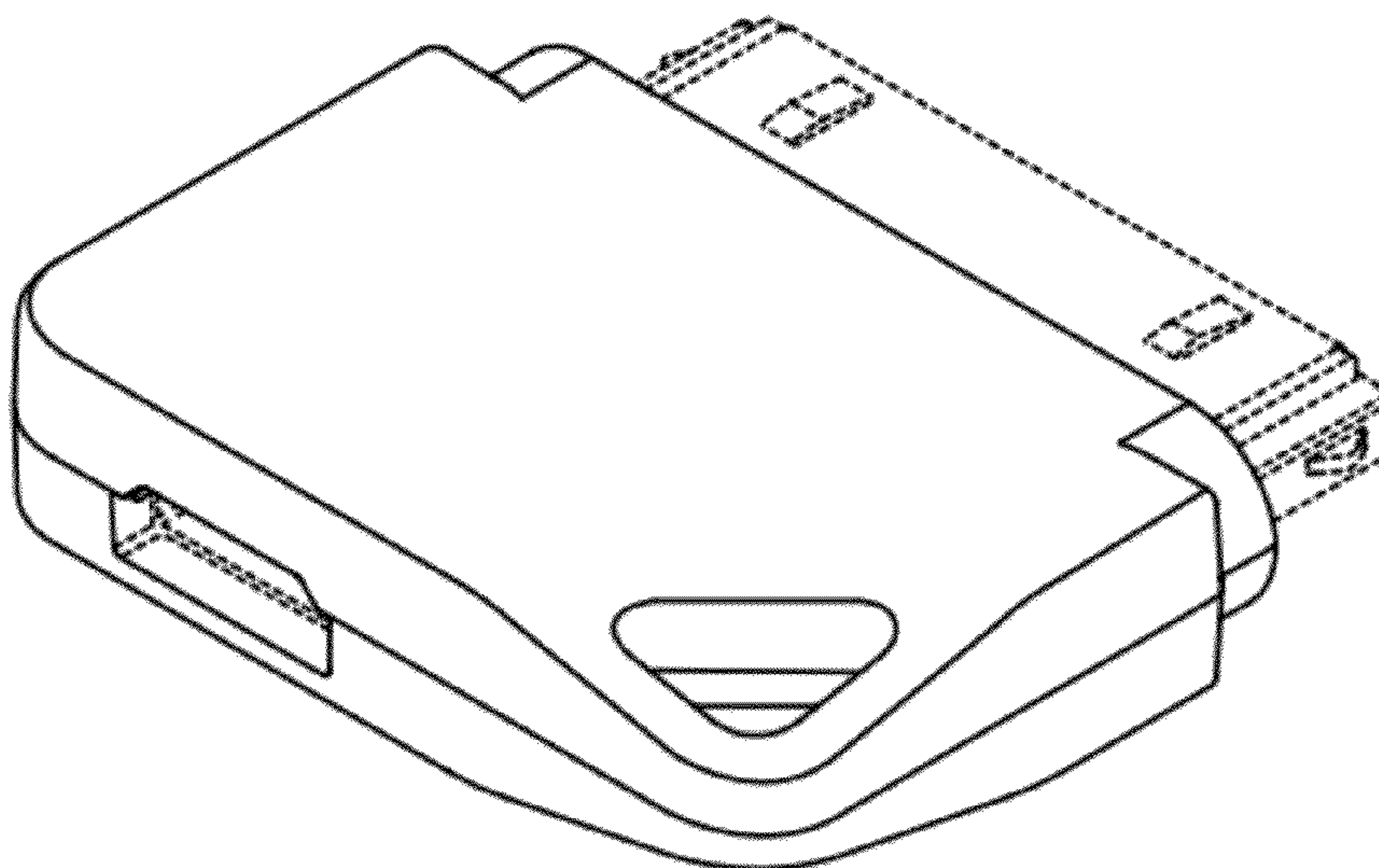
FIG. 5 is a rear view thereof;

FIG. 6 is a left side view thereof; and,

FIG. 7 is a right side view thereof.

The broken lines shown in the drawings are included for the purpose of illustrating unclaimed portions of the micro universal serial bus adapter and form no part of the claimed design.

1 Claim, 7 Drawing Sheets



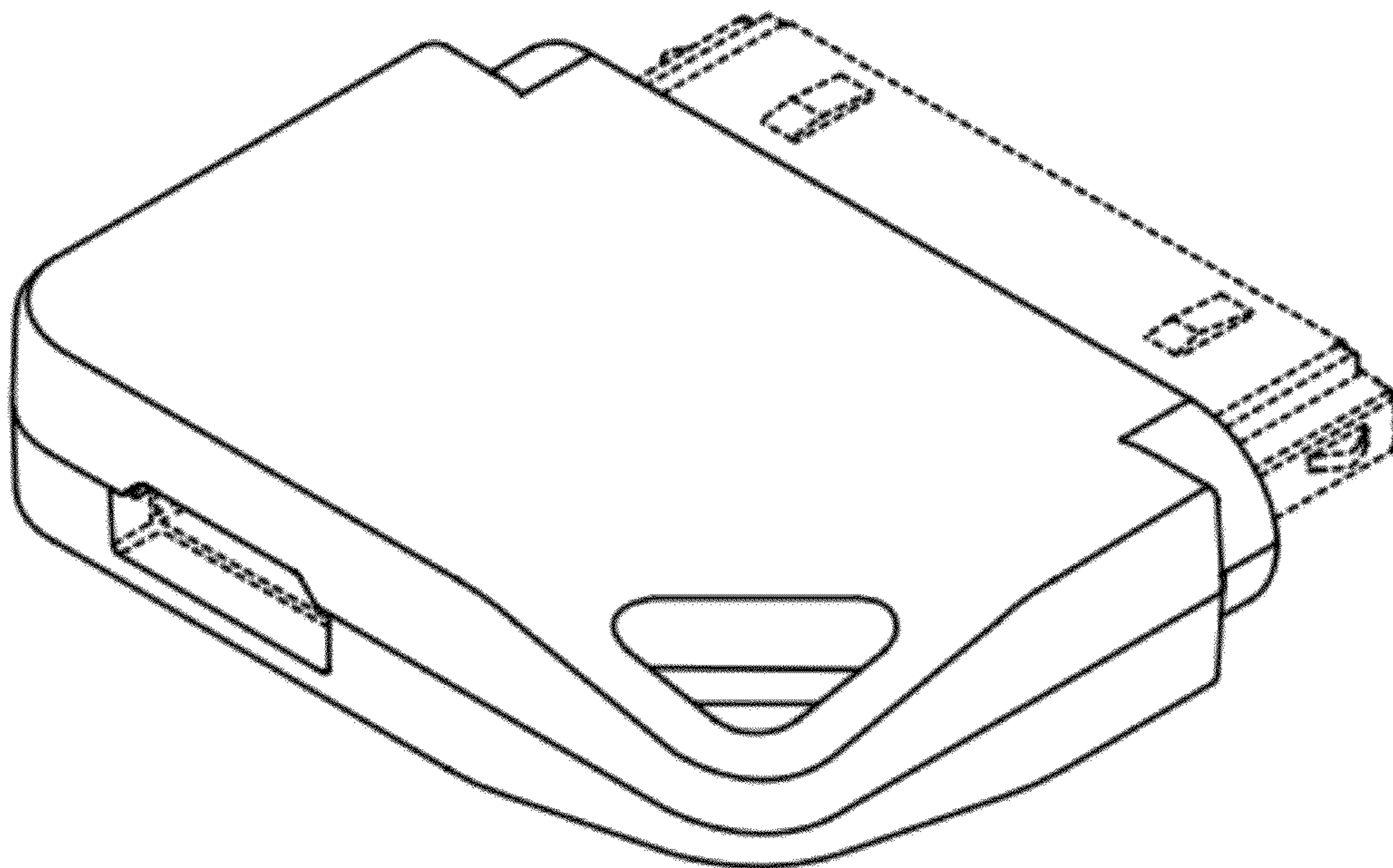


Fig. 1

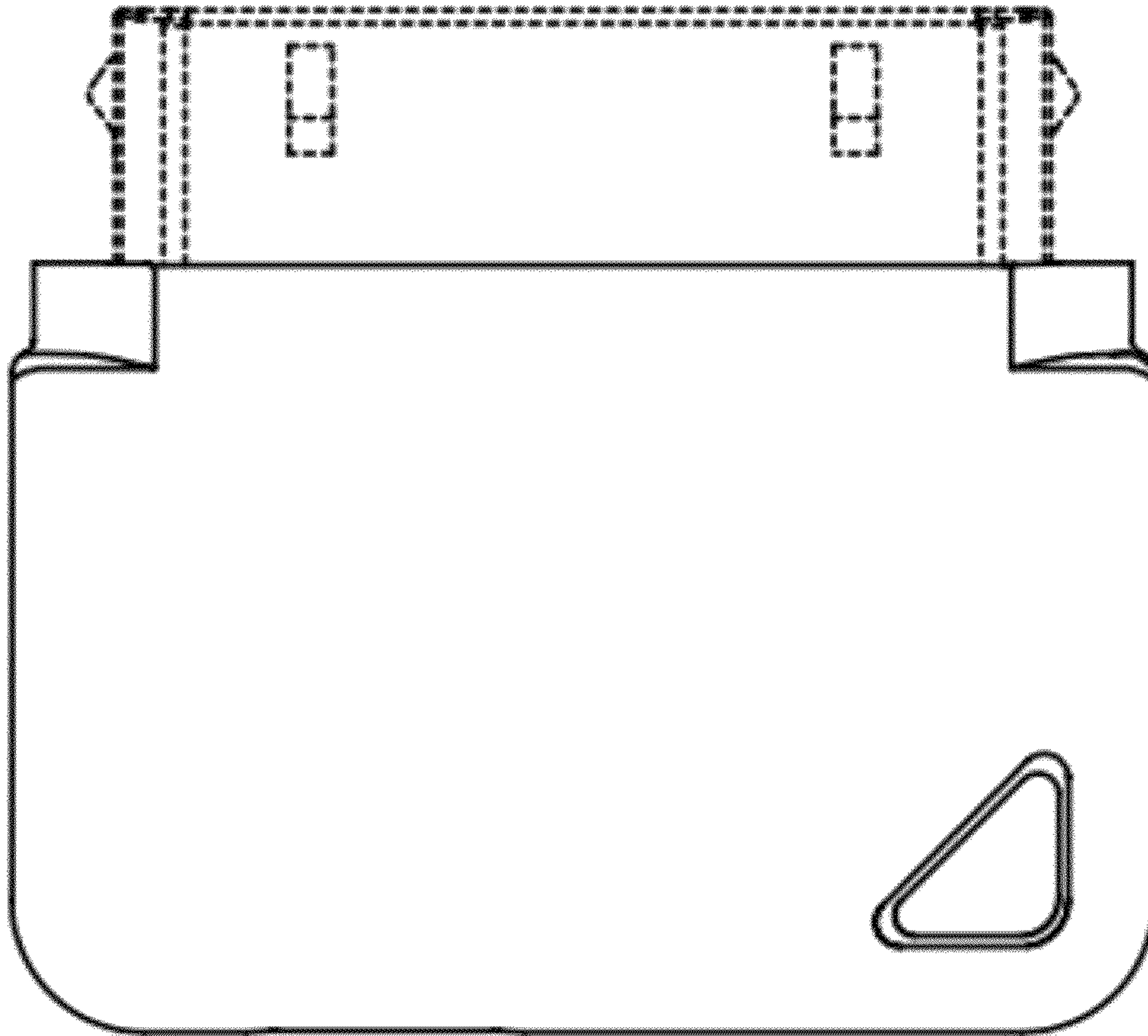


Fig. 2

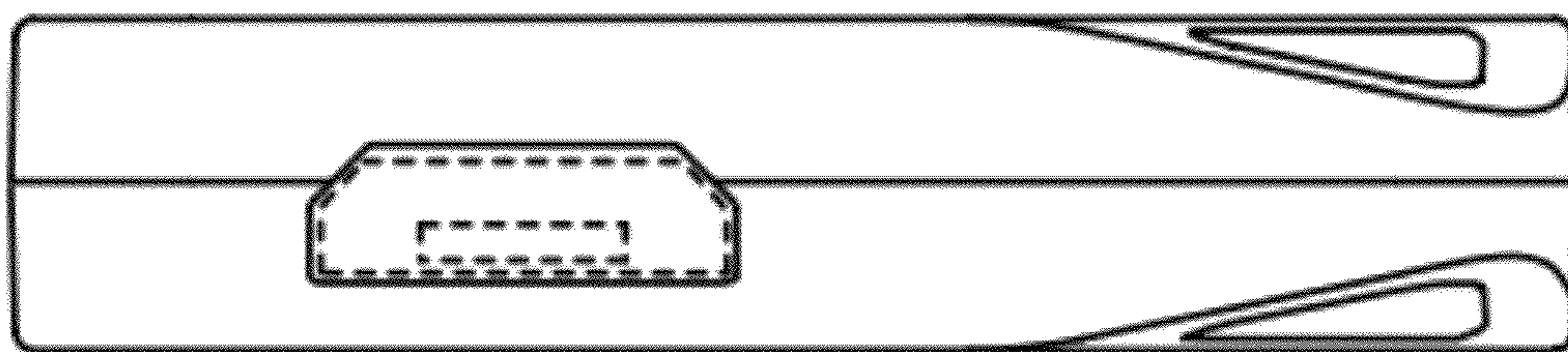


Fig. 3

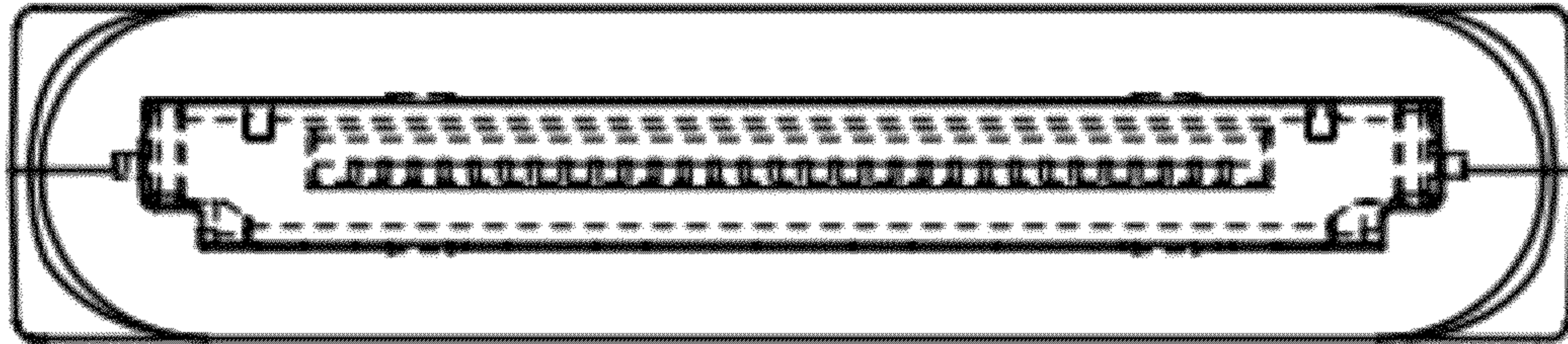


Fig. 4

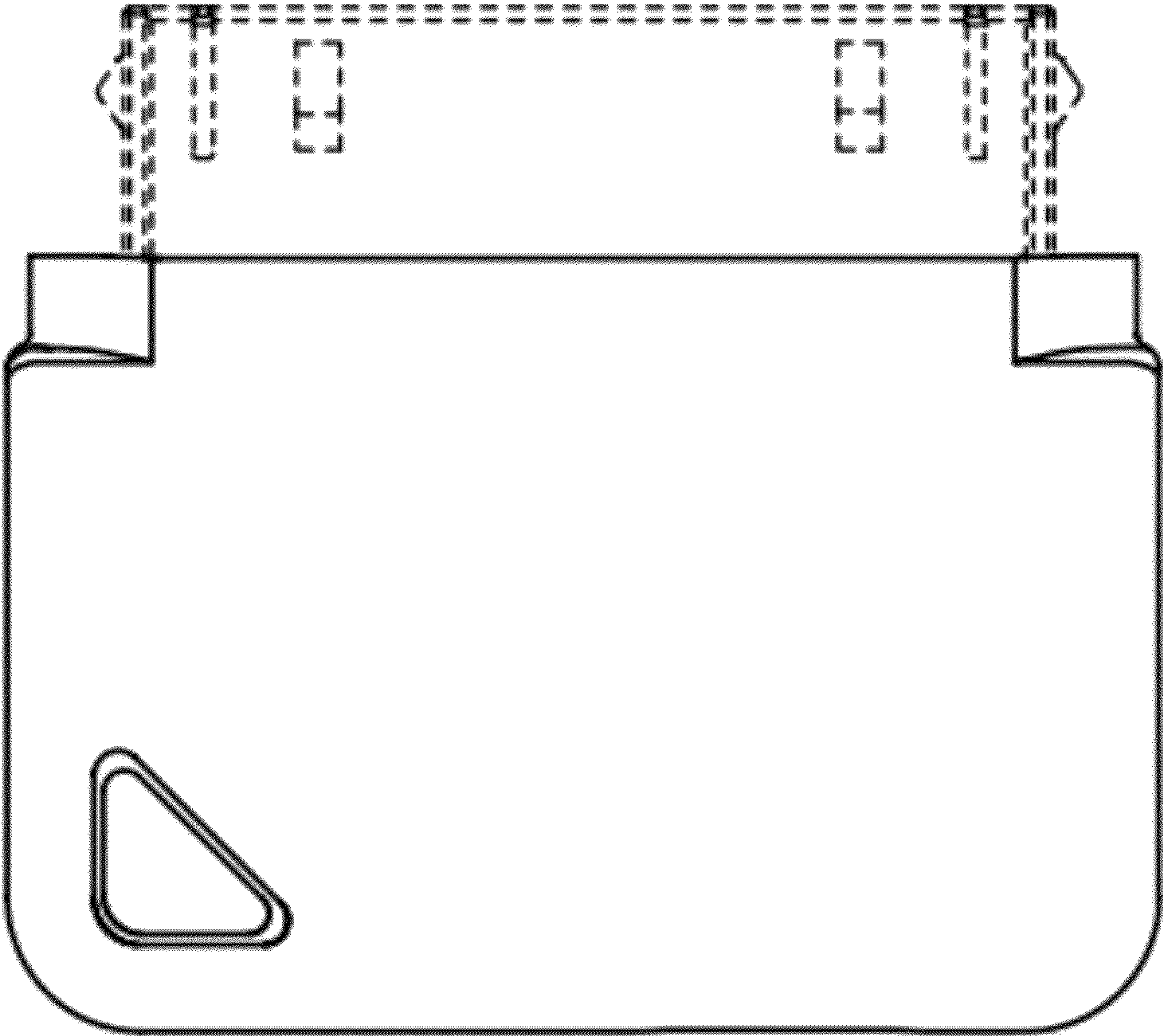


Fig. 5

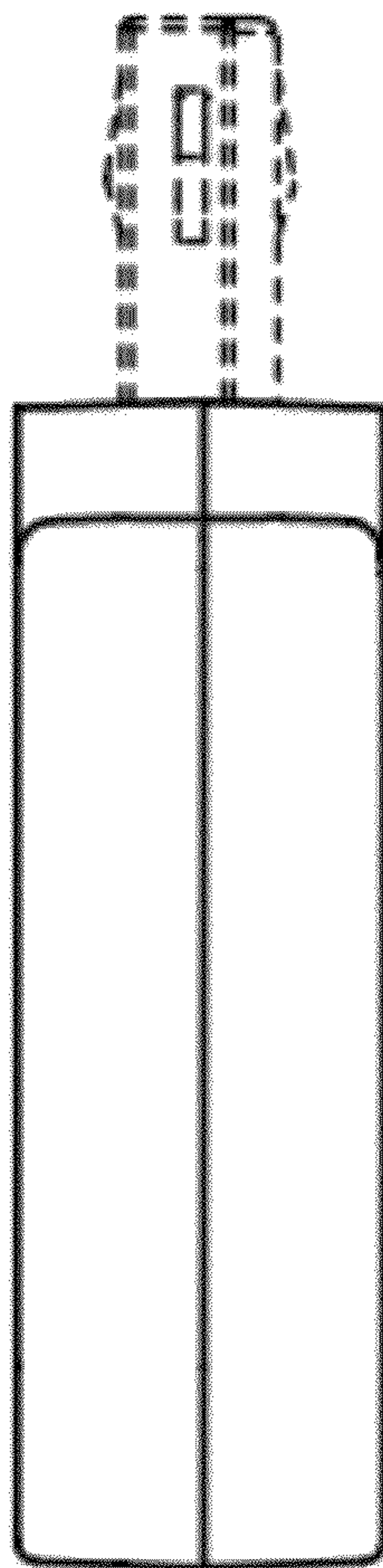


Fig. 6

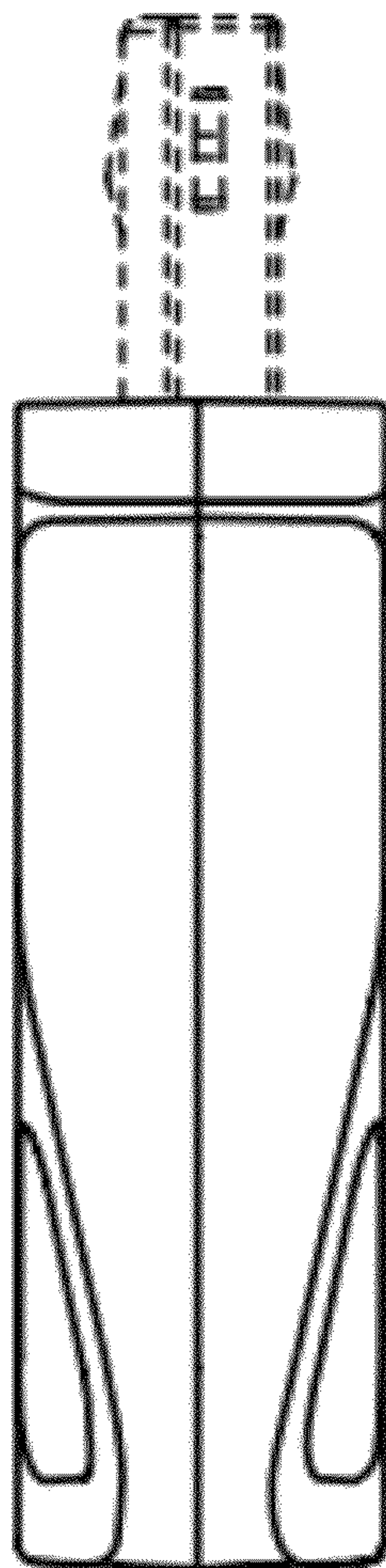


Fig. 7