



US00D672377S

(12) **United States Design Patent**  
**Yoshikawa et al.**

(10) **Patent No.:** **US D672,377 S**  
(45) **Date of Patent:** **\*\* Dec. 11, 2012**

(54) **DIELECTRIC WINDOW FOR PLASMA PROCESSING DEVICE**

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(73) Assignee: **Tokyo Electron Limited** (JP)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/369,650**

(22) Filed: **Sep. 10, 2010**

(30) **Foreign Application Priority Data**

Mar. 31, 2010 (JP) ..... 2010-008110

(51) **LOC (9) Cl.** ..... **15-09**

(52) **U.S. Cl.** ..... **D15/138**

(58) **Field of Classification Search** ..... D13/182;  
D15/138, 144, 144.1, 144.2; 118/723; 156/345,  
156/345.36, 345.41, 345.48; 204/298.06,  
204/298.08, 298.11, 298.34; 336/232  
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a dielectric window for plasma processing device, as shown and described.

**DESCRIPTION**

FIG. 1 is a perspective view of a dielectric window for plasma processing device showing our new design;  
FIG. 2 is a front view thereof;  
FIG. 3 is a rear view thereof;  
FIG. 4 is a right side view thereof;  
FIG. 5 is a top plan view thereof; and,  
FIG. 6 is a cross-sectional view taken along line 6-6 of FIG. 2. The broken lines are shown for illustrative purposes only and form no part of the claimed design.

**1 Claim, 6 Drawing Sheets**

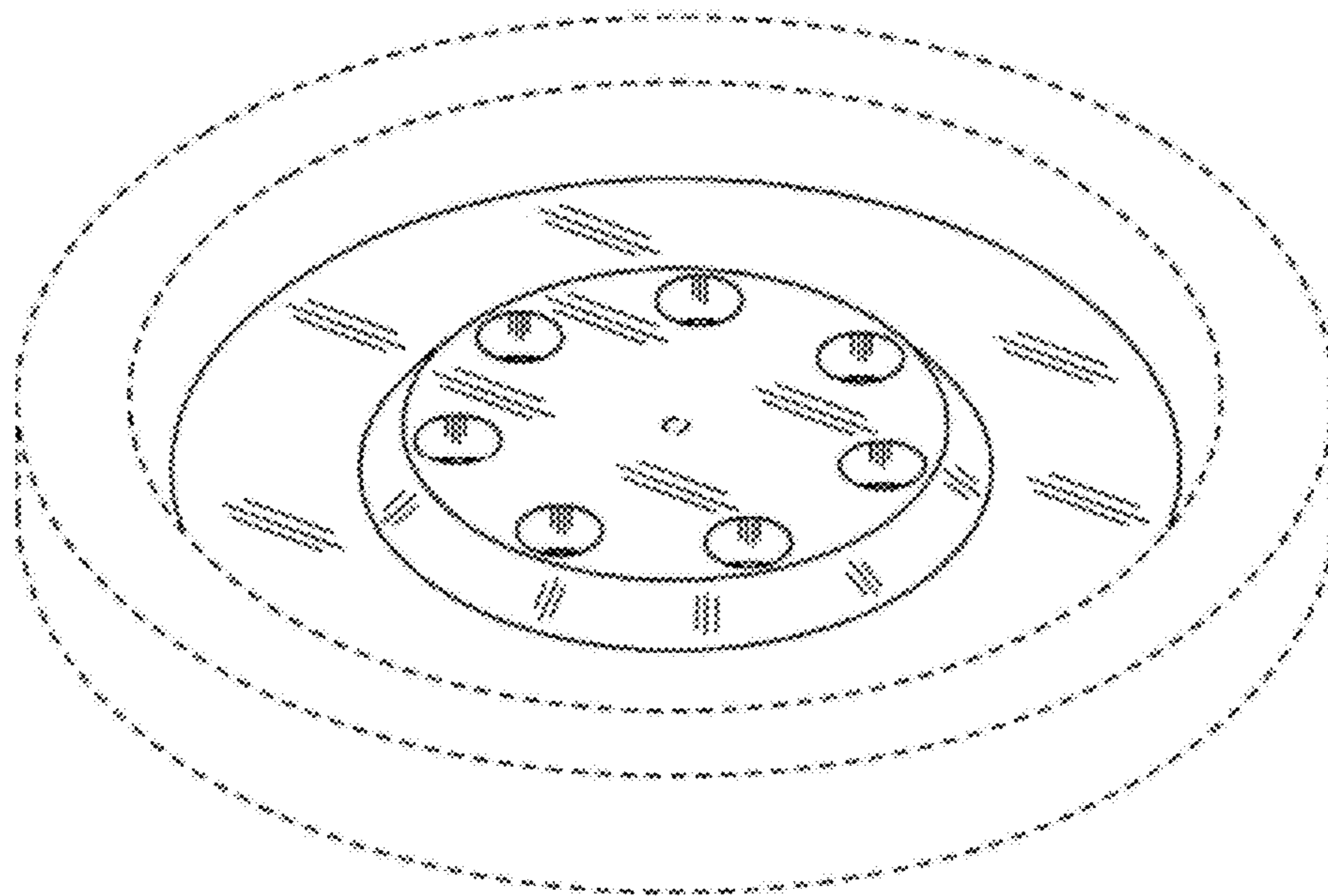


FIG. 1

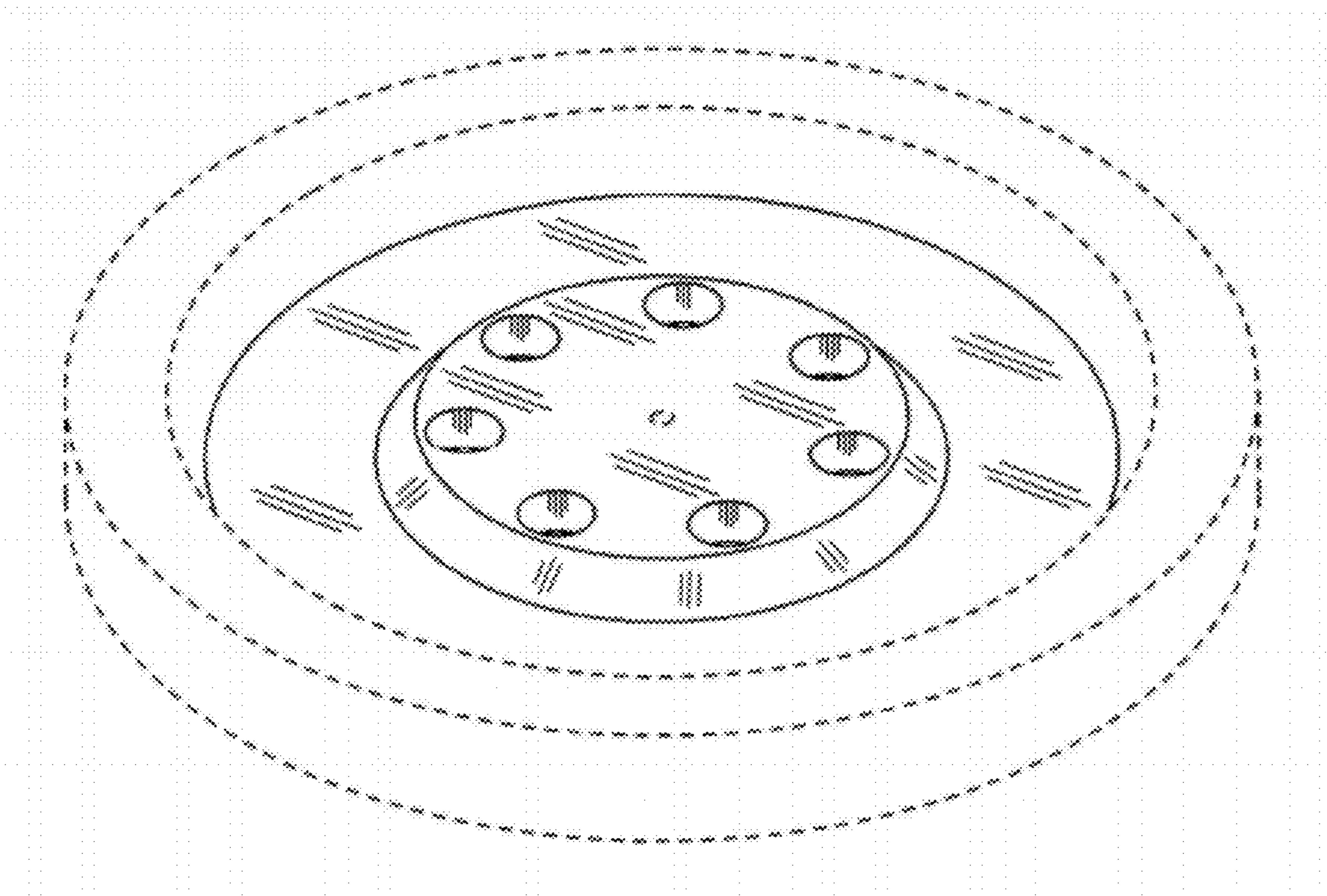


FIG. 2

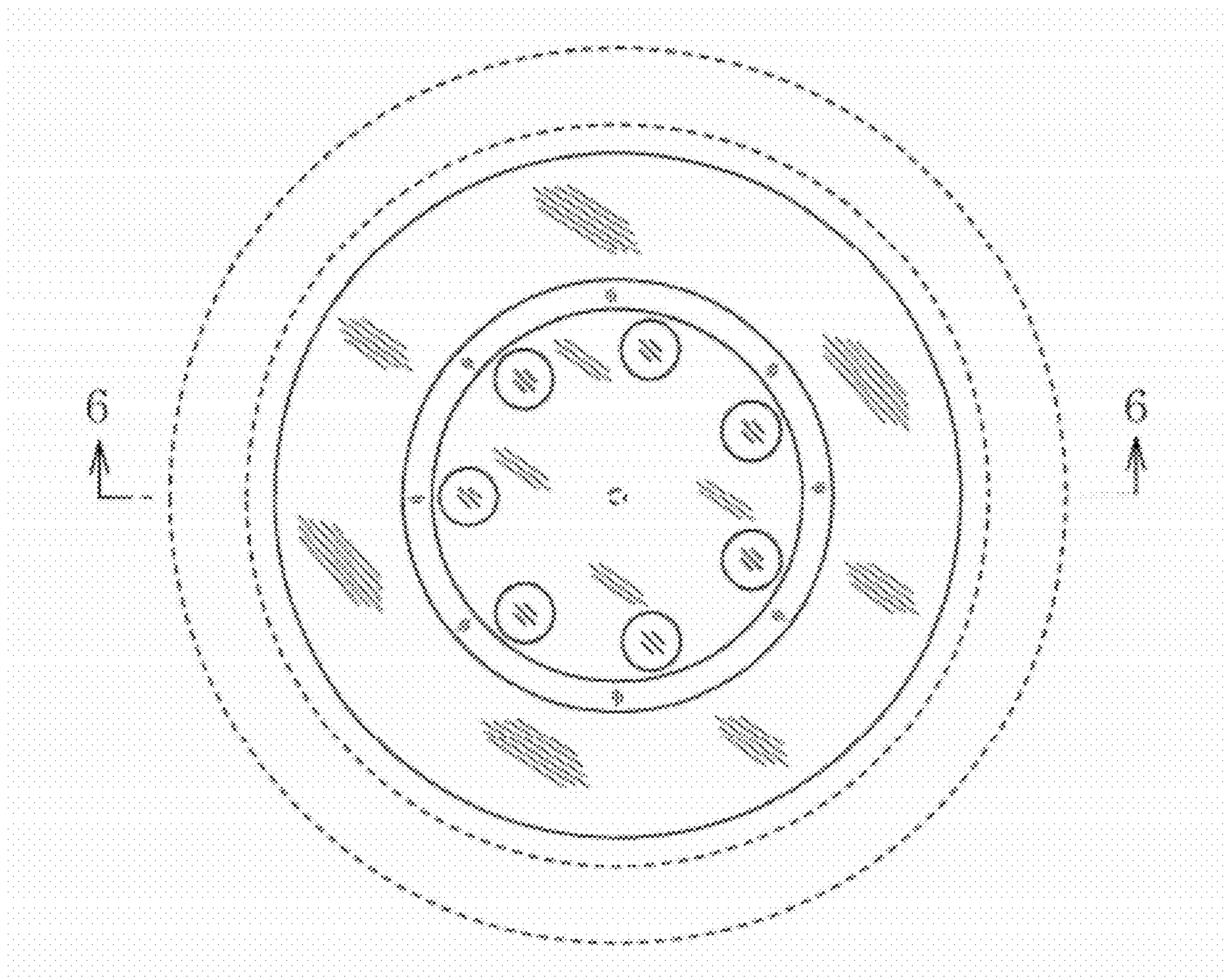




FIG. 3

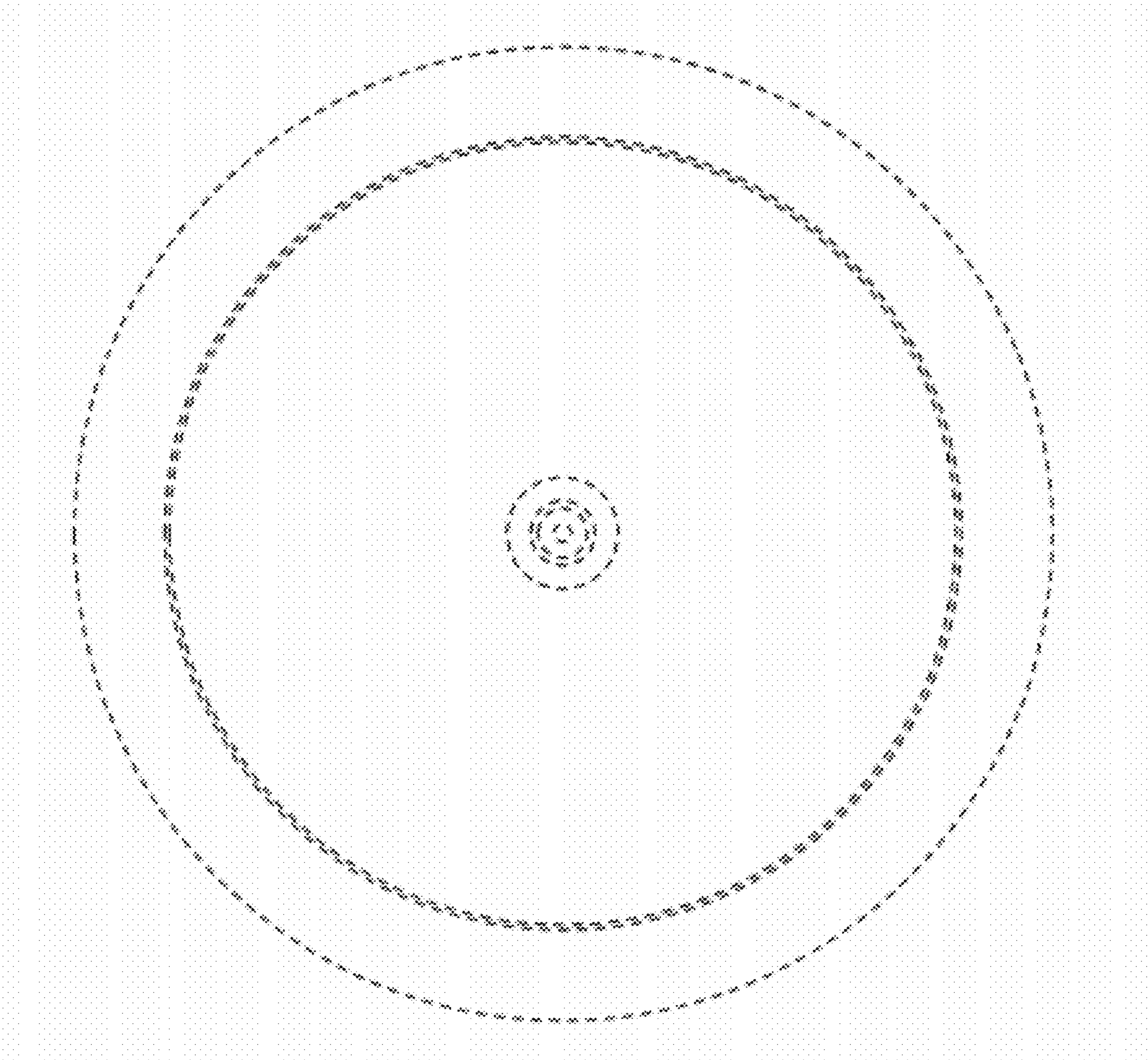


FIG. 4

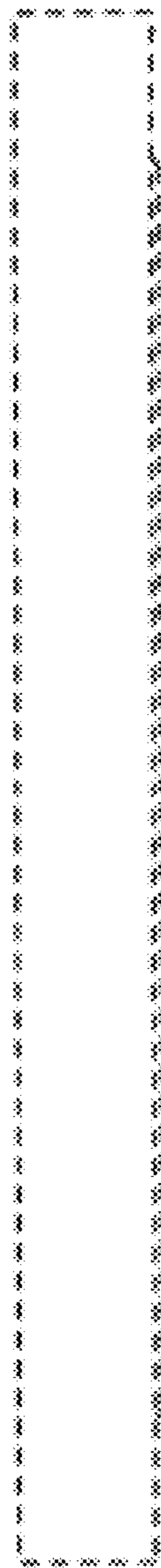


FIG. 5



FIG. 6

