



US00D669456S

(12) **United States Design Patent**
Li

(10) **Patent No.:** **US D669,456 S**
(45) **Date of Patent:** **** Oct. 23, 2012**

(54) **COVER OF AN ELECTRONIC DEVICE
HAVING SURFACE ORNAMENTATION**

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(75) Inventor: **Bin Li**, Shenzhen (CN)

(Continued)

(73) Assignee: **FIH (Hong Kong) Limited**, Kowloon
(HK)

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Flower garden quilt pattern of playing cards, © Pat Hickman 1979,
<URL:http://www.jra.org/resources/Hickman.htm>, retrieved from
internet on Aug. 2, 2012.*

(**) Term: **14 Years**

(Continued)

(21) Appl. No.: **29/423,552**

(22) Filed: **Jun. 1, 2012**

Primary Examiner — Carla Jobe Wright

(51) **LOC (9) Cl.** **14-03**

(74) *Attorney, Agent, or Firm* — Altis Law Group, Inc.

(52) **U.S. Cl.** **D14/248**

(57) **CLAIM**

(58) **Field of Classification Search** D14/248,
D14/138 R, 138 AA, 138 AB, 138 AC, 138 AD,
D14/138 C, 138 G, 203.7, 341–347, 250,
D14/440, 140, 240, 251–253, 439, 299, 238.1,
D14/217, 218, 168–169, 172, 496, 247, 203;
D3/201, 218, 269, 273, 301, 303, 248; D21/458,
D21/376; 455/575.1, 575.3, 575.4, 575.8,
455/556.1, 556.2; D25/103, 110, 111, 117,
D25/118, 145, 154, 155, 156, 157, 158; D5/2,
D5/3, 4, 7, 19, 23, 24, 32, 36, 39, 46, 48,
D5/55, 56, 66, 99, 37–38, 41, 58–63, 1, 47,
D5/26, 20, 57, 53, 12, 17, 11, 49, 5, 8, 27,
D5/64, 65; D13/103, 108, 107; D6/595,
D6/596, 601, 602, 603, 610, 611, 612, 616,
D6/617, 621, 622, 615, 585; D11/22, 96,
D11/131, 132, 140, 1, 125; D32/40; D12/213;
15/215; 428/30, 38; 52/506.06, 316; D1/104,
D1/105, 108; D2/741; D24/208; D29/108
See application file for complete search history.

The ornamental design for a cover of an electronic device
having surface ornamentation, as shown and described.

DESCRIPTION

The present application is related to copending U.S. patent
applications Ser. Nos. 29/423,547, 29/423,548, 29/423,549,
29/423,551, 29/423,555, 29/423,557, 29/423,559, and
29/423,560, each, entitled “Cover of an Electronic Device
Having Surface Ornamentation”, by Bin Li. These applica-
tions have the same assignee as the present application and
have been concurrently filed herewith. The above-identified
applications are incorporated herein by reference.

FIG. 1 is a perspective view of a cover of an electronic device
having surface ornamentation showing my new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a side elevational view thereof; and,

FIG. 4 is an enlarged fragmentary view thereof showing
circled portion 4 in FIG. 2.

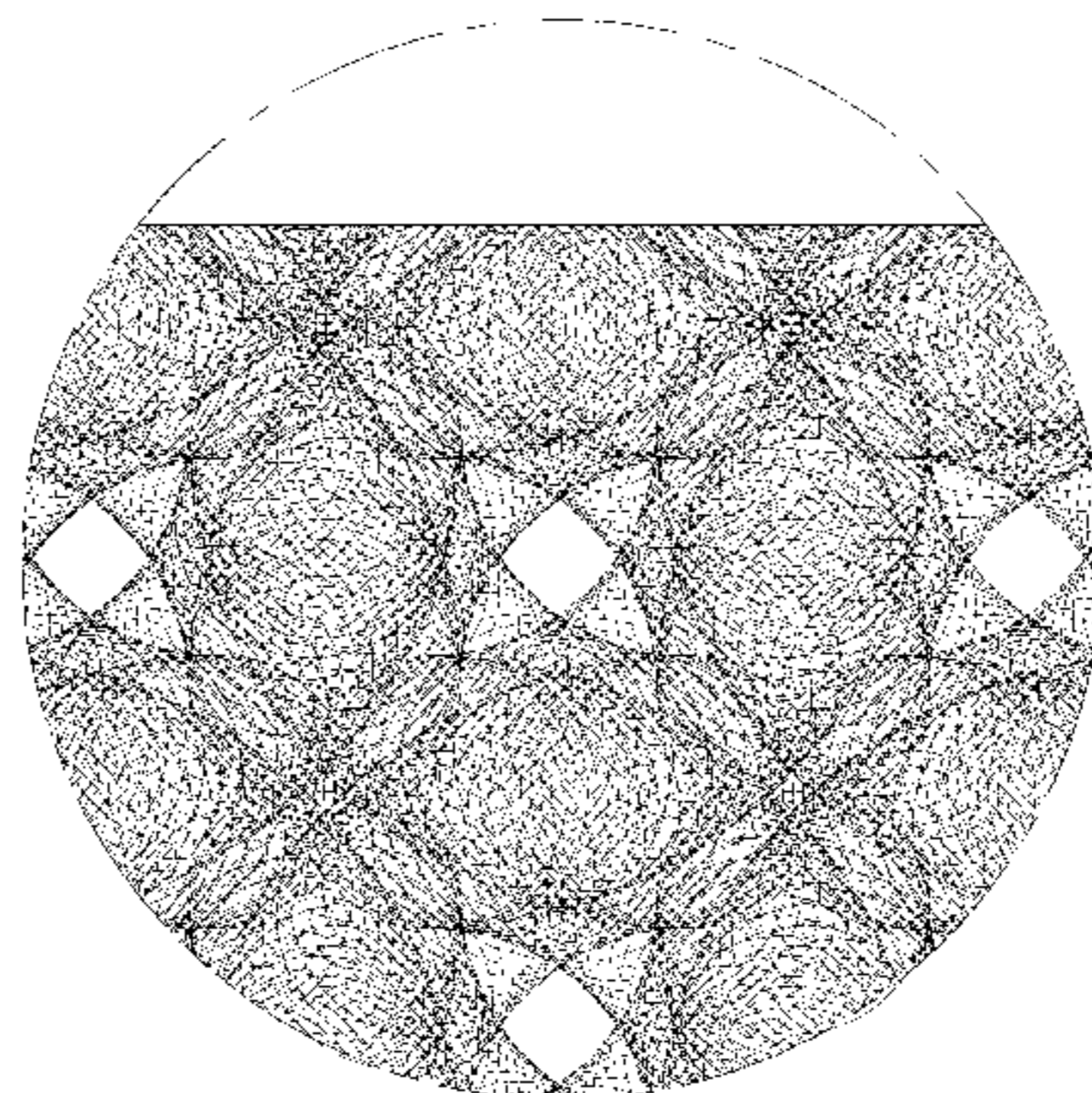
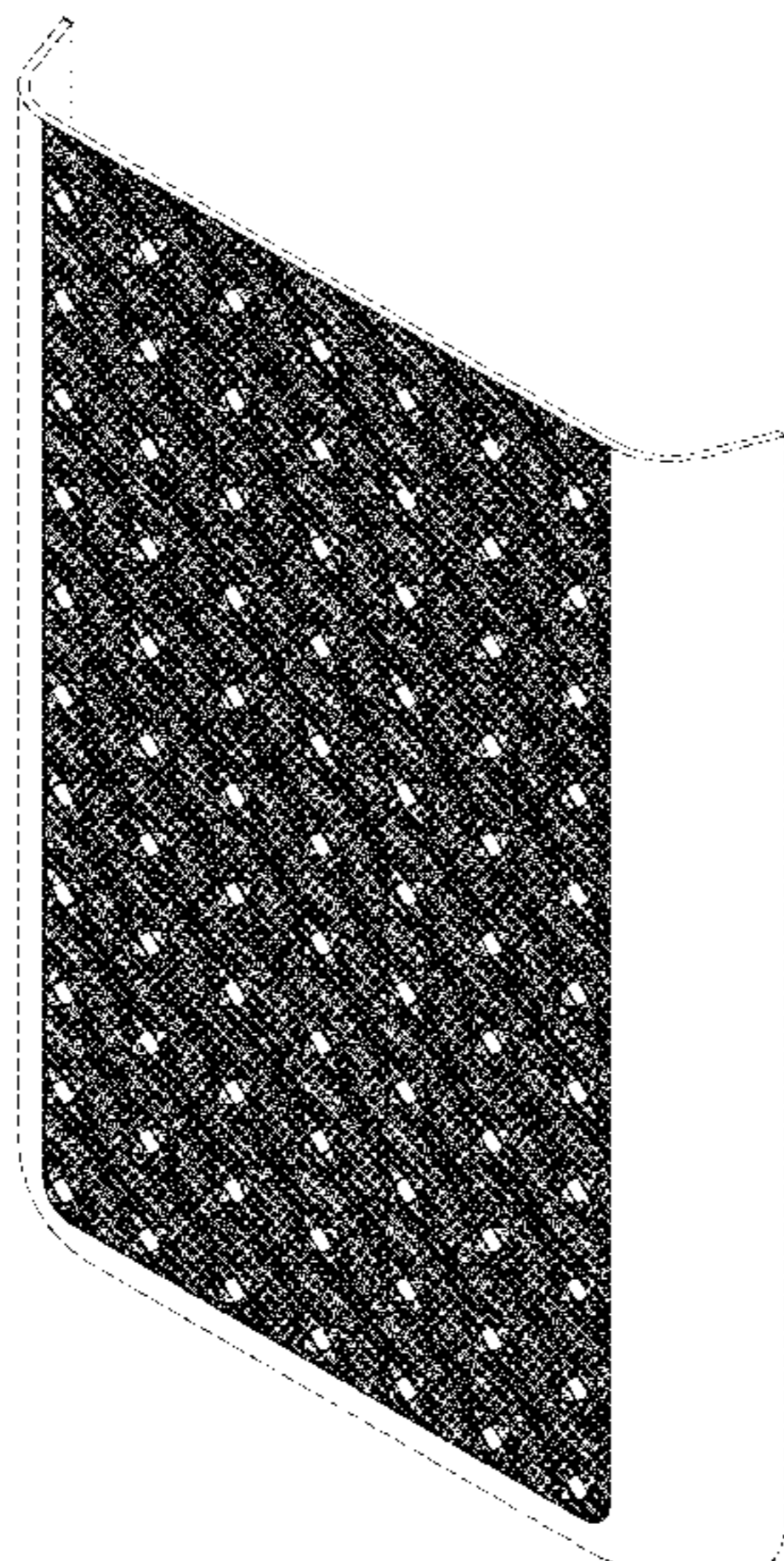
In FIGS. 1-3, the evenly segmented broken lines indicate
portions of the cover of an electronic device having surface
ornamentation that form no part of the claimed design. In
FIGS. 2 and 4, the unevenly segmented broken lines indicate
the portion of the design that is shown on an enlarged scale in
FIG. 4. The broken lines form no part of the claimed design.

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1 Claim, 4 Drawing Sheets



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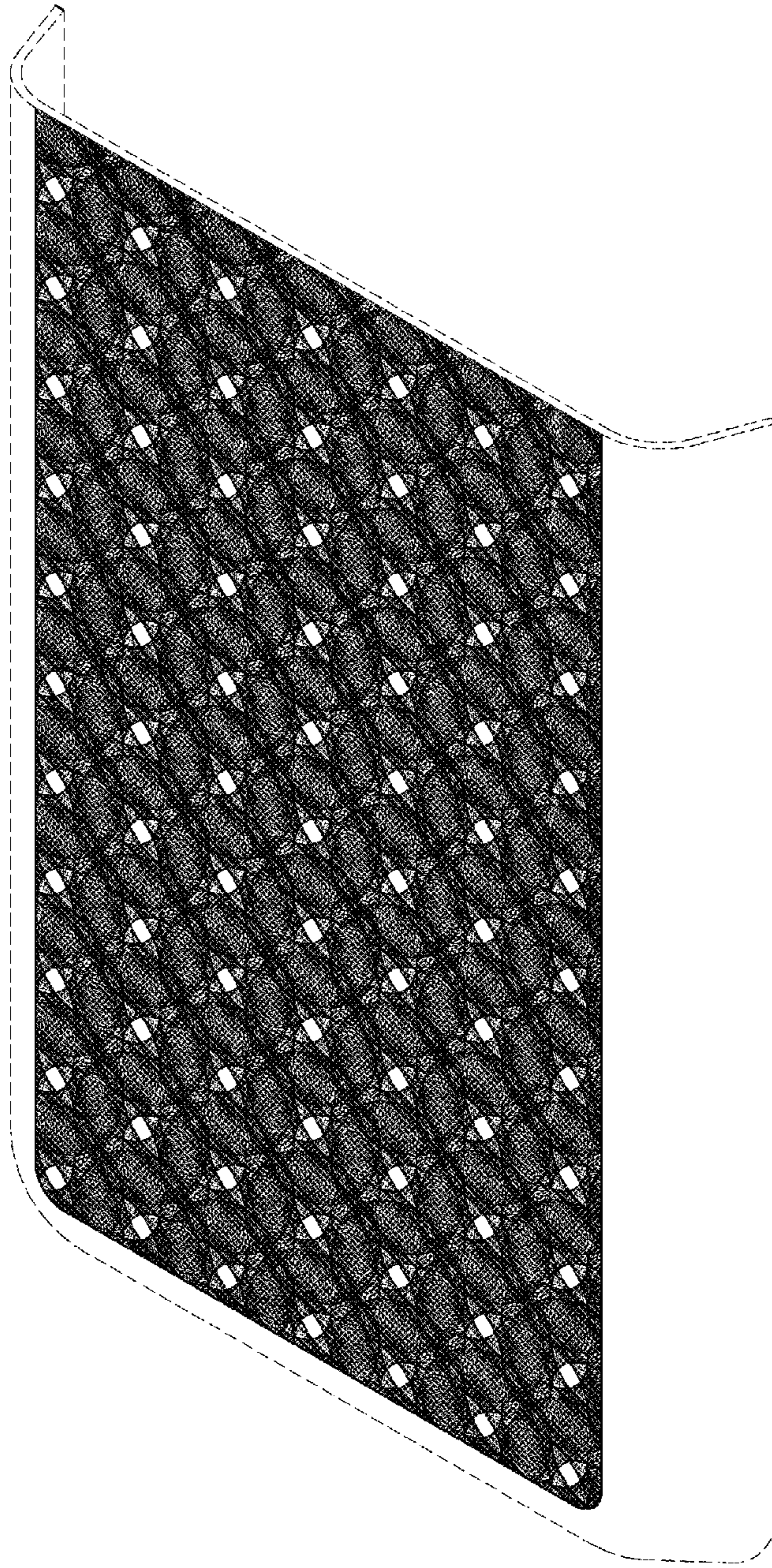


FIG. 1

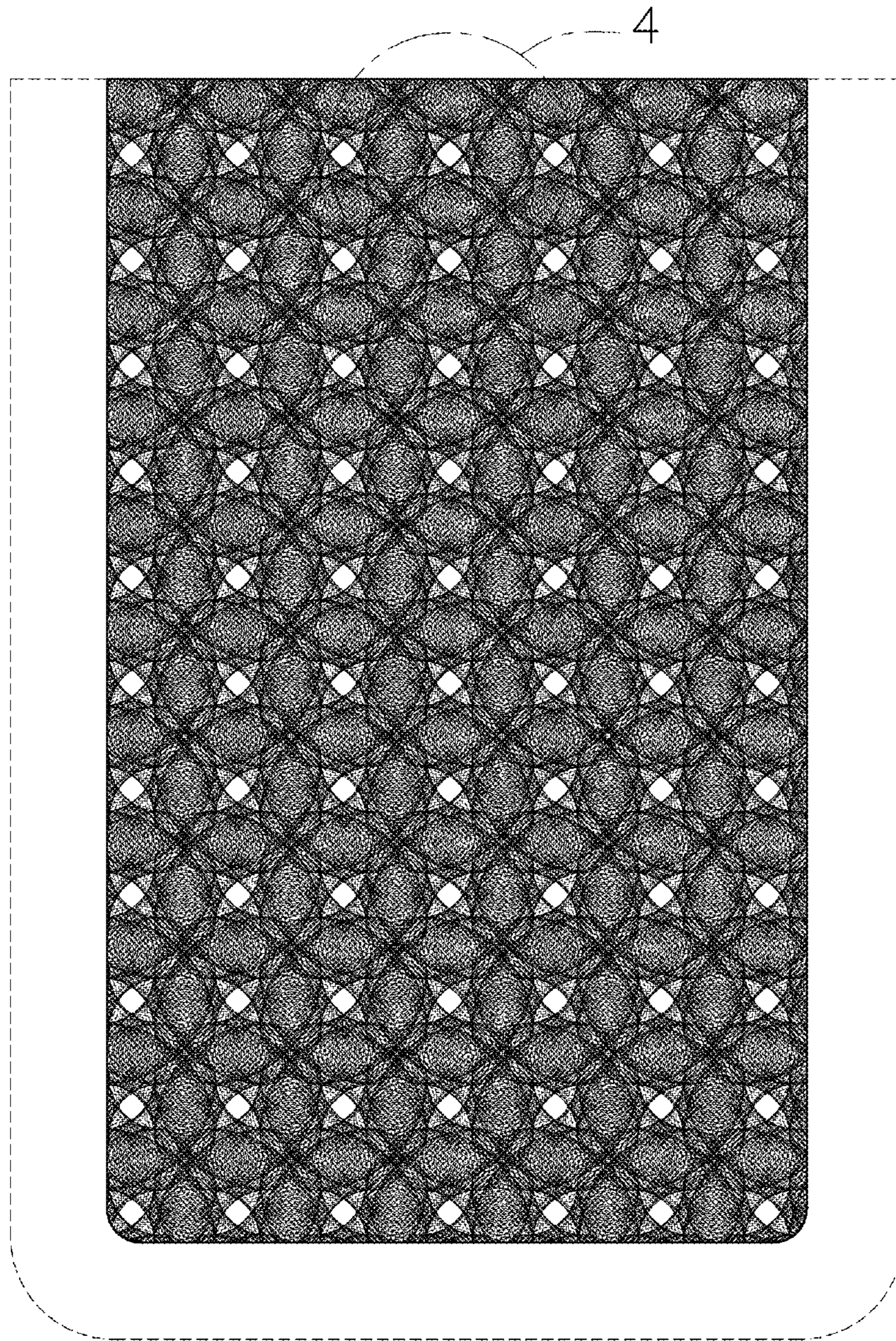


FIG. 2

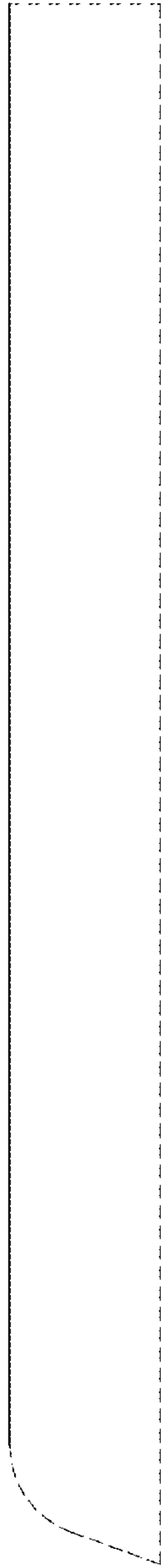


FIG. 3

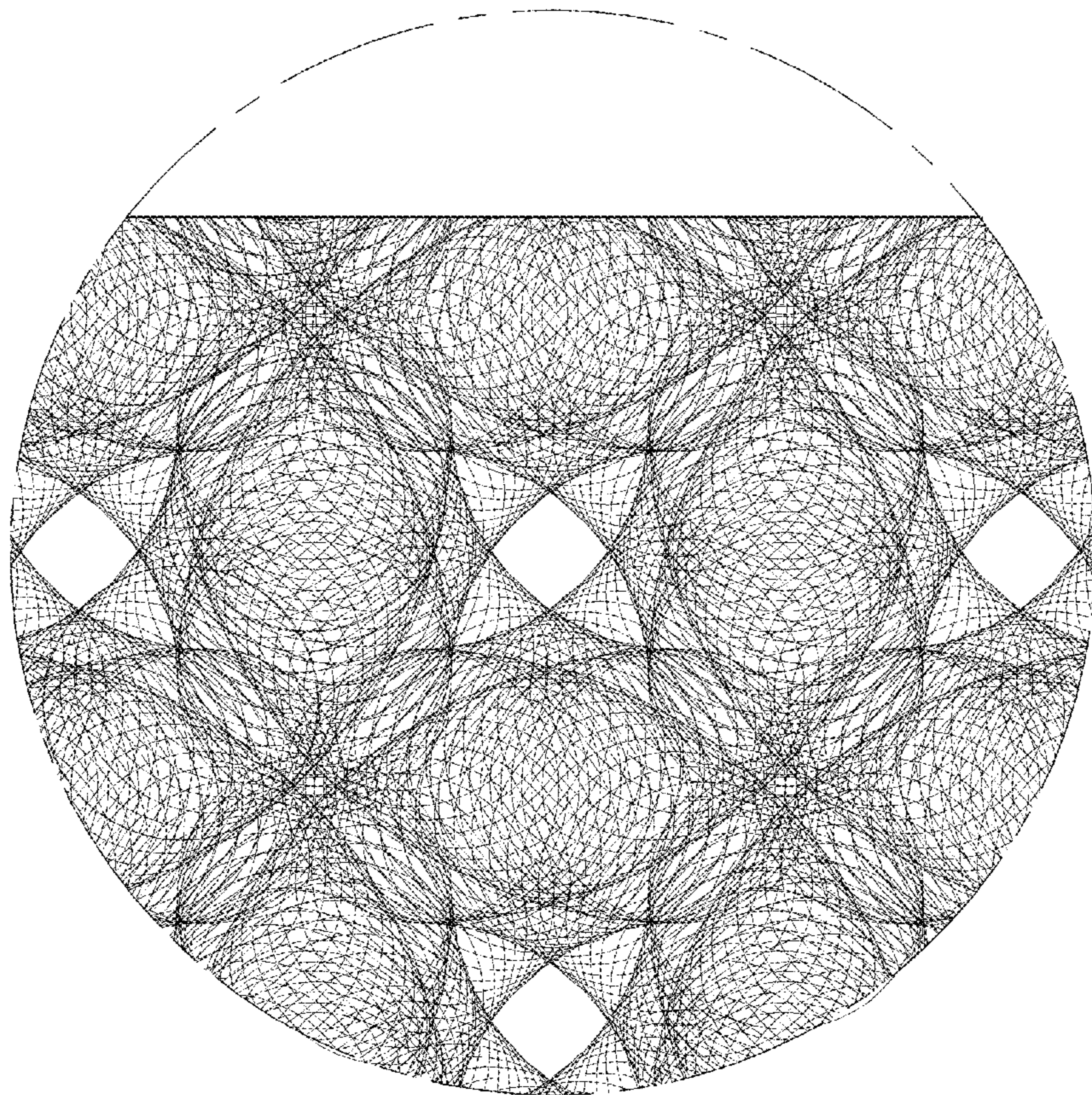


FIG. 4