

US00D668426S

(12) **United States Design Patent**
Zepf

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(45) **Date of Patent:** **** Oct. 9, 2012**

(54) **CHIP**

(76) Inventor: **Frank Zepf**, Palm Harbor, FL (US)

(**) Term: **14 Years**

(21) Appl. No.: **29/404,533**

(22) Filed: **Oct. 21, 2011**

(51) **LOC (9) Cl.** **01-01**

(52) **U.S. Cl.** **D1/125; D1/128**

(58) **Field of Classification Search** D1/100-199;
D13/100; D30/160; D24/100-103; D25/119-135;
D28/8.2; D21/386, 707; 426/89, 94-95,
426/128, 138-139, 144, 293, 383, 438-439,
426/446, 450, 496, 502-504, 549, 556, 805,
426/808; 119/702, 706-711

See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a chip, as shown and described.

DESCRIPTION

FIG. 1 is a left-side, front perspective view of a chip showing my new design;

FIG. 2 is a bottom, left-side, front perspective view thereof;

FIG. 3 is a top, right side front perspective view thereof;

FIG. 4 is a top plan view thereof;

FIG. 5 is a bottom plan view thereof;

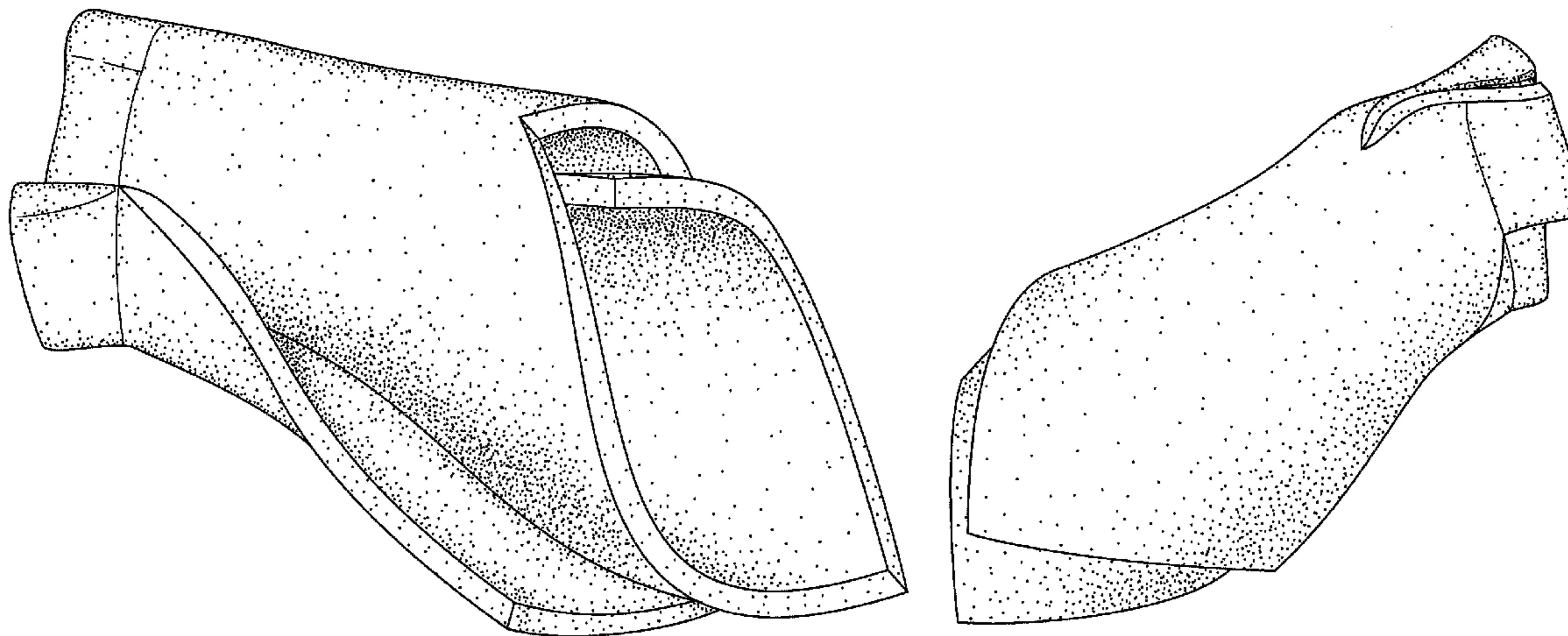
FIG. 6 is a front plan view thereof;

FIG. 7 is a rear plan view thereof

FIG. 8 is a left-side plan view thereof; and,

FIG. 9 is a right-side plan view thereof.

1 Claim, 5 Drawing Sheets



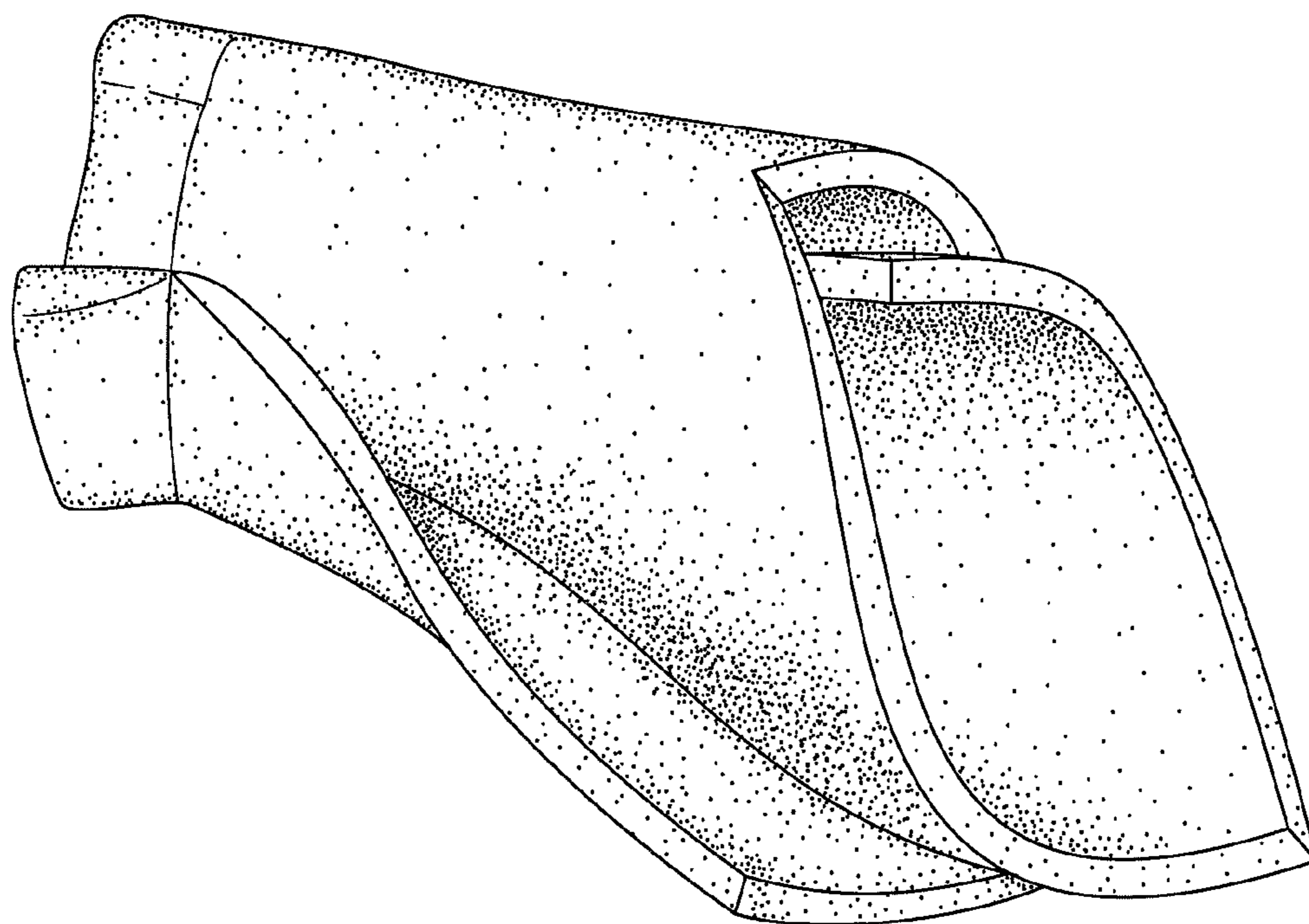


Fig. 1

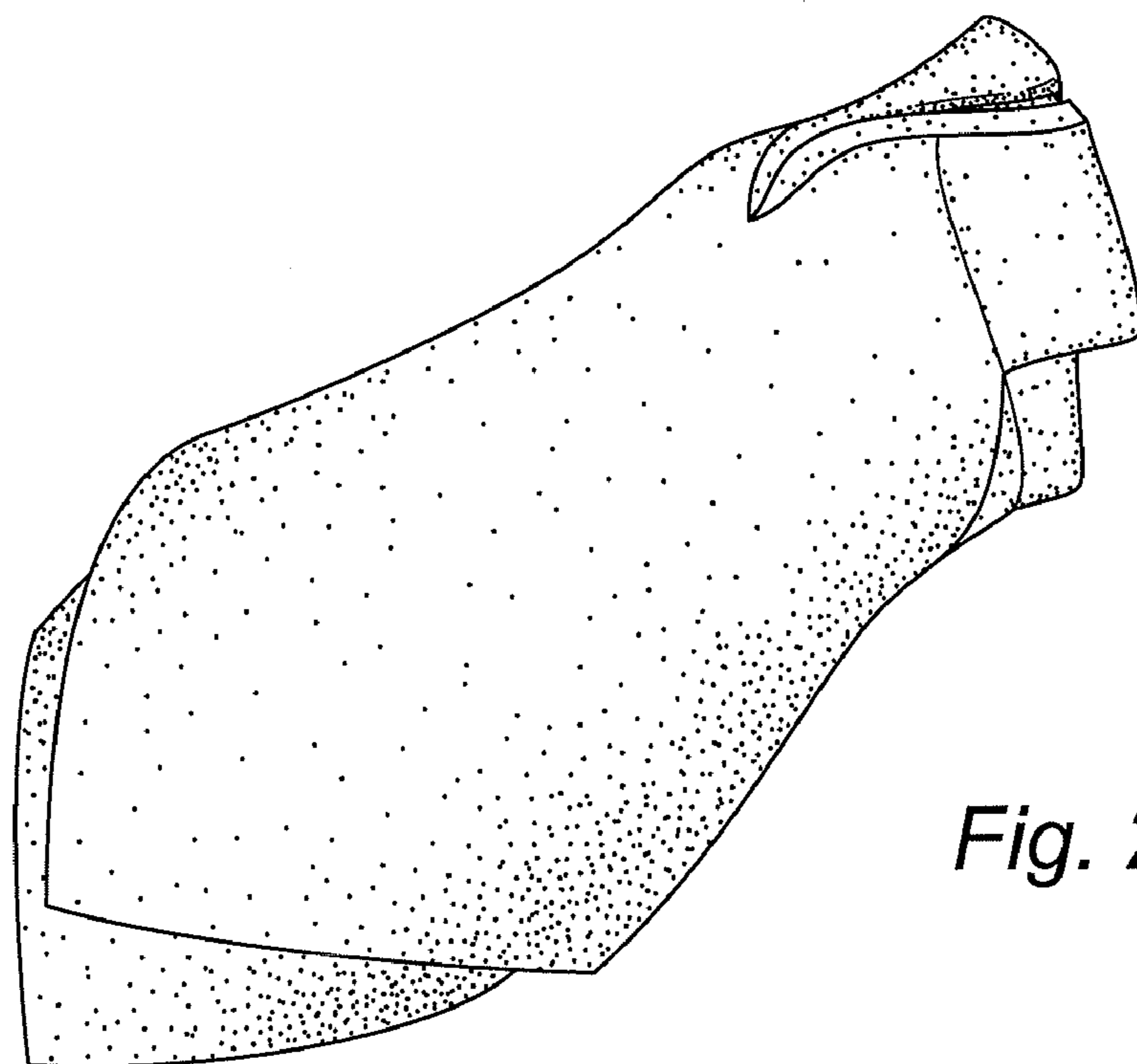


Fig. 2

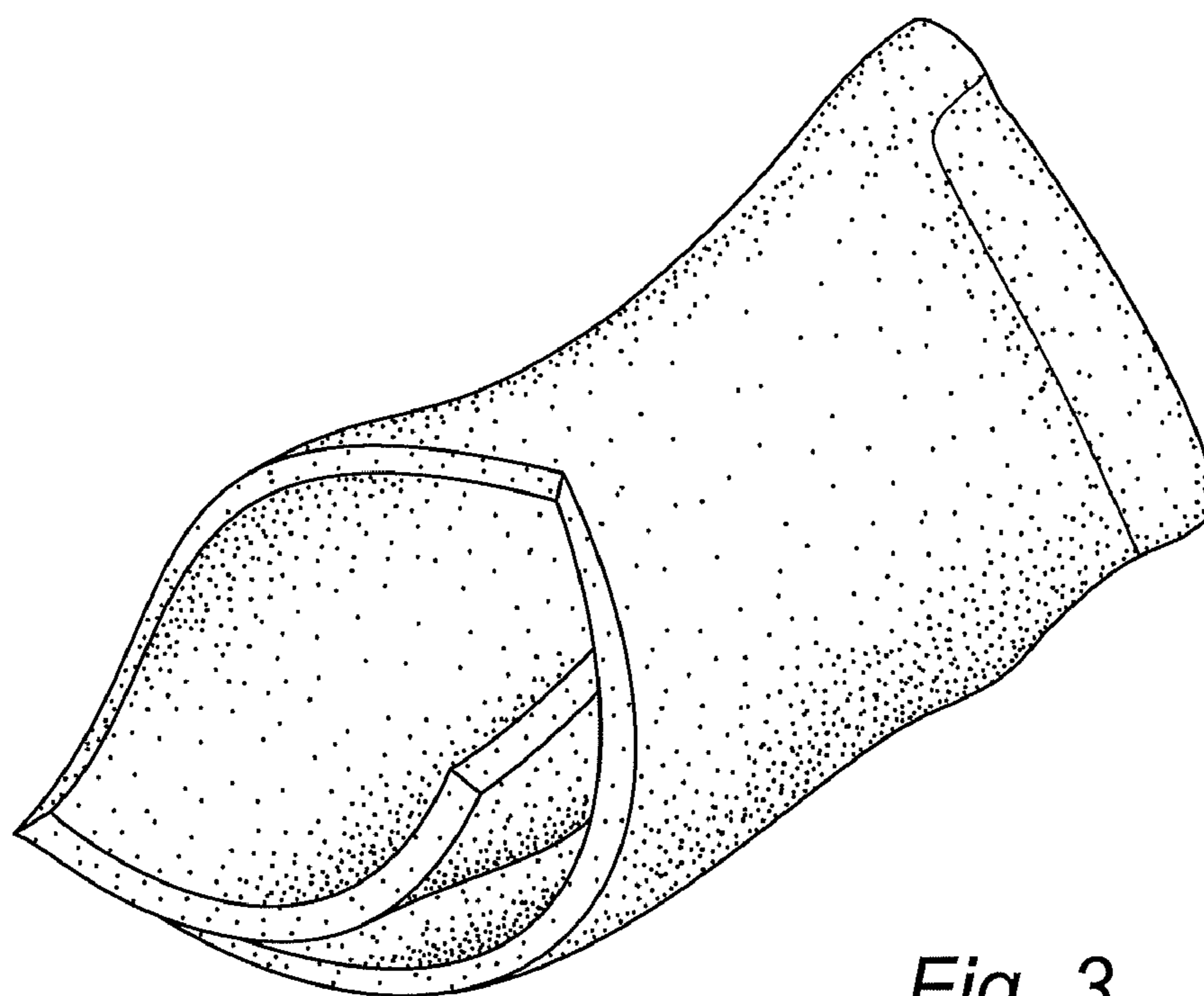


Fig. 3

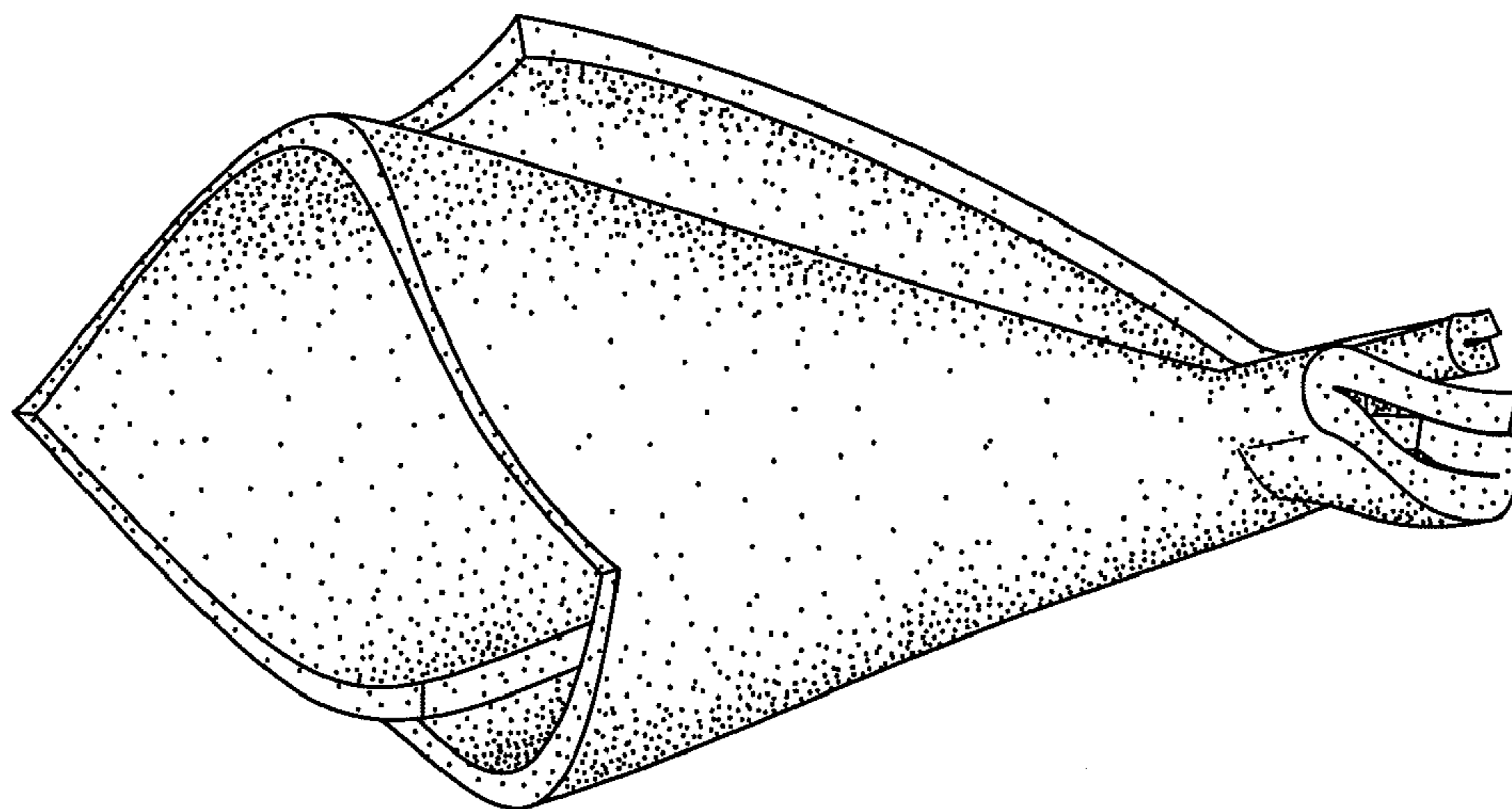


Fig. 4

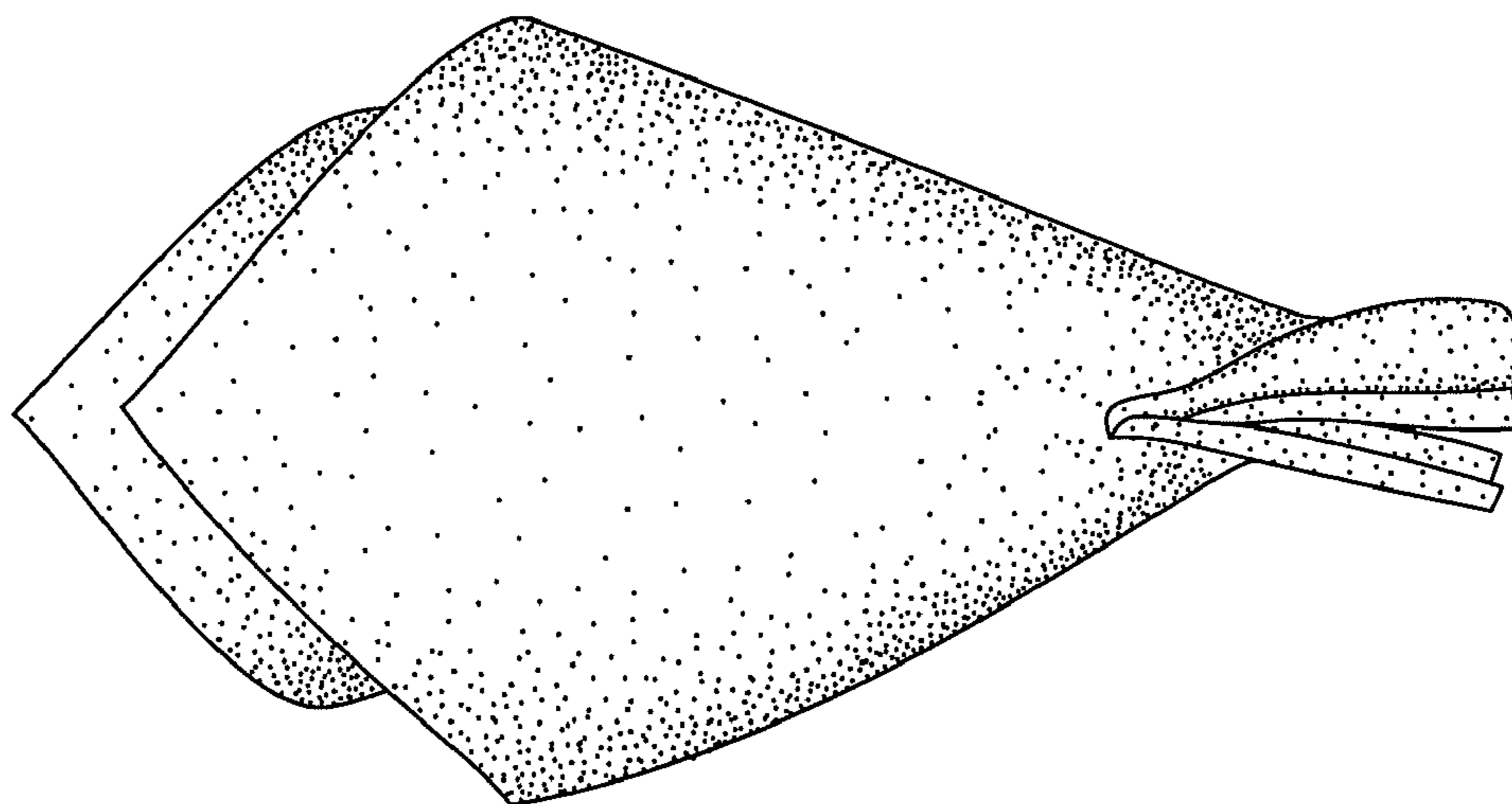


Fig. 5

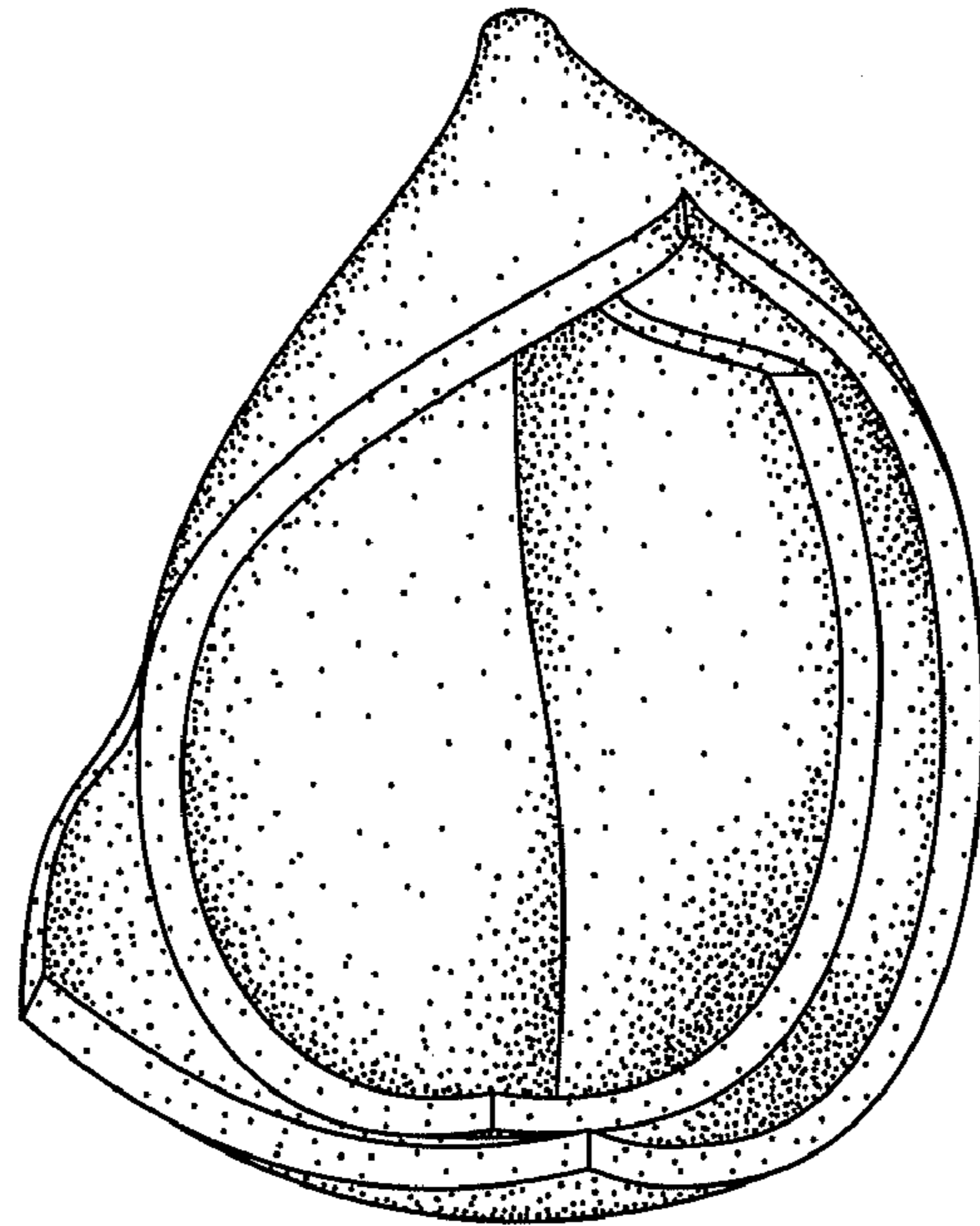


Fig. 6

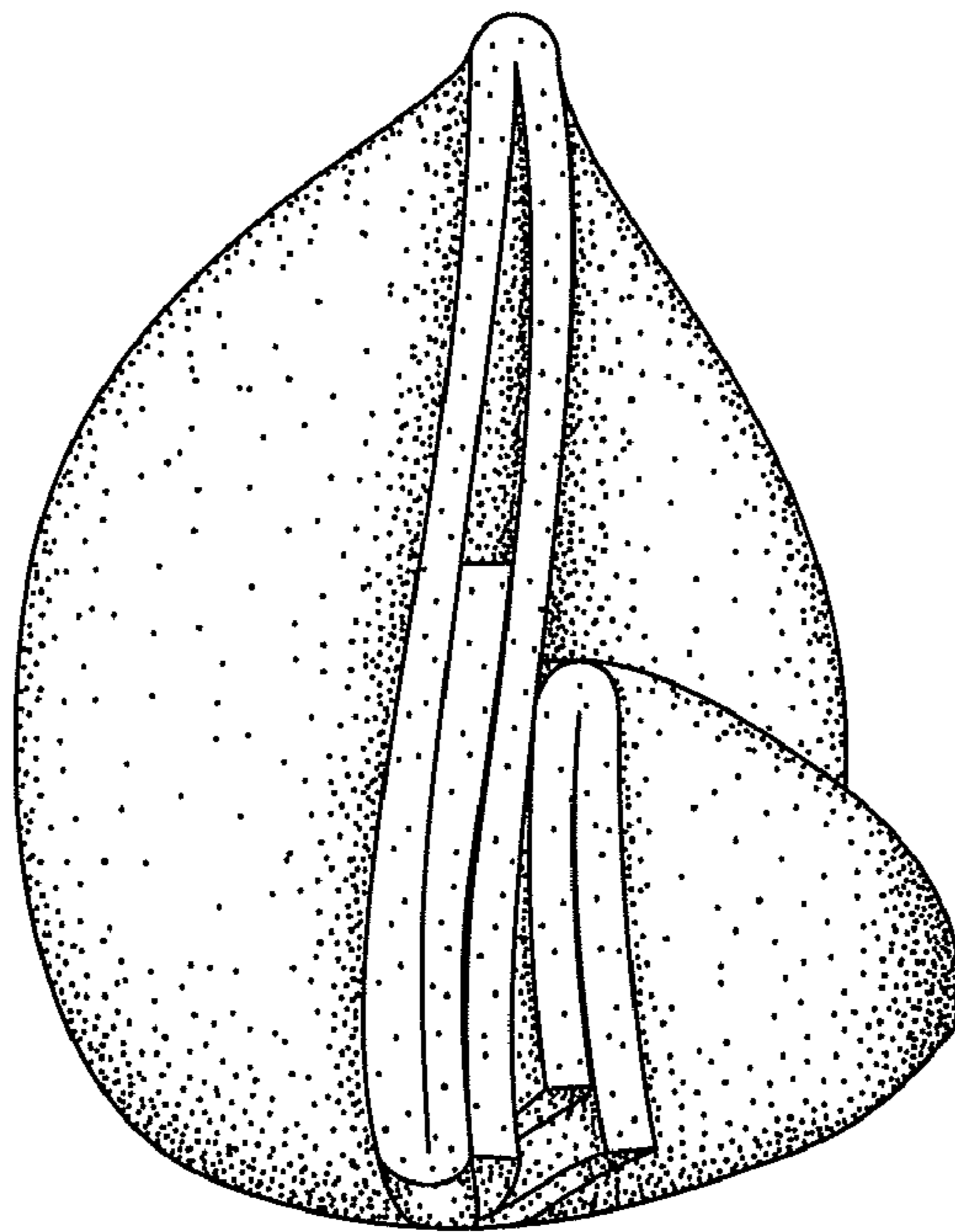


Fig. 7

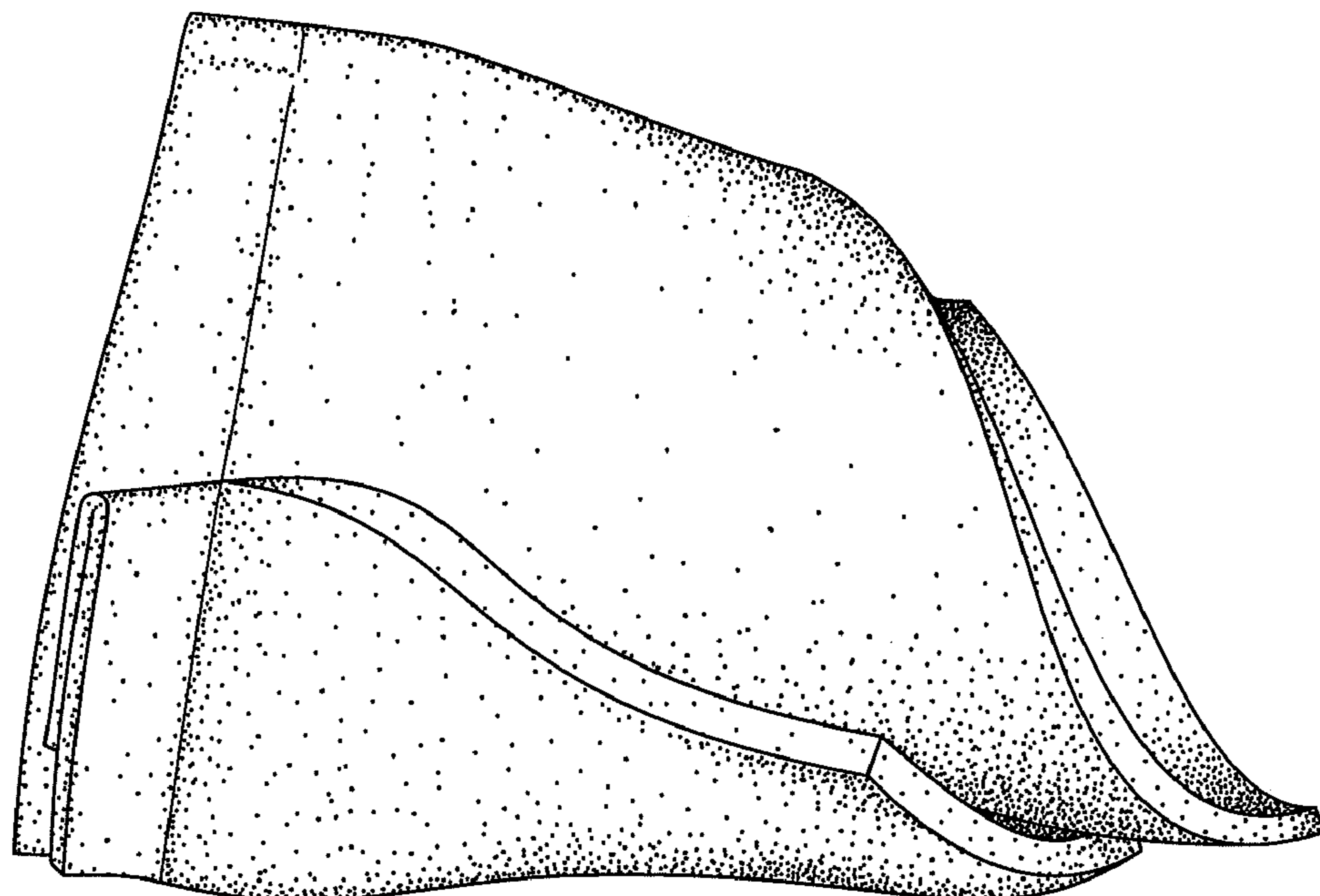


Fig. 8

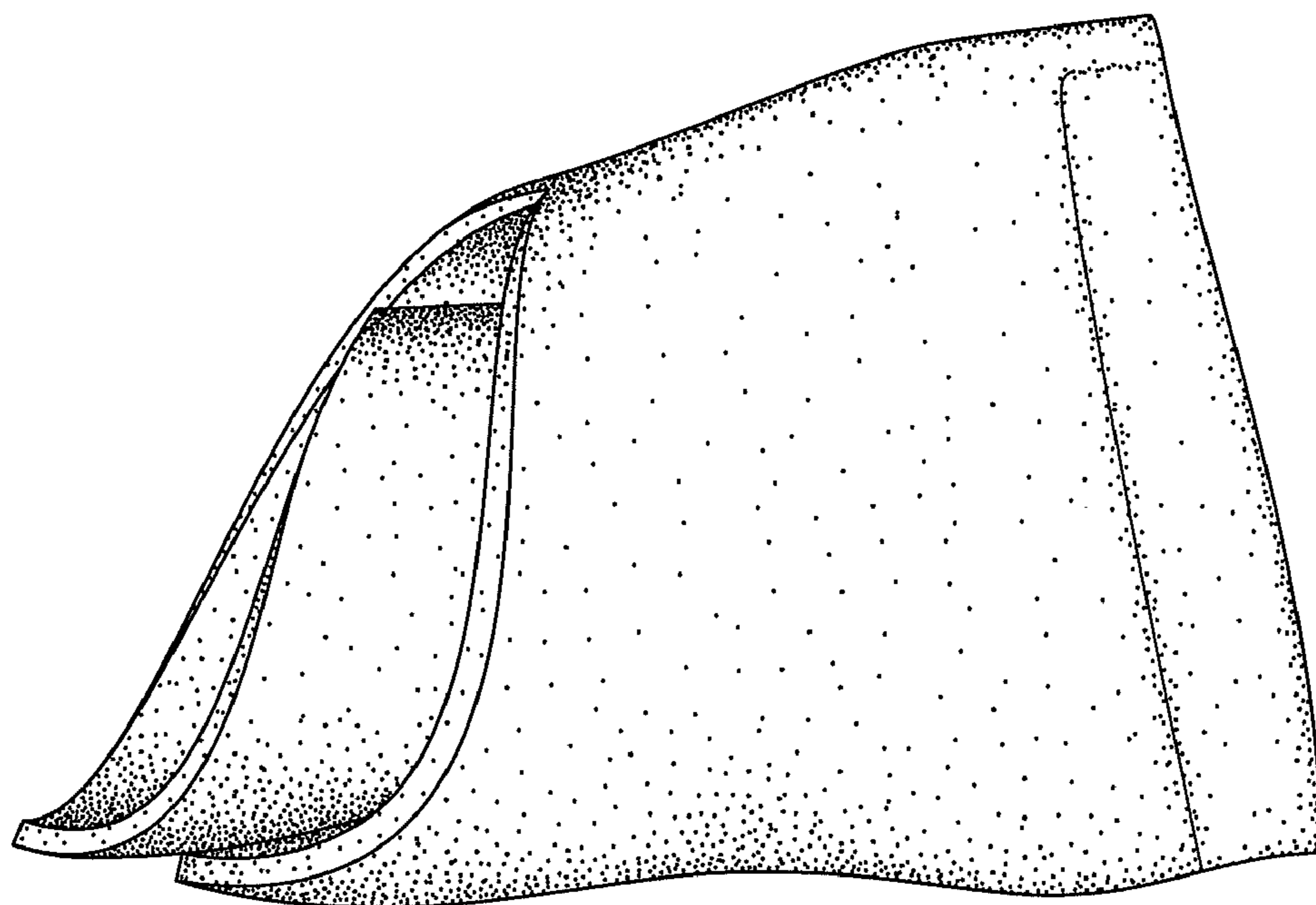


Fig. 9