



US00D666189S

(12) **United States Design Patent**
Li

(10) **Patent No.:** **US D666,189 S**
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(54) **COVER OF AN ELECTRONIC DEVICE
HAVING SURFACE ORNAMENTATION**

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(**) Term: **14 Years**

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(51) **LOC (9) Cl.** **14-03**

(52) **U.S. Cl.** **D14/248**

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D14/217, 218, 168-169, 172, 496, 247, 203;
D3/201, 218, 269, 273, 301, 303, 248; D21/458;
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D25/103, 110, 111, 117, 118, 145, 154, 155,
D25/156, 157, 158; D5/2, 3, 4, 7, 19, 23,
D5/24, 32, 36, 39, 46, 48, 55, 56, 66, 99,
D5/37-38, 41, 58-63, 1, 47, 26, 20, 57, 53,
D5/12, 17, 11, 49, 5, 8, 27, 64, 65; D13/103,
D13/108, 107; D6/595, 596, 601, 602, 603,
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D11/22, 96, 131, 132, 140; D32/40; D12/213;
15/215; 428/30

See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a cover of an electronic device
having surface ornamentation, as shown and described.

DESCRIPTION

The present application is related to copending U.S. patent
application Ser. Nos. 29/415,338, 29/415,537, 29/415,532,
29/415,534, 29/415,340, 29/415,530, 29/415,529, 29/415,
528, 29/415,526, 29/415,523, 29/415,521, 29/415,634,
29/415,633, 29/415,520, 29/415,632, 29/415,631, 29/415,
629, 29/415,628, 29/415,635, 29/415,630, 29/415,636, each
entitled “Cover of an Electronic Device Having Surface
Ornamentation”, by Bin Li. These applications have the same
assignee as the present application and have been concurr-
ently filed herewith. The above-identified applications are
incorporated herein by reference.

FIG. 1 is a perspective view of a cover of an electronic device
having surface ornamentation showing my new design;

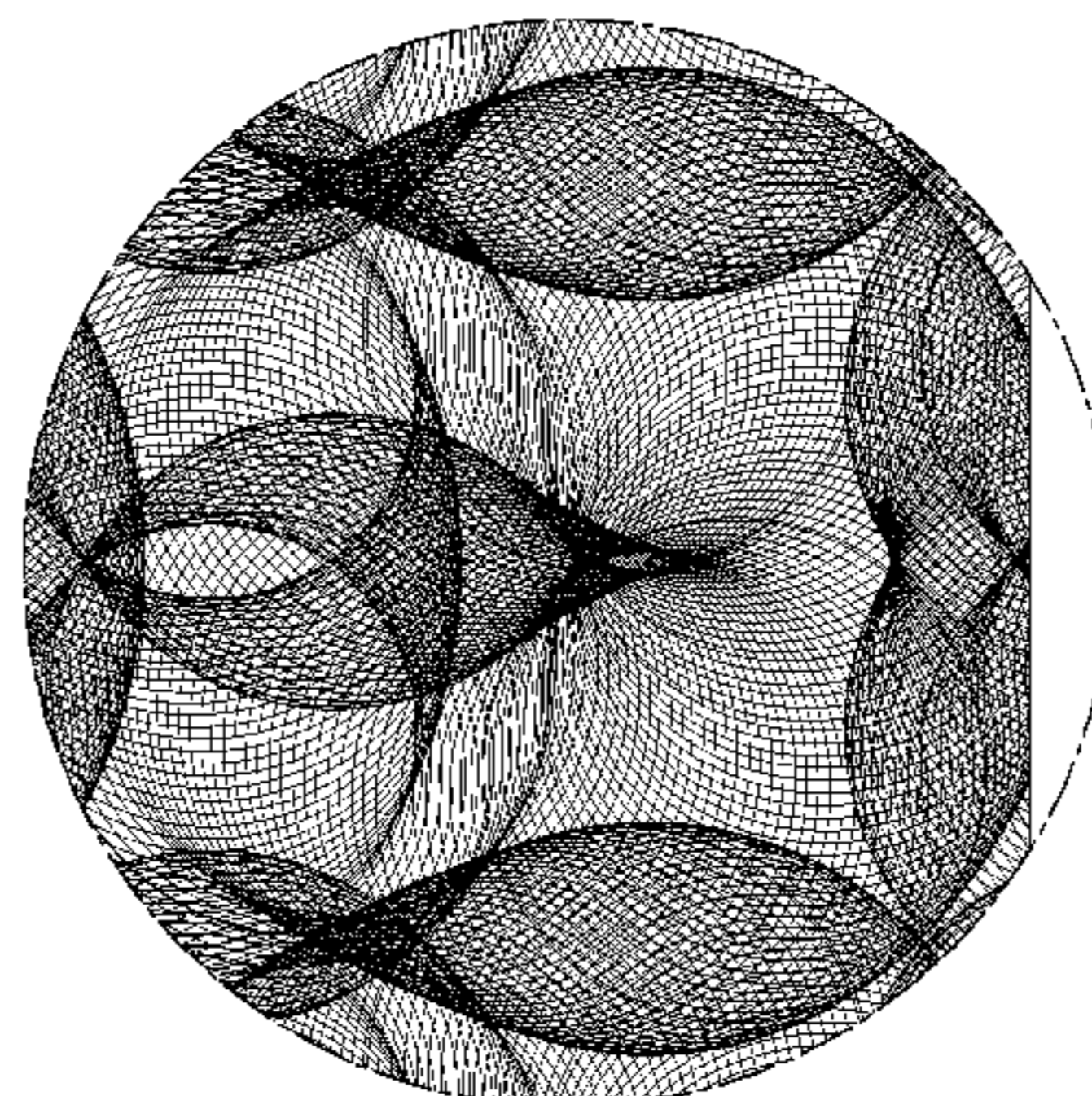
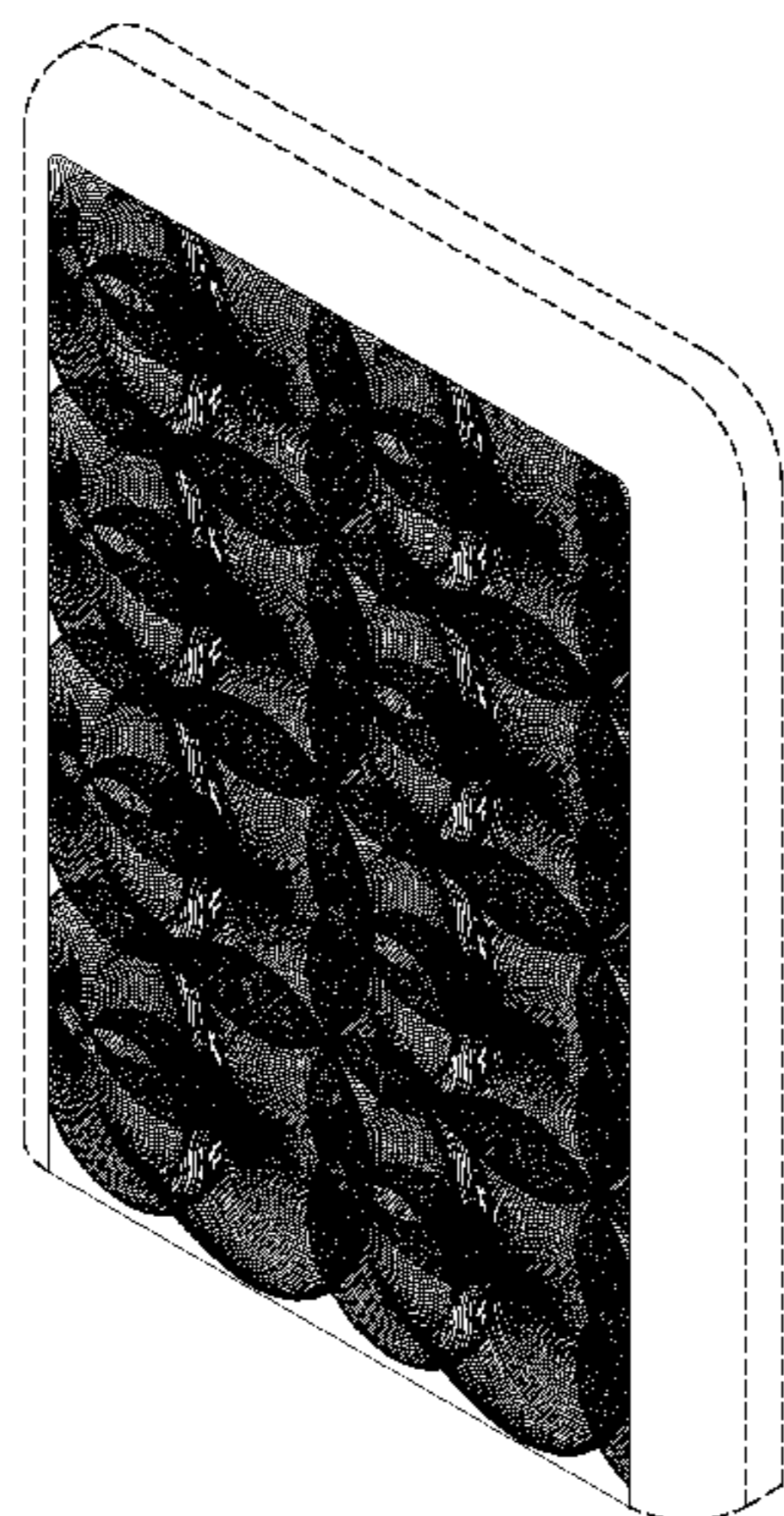
FIG. 2 is a front elevational view thereof;

FIG. 3 is a side elevational view thereof; and,

FIG. 4 is an enlarged fragmentary view thereof showing
circled portion 4 in FIG. 2.

In FIGS. 1-3, the evenly segmented broken lines indicate
portions of the cover of an electronic device having surface
ornamentation that form no part of the claimed design. In
FIGS. 2 and 4, the unevenly segmented broken lines indicate
the portion of the cover of an electronic device having surface
ornamentation that is shown on an enlarged scale in FIG. 4.
The broken lines form no part of the claimed design.

1 Claim, 4 Drawing Sheets



US D666,189 S

Page 2

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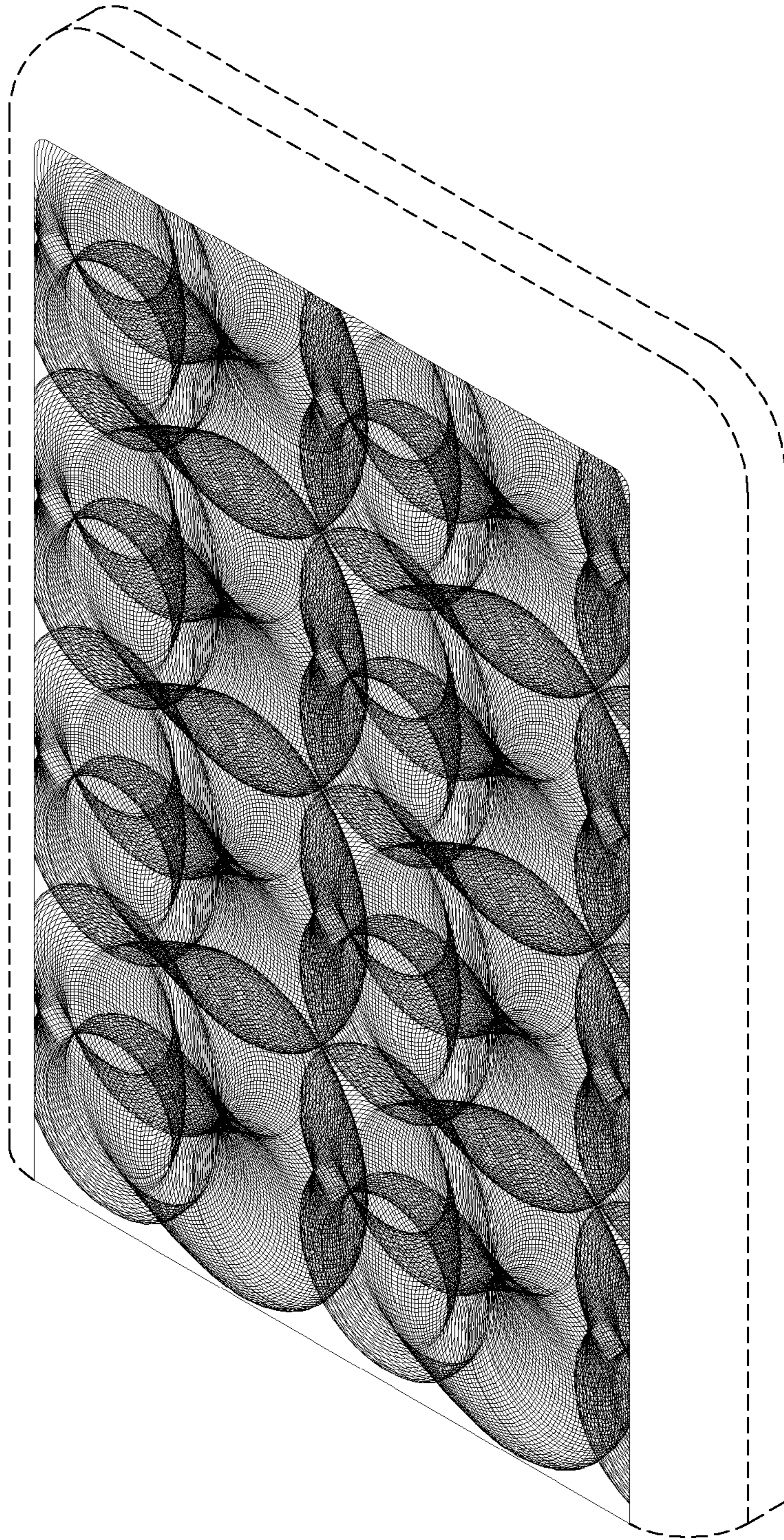


FIG. 1

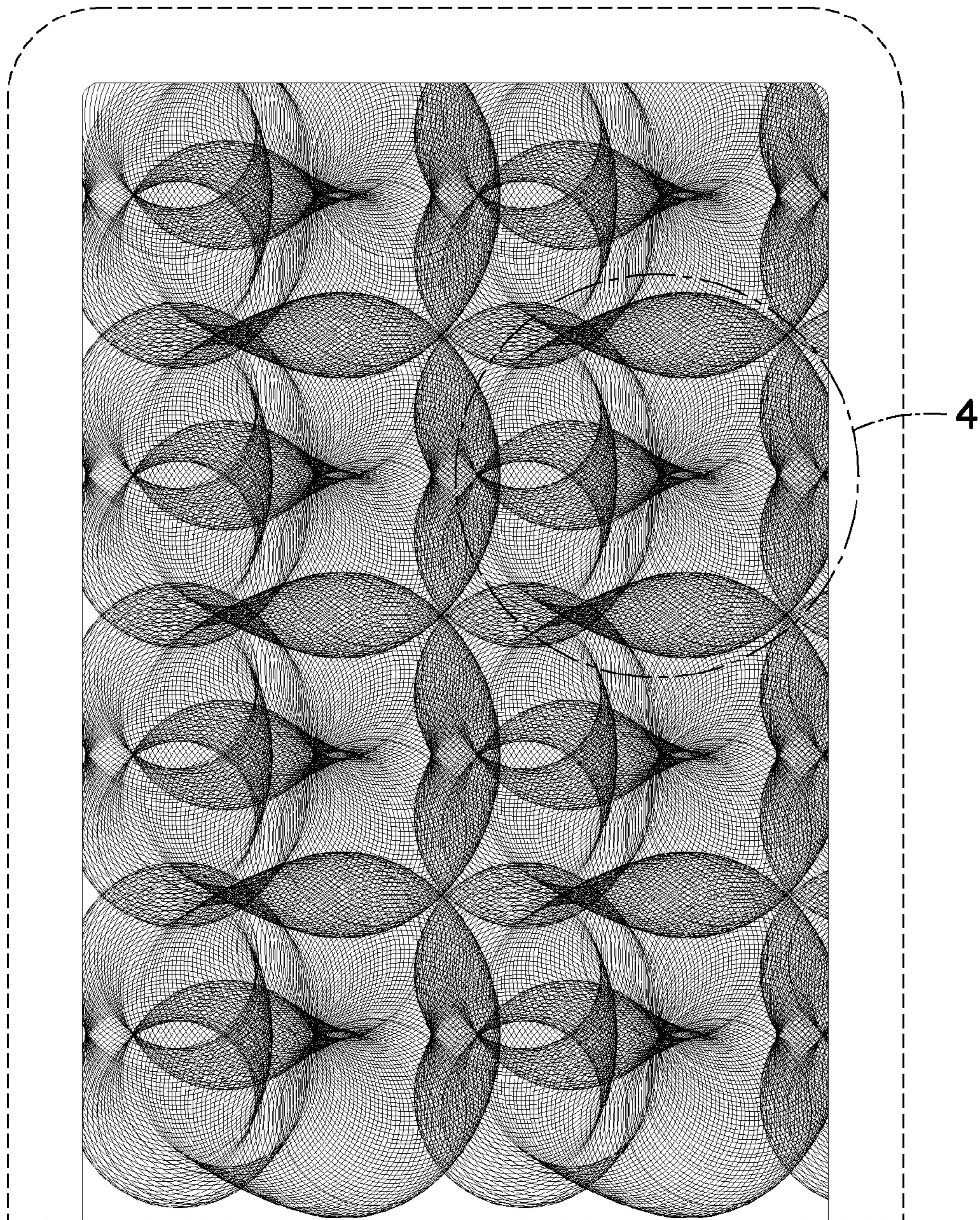


FIG. 2

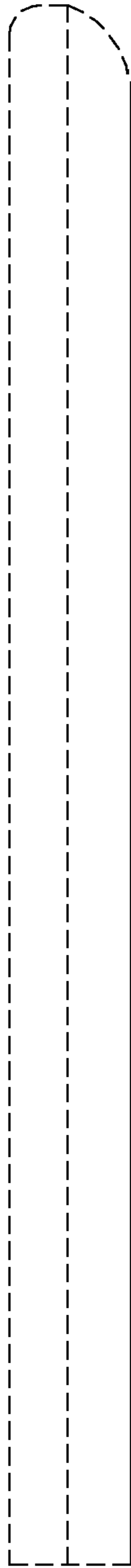


FIG. 3

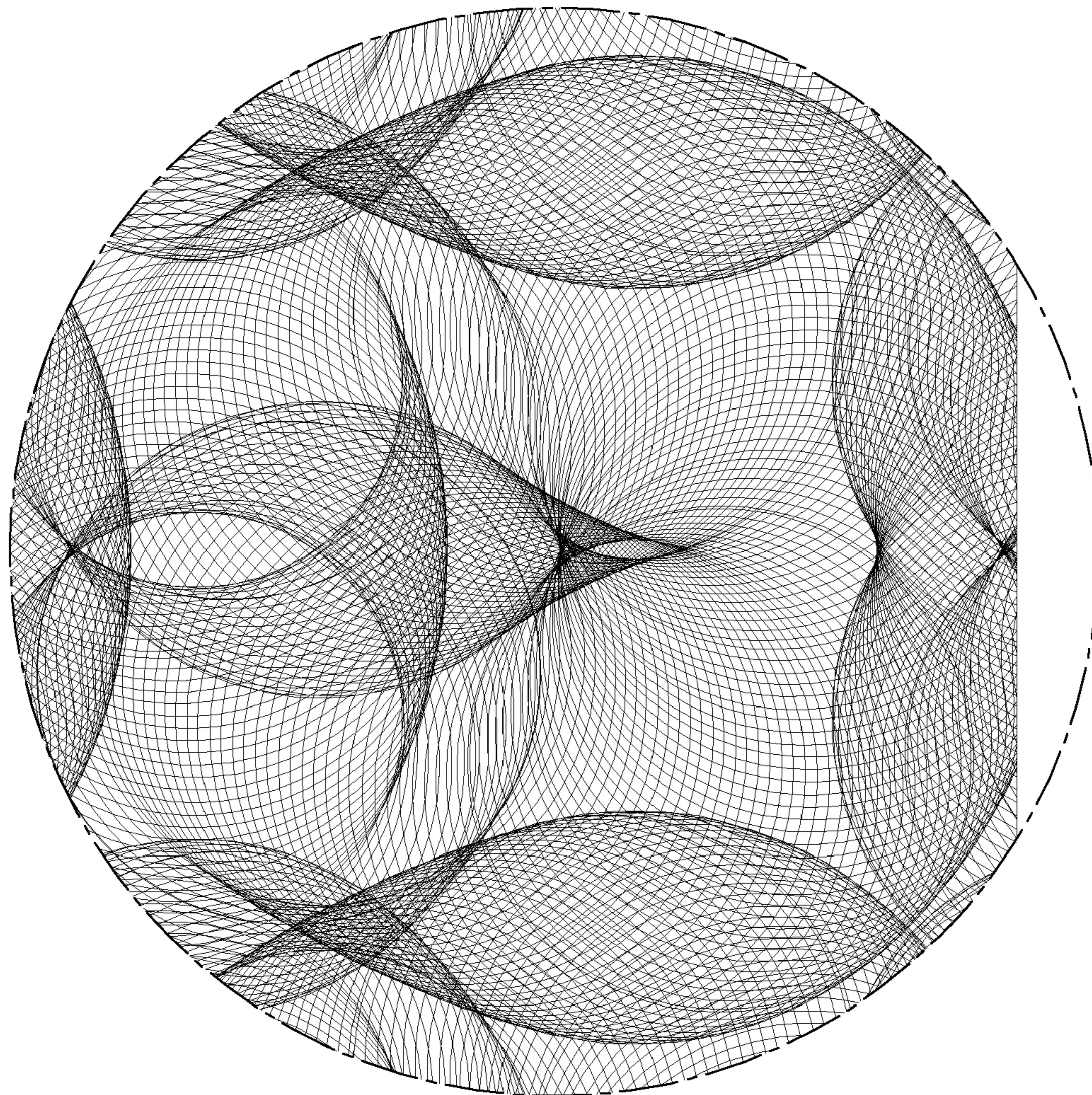


FIG. 4