



US00D665792S

(12) **United States Design Patent**
Li

(10) **Patent No.:** **US D665,792 S**
(45) **Date of Patent:** **** Aug. 21, 2012**

(54) **COVER OF AN ELECTRONIC DEVICE
HAVING SURFACE ORNAMENTATION**

D23,488 S * 7/1894 White D6/603
1,189,497 A * 7/1916 Schwartzman 59/80
D70,687 S * 7/1926 Humphreys D5/37

(75) Inventor: **Bin Li**, Shenzhen (CN)

(Continued)

(73) Assignee: **FIH (Hong Kong) Limited**, Kowloon
(HK)

OTHER PUBLICATIONS

Zotac GeForce GTX 560 AMP, <URL:http://tbreak.com/tech/2012/04/win-a-zotac-geforce-gtx-560-amp-edition-graphics-card/>, retrieved from internet on Jun. 4, 2012.*

(**) Term: **14 Years**

Primary Examiner — Carla Wright

(21) Appl. No.: **29/415,520**

(74) *Attorney, Agent, or Firm* — Altis Law Group, Inc.

(22) Filed: **Mar. 12, 2012**

(57) **CLAIM**

(51) **LOC (9) Cl.** **14-03**

The ornamental design for a cover of an electronic device having surface ornamentation, as shown and described.

(52) **U.S. Cl.** **D14/248**

(58) **Field of Classification Search** D14/248,
D14/138 R, 138 AA, 138 AB, 138 AC, 138 AD,
D14/138 C, 138 G, 203.7, 341-347, 250,
D14/440, 140, 240, 251-253, 439, 299, 238.1,
D14/217, 218, 168-169, 172, 496, 247, 203;
D3/201, 218, 269, 273, 301, 303, 248; D21/458;
455/575.1, 575.3, 575.4, 575.8, 556.1, 556.2;
D25/103, 110, 111, 117, 118, 145, 154, 155,
D25/156, 157, 158; D5/2, 3, 4, 7, 19, 23,
D5/24, 32, 36, 39, 46, 48, 55, 56, 66, 99,
D5/37-38, 41, 58-63, 1, 47, 26, 20, 57, 53,
D5/12, 17, 11, 49, 5, 8, 27, 64, 65; D13/103,
D13/108, 107; D6/595, 596, 601, 602, 603,
D6/610, 611, 612, 616, 617, 621, 622, 615,
D6/585; D11/22, 96, 131, 132, 140, 1, 125;
D32/40; D12/213; 15/215; 428/30, 38;
52/506.06, 316; D1/104, 105, 108; D2/741;
D24/208; D29/108

DESCRIPTION

The present application is related to copending U.S. patent applications Ser. Nos. 29/415,338, 29/415,537, 29/415,532, 29/415,534, 29/415,340, 29/415,530, 29/415,529, 29/415,528, 29/415,526, 29/415,524, 29/415,523, 29/415,521, 29/415,634, 29/415,633, 29/415,632, 29/415,631, 29/415,629, 29/415,628, 29/415,635, 29/415,630, 29/415,636, each entitled "Cover of an Electronic Device Having Surface Ornamentation", by Bin Li. These applications have the same assignee as the present application and have been concurrently filed herewith. The above-identified applications are incorporated herein by reference.

See application file for complete search history.

FIG. 1 is a perspective view of a cover of an electronic device having surface ornamentation showing my new design; FIG. 2 is a front elevational view thereof; FIG. 3 is a side elevational view thereof; and, FIG. 4 is an enlarged fragmentary view thereof showing circled portion 4 in FIG. 2.

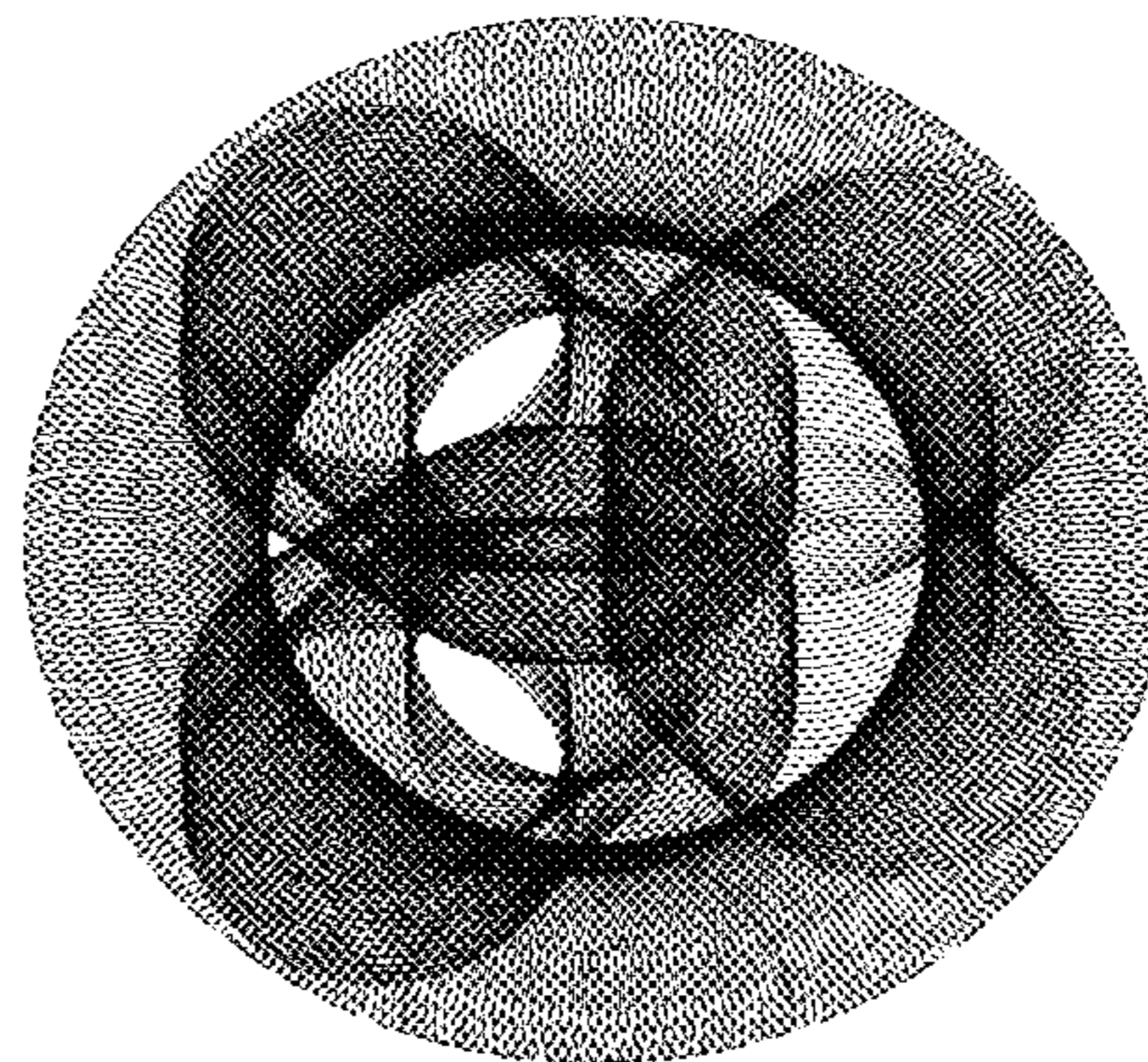
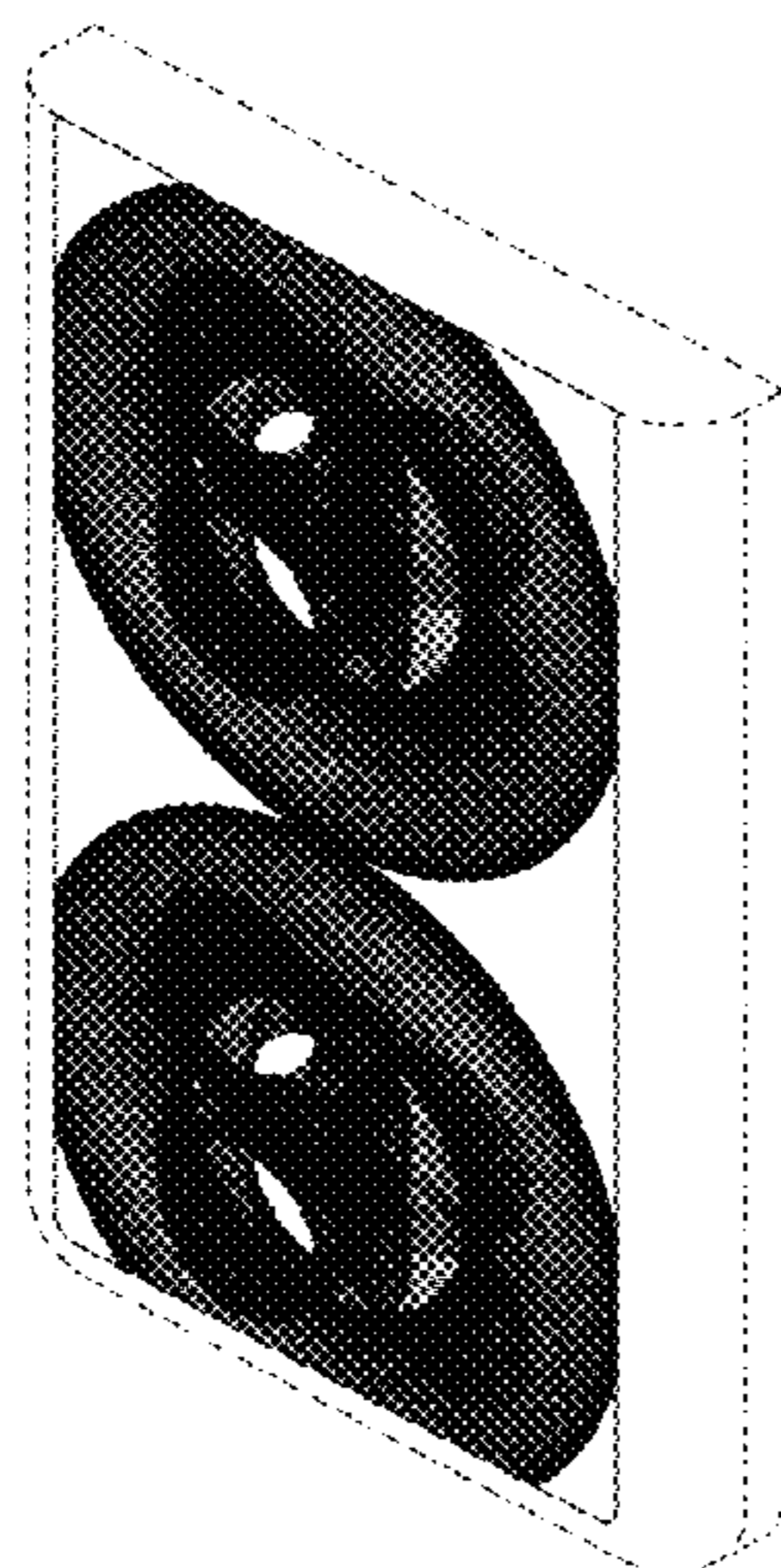
In FIGS. 1-3, the evenly segmented broken lines indicate portions of the cover of an electronic device having surface ornamentation that form no part of the claimed design. In FIGS. 2 and 4, the unevenly segmented broken lines indicate the portion of the design that is shown on an enlarged scale in FIG. 4. The broken lines form no part of the claimed design.

(56) **References Cited**

U.S. PATENT DOCUMENTS

299,181 A * 5/1884 Tiffany 52/316
455,659 A * 7/1891 Cook 52/316

1 Claim, 4 Drawing Sheets



US D665,792 S

Page 2

U.S. PATENT DOCUMENTS

D84,480 S *	6/1931	Kateb	D5/59	D573,597 S *	7/2008	Shieh	D14/439
D88,875 S *	1/1933	Hellman	D5/63	D585,906 S *	2/2009	Berg et al.	D14/486
2,875,543 A *	3/1959	Sylvester et al.	428/30	D597,054 S *	7/2009	Park et al.	D14/138 G
D238,611 S *	1/1976	Fukuda	D14/195	D607,446 S *	1/2010	Ferrari et al.	D14/250
6,071,082 A *	6/2000	Lecinski et al.	417/53	D611,874 S *	3/2010	Bell	D11/131
7,036,339 B1 *	5/2006	Chia et al.	63/26	D613,278 S *	4/2010	Chu et al.	D14/248
D530,696 S *	10/2006	Lee	D14/138 AA	D640,678 S *	6/2011	Madsen et al.	D14/248
D531,157 S *	10/2006	Lheem	D14/138 AB	D647,085 S *	10/2011	Chung et al.	D14/250
D548,856 S *	8/2007	Zocco	D25/103	D654,902 S *	2/2012	Chung	D14/138 AD
D549,686 S *	8/2007	Leung	D14/168				

* cited by examiner

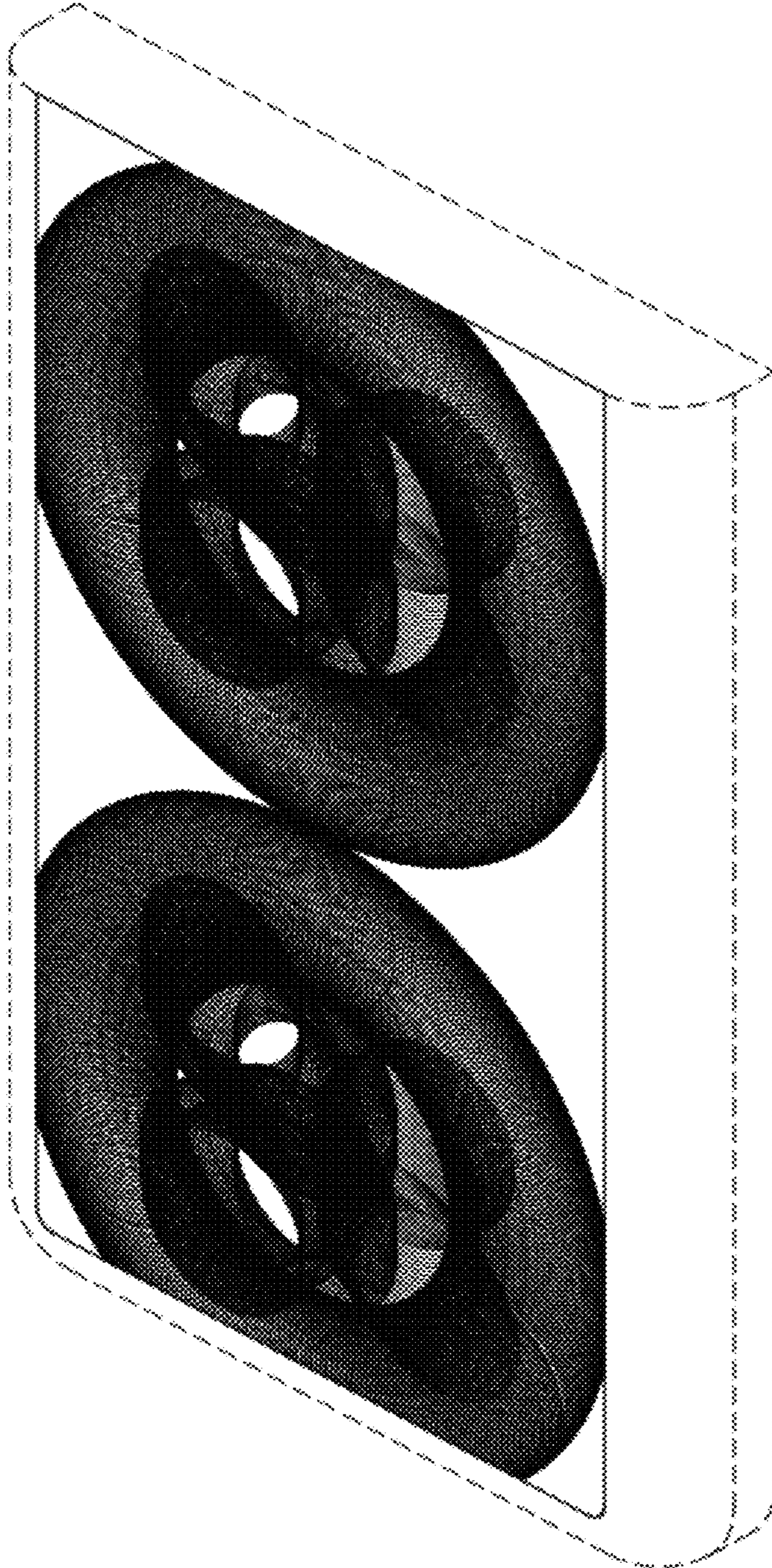


FIG. 1

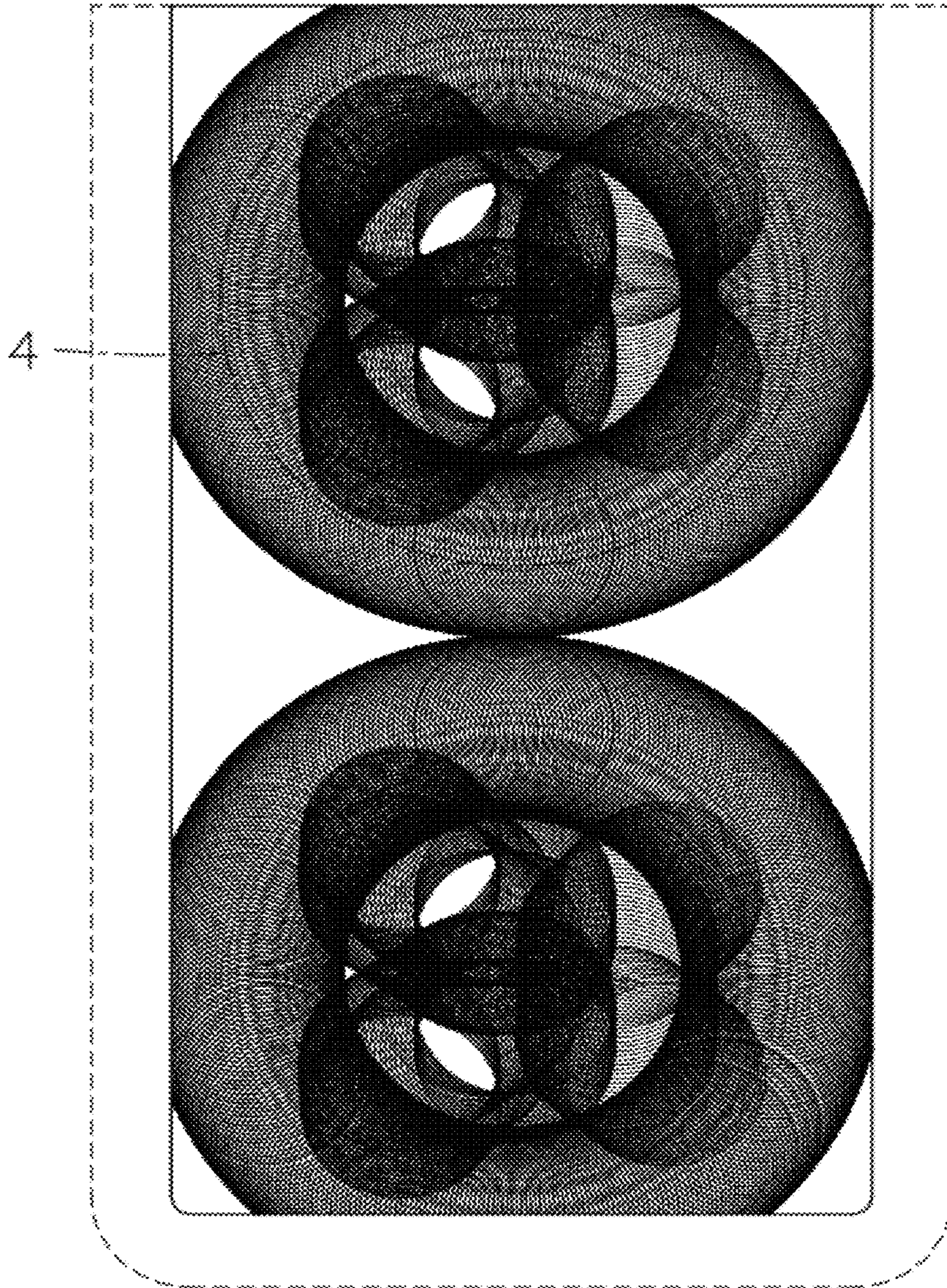


FIG. 2



FIG. 3

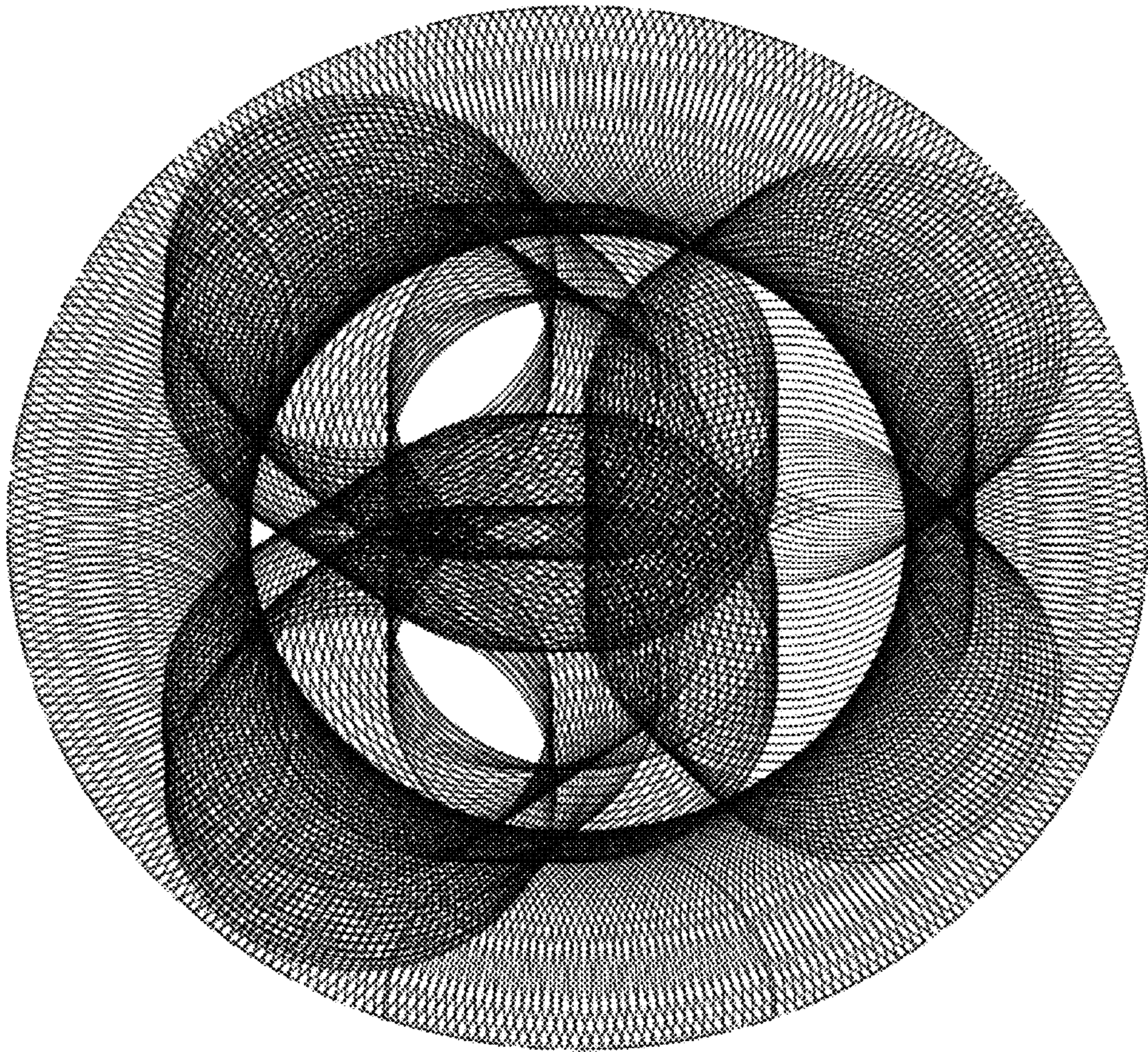


FIG. 4