



US00D659998S

(12) **United States Design Patent**  
**Austin**

(10) **Patent No.:** **US D659,998 S**

(45) **Date of Patent:** **\*\* May 22, 2012**

(54) **HARDSHELL ELECTRONICS CASE**

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(73) Assignee: **PC Treasures, Inc.**, Oxford, MI (US)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/390,291**

(22) Filed: **Apr. 22, 2011**

(51) **LOC (9) Cl.** ..... **03-01**

(52) **U.S. Cl.** ..... **D3/301**

(58) **Field of Classification Search** ..... D3/273,  
D3/294, 232, 301; 150/100, 103-110, 113-114,  
150/118-119; 206/576, 701, 320

See application file for complete search history.

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(57) **CLAIM**

I claim the ornamental design for a hardshell electronics case, as shown and described.

**DESCRIPTION**

FIG. 1 is a perspective view of a hardshell electronics case in an open position;

FIG. 2 is a left side view of the hardshell electronics case in the open position;

FIG. 3 is a bottom view of the hardshell electronics case in the open position;

FIG. 4 is a top view of the hardshell electronics case in the open position;

FIG. 5 is a right side view of the hardshell electronics case in the open position;

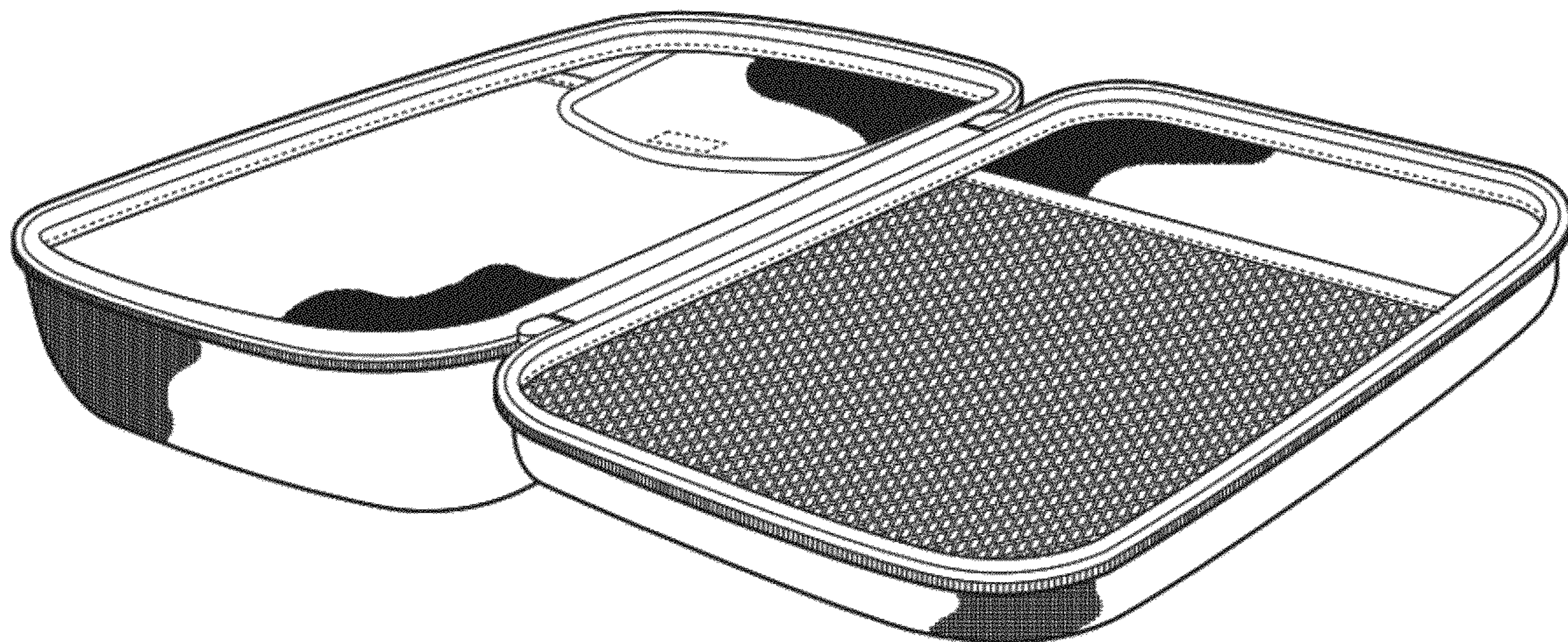
FIG. 6 is a front view of the hardshell electronics case in the open position;

FIG. 7 is a back view of the hardshell electronics case in the open position; and,

FIG. 8 is a perspective view of the hardshell electronics case in a closed position.

Any textural treatment of a surface shown in 1-8 is intended to extend over the entirety of the respective surface of the hardshell electronics case. Any shown surface texture is not intended to represent any one particular surface texture or to represent a commonality of surface textures between different surfaces. The dashed lines shown in FIGS. 1 and 6 are intended to show stitching and is part of the claimed design.

**1 Claim, 6 Drawing Sheets**



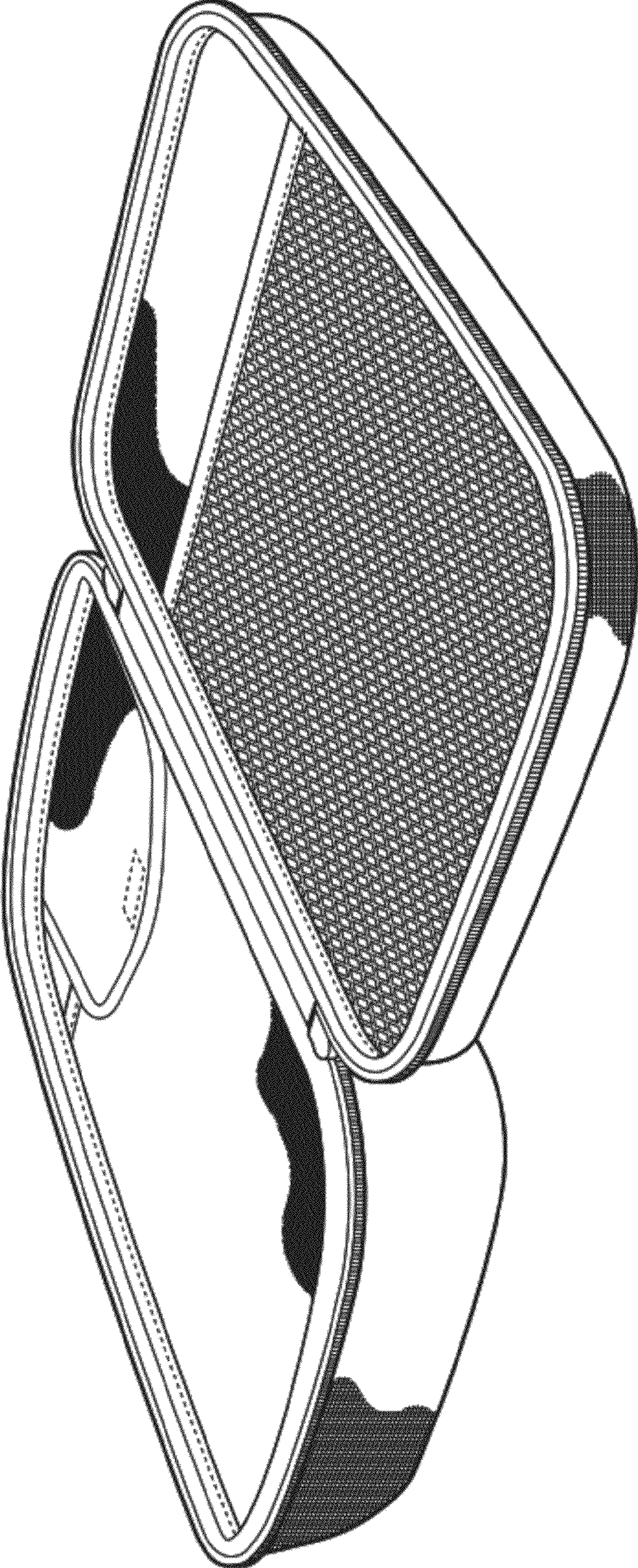


FIG. 1

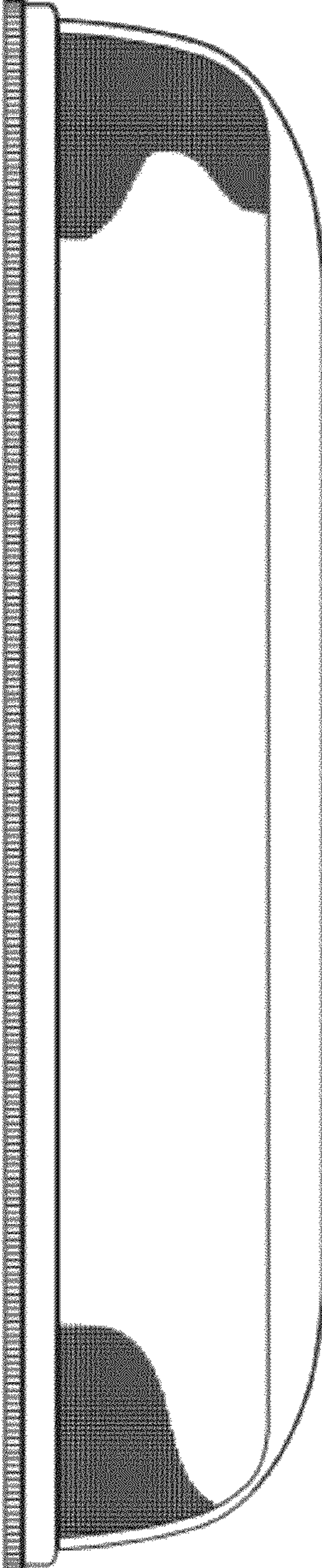


FIG. 2

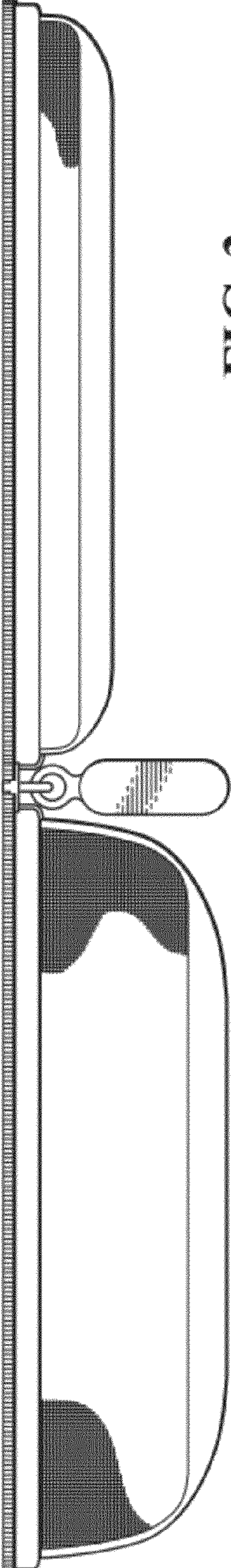


FIG. 3

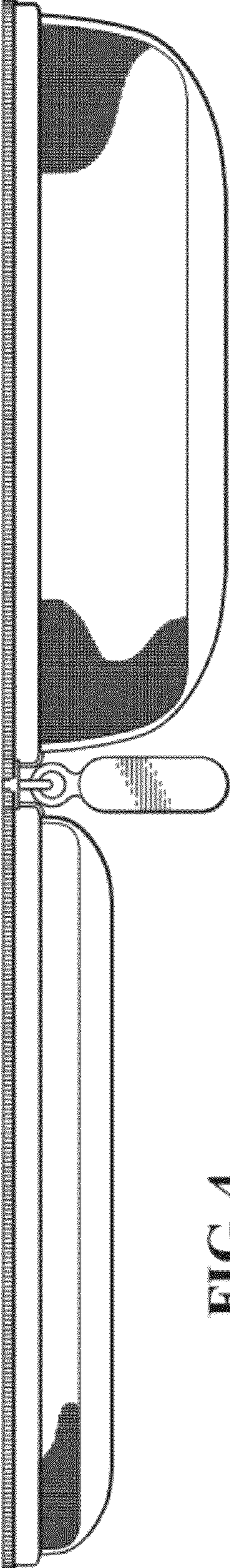


FIG. 4

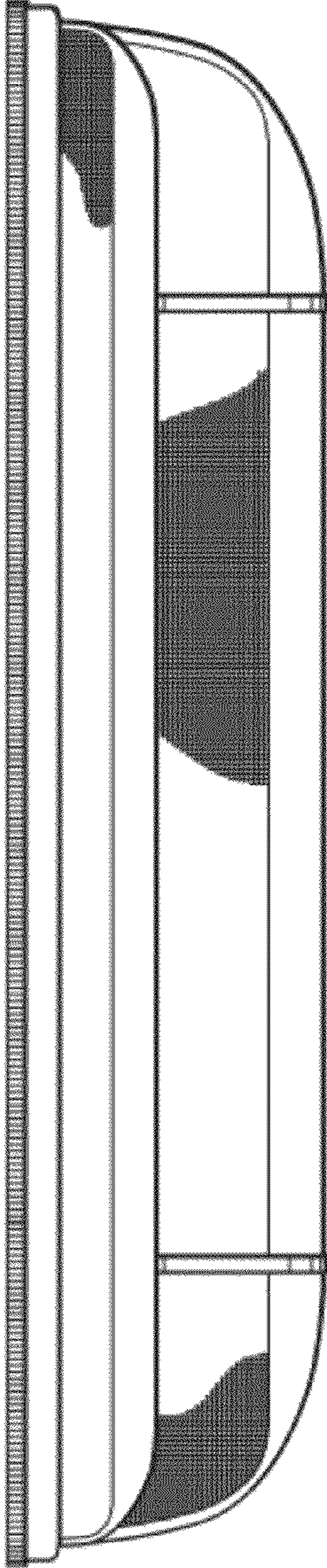


FIG. 5

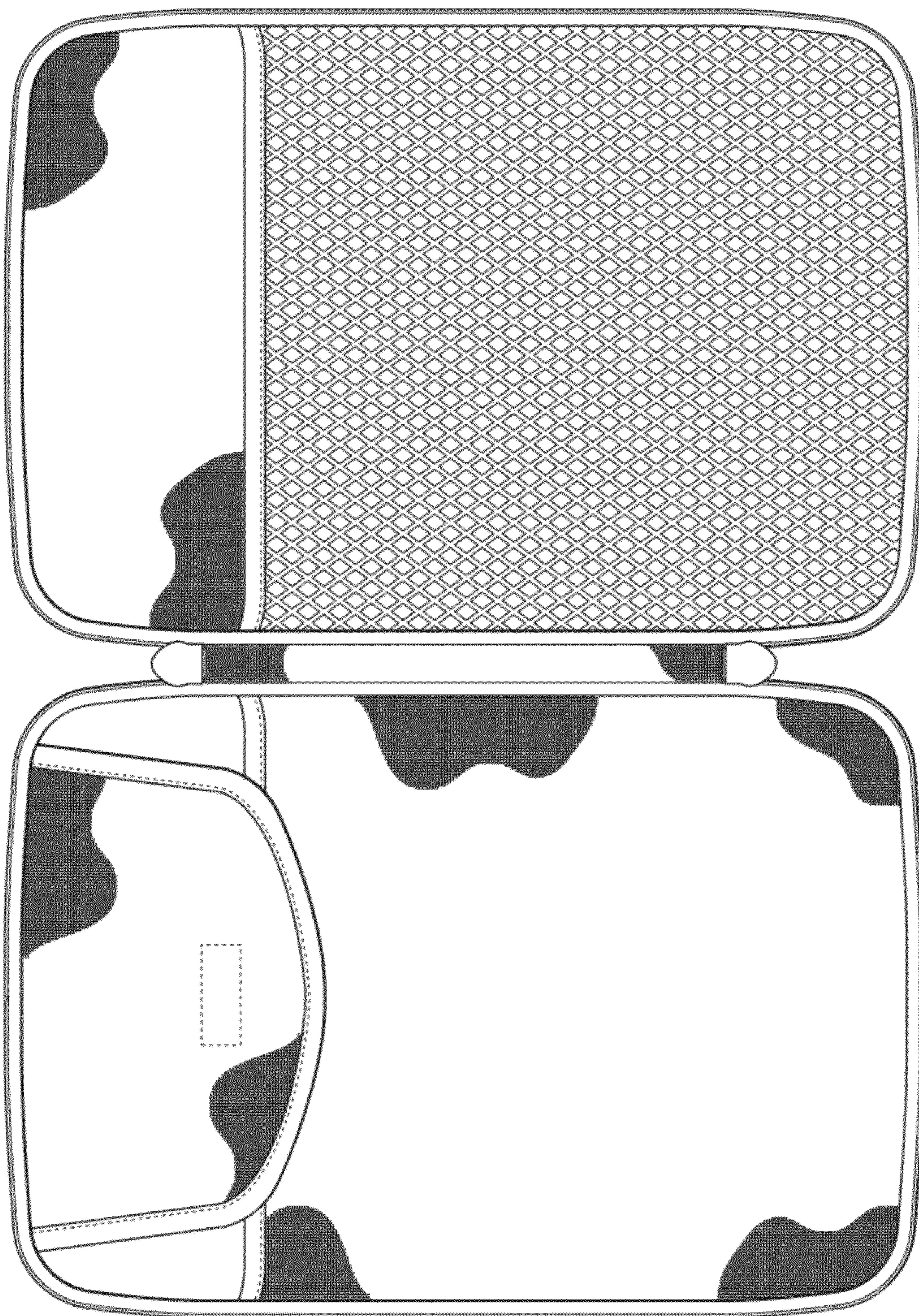


FIG. 6

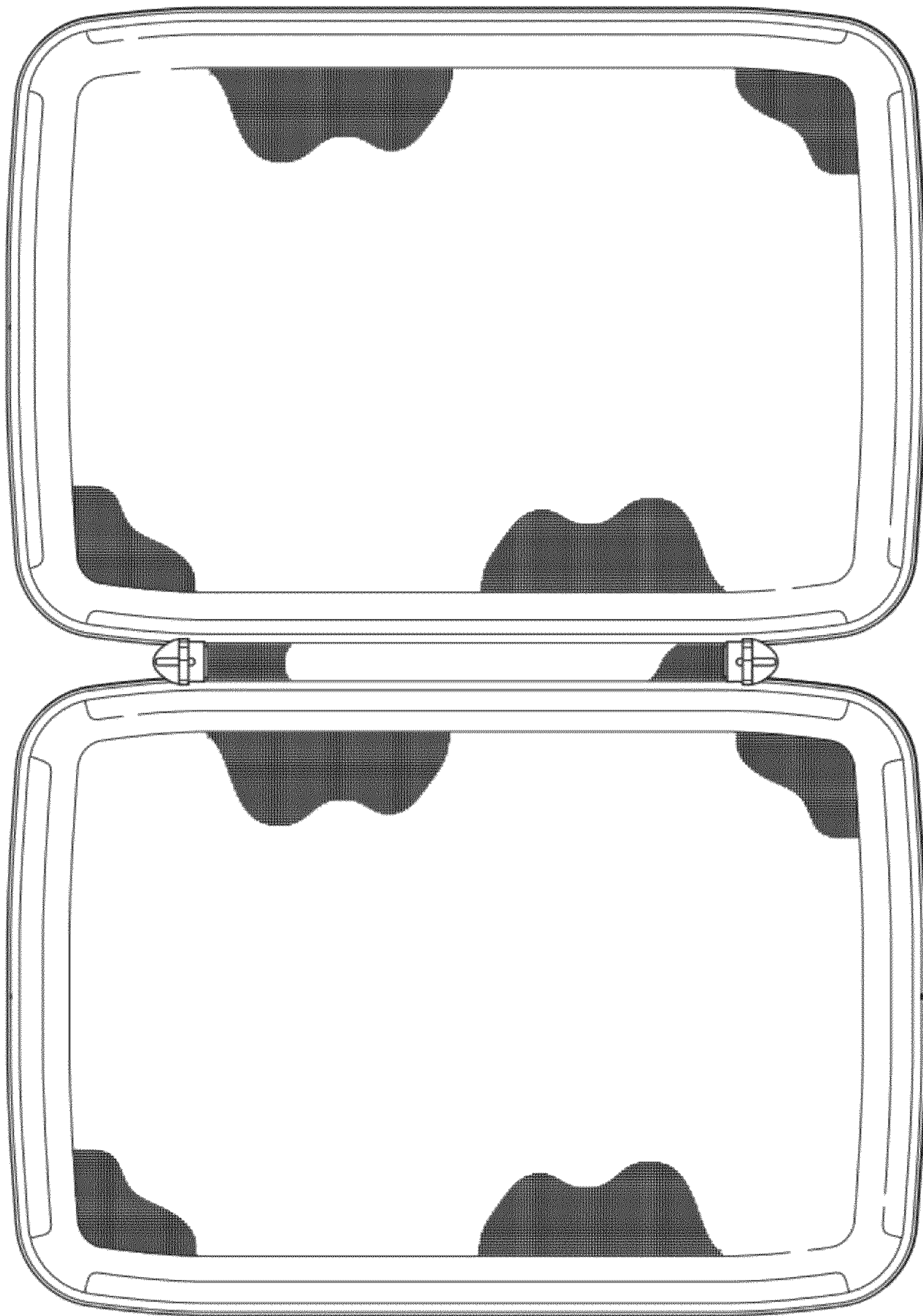


FIG. 7

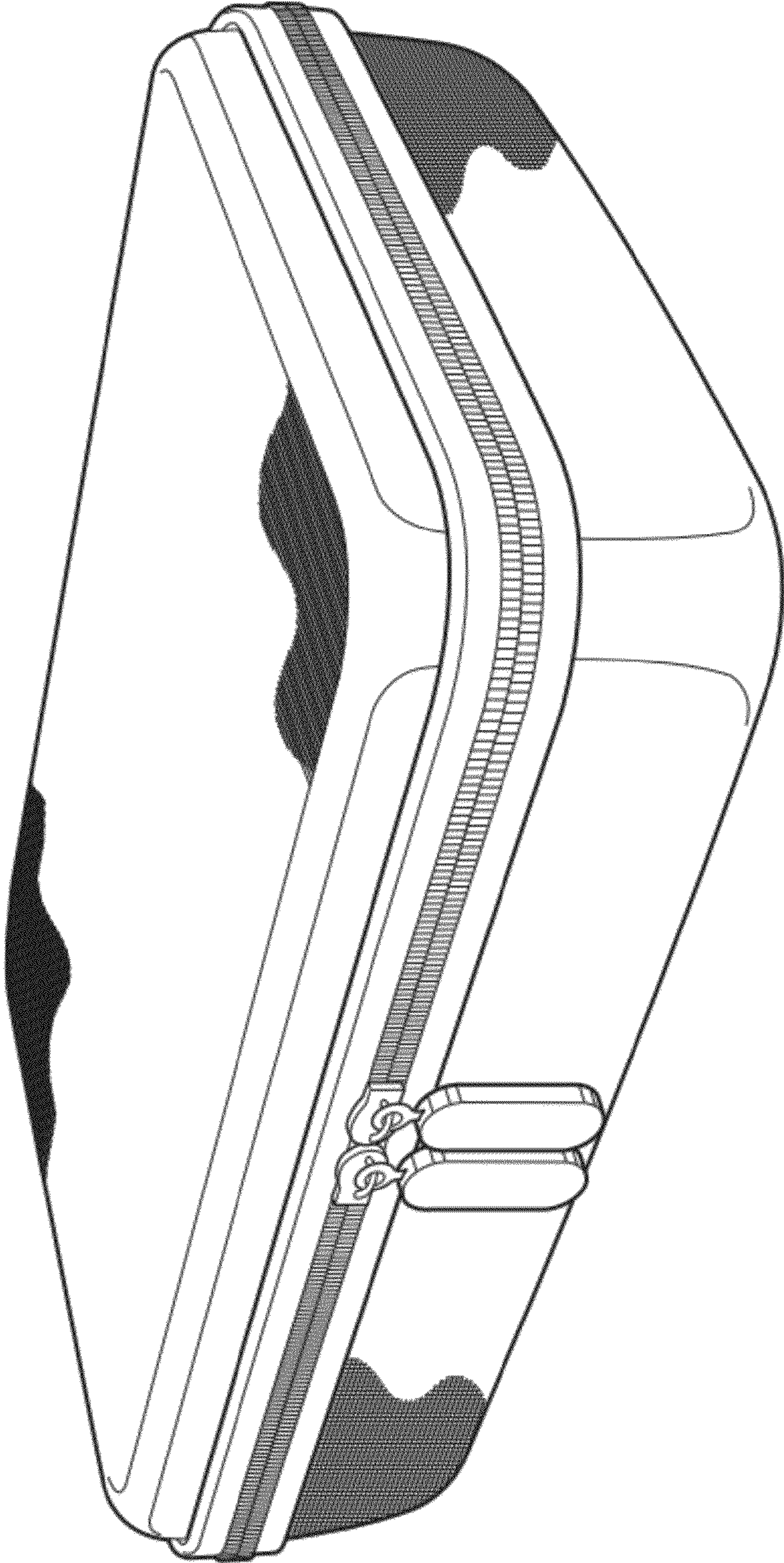


FIG. 8