



US00D658989S

(12) **United States Design Patent**
Nelissen

(10) **Patent No.:** **US D658,989 S**
(45) **Date of Patent:** **** May 8, 2012**

(54) **TRAY FOR SHIPPING ELECTRONIC COMPONENTS**

FOREIGN PATENT DOCUMENTS

DE 2527142 5/1975
(Continued)

(75) Inventor: **Franciscus Petrus Nicolaas Nelissen,**
Venray (NL)

OTHER PUBLICATIONS

(73) Assignee: **Premier Farnell UK Limited** (GB)

Search Report for Application No. GB0715503.9, dated Oct. 30, 2007, UK IP Office.

(**) Term: **14 Years**

(Continued)

(21) Appl. No.: **29/317,417**

Primary Examiner — Mark Goodwin

(22) Filed: **Apr. 29, 2008**

(74) *Attorney, Agent, or Firm* — Bourque and Associates, PA

(51) **LOC (9) Cl.** **09-07**

(52) **U.S. Cl.** **D9/456; D9/756**

(58) **Field of Classification Search** D9/456,
D9/737, 756, 759, 424, 425; D7/16, 37,
D7/1; 206/203, 557

(57) **CLAIM**

The ornamental design for a tray for shipping electronic components, as shown and described herein.

See application file for complete search history.

(56) **References Cited**

DESCRIPTION

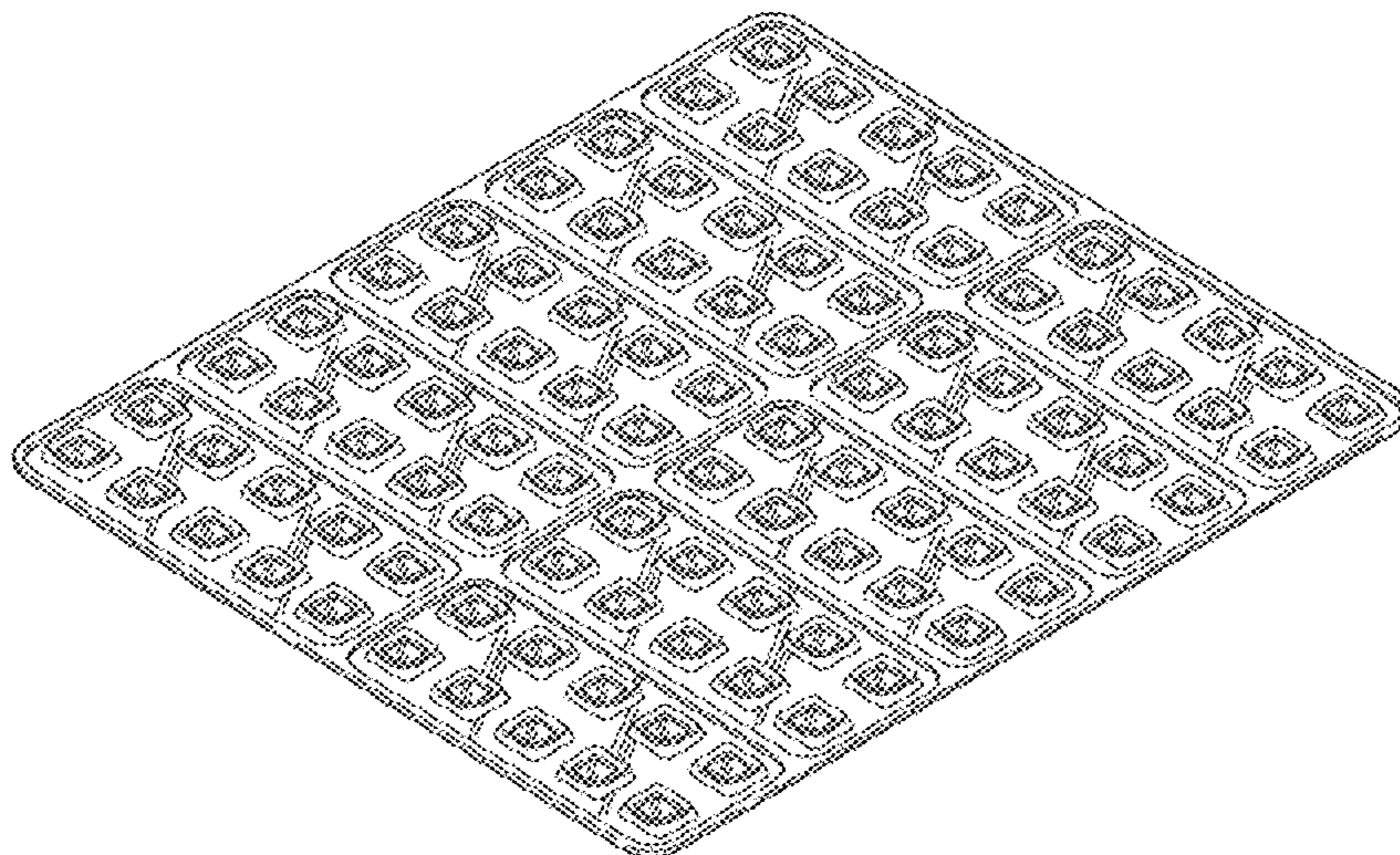
U.S. PATENT DOCUMENTS

D103,860	S	*	3/1937	Randall et al.	D9/456
D103,861	S	*	3/1937	Randall et al.	D9/456
D103,862	S	*	3/1937	Randall et al.	D9/456
D184,354	S	*	2/1959	Joyce, III	D9/456
D216,100	S	*	11/1969	Hyman	D9/456
4,049,121	A		9/1977	White		
D260,612	S	*	9/1981	Wilkerson	D9/424
D260,613	S	*	9/1981	Wilkerson	D9/424
D270,703	S	*	9/1983	El-Hag et al.	D7/357
D283,012	S	*	3/1986	Jankens	D9/456
4,657,137	A		4/1987	Johnson		
5,033,252	A		7/1991	Carter		
D359,900	S	*	7/1995	Hawthorne	D9/732
D370,127	S	*	5/1996	Bonaddio et al.	D5/58
D399,134	S	*	10/1998	Lidle, Jr.	D9/757
5,927,500	A		7/1999	Godfrey et al.		
D413,390	S	*	8/1999	Southern et al.	D24/224
D457,434	S	*	5/2002	Soh et al.	D9/456
D475,625	S	*	6/2003	Lin	D9/456
D516,907	S	*	3/2006	Tada	D9/424
D554,820	S	*	11/2007	Dretzka	D32/57

(Continued)

FIG. (1) is a perspective view of the bottom of the tray for shipping electronic components according to the invention; FIG. (2) is a top view of the tray for shipping electronic components according to the invention with an opaque shipping layer in place; FIG. (3) is a top view of the tray for shipping electronic components according to the invention; FIG. (4) is bottom view of the tray for shipping electronic components; FIG. (5) is a left side view of the tray for shipping electronic components according to the invention, the right side view being a mirror image thereof; FIG. (6) is a top end view of the tray for shipping electronic components according to the invention, the bottom end view being a mirror image thereof; and, FIG. (7) is a cross-section taken along lines 7-7 of FIG. 3 showing the contours of the present design.

1 Claim, 6 Drawing Sheets



US D658,989 S

Page 2

U.S. PATENT DOCUMENTS

D571,608 S * 6/2008 Yeung D7/388
D581,120 S * 11/2008 Sofy et al. D32/57
D629,687 S * 12/2010 Baker D9/456
2005/0269242 A1* 12/2005 Crisp 206/710

FOREIGN PATENT DOCUMENTS

EP 0397185 11/1990
FR 2774568 2/1998
GB 1580161 11/1980

GB 2163401 2/1986
SU 2039663 10/1991
WO 9941027 8/1999
WO 0183331 11/2001

OTHER PUBLICATIONS

International Search Report for PCT/GB2007/003554, dated Apr. 21,
2008, European Patent Office.

* cited by examiner

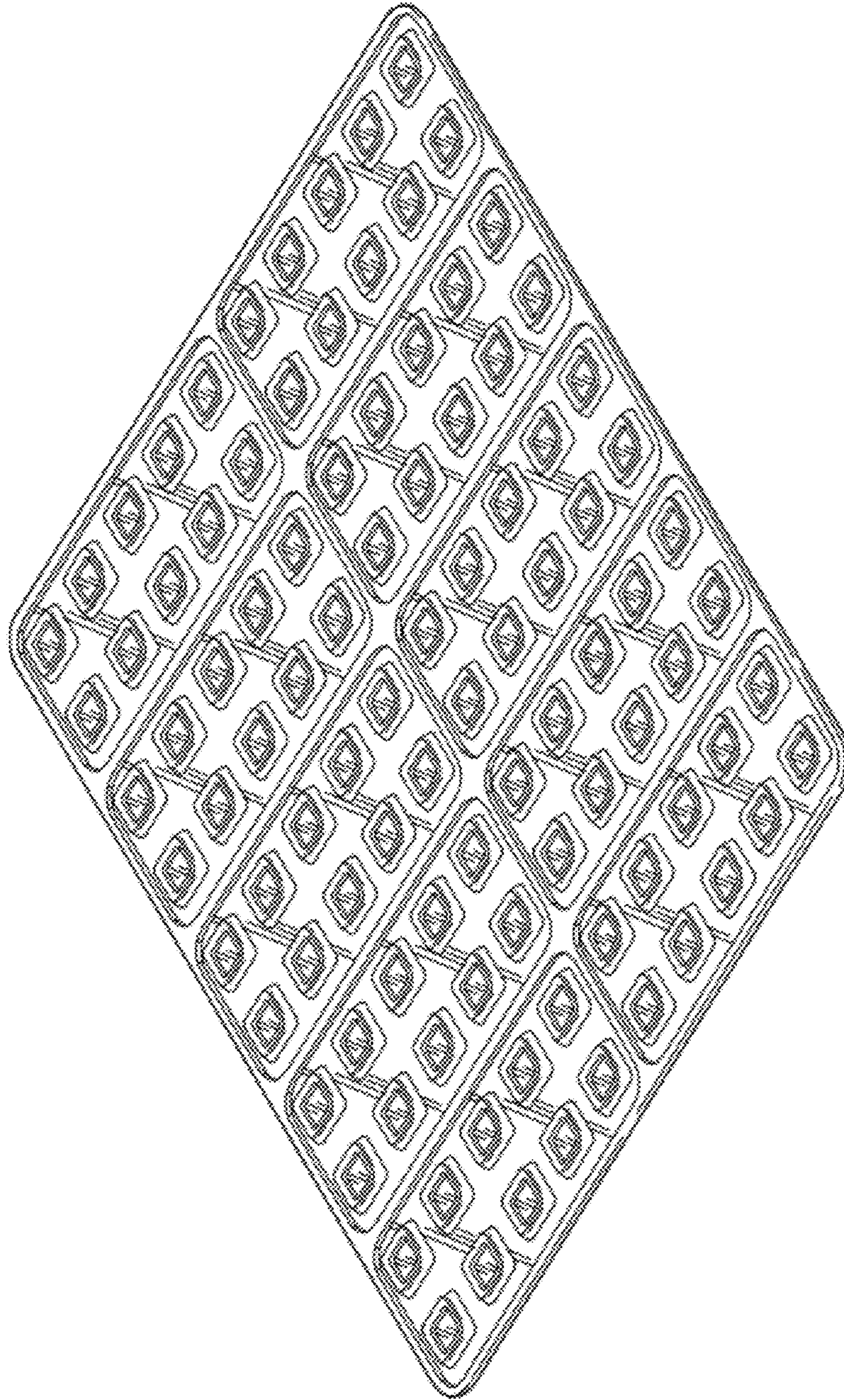


FIG. 1

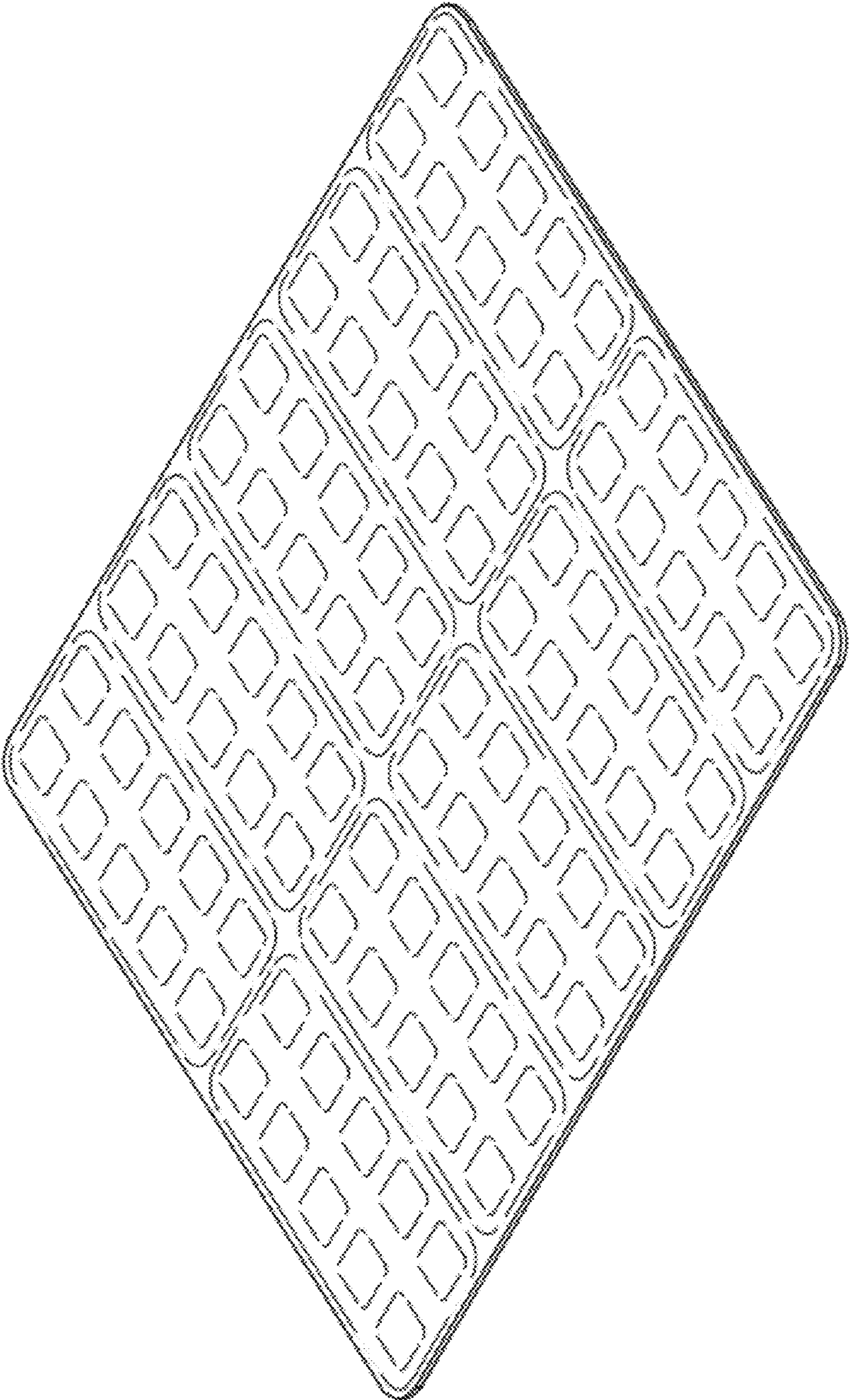


FIG. 2

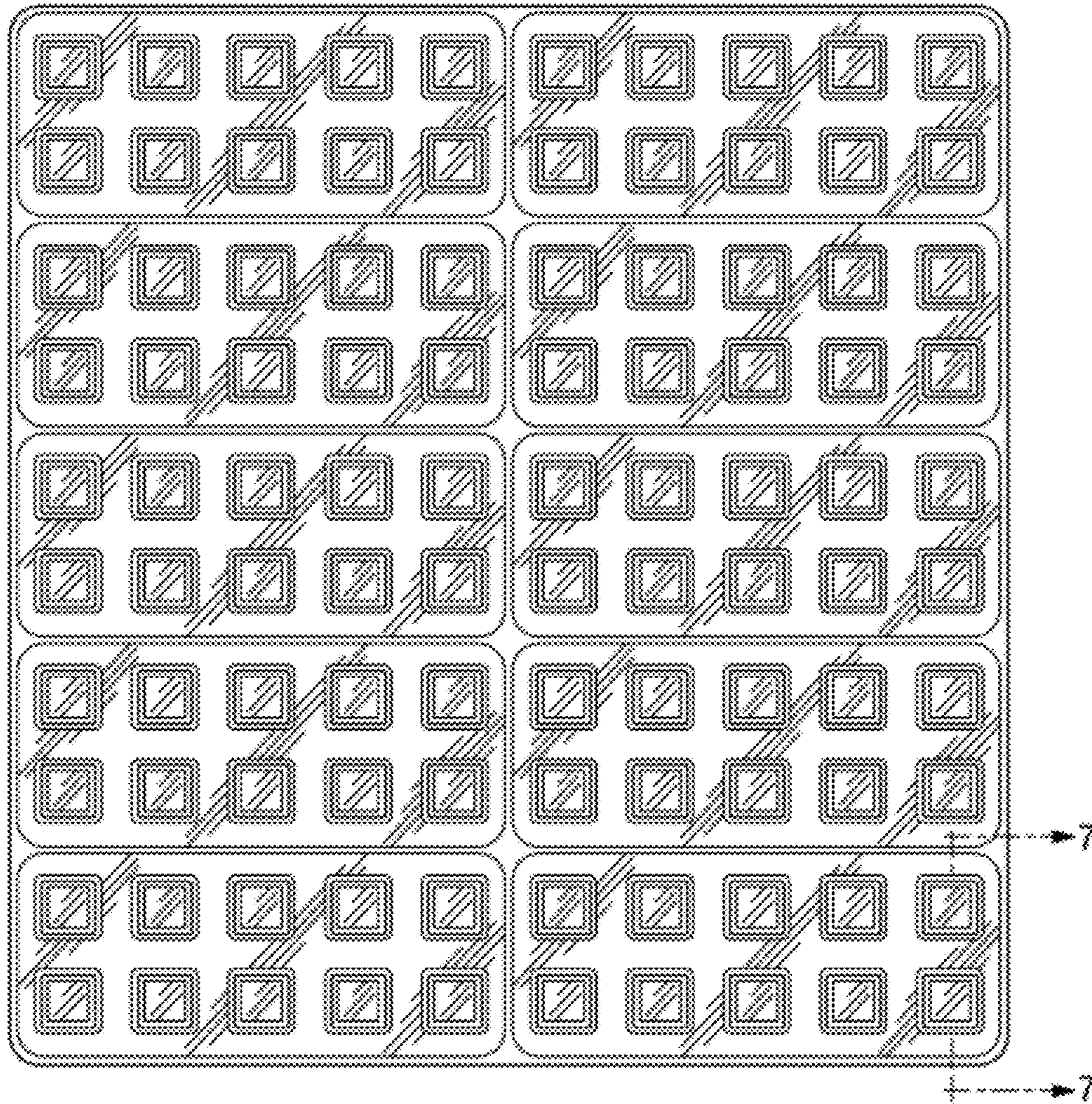


FIG. 3

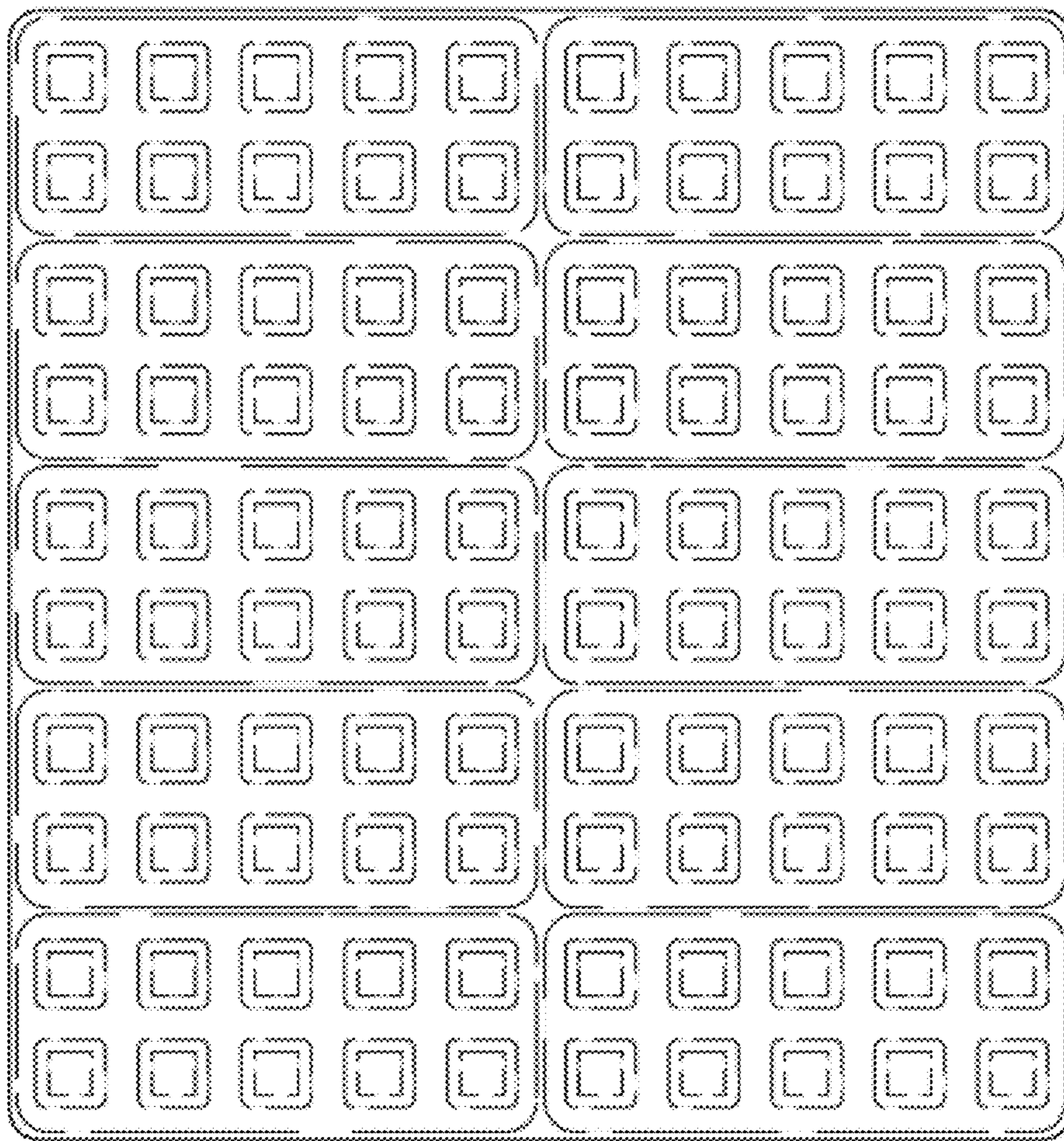


FIG. 4



FIG. 5

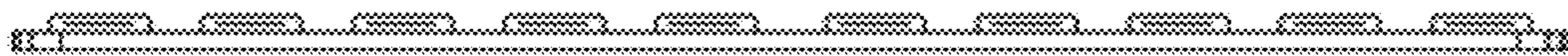


FIG. 6

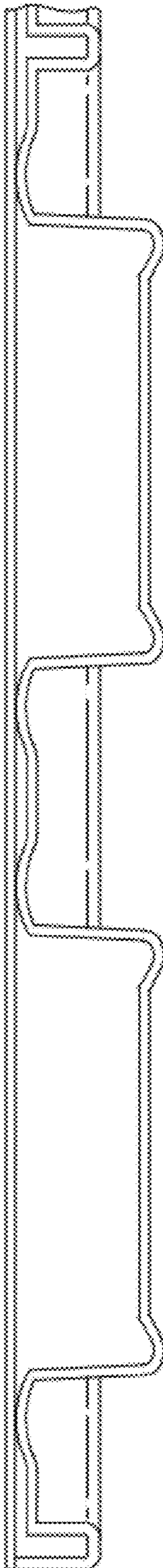


FIG. 7