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(12) **United States Design Patent**
Shioda et al.

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(45) **Date of Patent:** **** Feb. 14, 2012**

(54) **ELECTRICAL CONNECTOR**

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(73) Assignee: **Iriso Electronics Co., Ltd.**, Kanagawa (JP)

(**) Term: **14 Years**

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(30) **Foreign Application Priority Data**

Sep. 1, 2010 (JP) 2010-021145

(51) **LOC (9) Cl.** **13-03**

(52) **U.S. Cl.** **D13/133**

(58) **Field of Classification Search** D13/133,
D13/146-147, 154, 156, 184; 439/180, 252,
439/278, 280, 374, 375, 378, 488, 489, 491,
439/527, 529, 533, 892, 894, 607.01, 607.04,
439/607.05, 607.06, 607.17, 607.21, 607.23,
439/607.25, 607.34, 607.41, 607.53, 660

See application file for complete search history.

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(57) **CLAIM**

The ornamental design for an electrical connector, as shown and described.

DESCRIPTION

FIG. 1 is a front elevation view of an electrical connector showing our new design;

FIG. 2 is a rear elevation view thereof;

FIG. 3 is a right side elevation view thereof;

FIG. 4 is a left side elevation view thereof;

FIG. 5 is a top plan view thereof;

FIG. 6 is a bottom plan view thereof;

FIG. 7 is a top perspective view thereof;

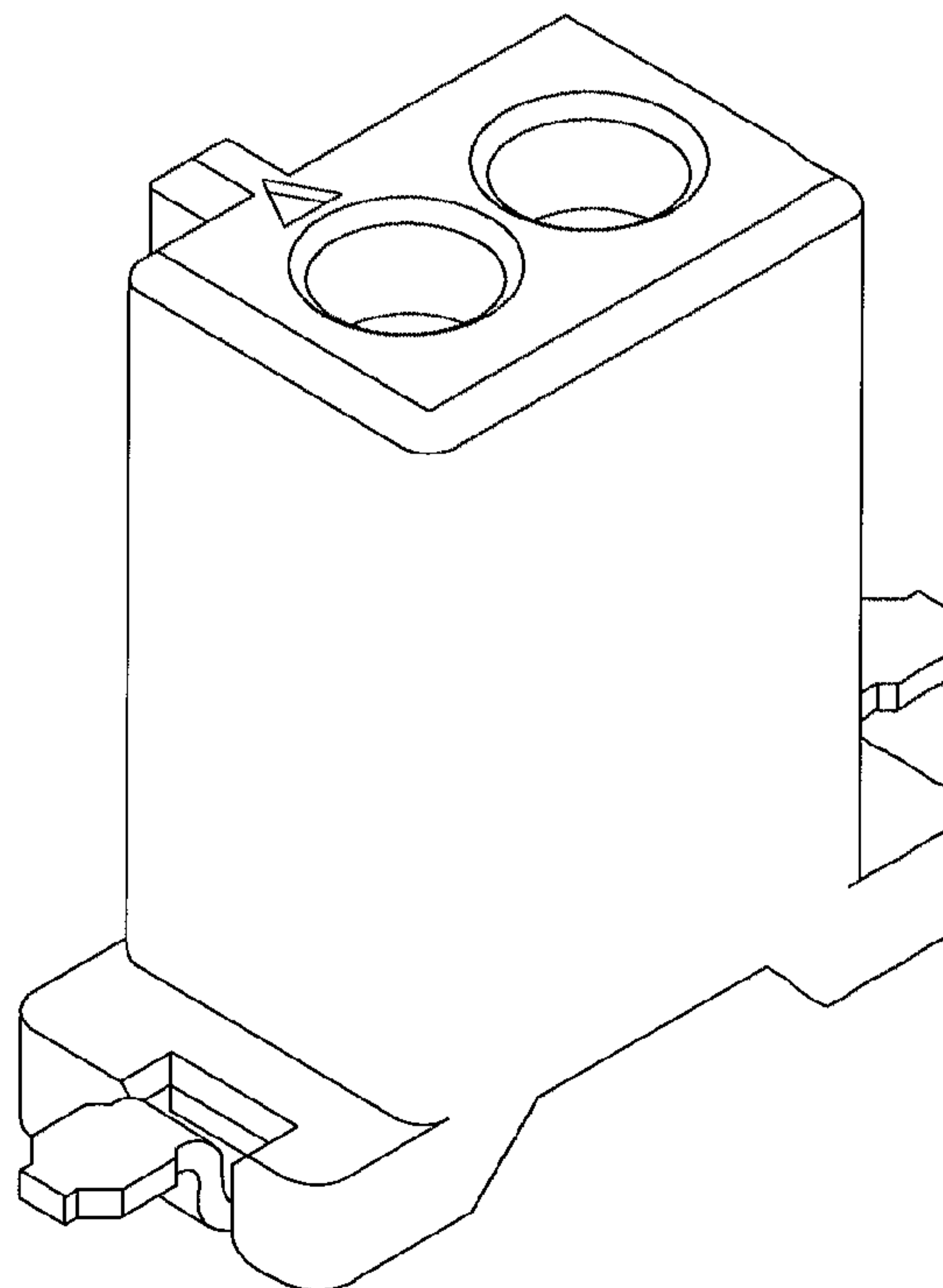
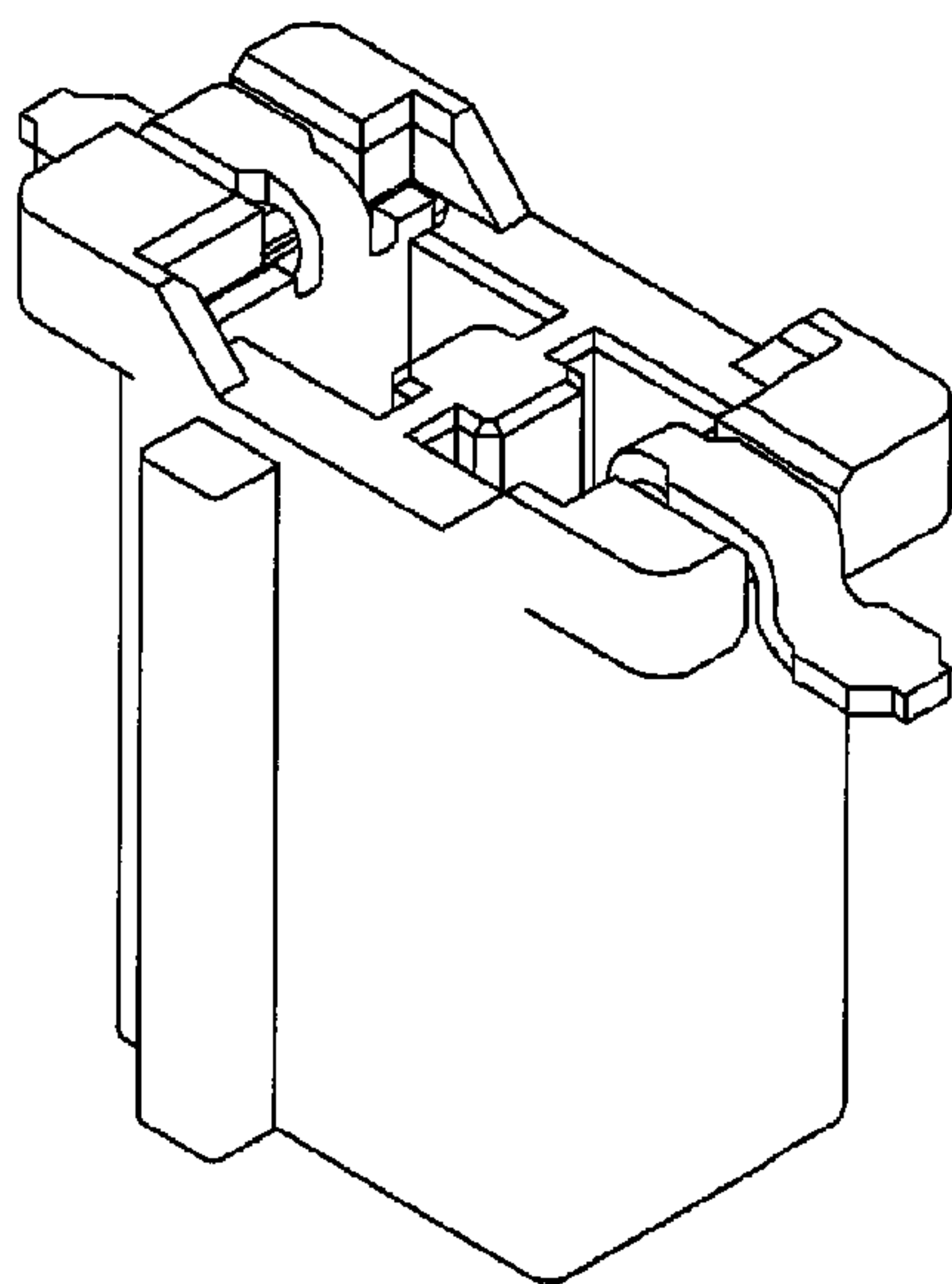
FIG. 8 is a bottom perspective view thereof;

FIG. 9 is a top perspective view thereof, in use condition, on a smaller scale; and,

FIG. 10 is an enlarged perspective view of portion "10" shown in FIG. 9.

The even-broken line shown in the figure drawings is included for the purpose of illustrating environment and forms no part of the claimed design.

1 Claim, 9 Drawing Sheets



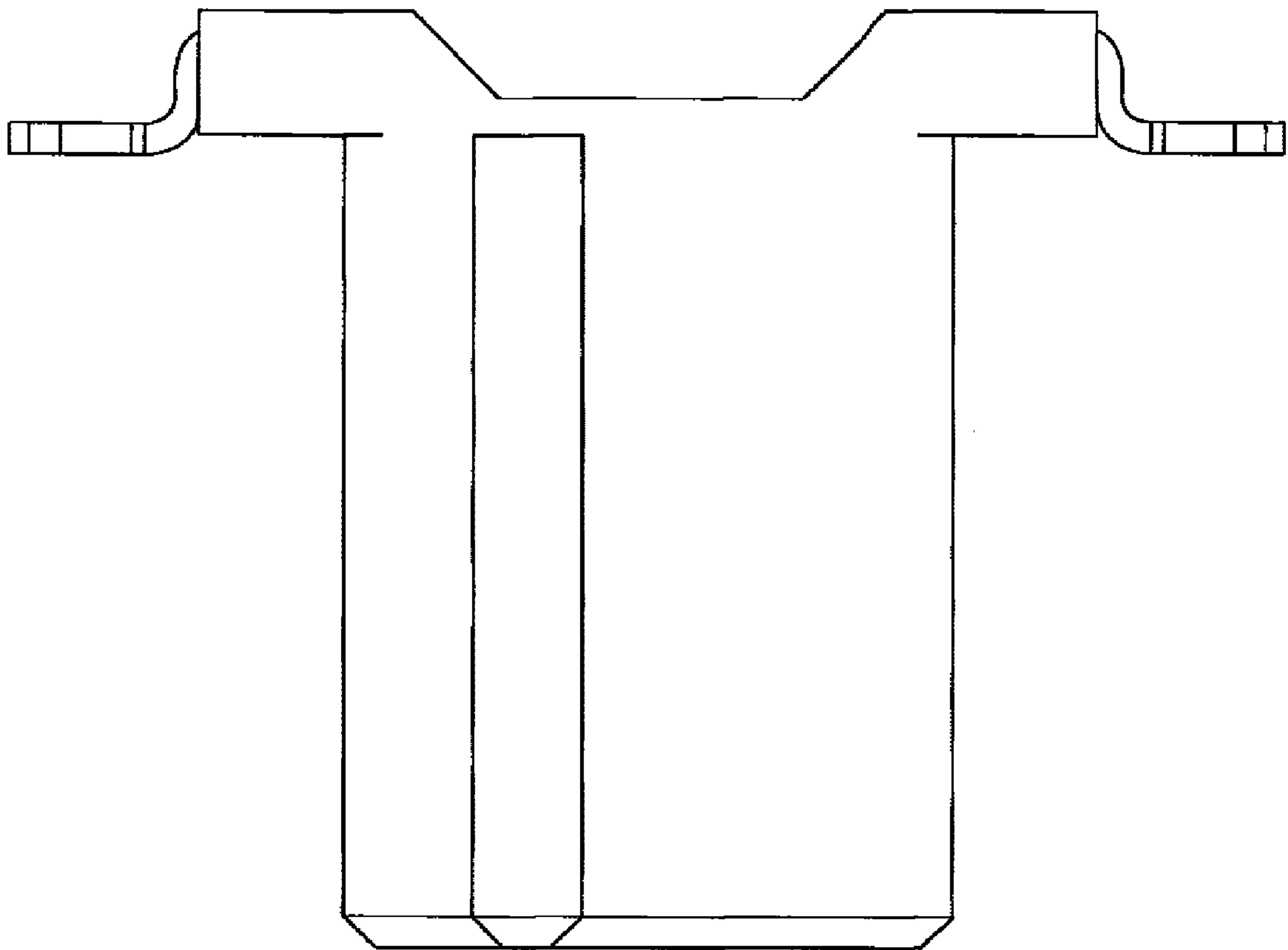


FIG. 1

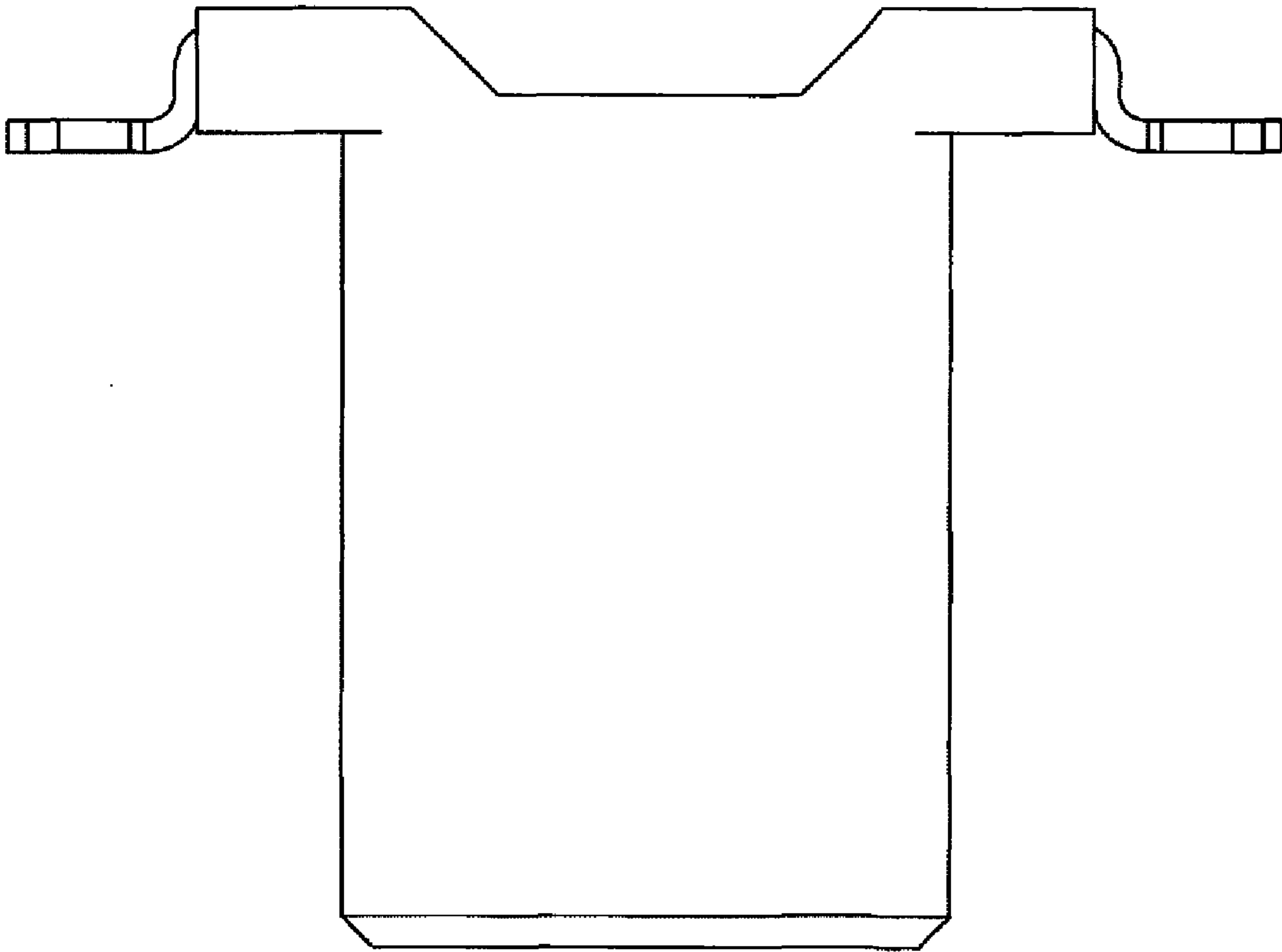


FIG. 2

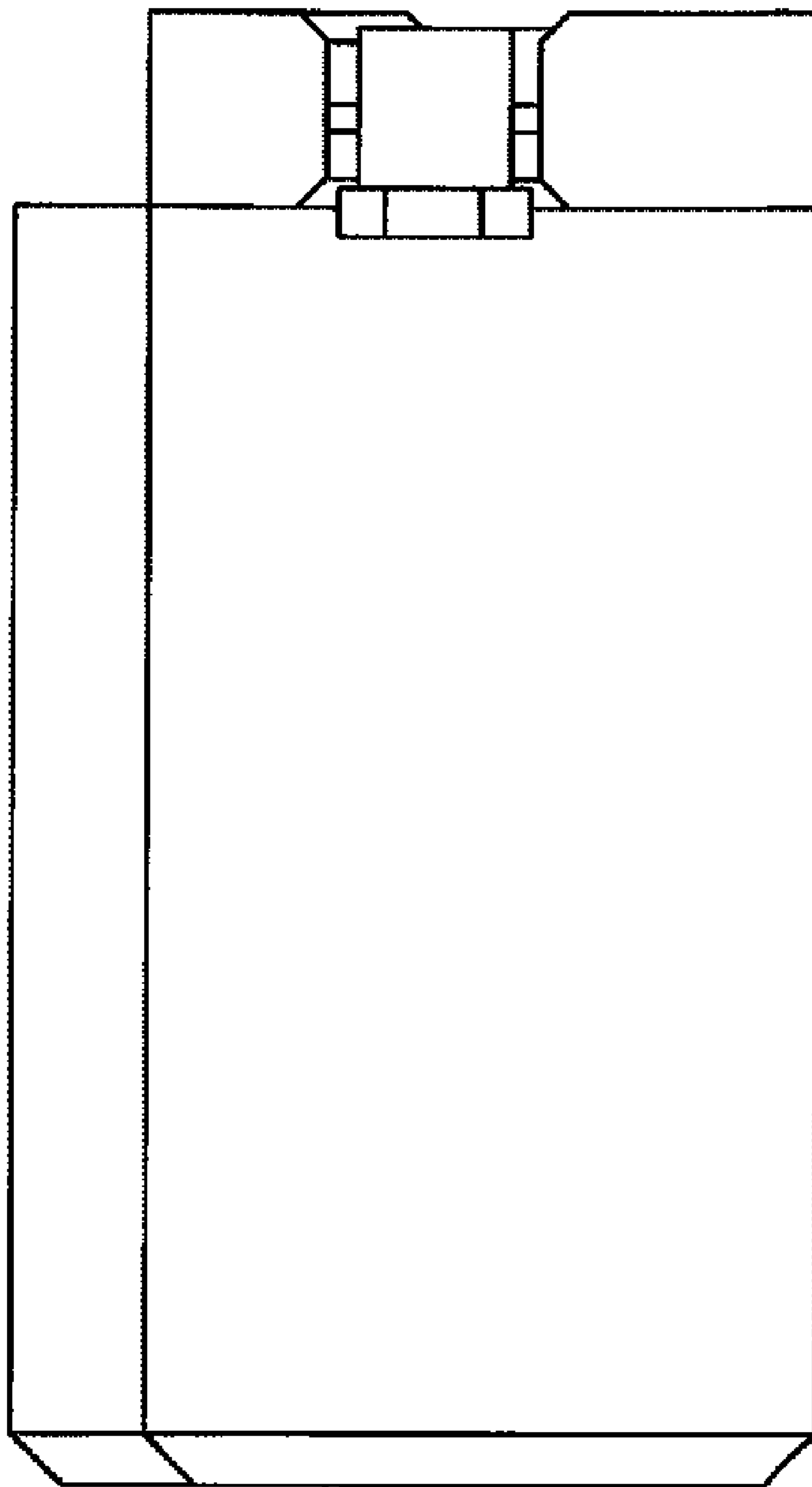


FIG. 3

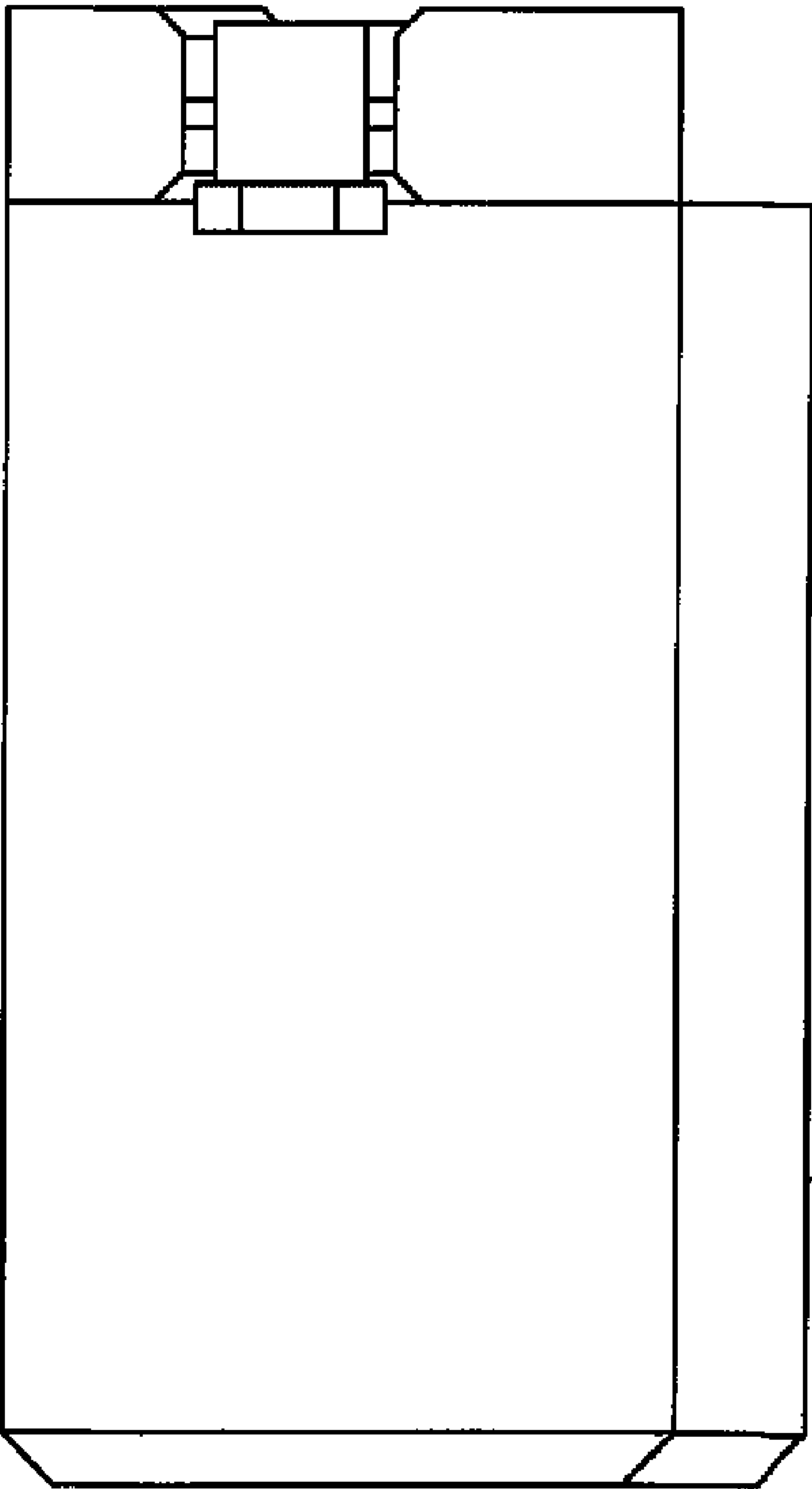


FIG. 4

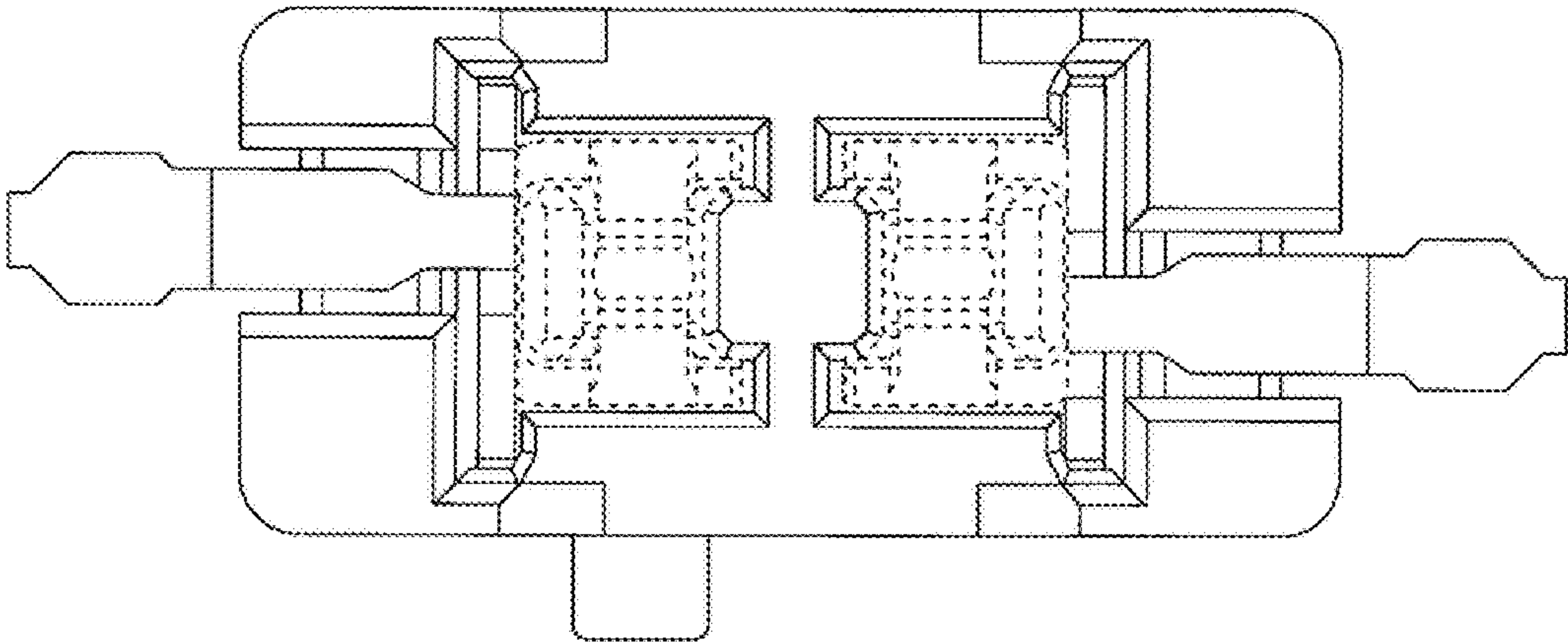


FIG. 5

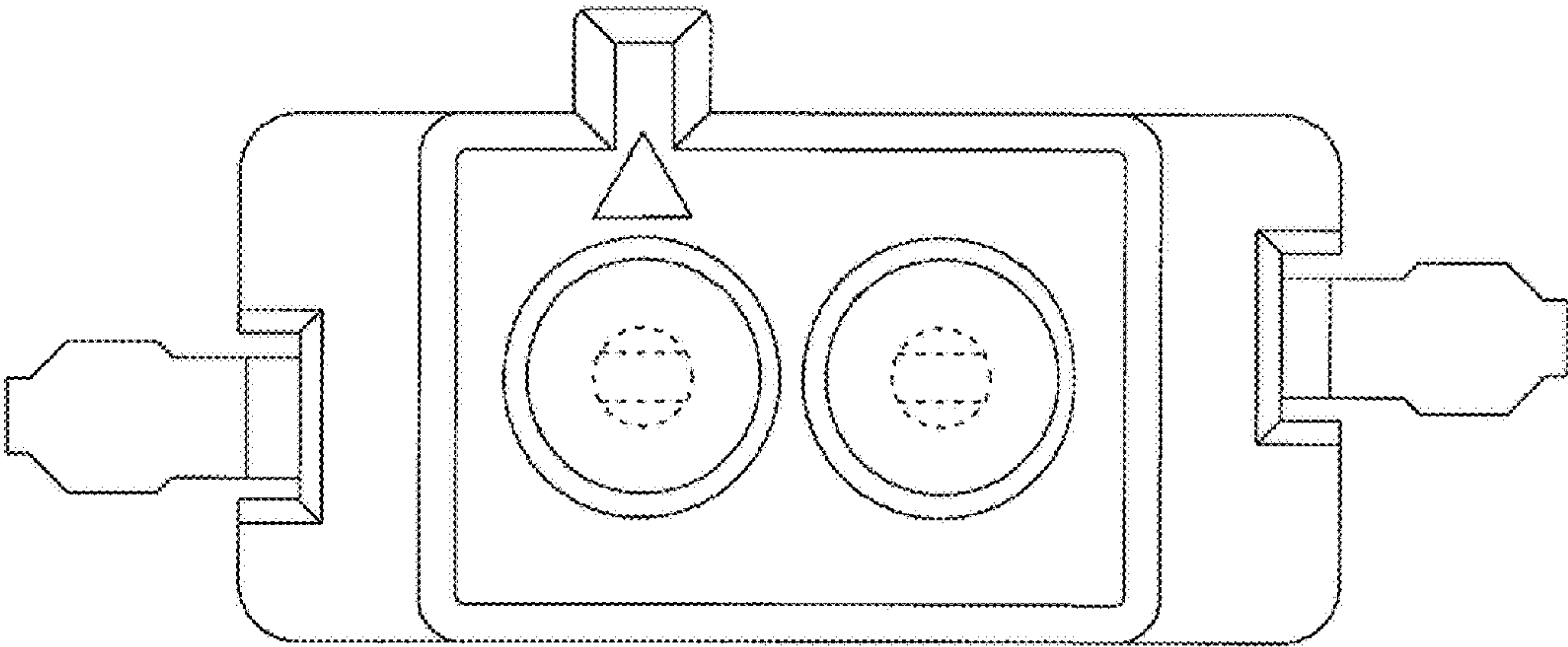


FIG. 6

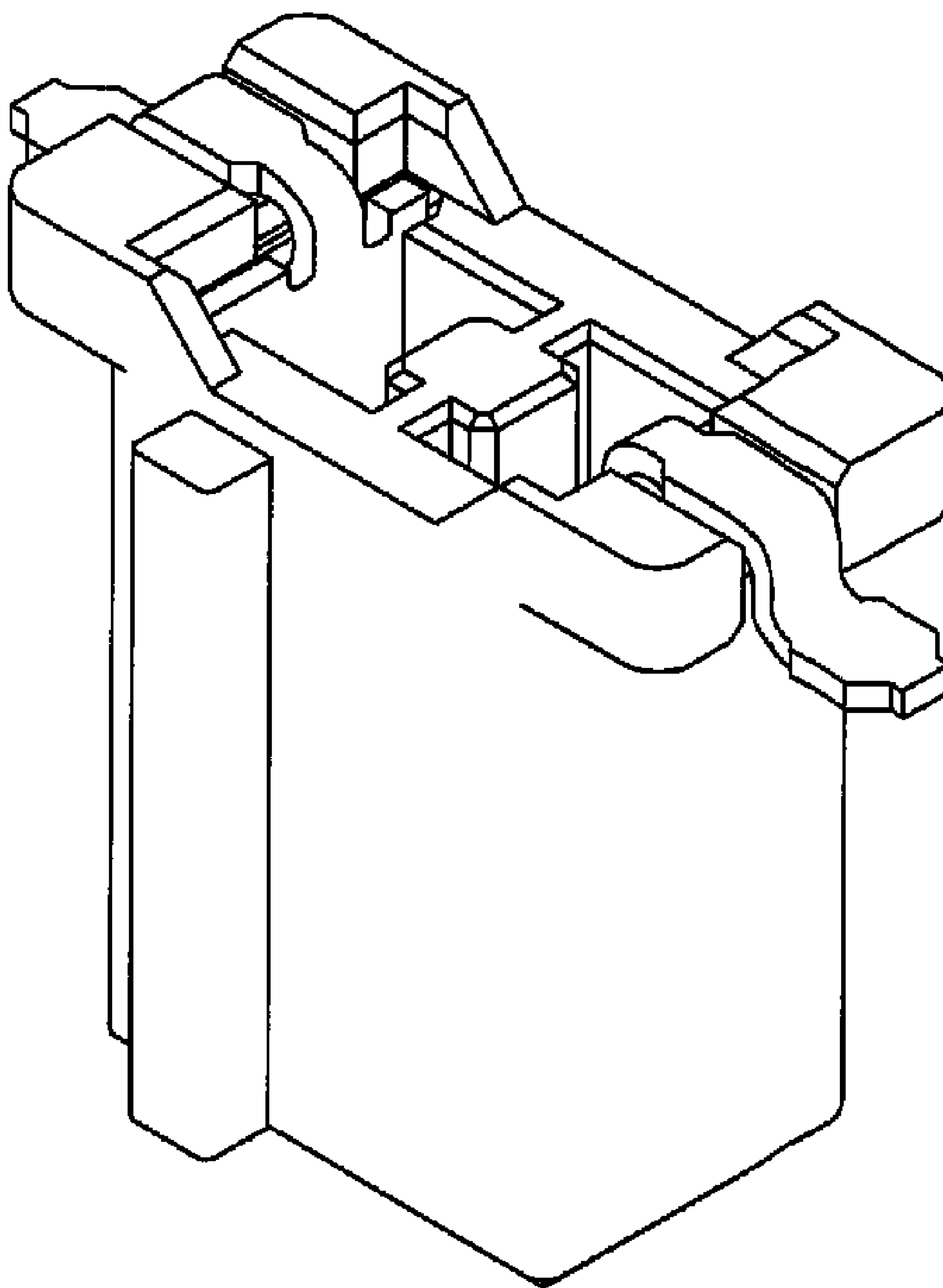


FIG. 7

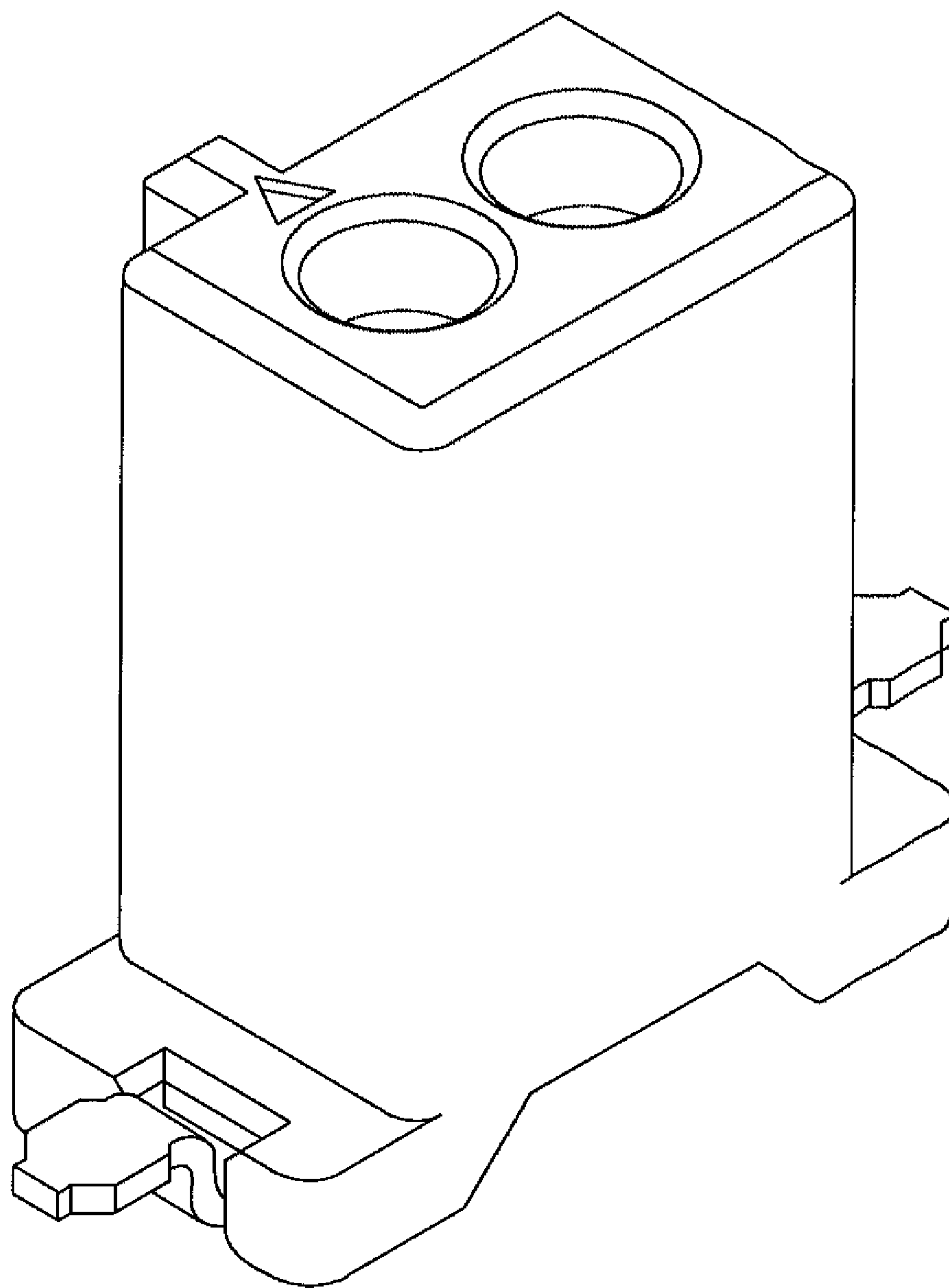


FIG. 8

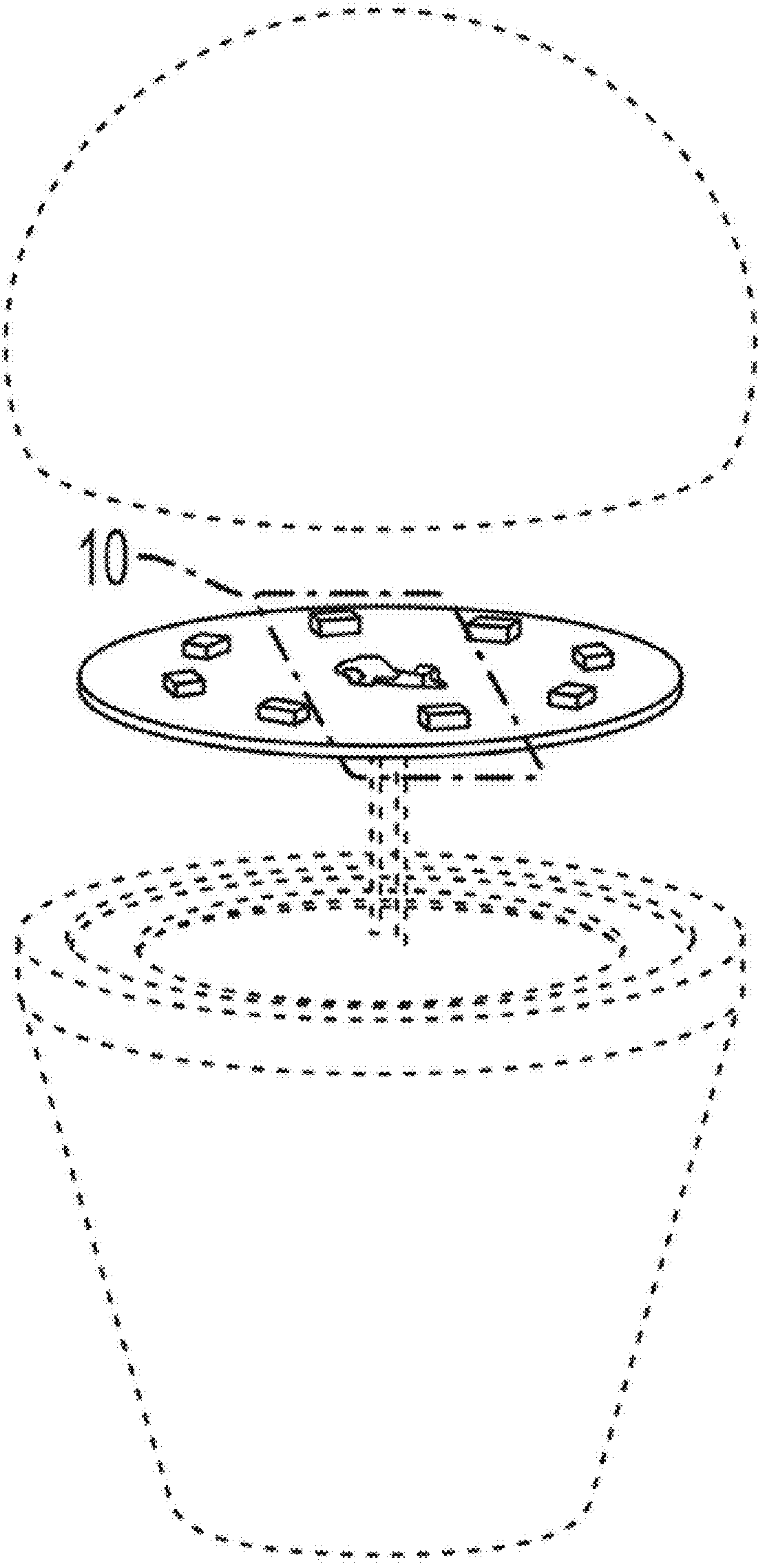


FIG. 9

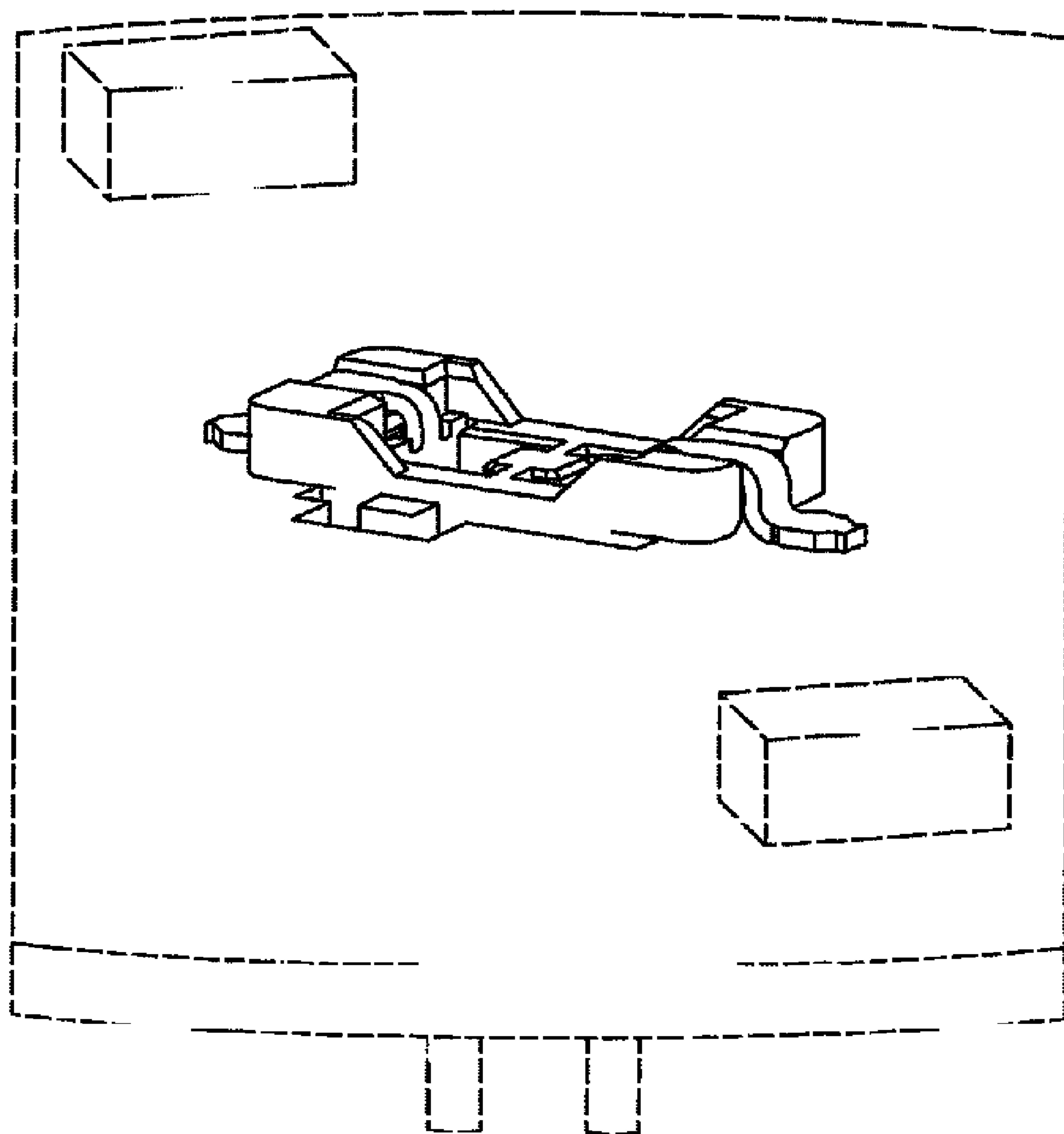


FIG. 10