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(12) **United States Design Patent**
Lyford et al.

(10) **Patent No.:** **US D649,971 S**

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(54) **ADAPTER USED WITH AN
ACCESSORY-HOST INTERFACE**

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(73) Assignee: **Tektronix, Inc.**, Beaverton, OR (US)

(*) Notice: This patent is subject to a terminal disclaimer.

(**) Term: **14 Years**

(21) Appl. No.: **29/383,024**

(22) Filed: **Jan. 11, 2011**

(51) **LOC (9) Cl.** **14-02**

(52) **U.S. Cl.** **D14/433; D13/133; D13/147**

(58) **Field of Classification Search** D14/356–358,
D14/383–385, 420, 426, 427, 432–439, 447,
D14/453, 454, 217, 253, 299; D13/107,
D13/108, 133, 146, 147, 152, 154, 156, 158,
D13/162, 162.1, 168, 173, 177; 385/53,
385/55; 439/680, 578, 638; D10/80
See application file for complete search history.

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Primary Examiner — Cathron Brooks

Assistant Examiner — Deanna Fluegeman

(74) *Attorney, Agent, or Firm* — William K. Bucher

(57) **CLAIM**

The ornamental design for an adapter used with an accessory-host interface, as shown and described.

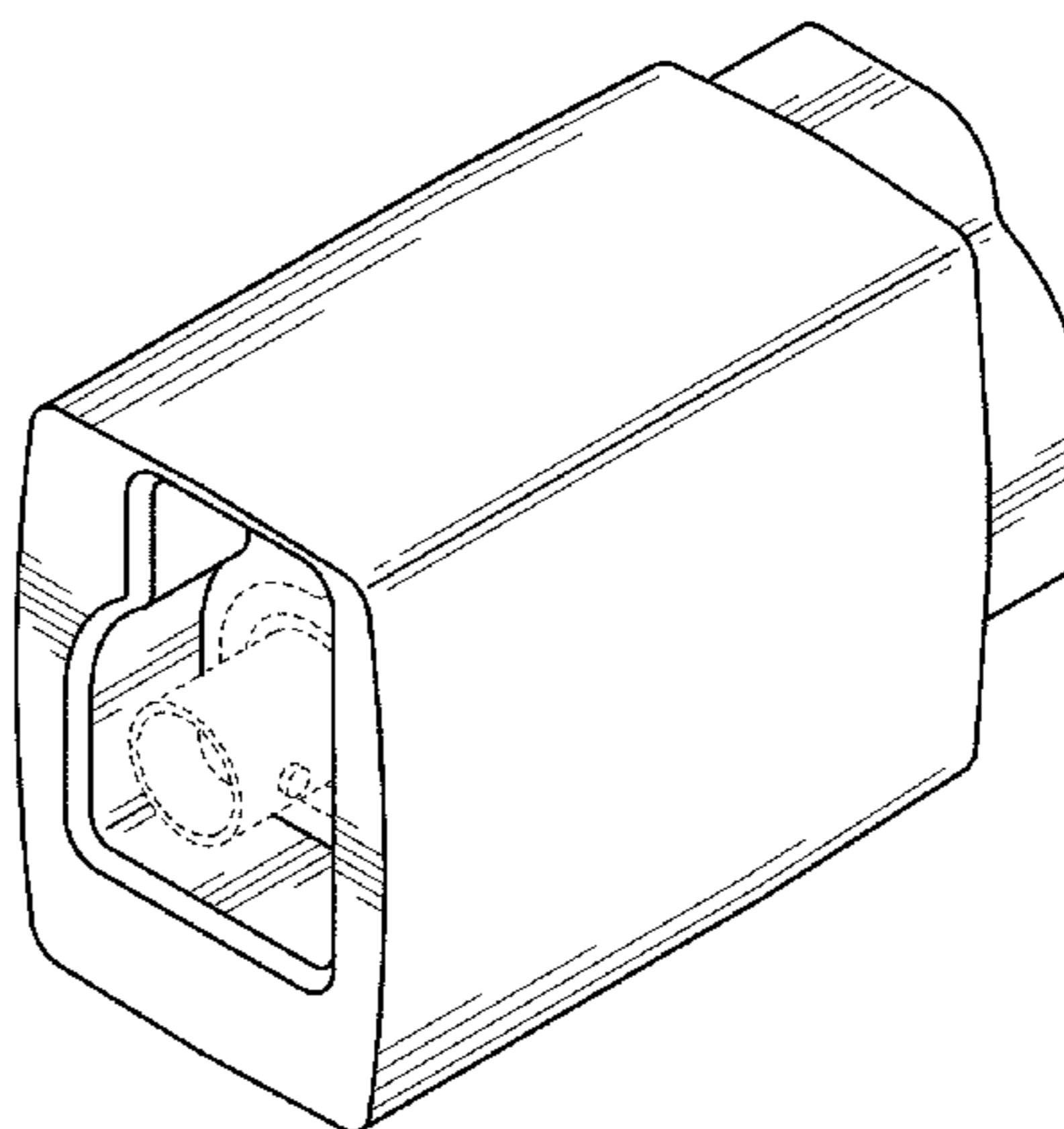
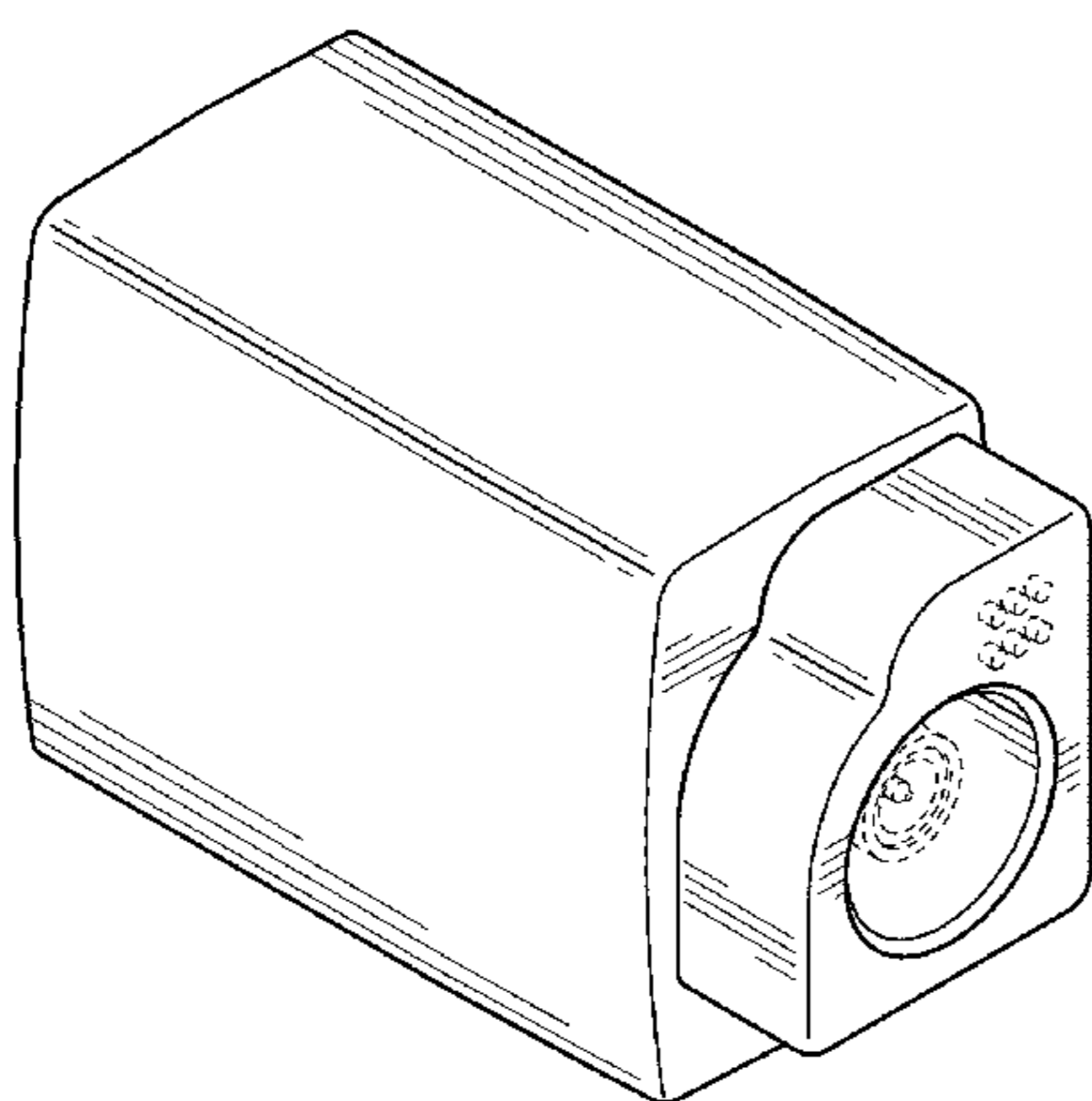
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DESCRIPTION

FIG. 1 is a front perspective view of a first embodiment showing the plug end of an adapter used with an accessory-host interface disposed in a host device;
FIG. 2 is a reverse perspective view of a first embodiment showing the receptacle end of the adapter used with an accessory-host interface;
FIG. 3 is a top plan view of a first embodiment of the adapter used with an accessory-host interface;
FIG. 4 is a left side elevation view of a first embodiment of the adapter used with an accessory-host interface;
FIG. 5 is a bottom plan view of a first embodiment of the adapter used with an accessory-host interface;
FIG. 6 is a right side elevation view of a first embodiment of the adapter used with an accessory-host interface;



The broken lines in the drawings illustrate unclaimed portions of the adapter used with an accessory-host interface and form no part of the claimed invention.

The ornamental design disclosed in this application is for an adapter used with an accessory-host interface wherein one end of the adapter is received in a receptacle disposed in a host

device, such as an oscilloscope, logic analyzer, spectrum analyzer, signal generator and the like, and the other end of the adapter receives an accessory device, such as a signal measurement probe, temperature probe and the like.

1 Claim, 45 Drawing Sheets

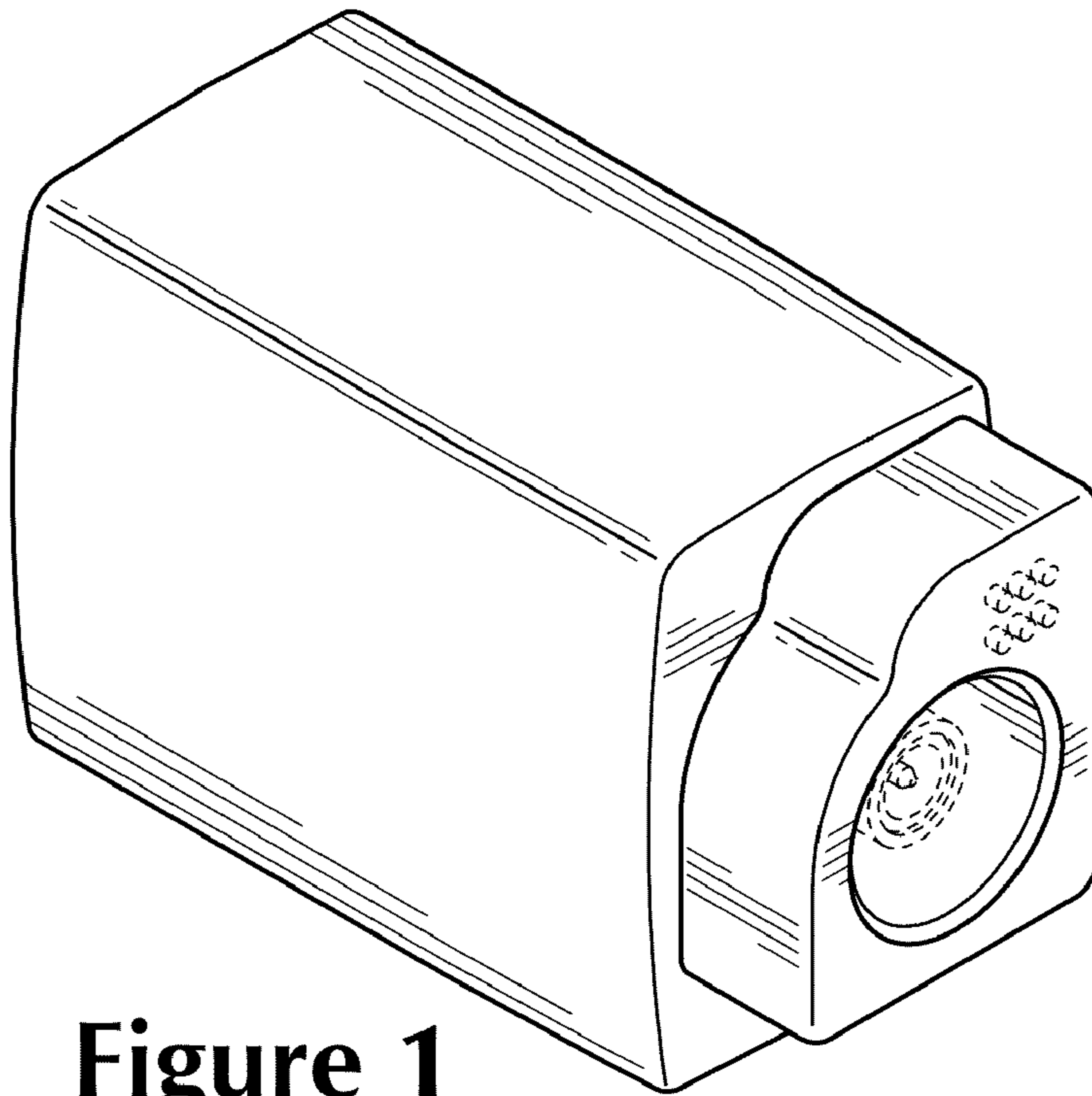


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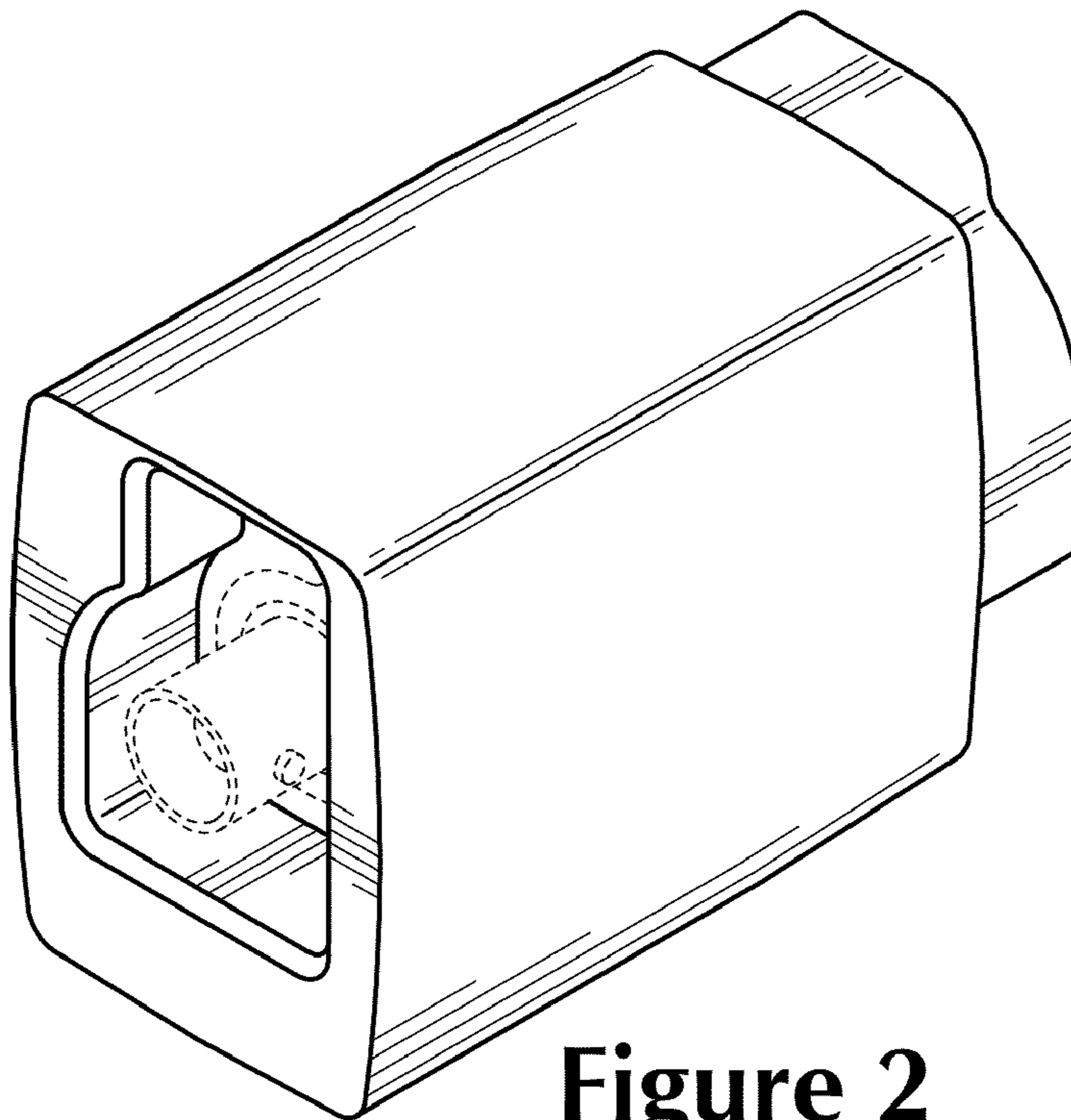


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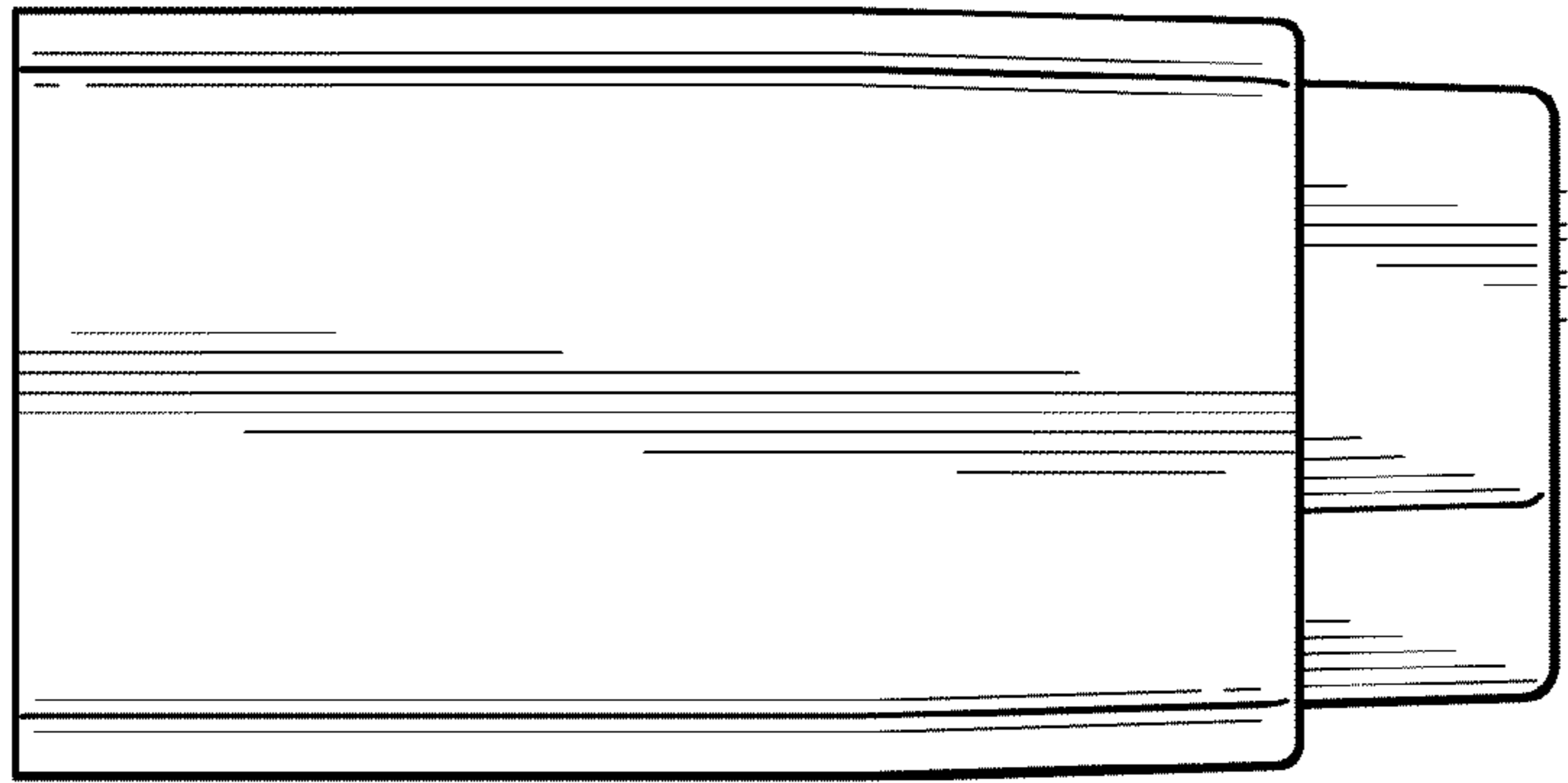


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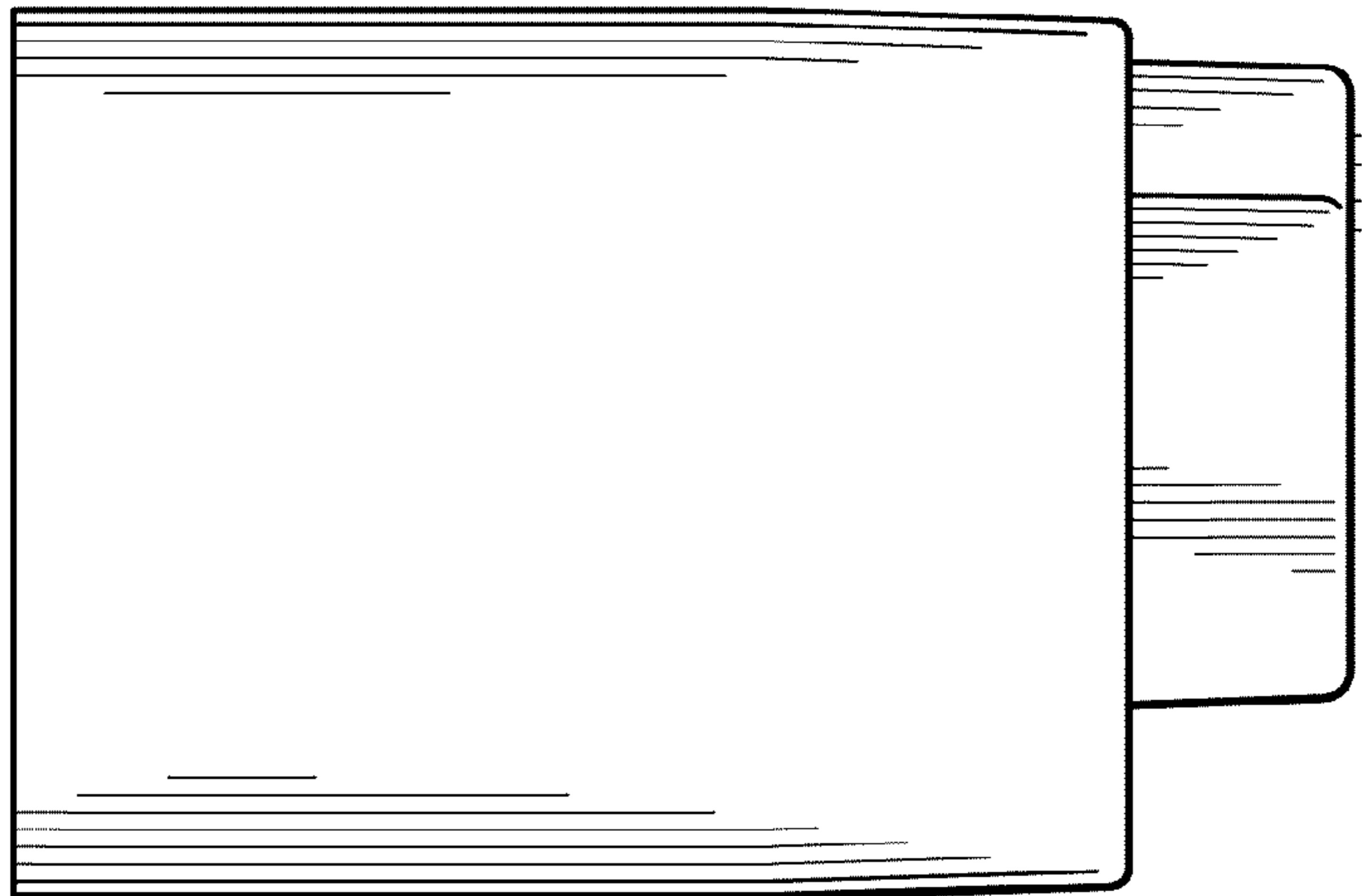


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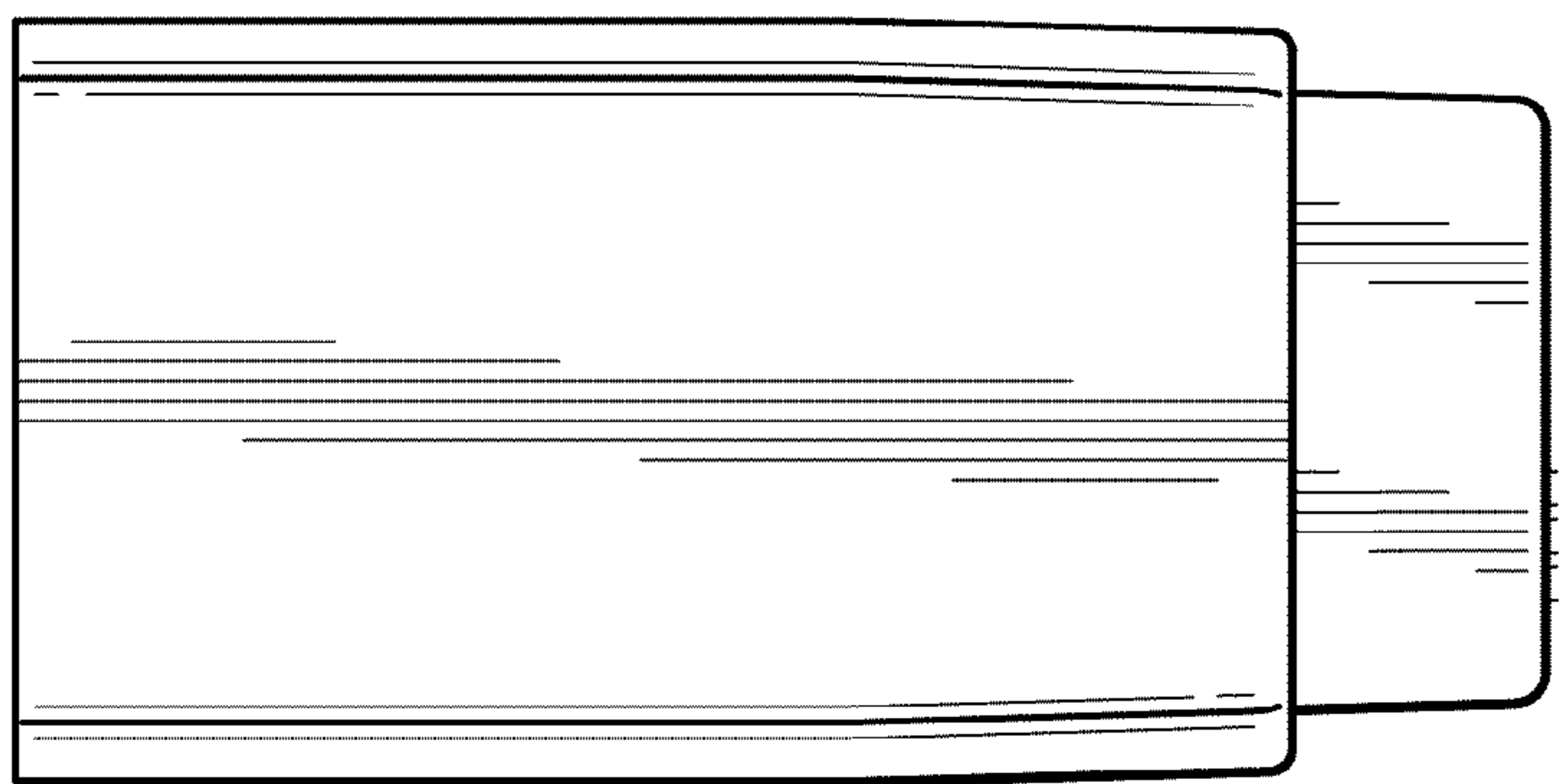


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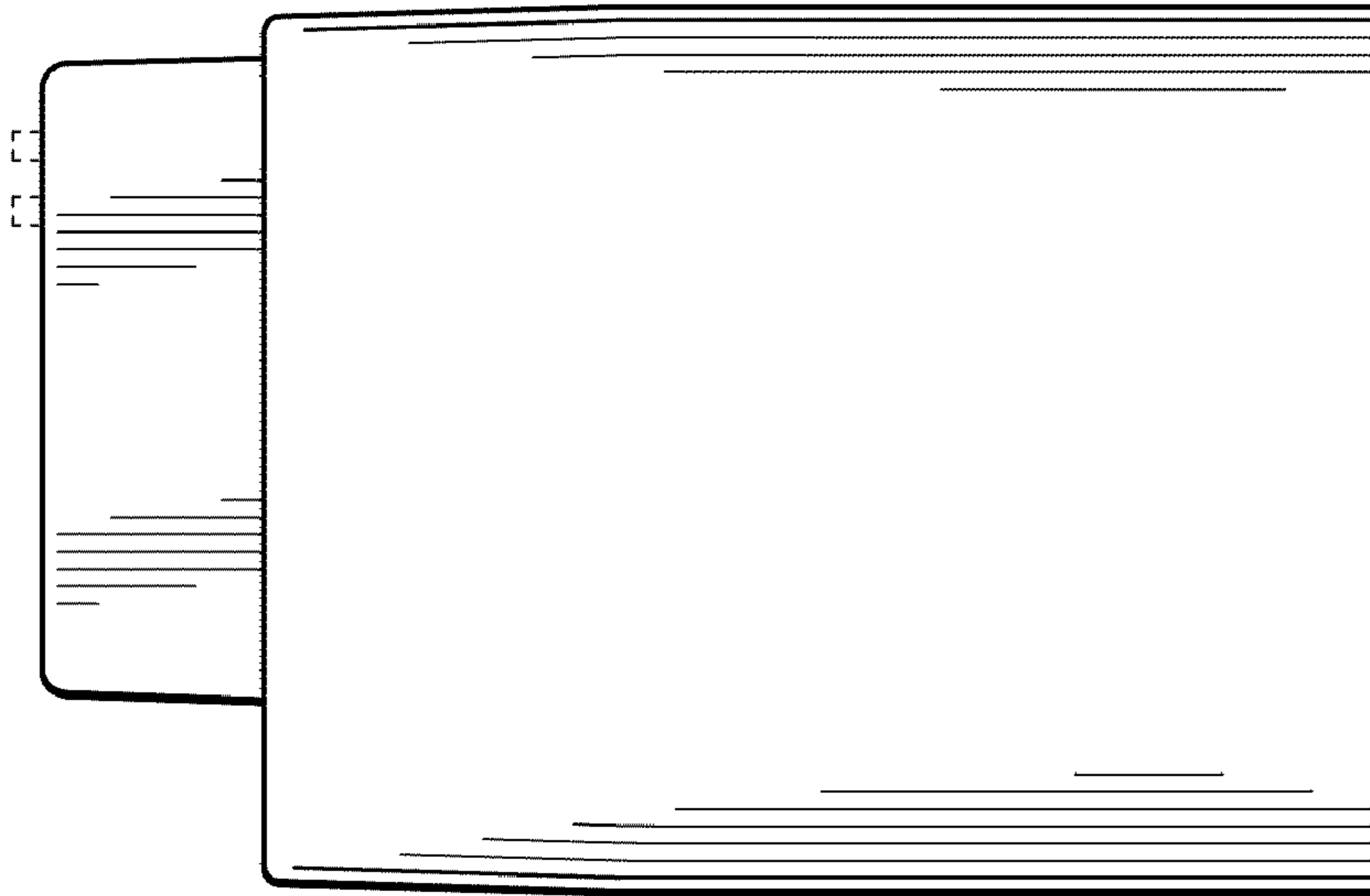


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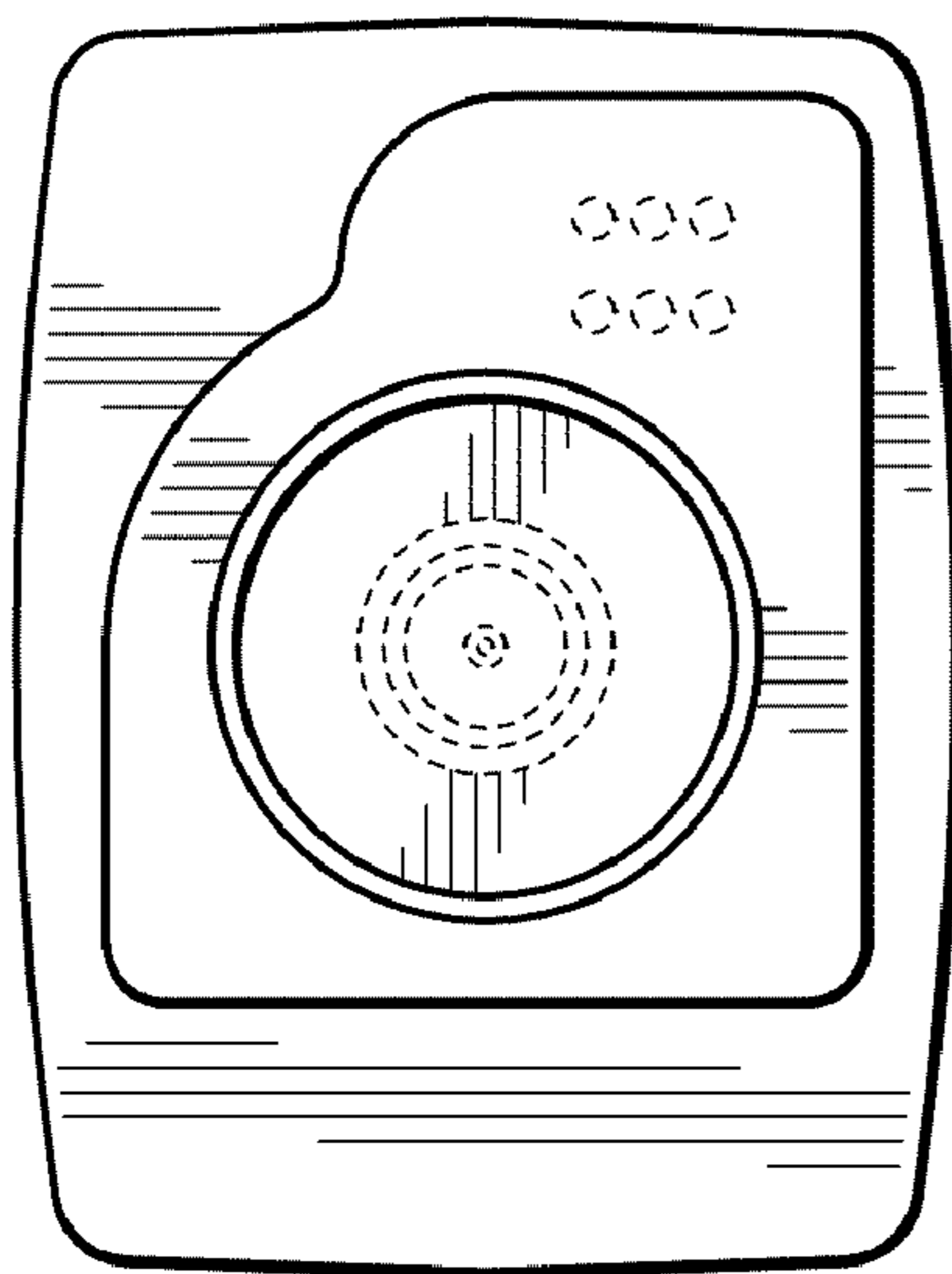


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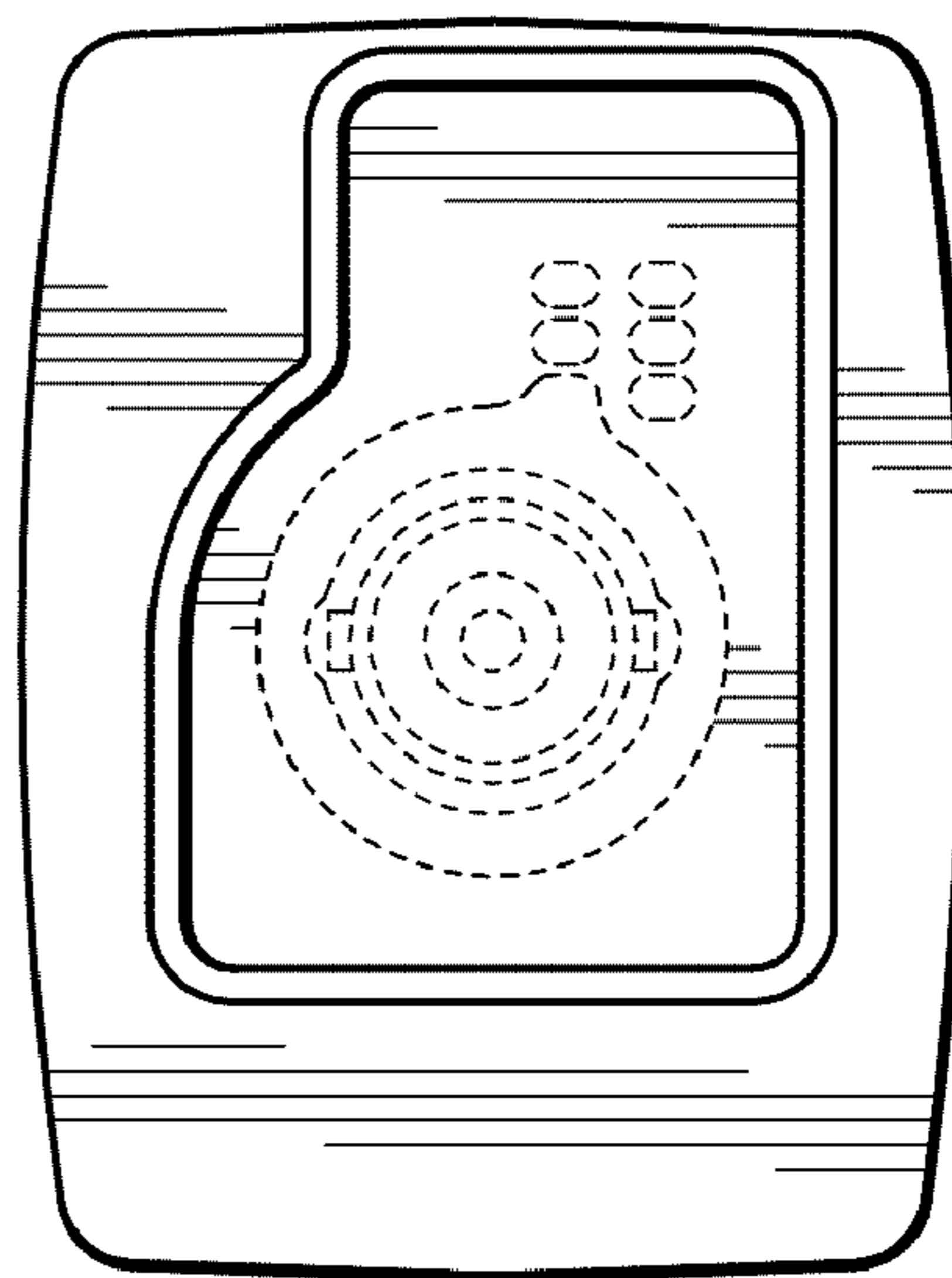


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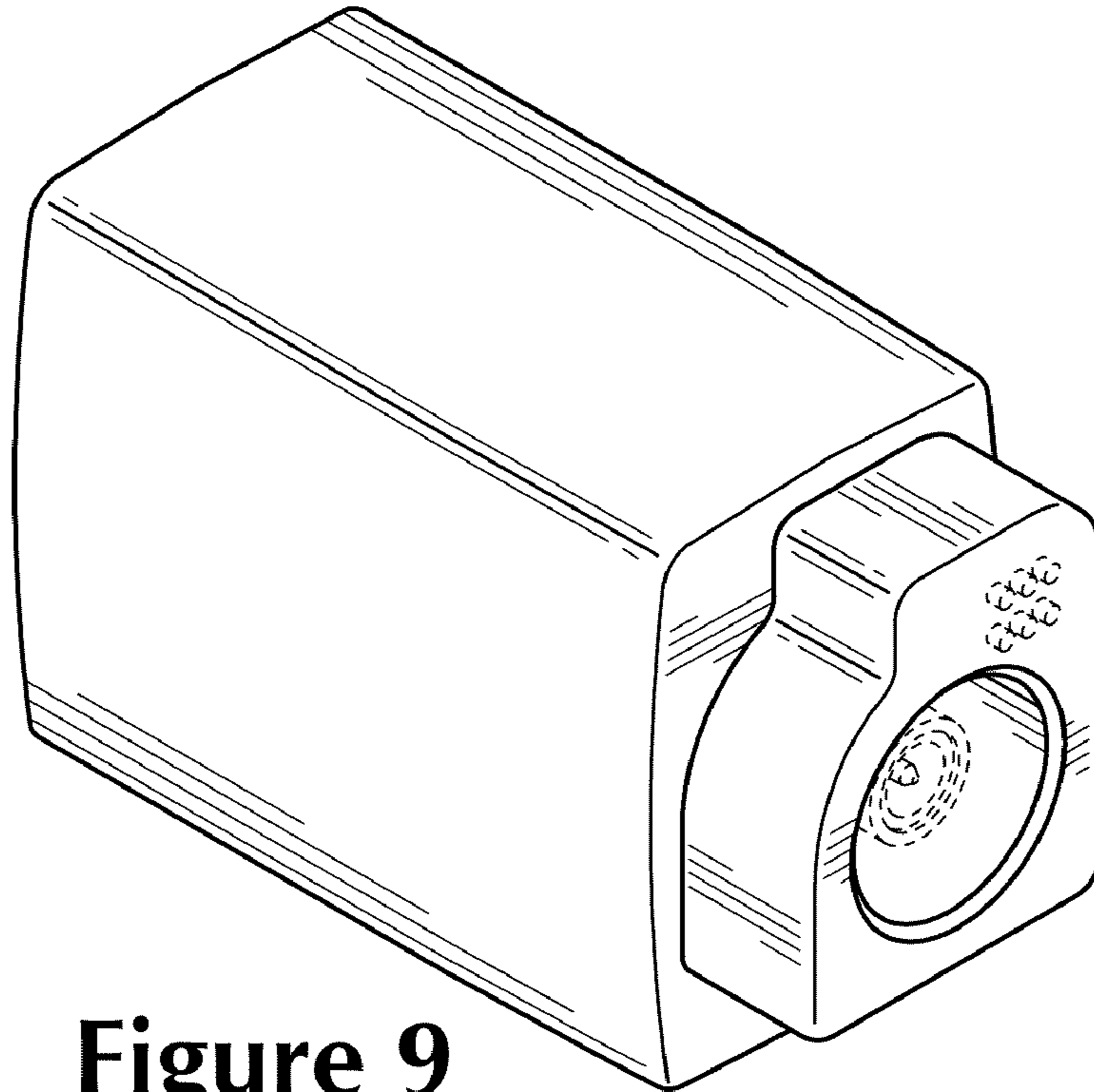


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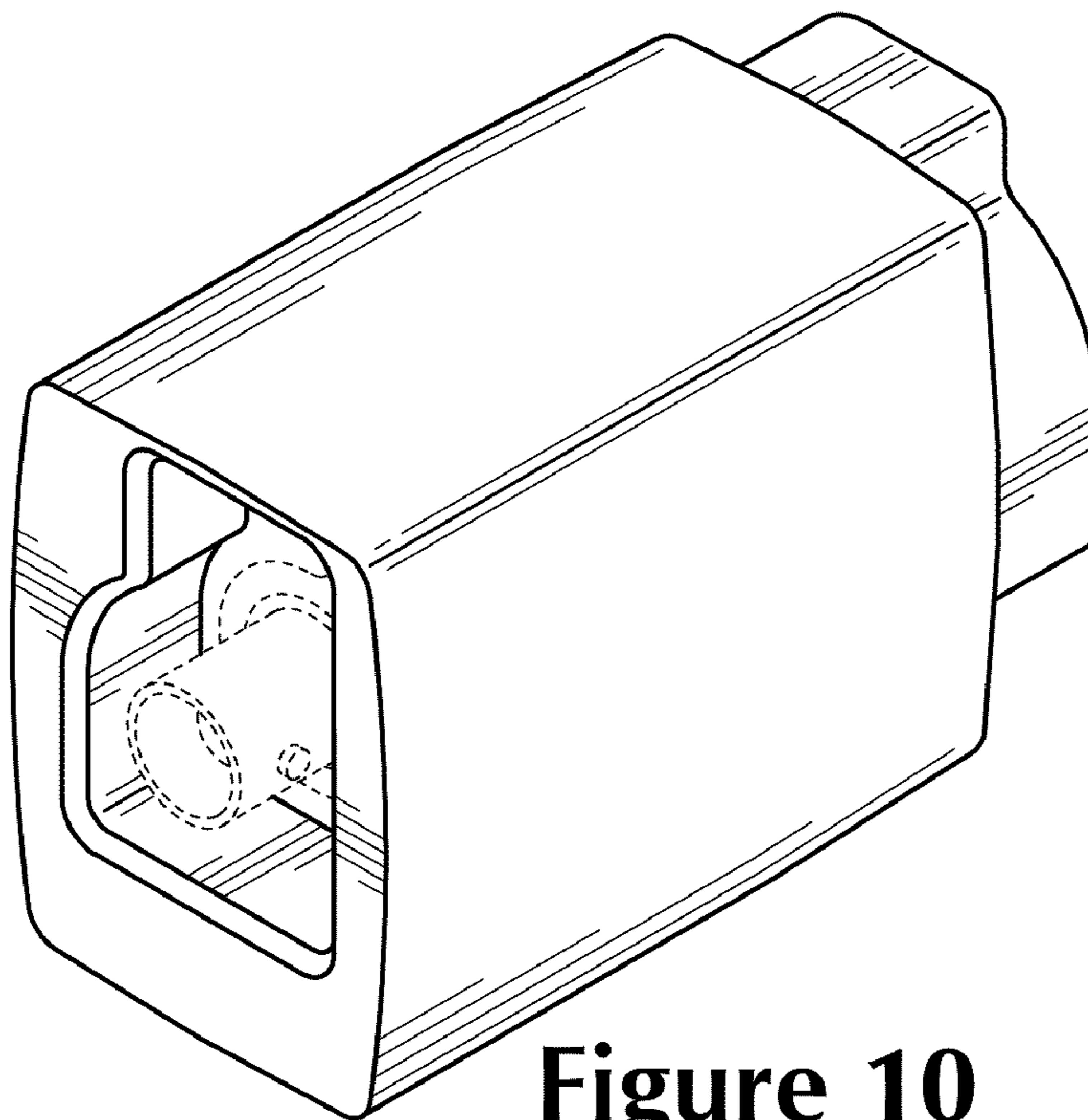


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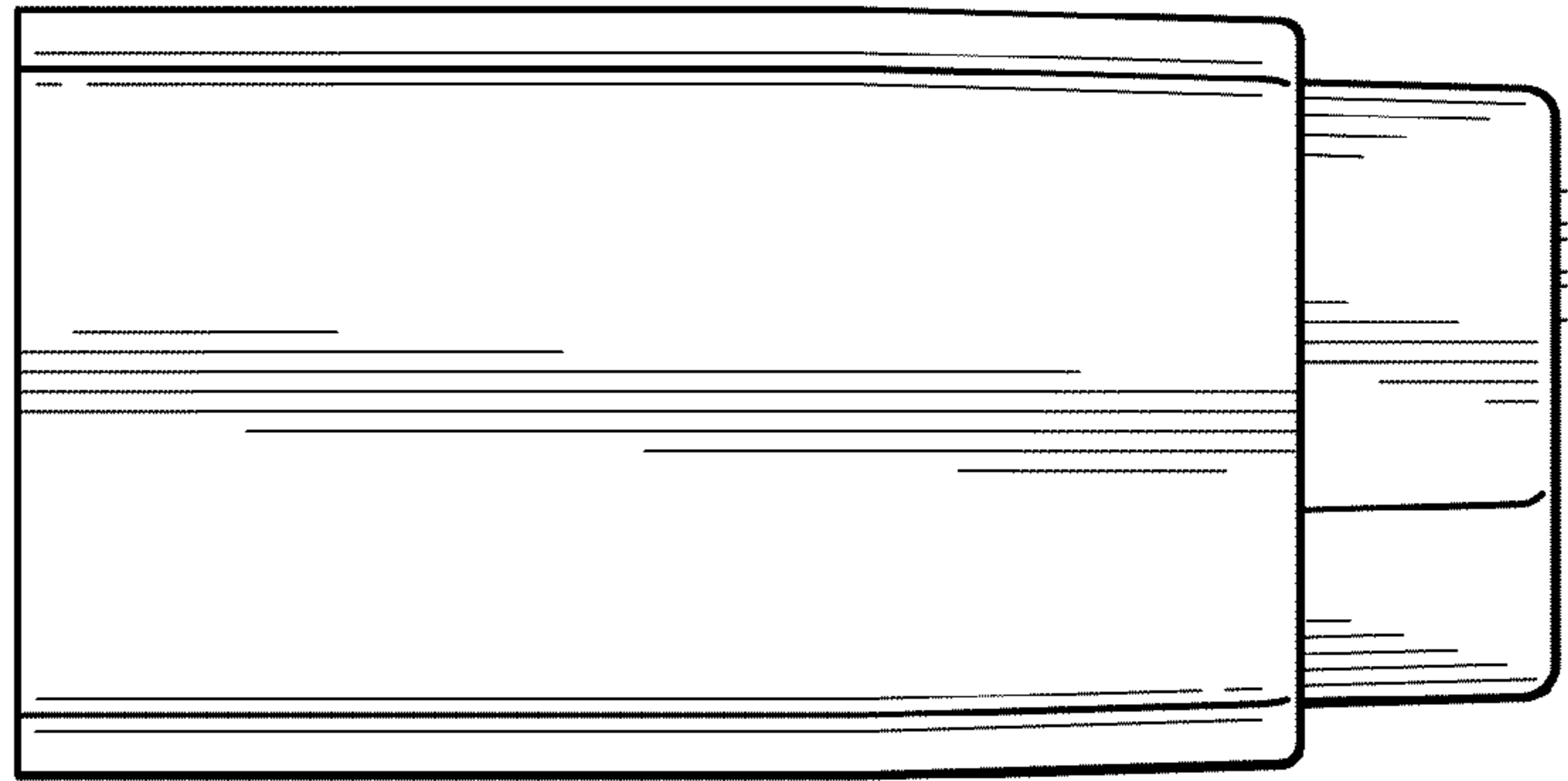


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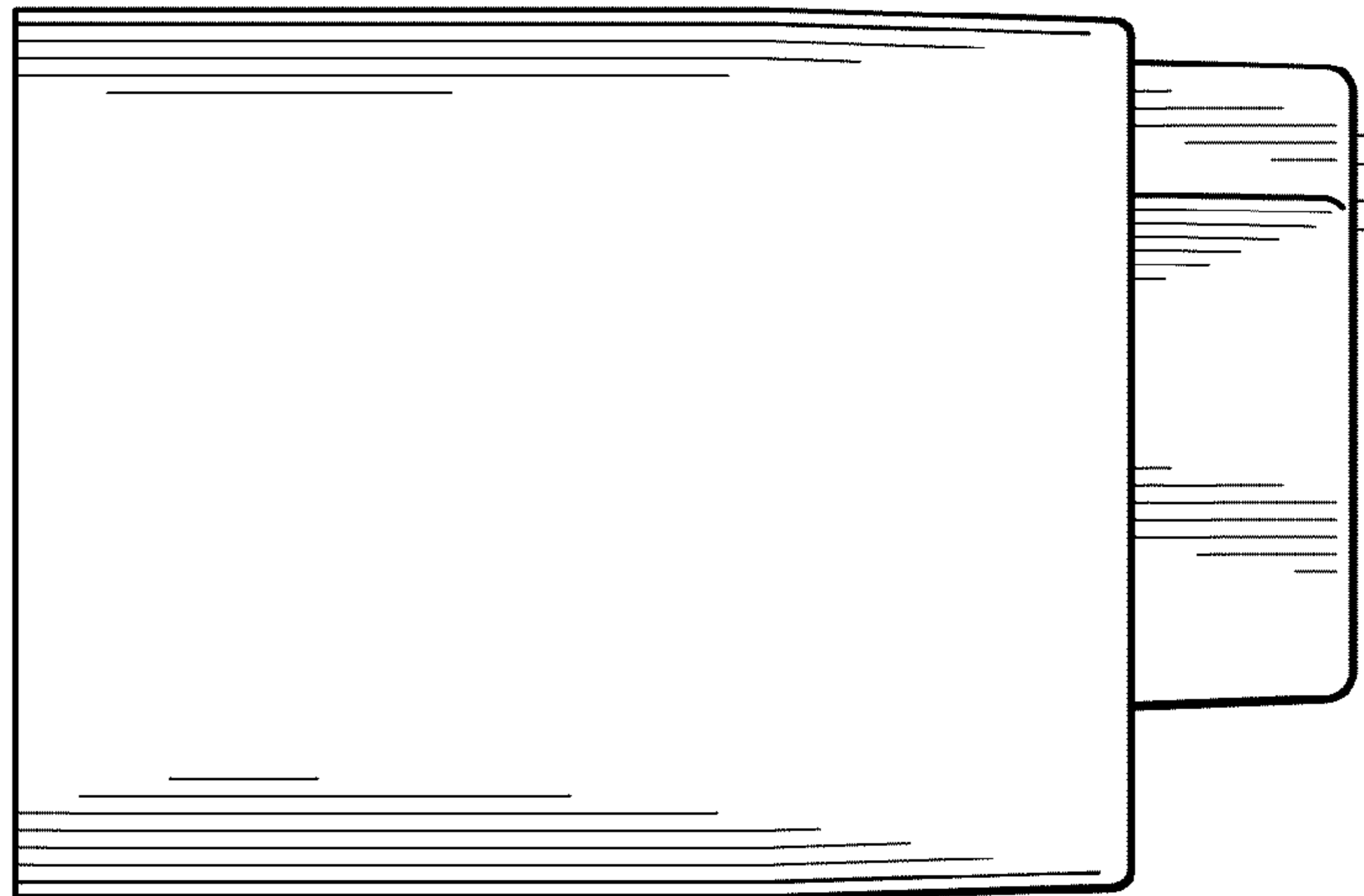


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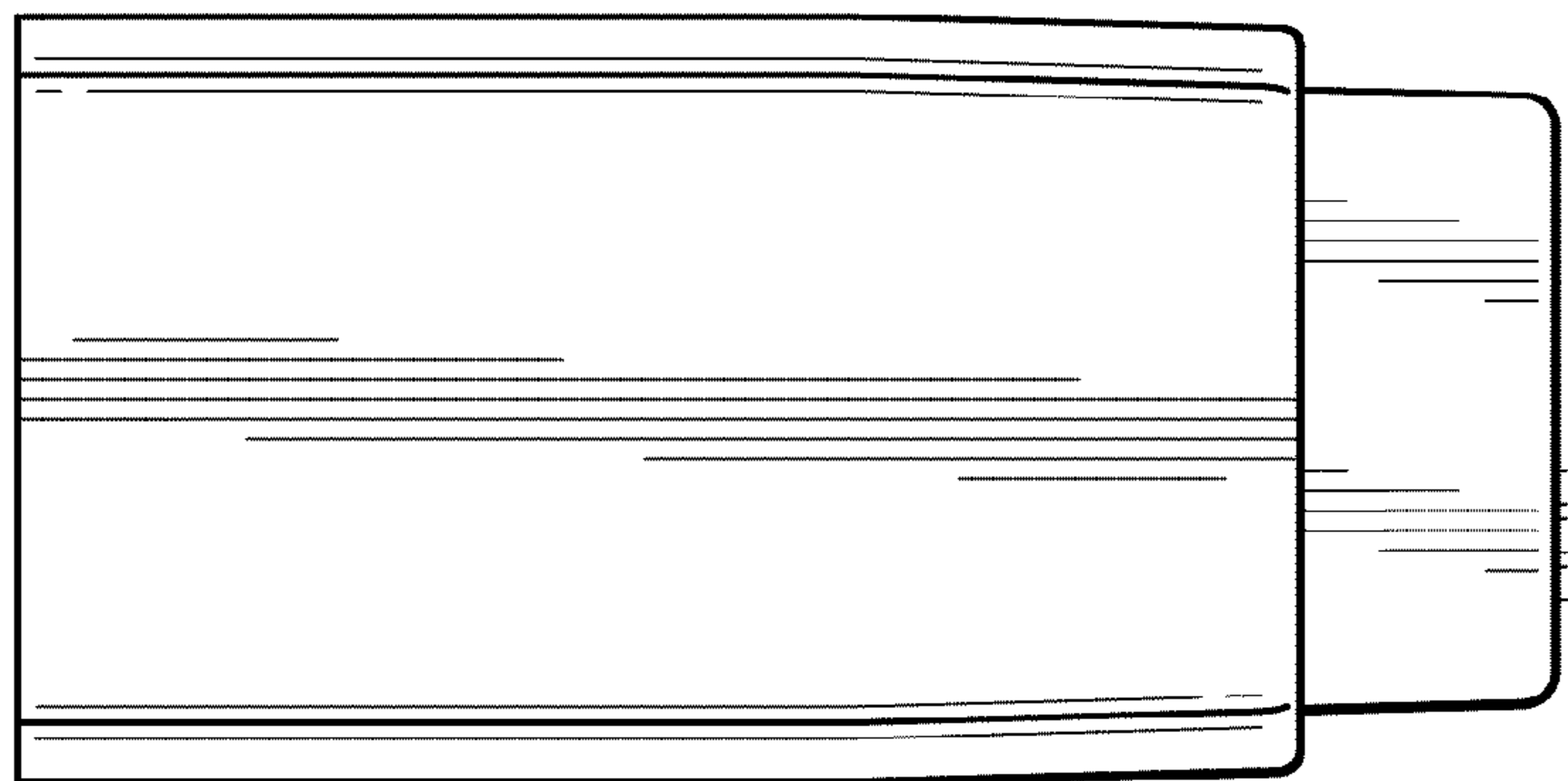


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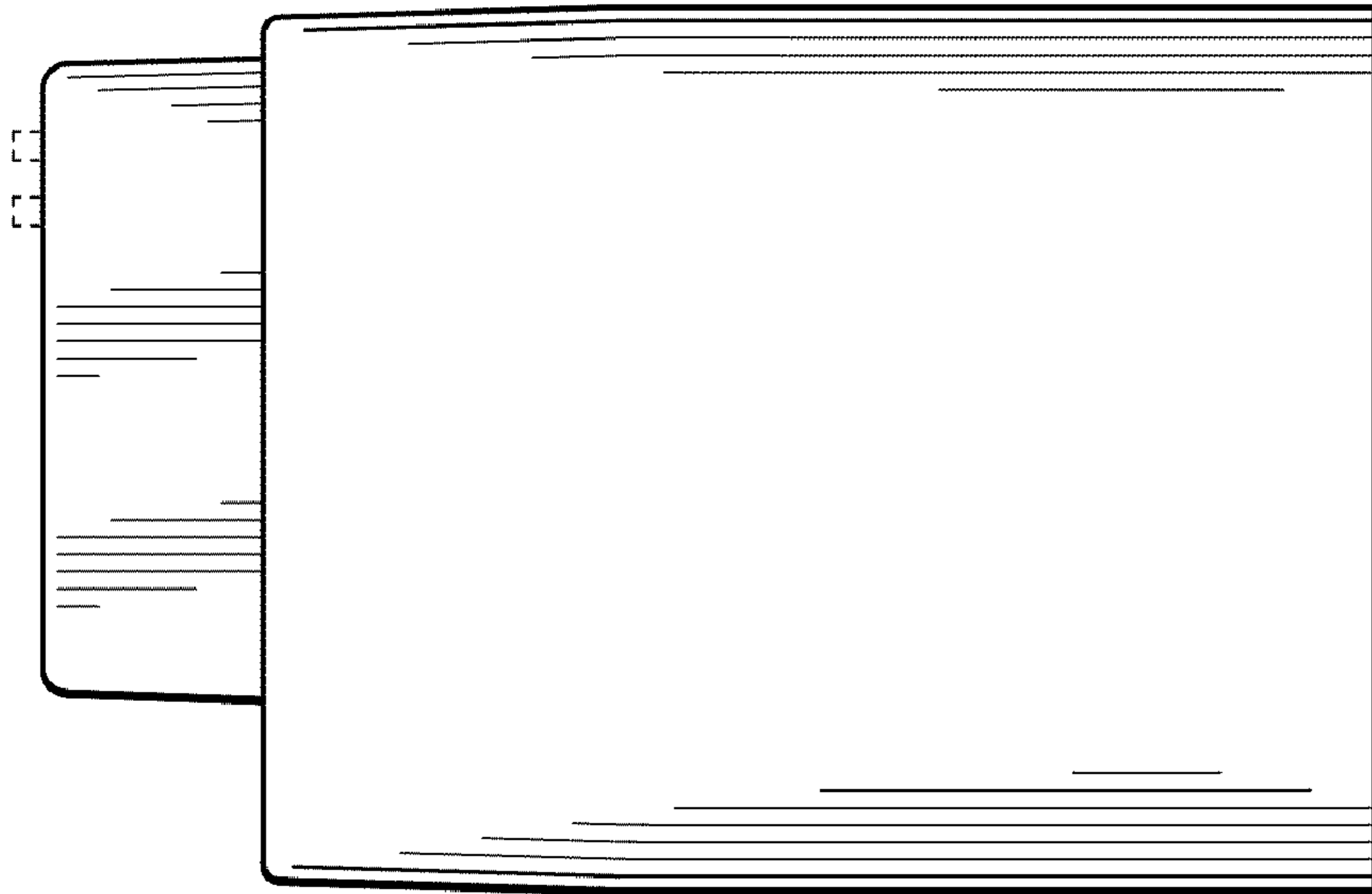


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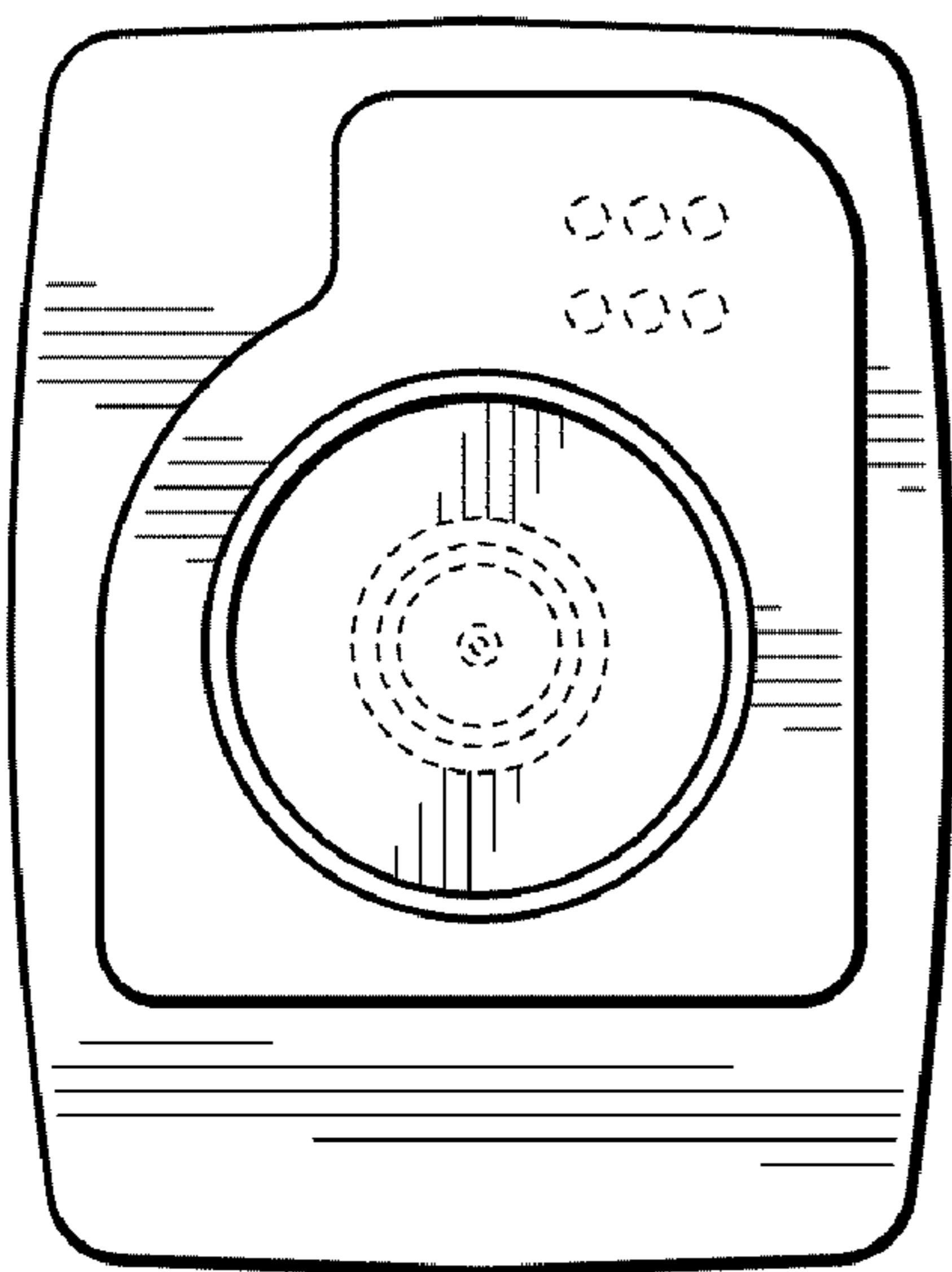


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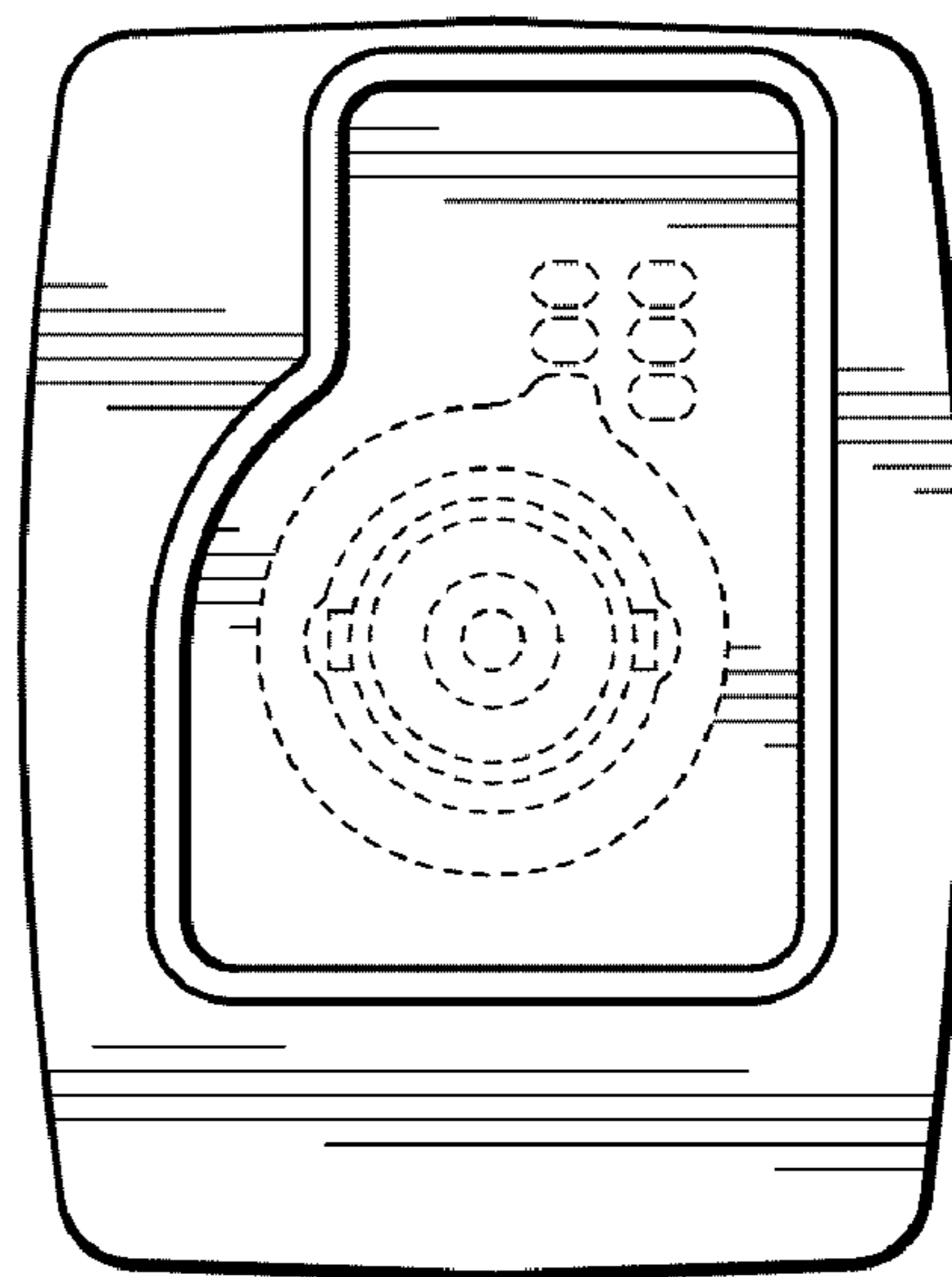


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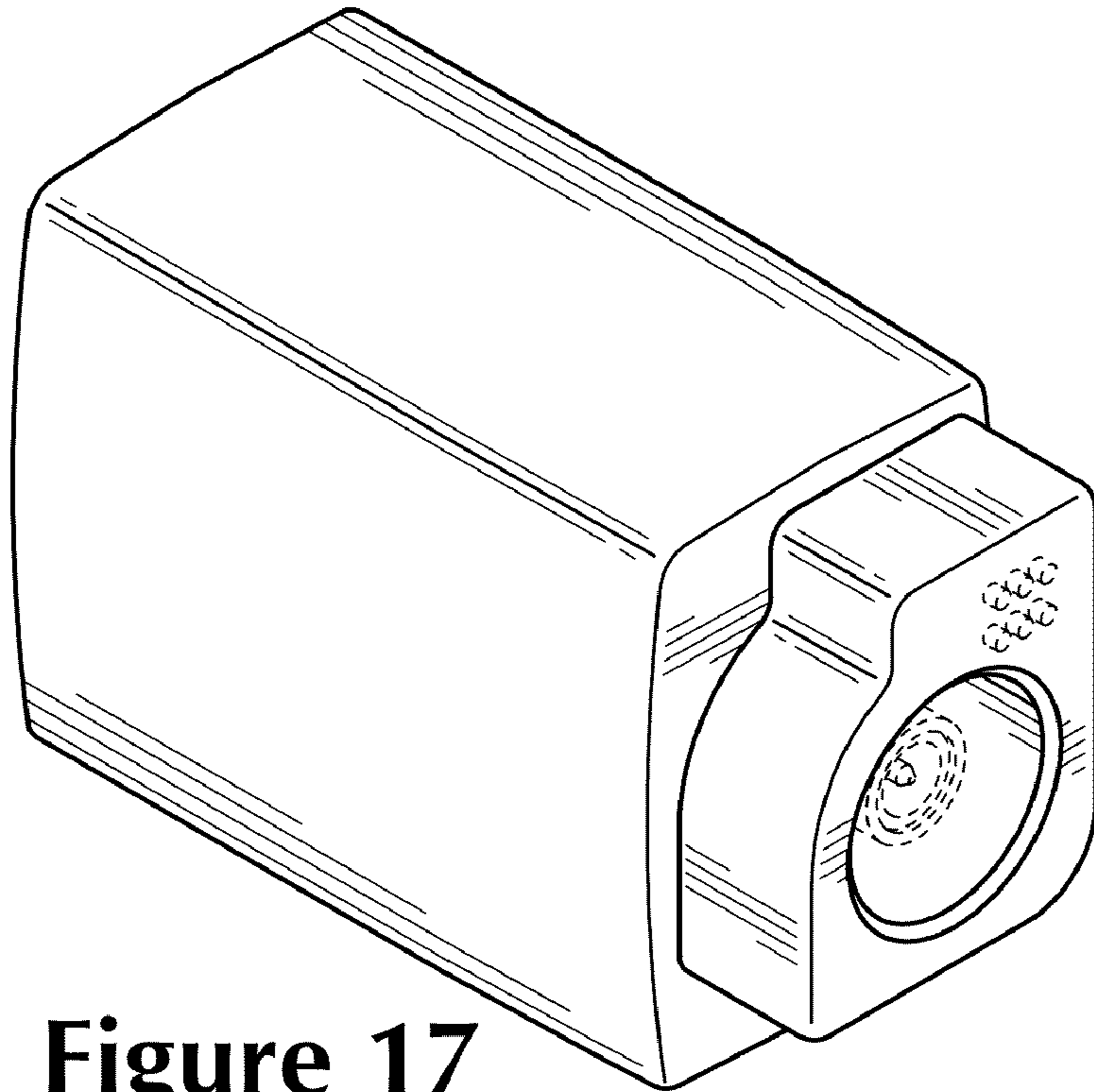


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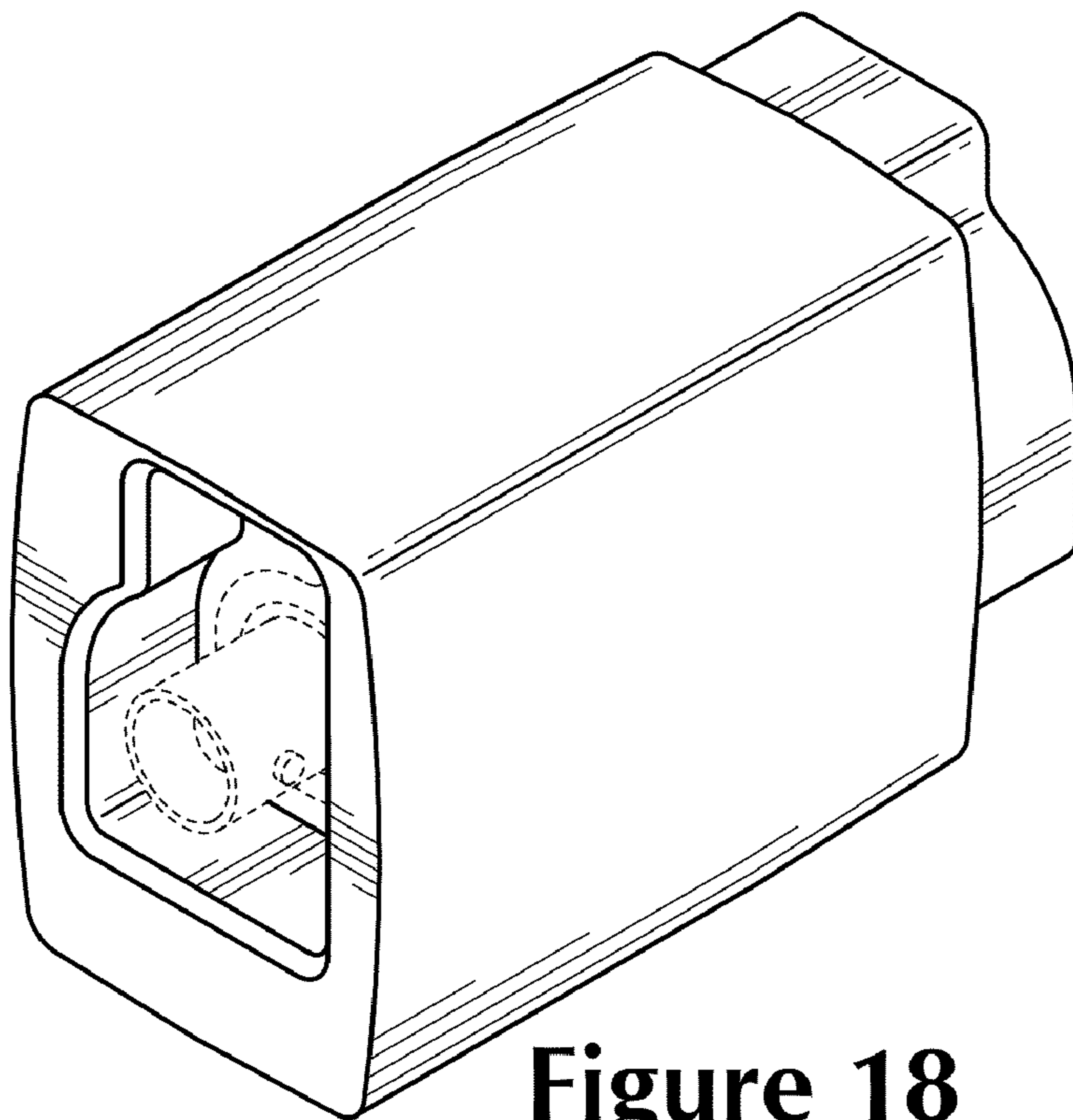


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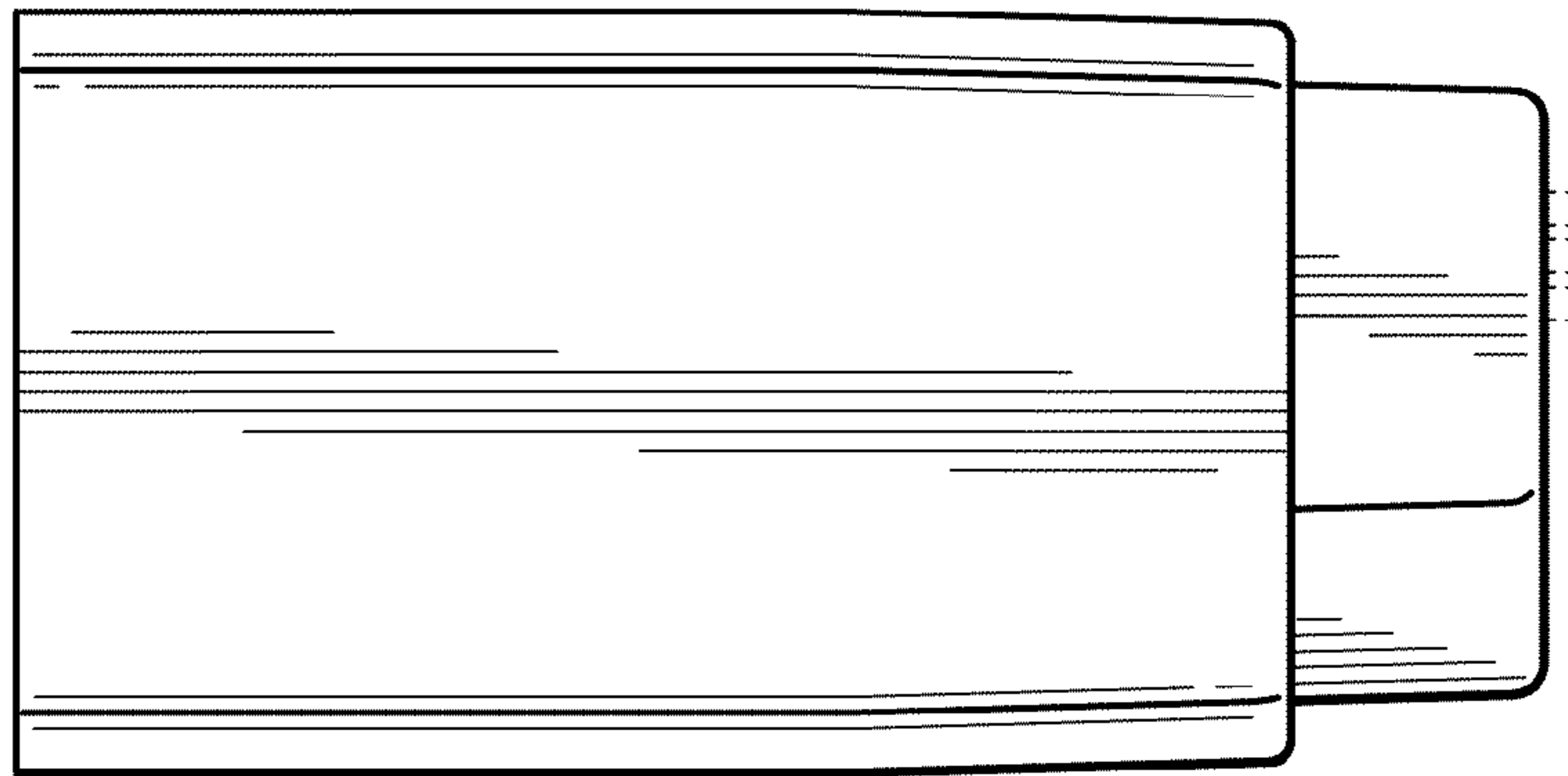


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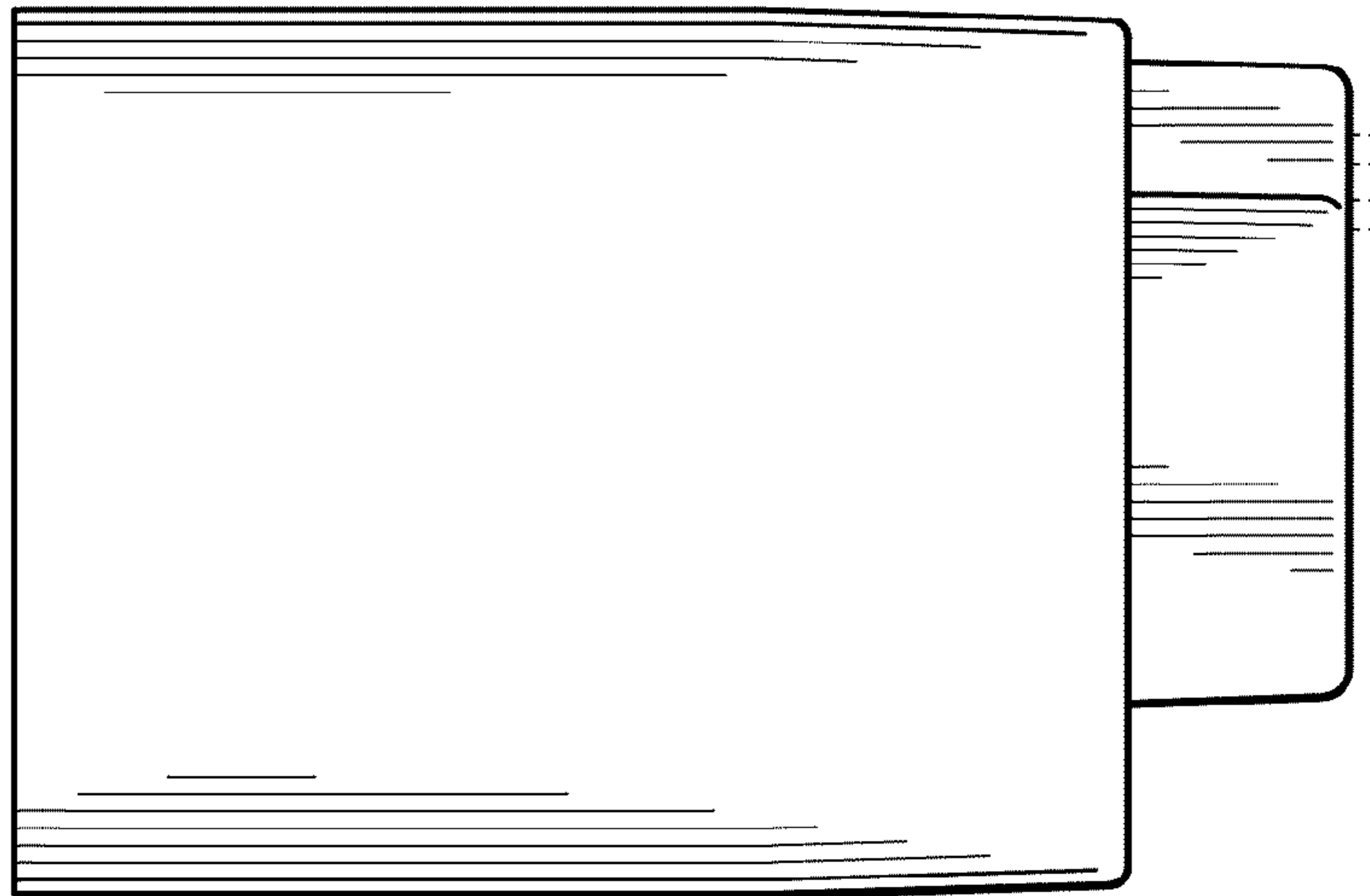


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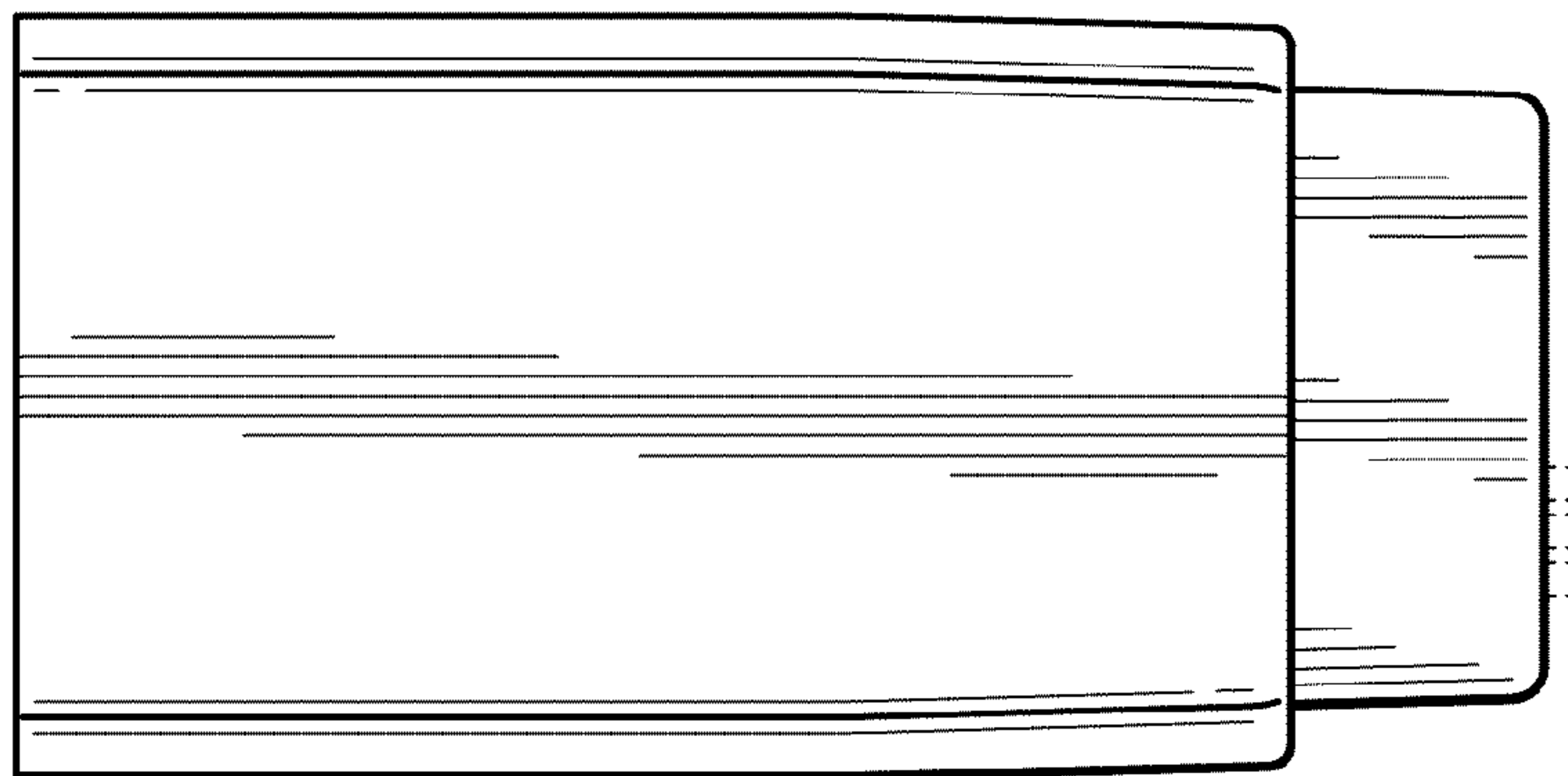


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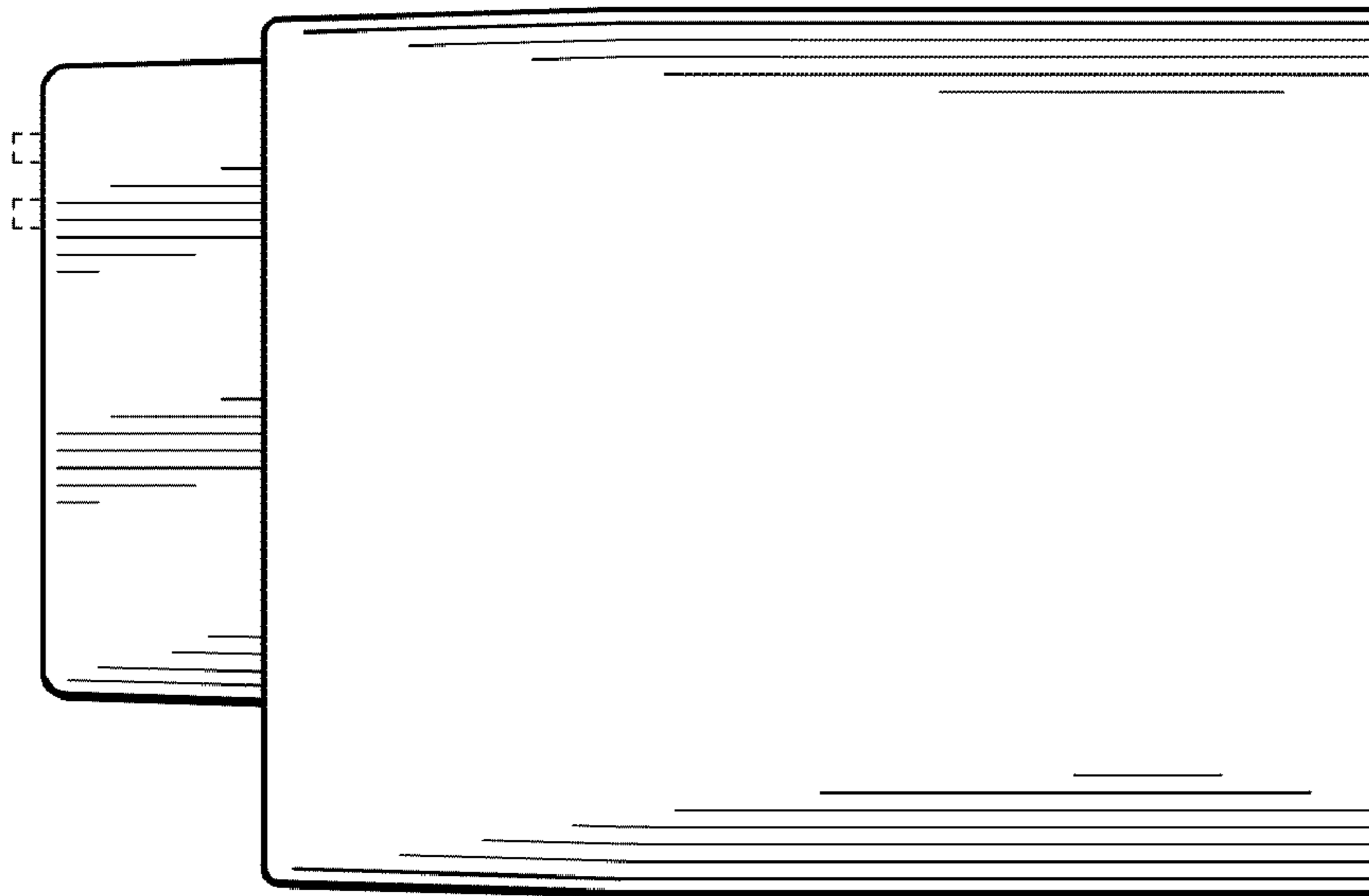


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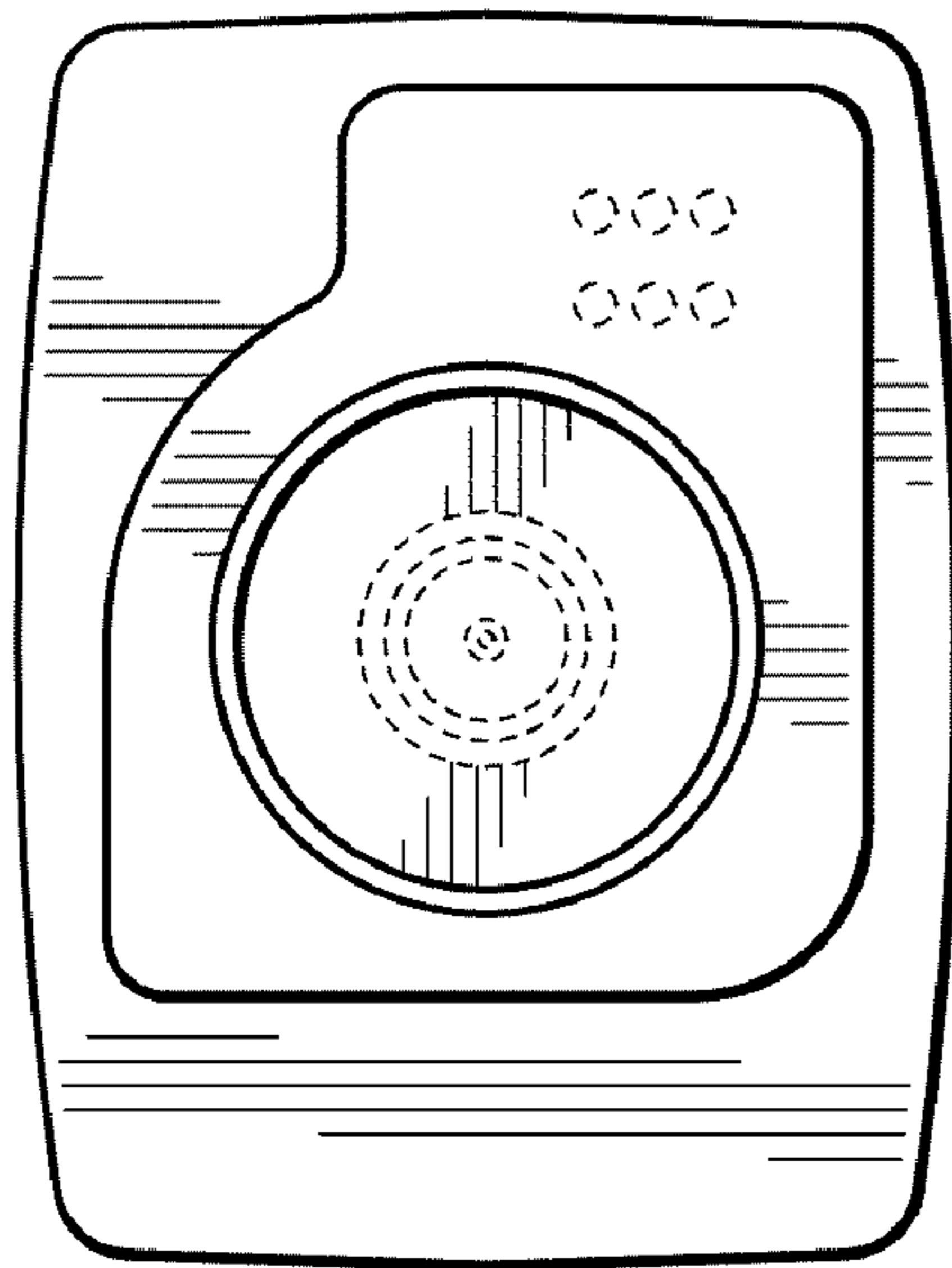


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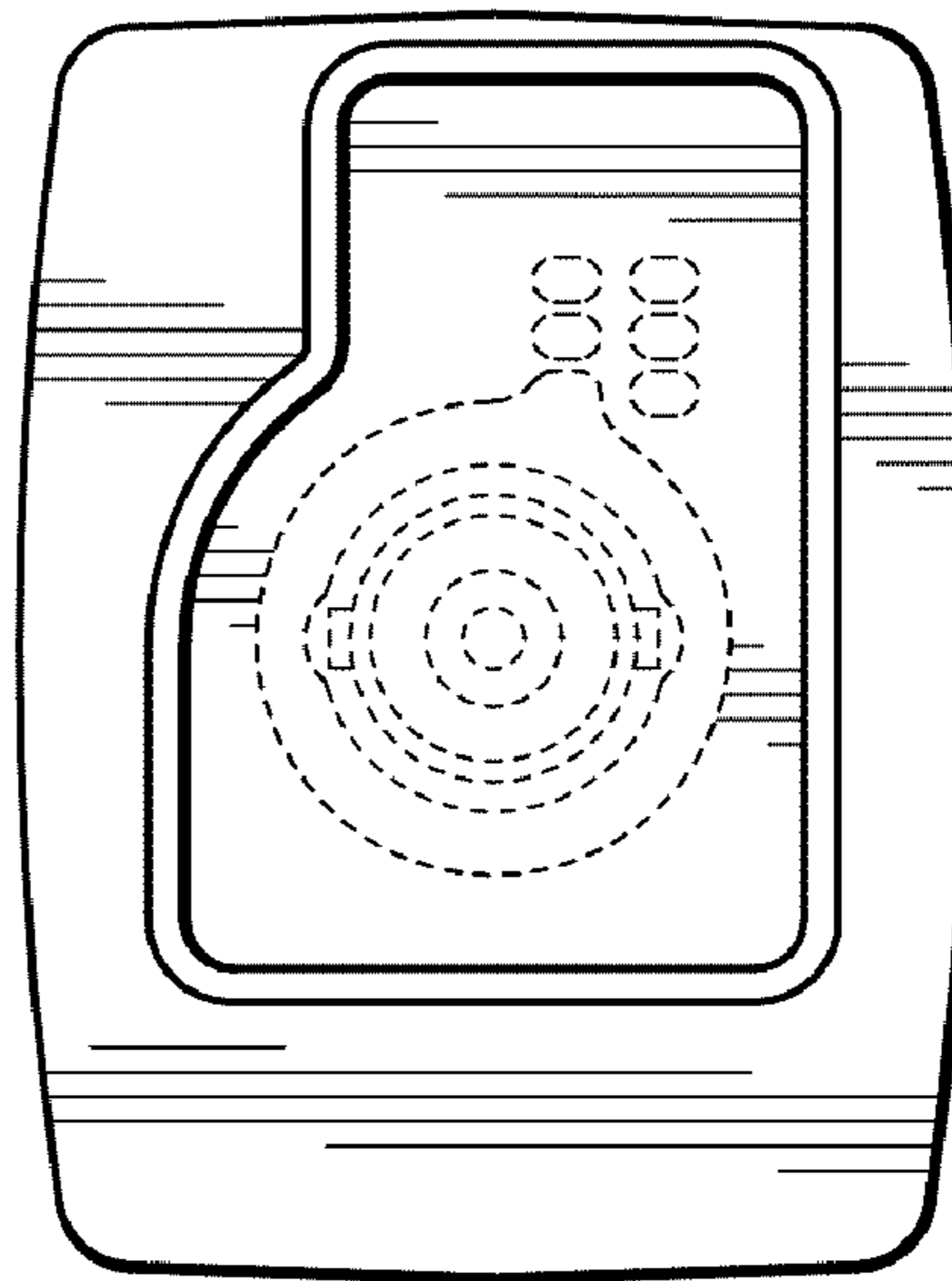


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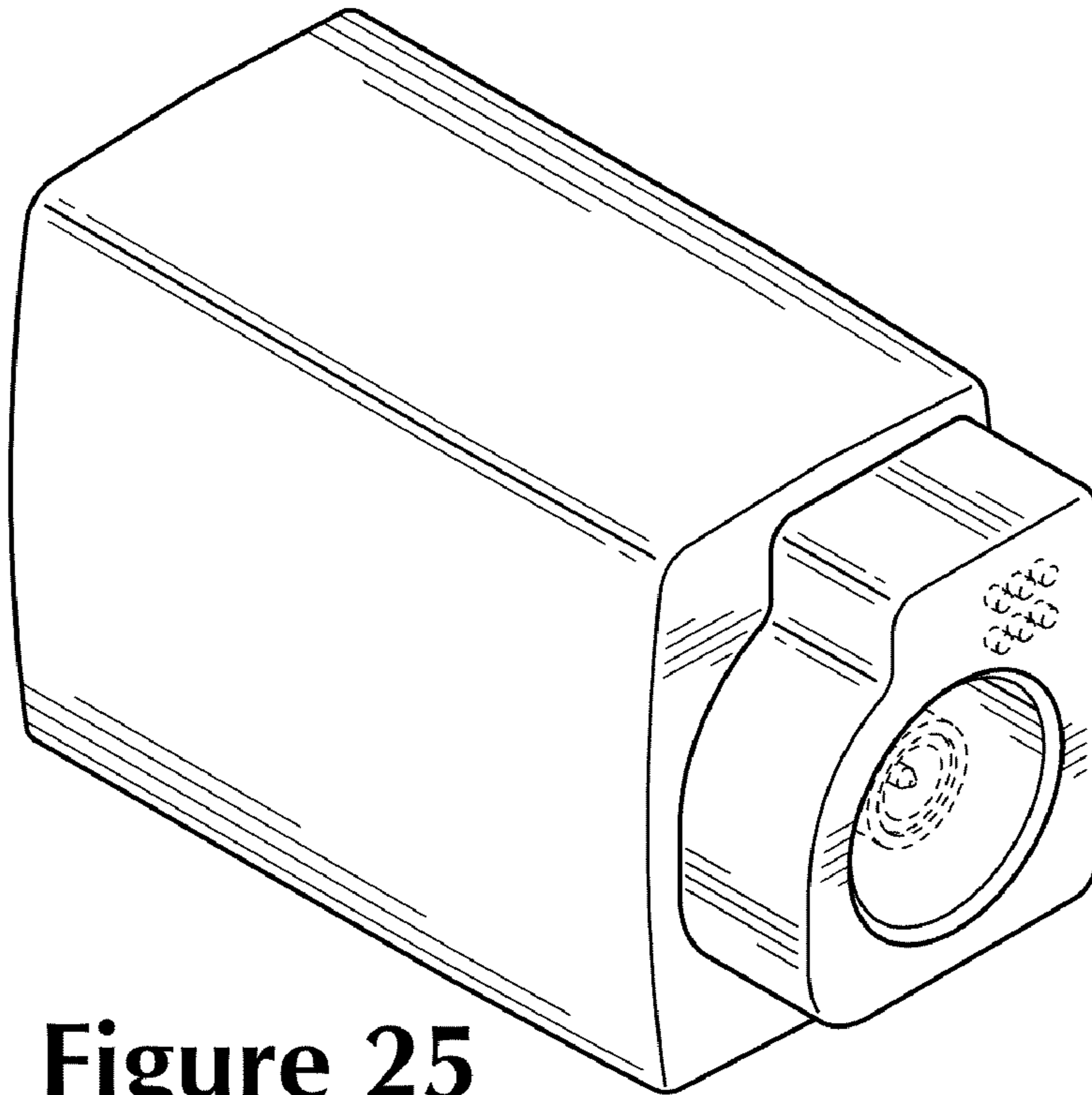


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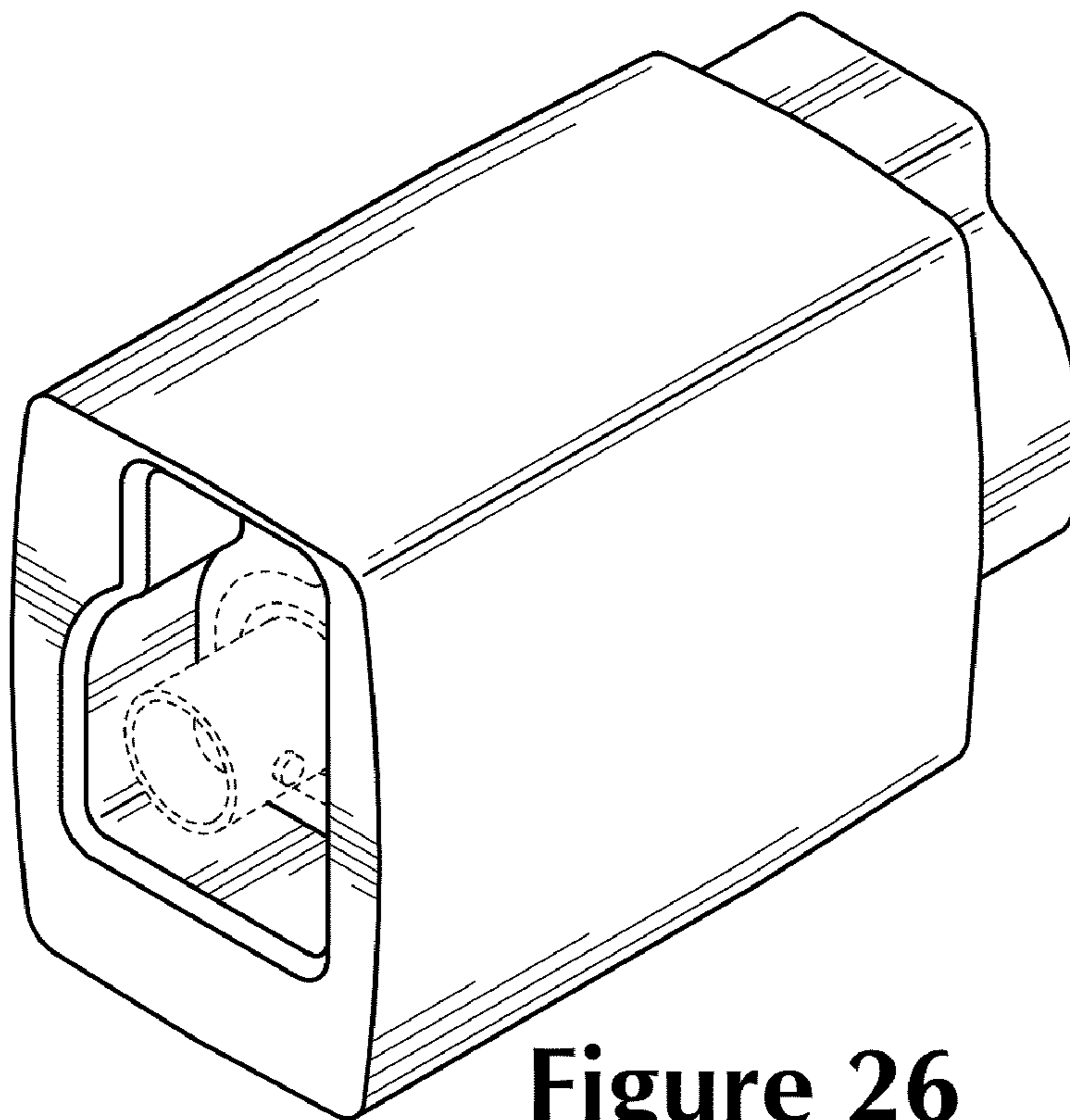


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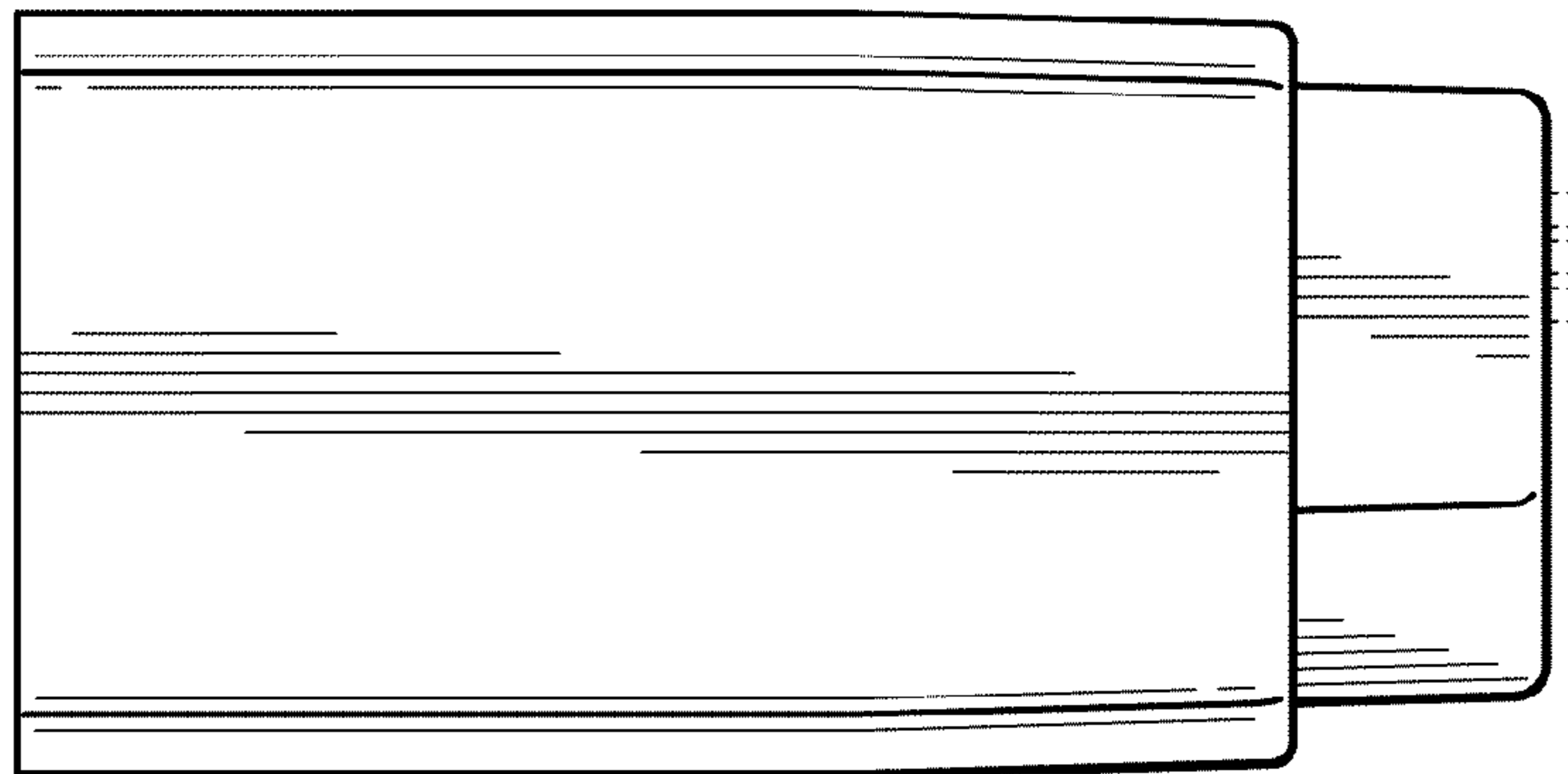


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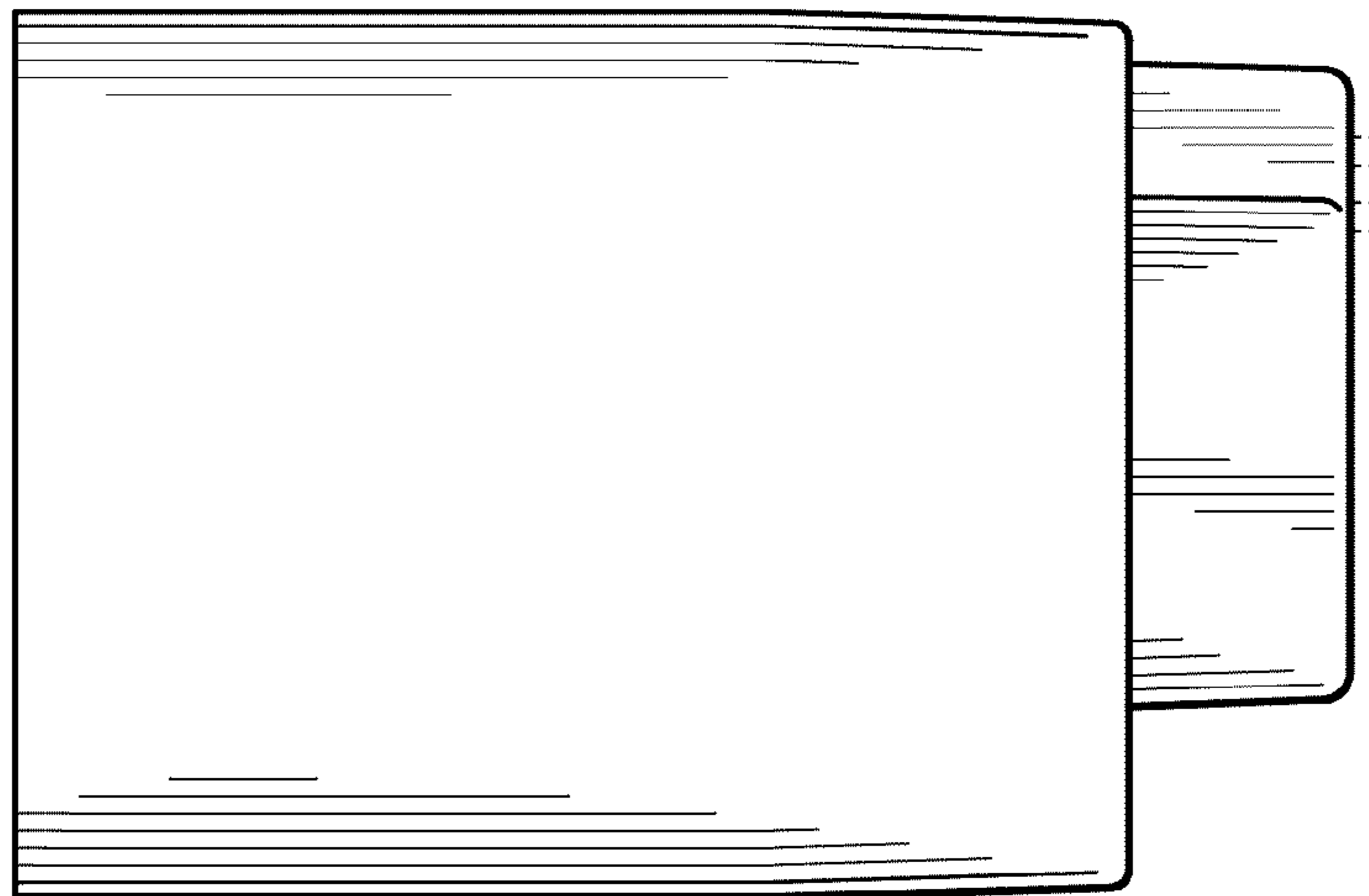


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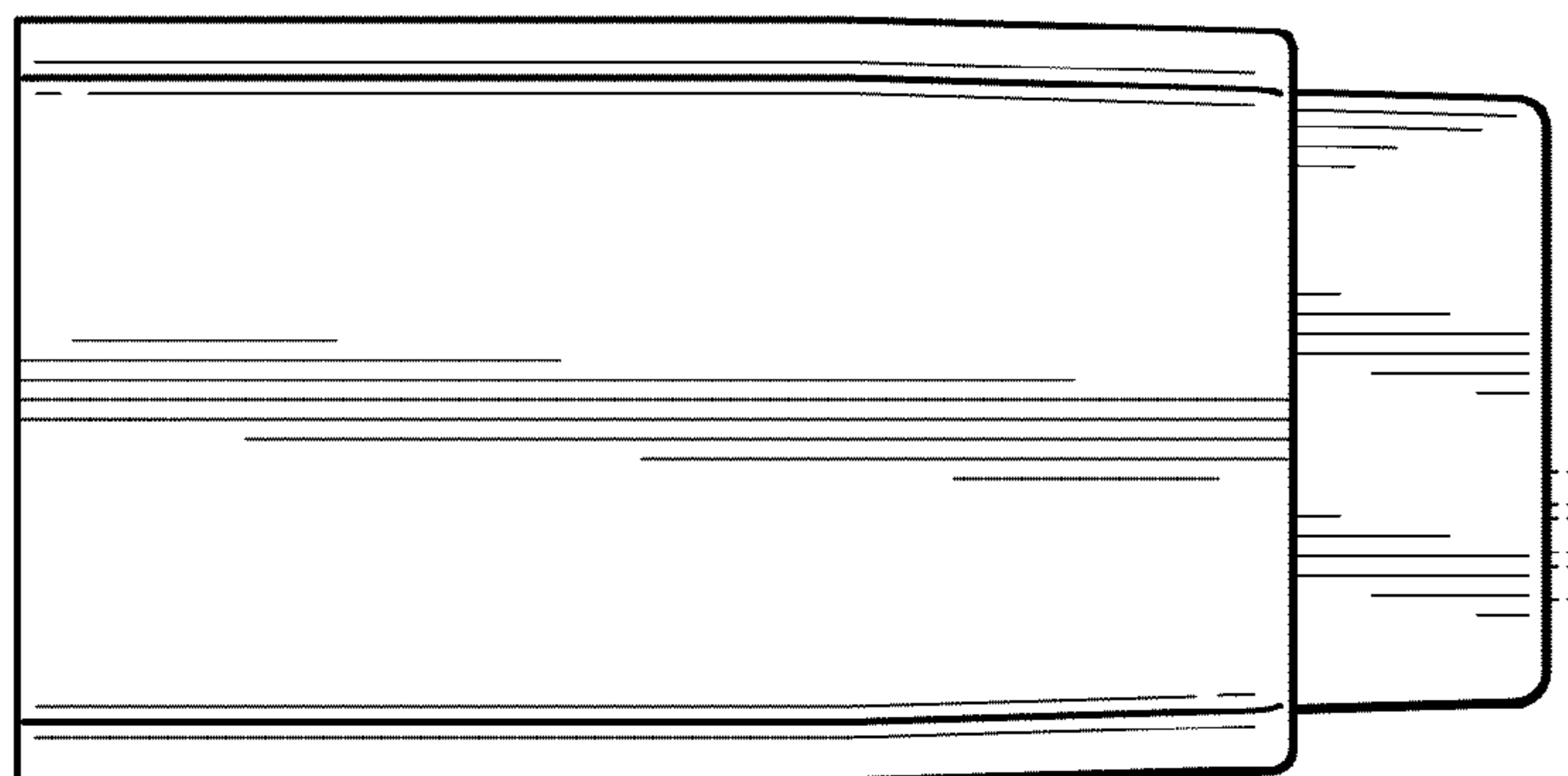


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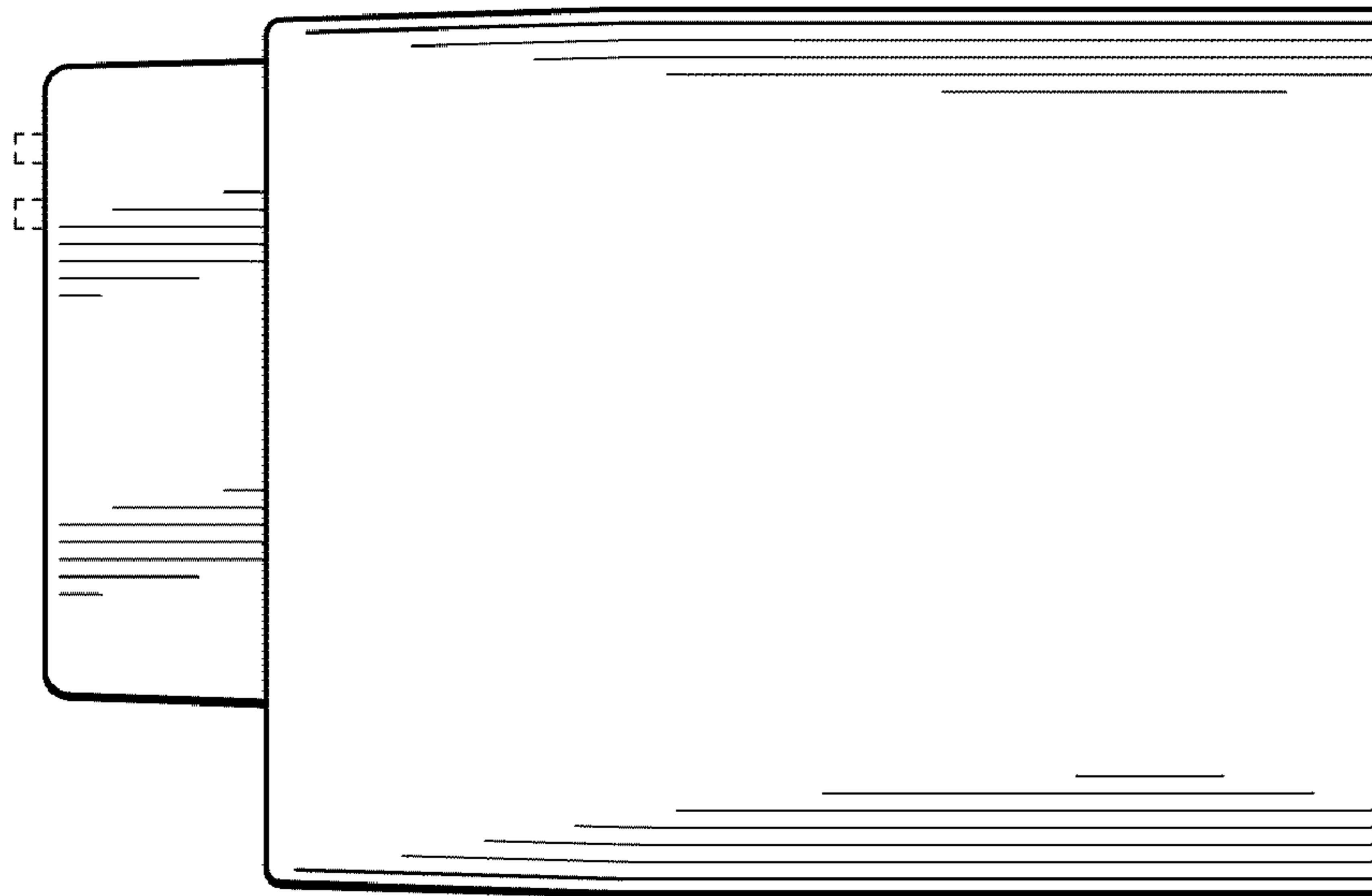


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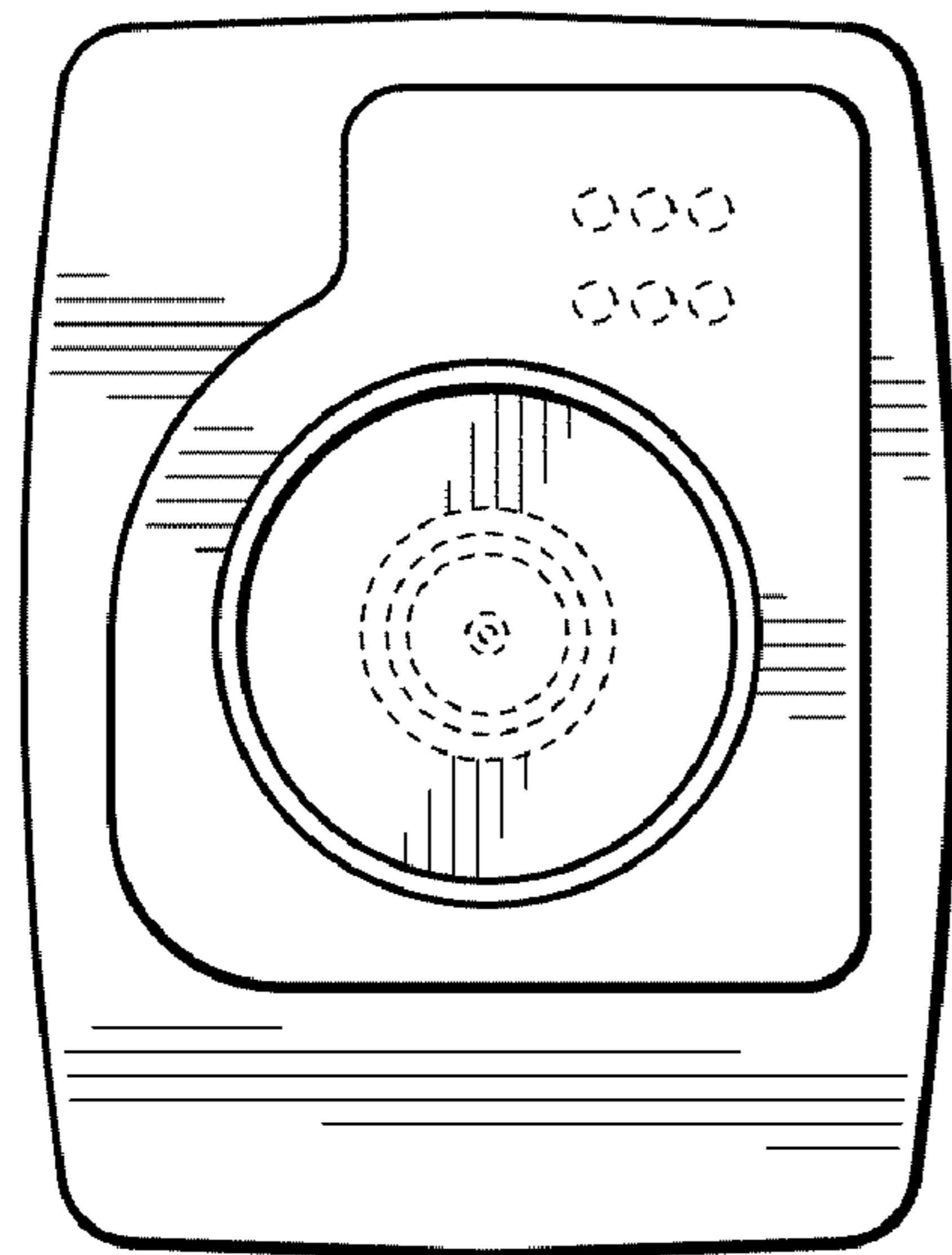


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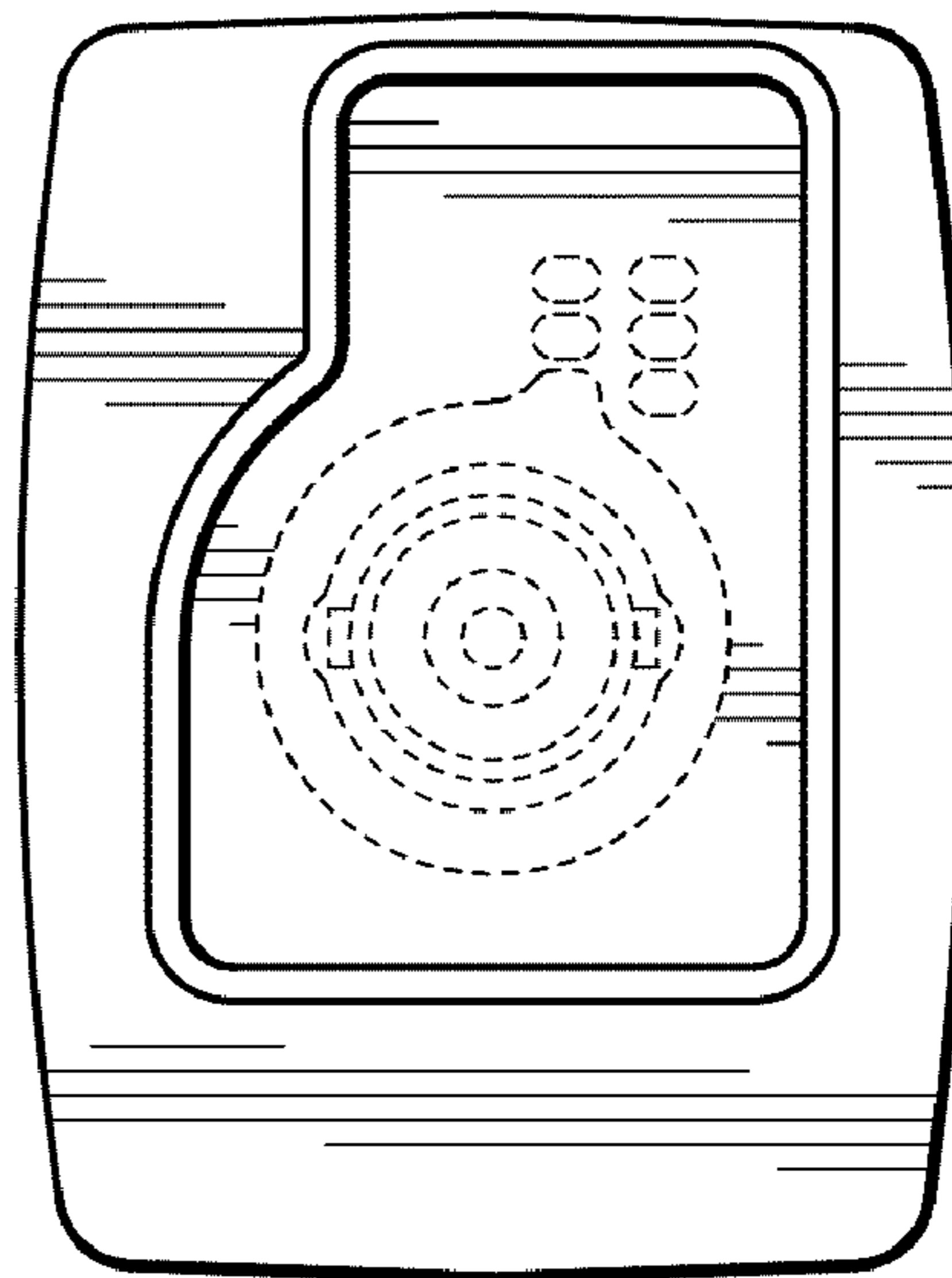


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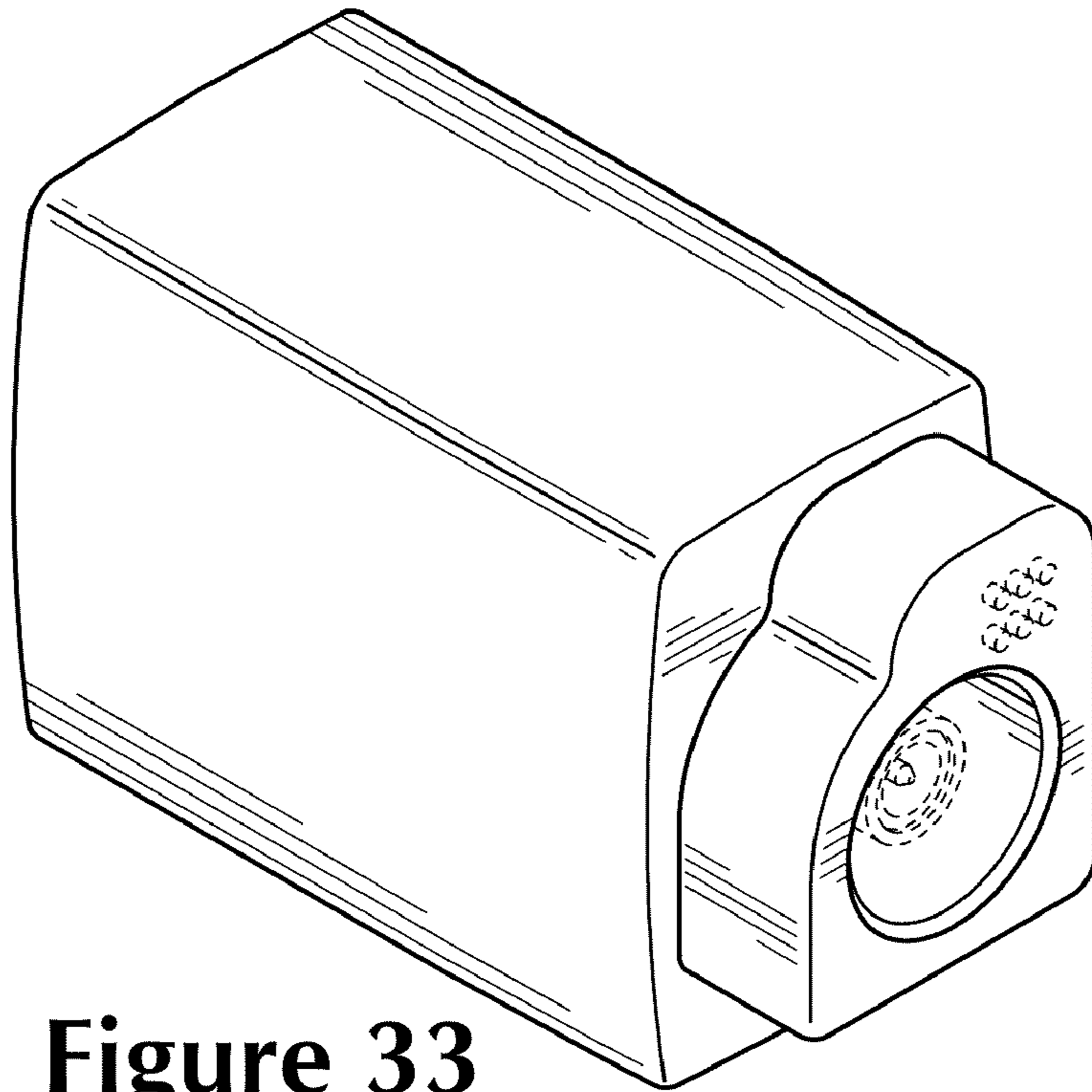


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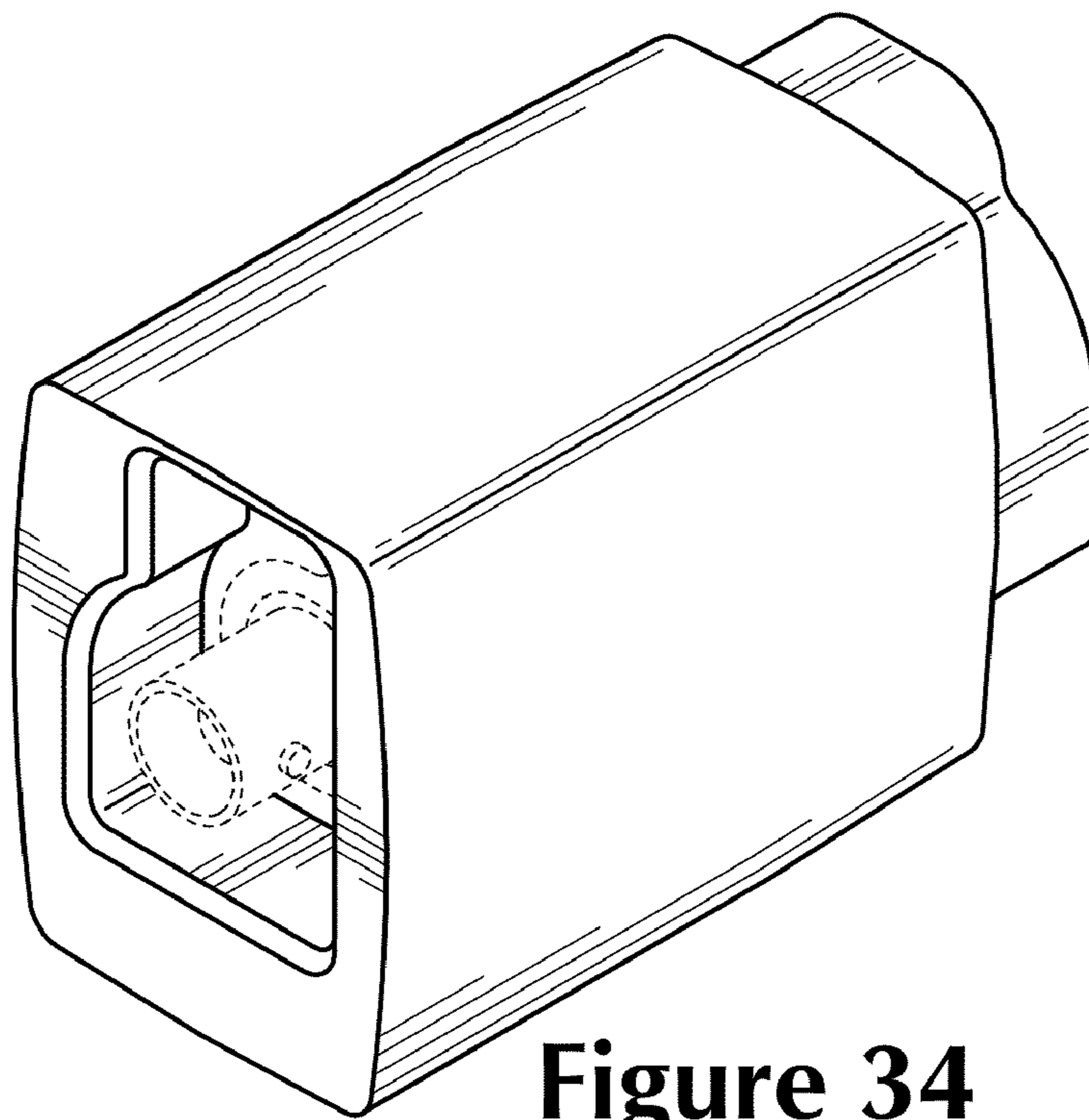


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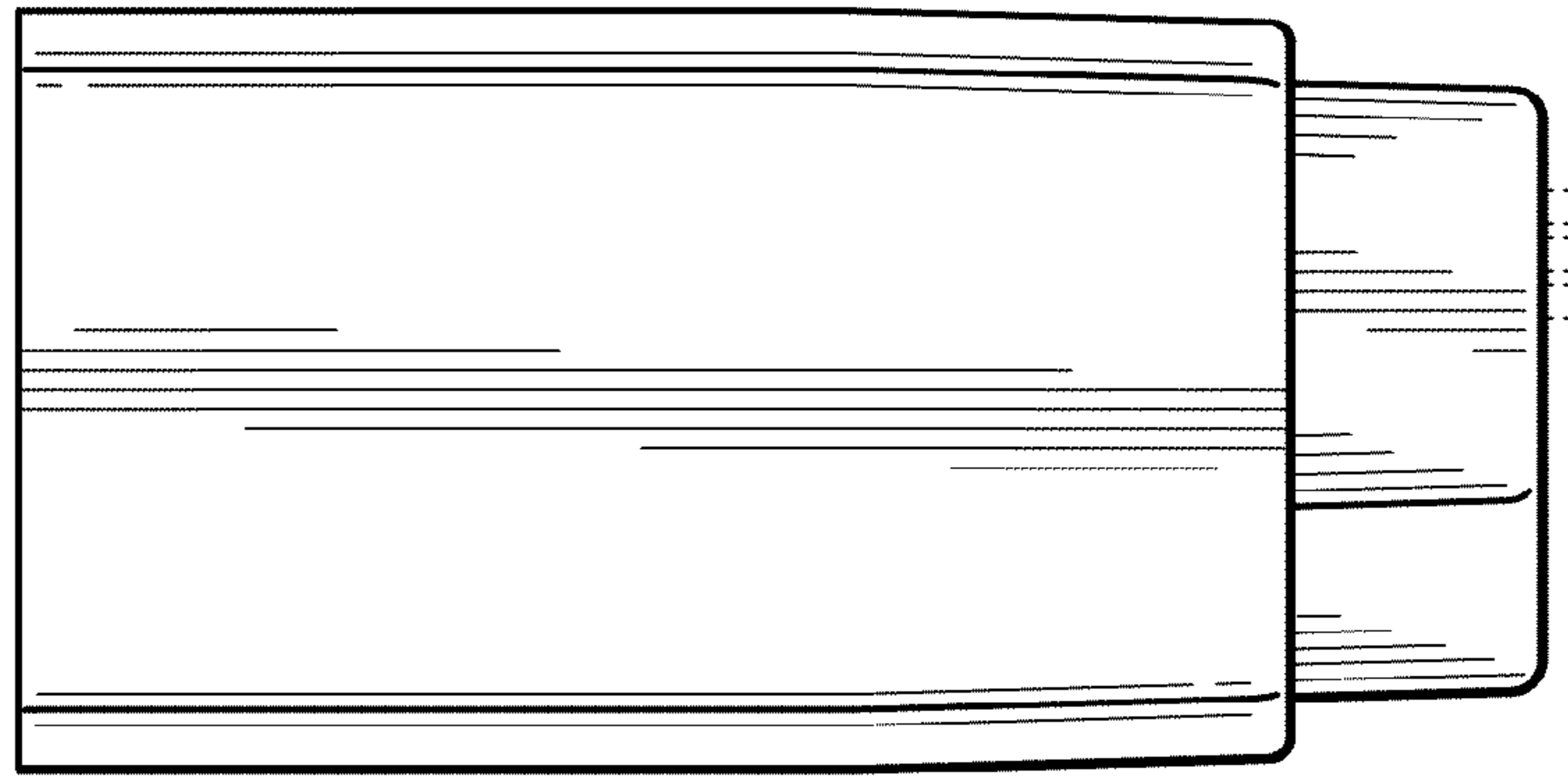


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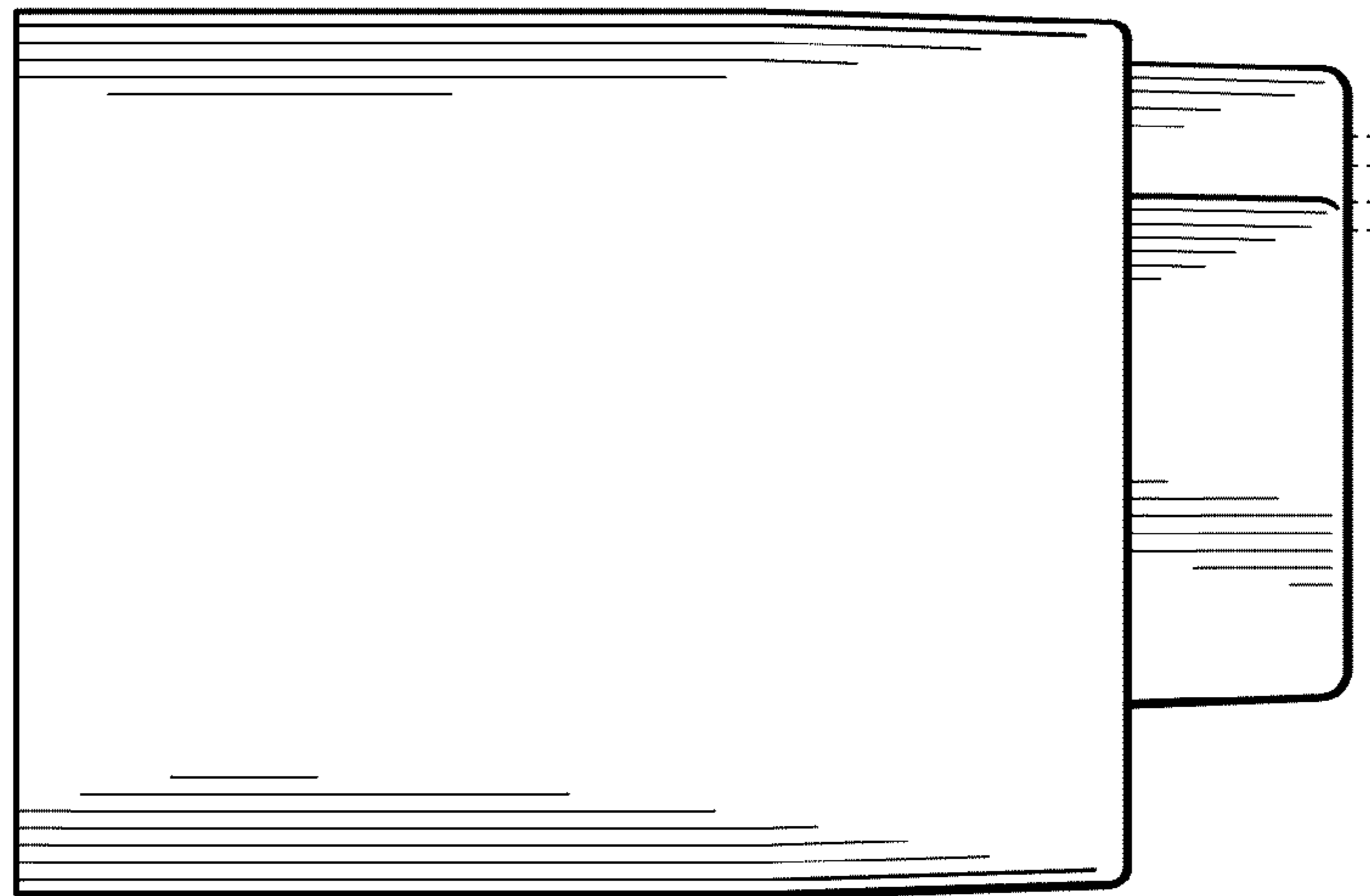


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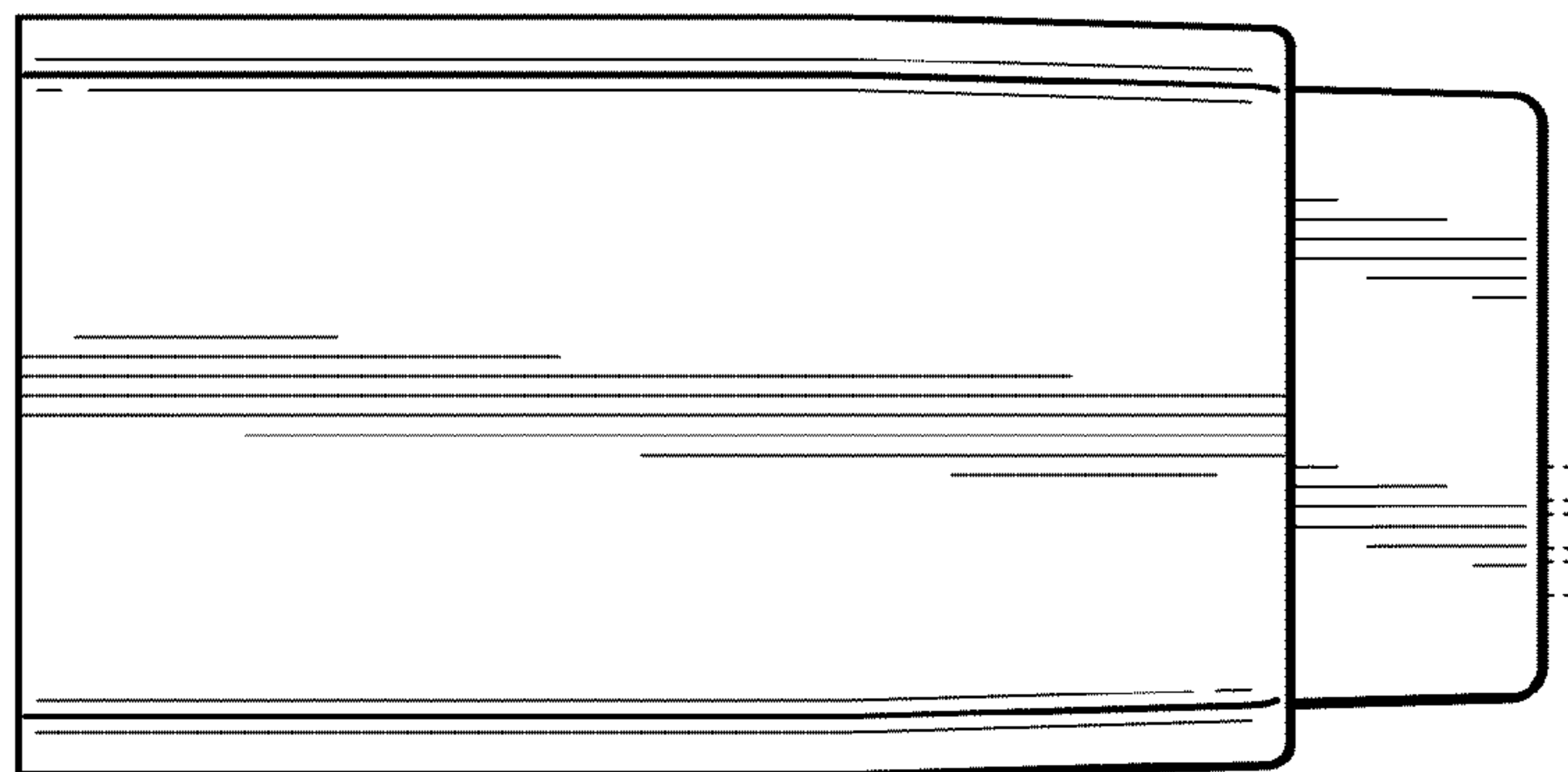


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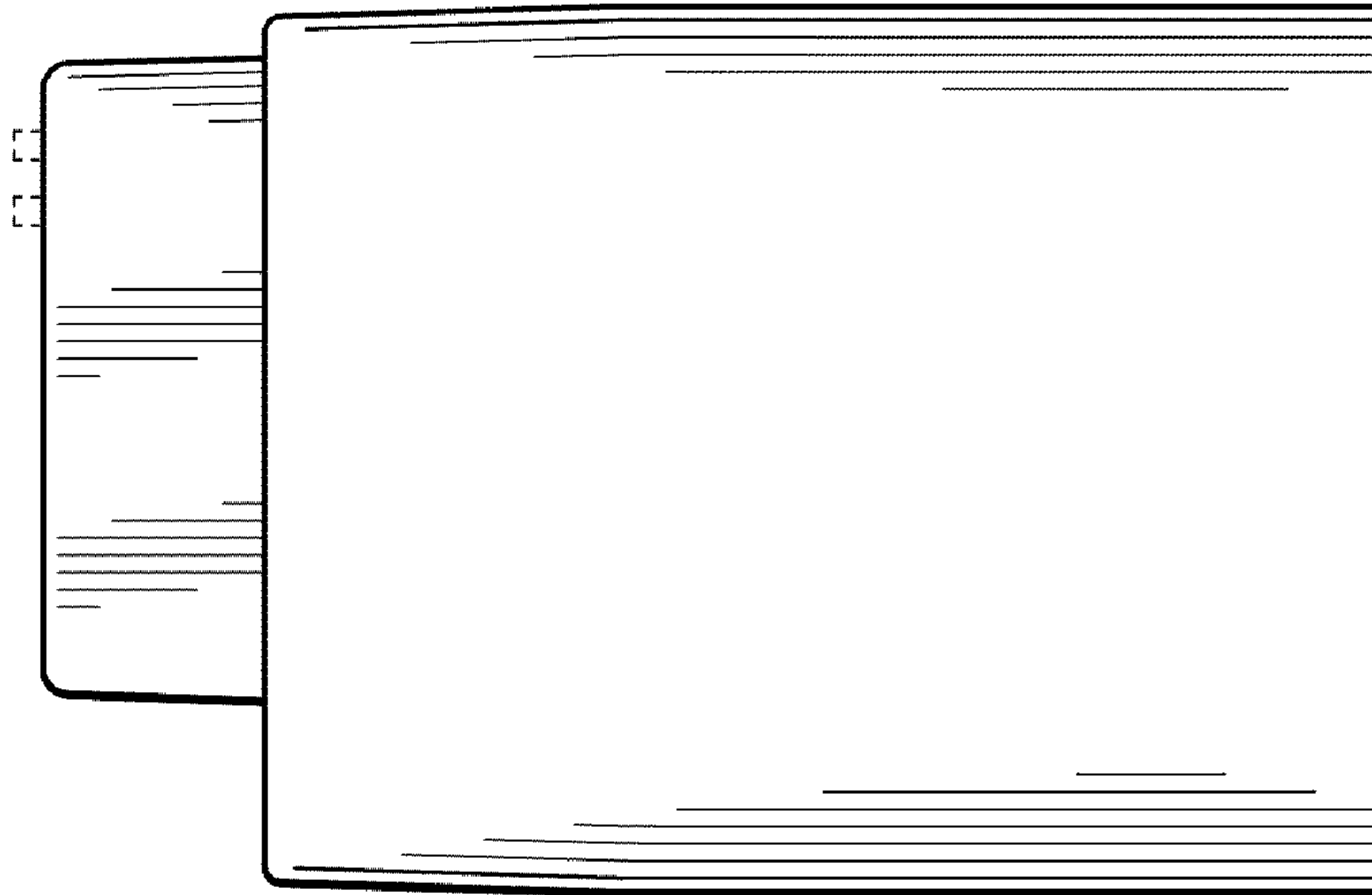


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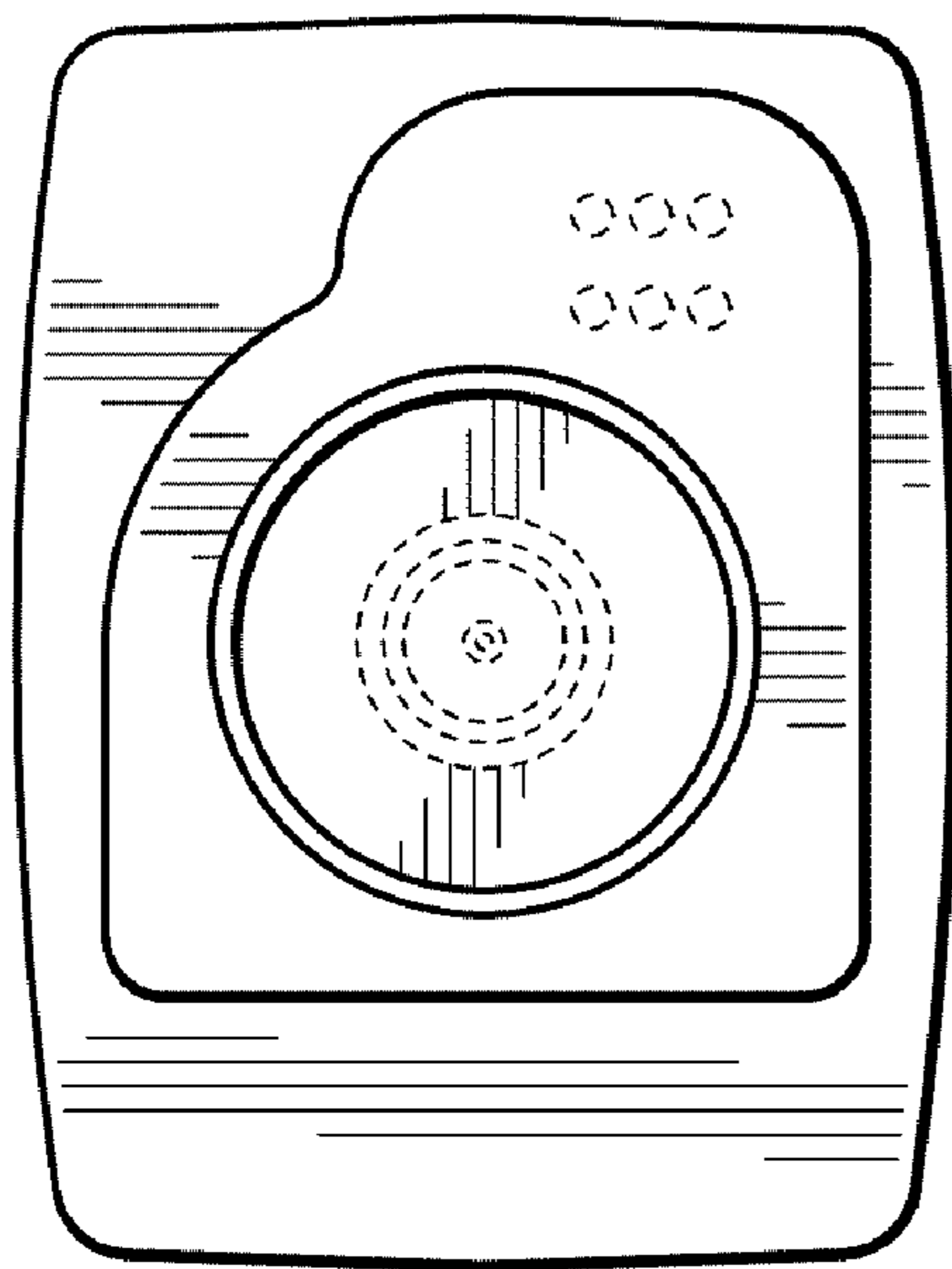


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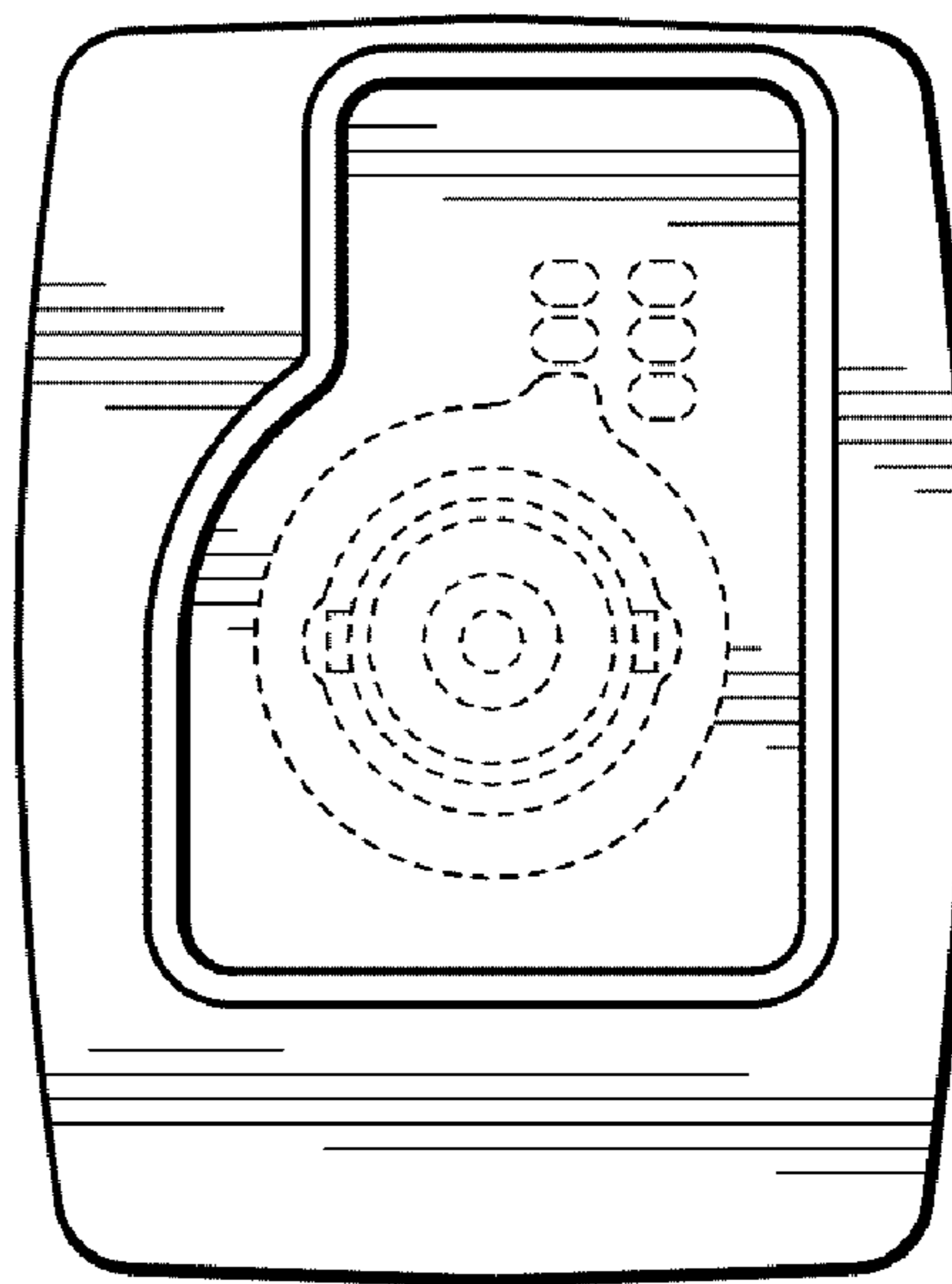


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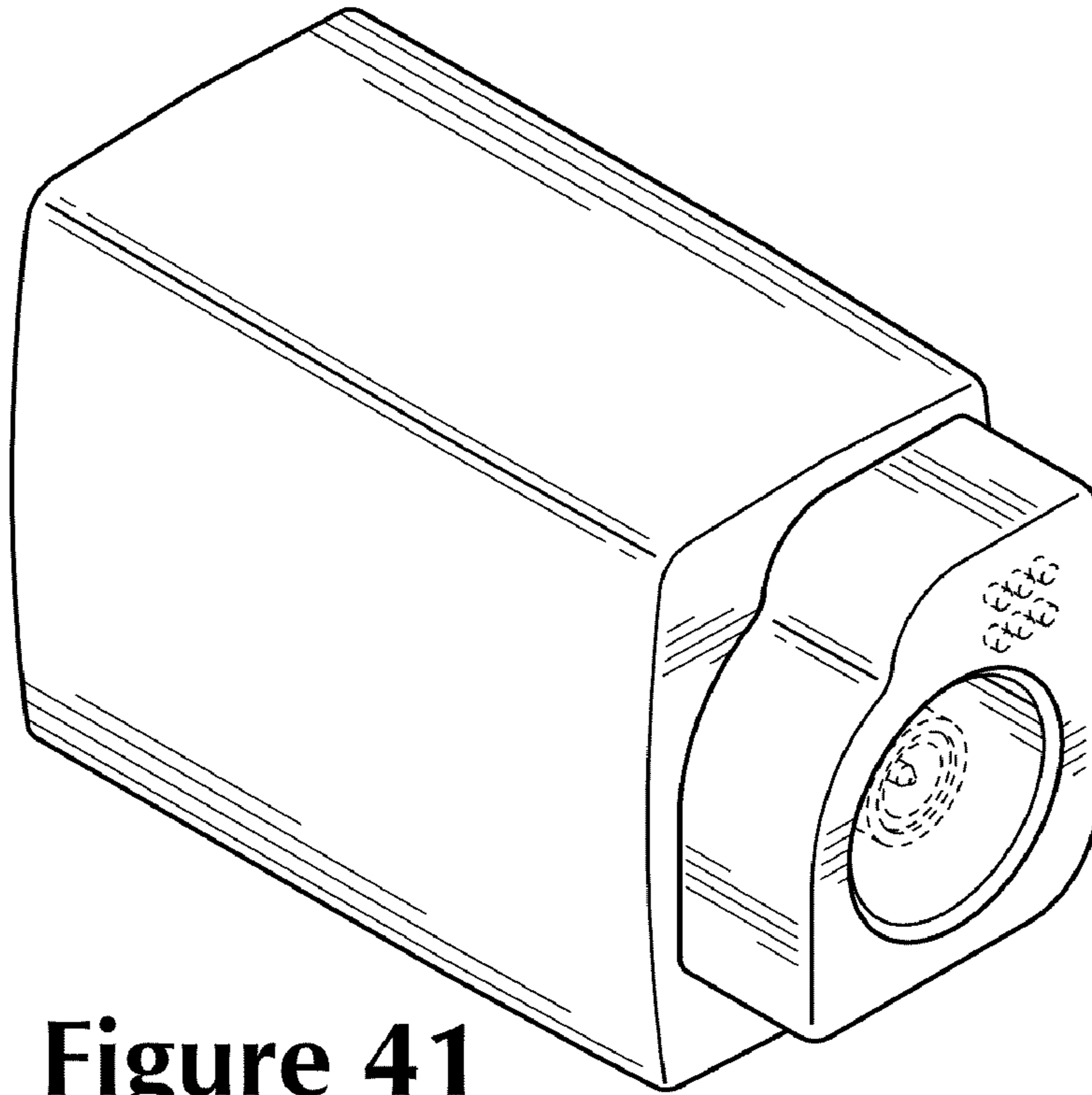


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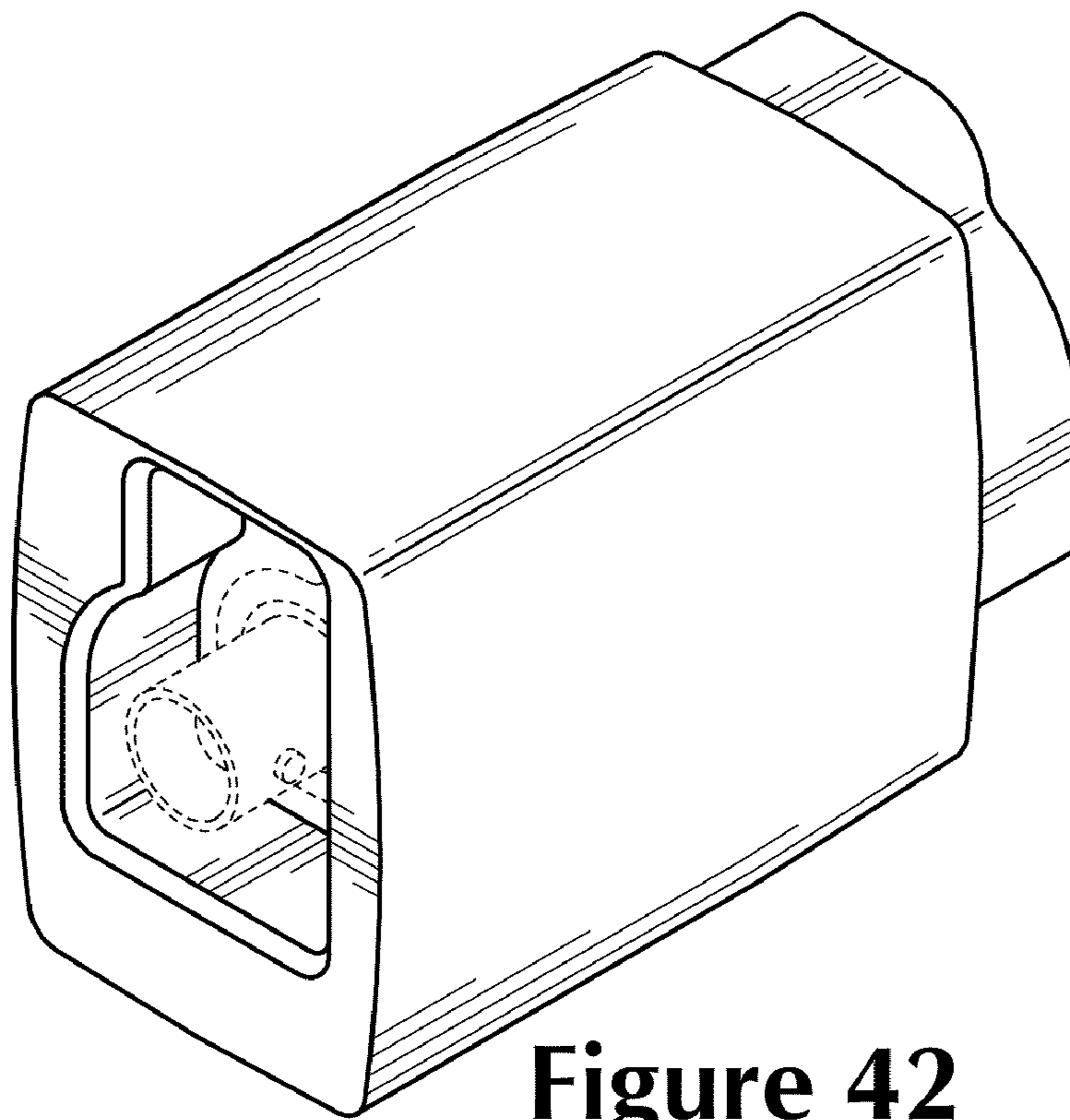


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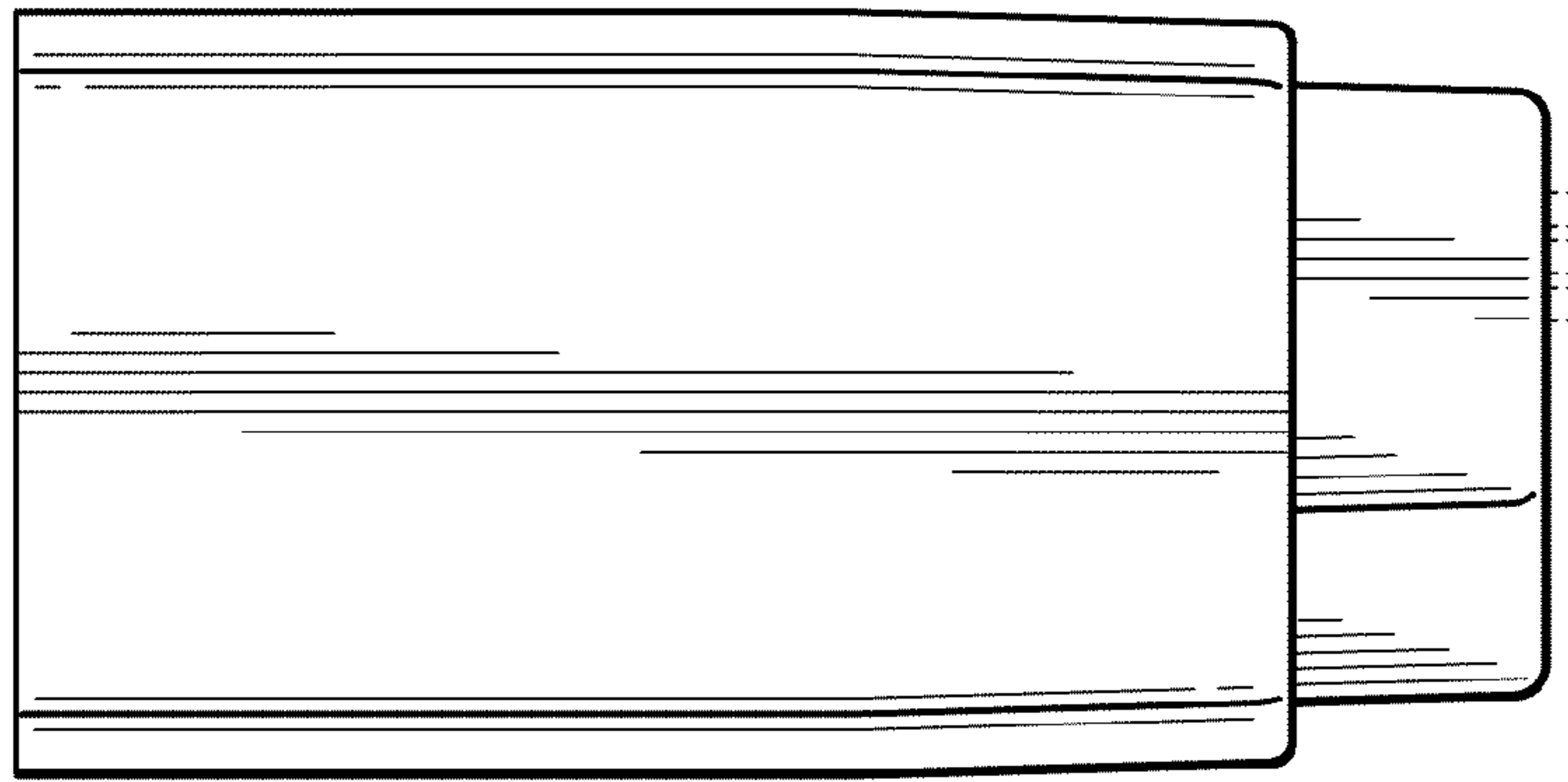


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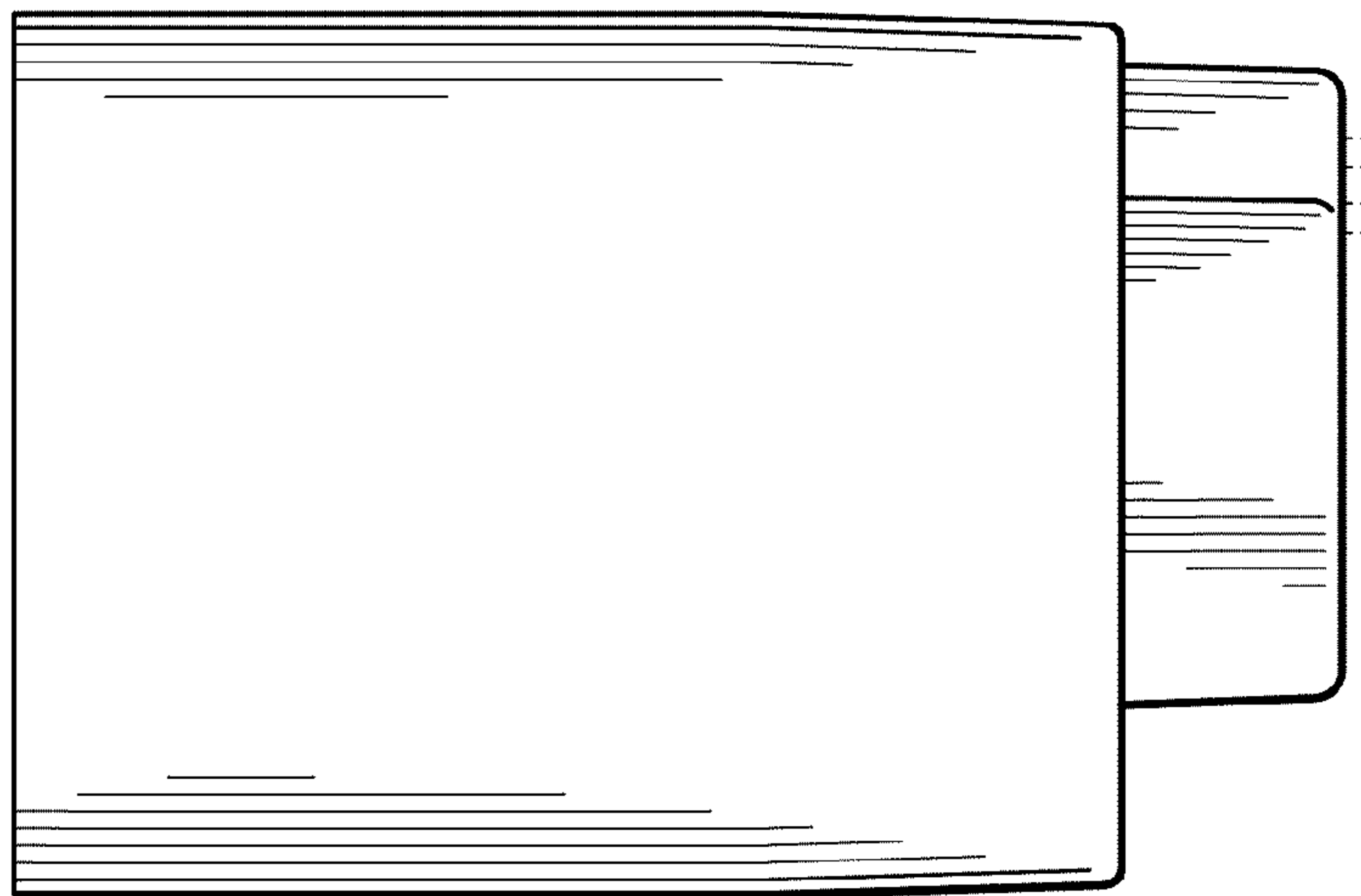


Figure 44



Figure 45

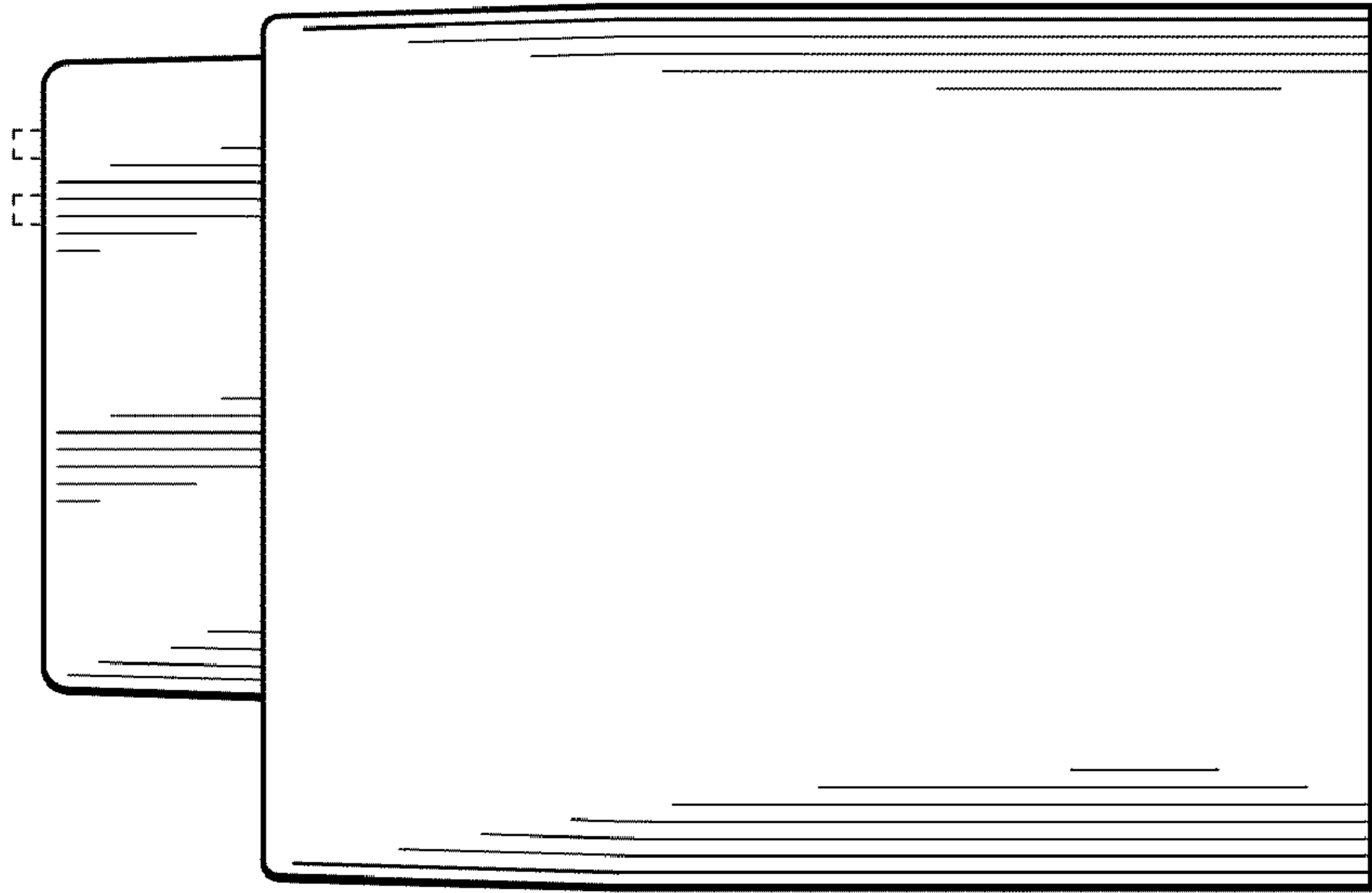


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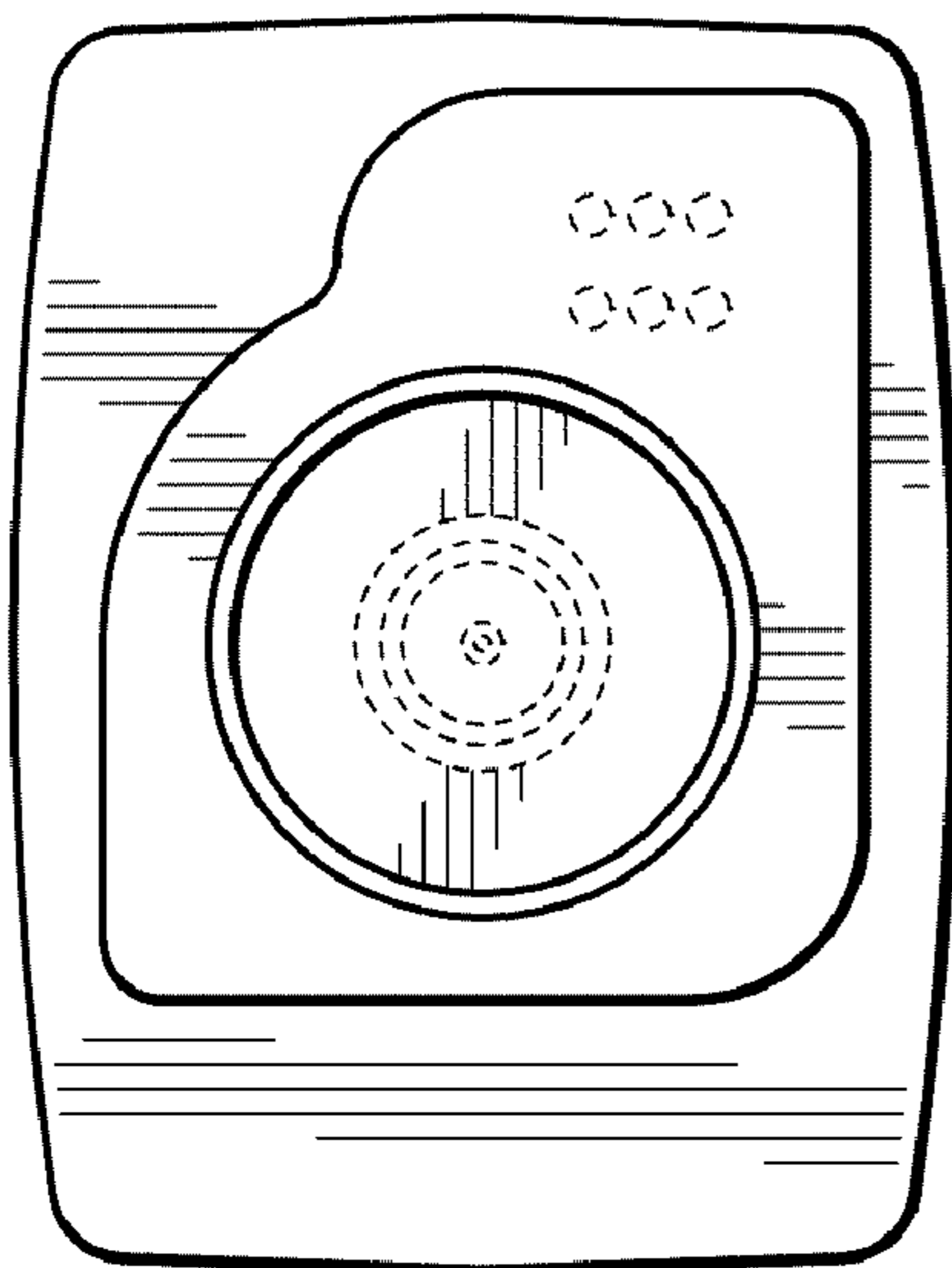


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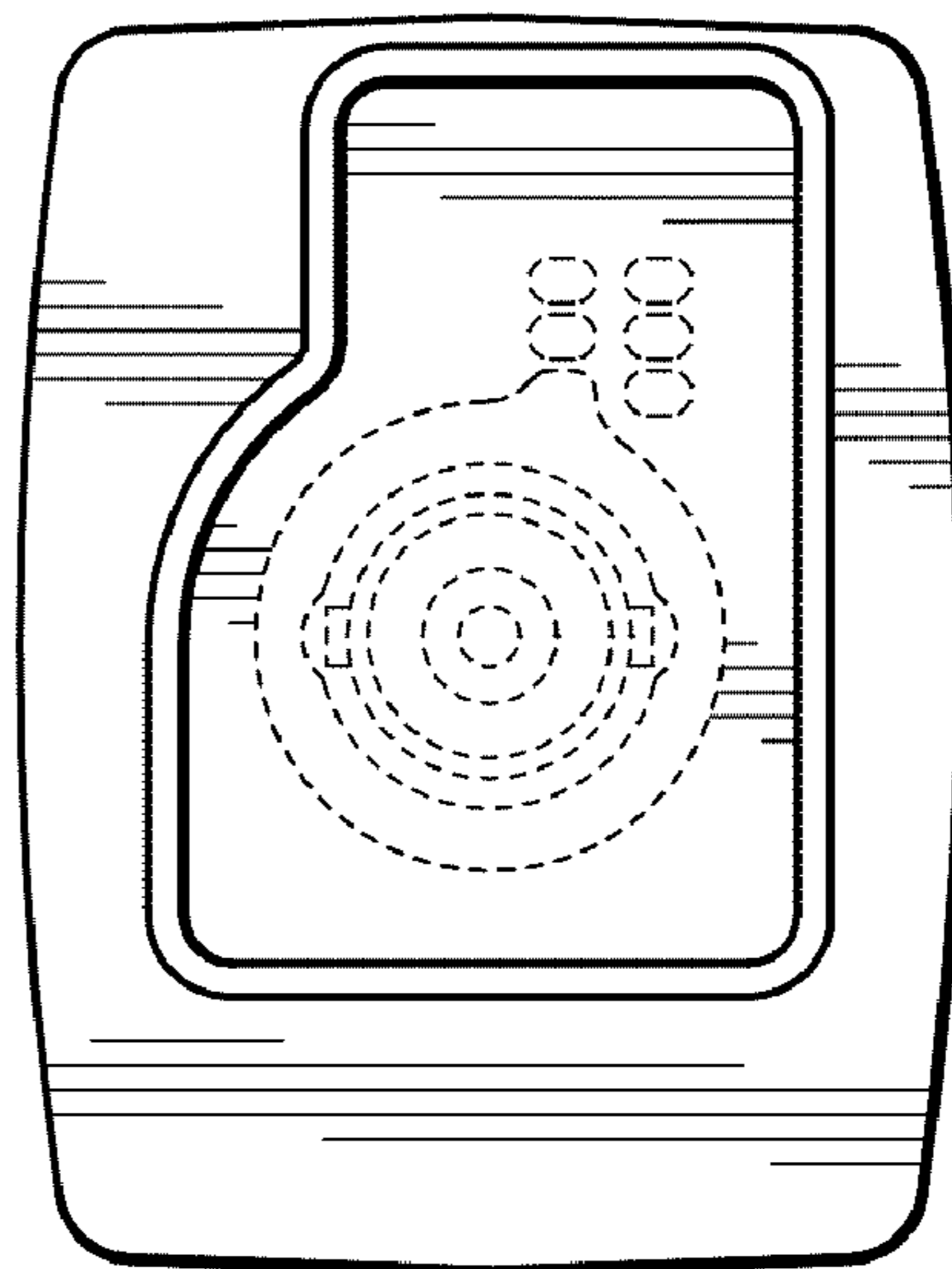


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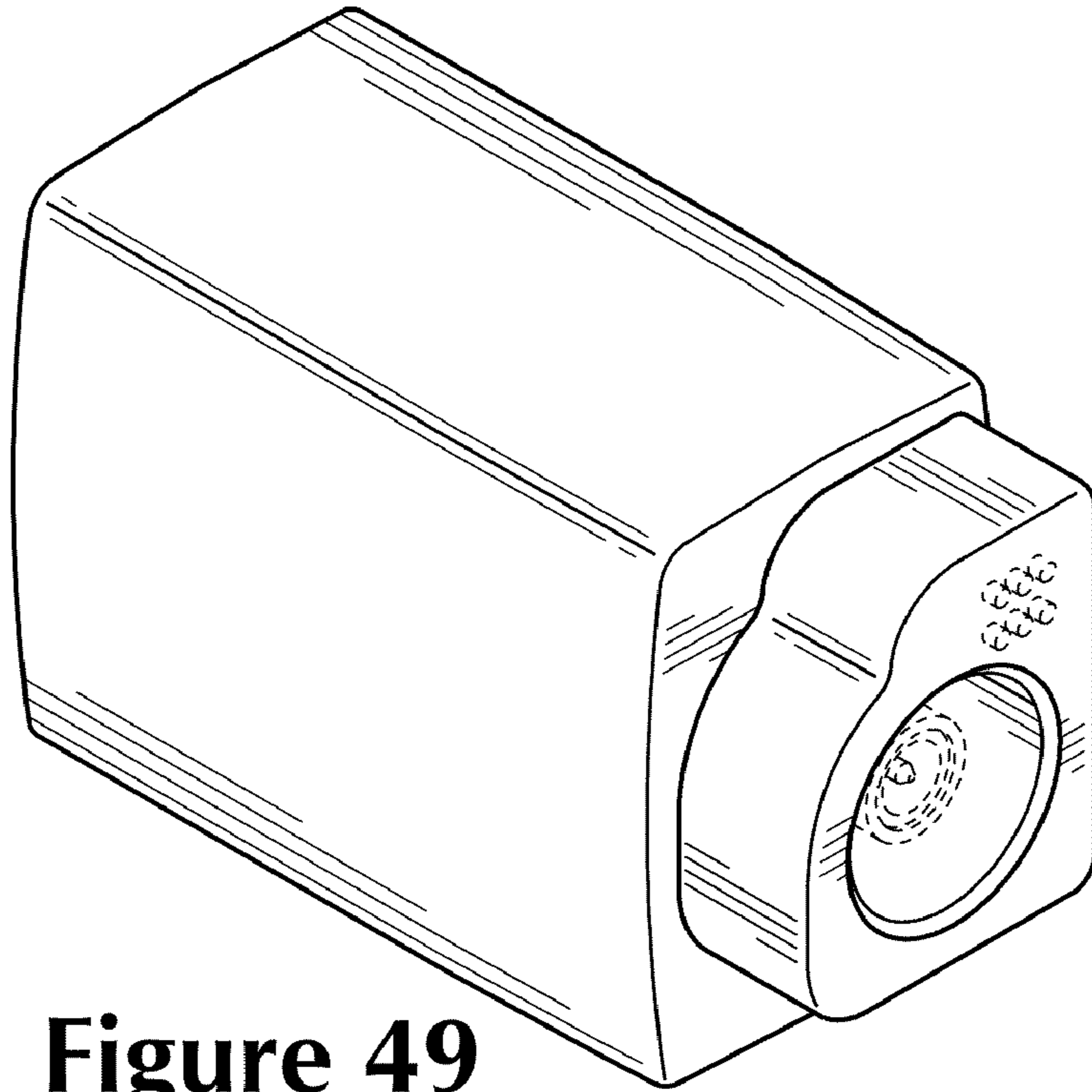


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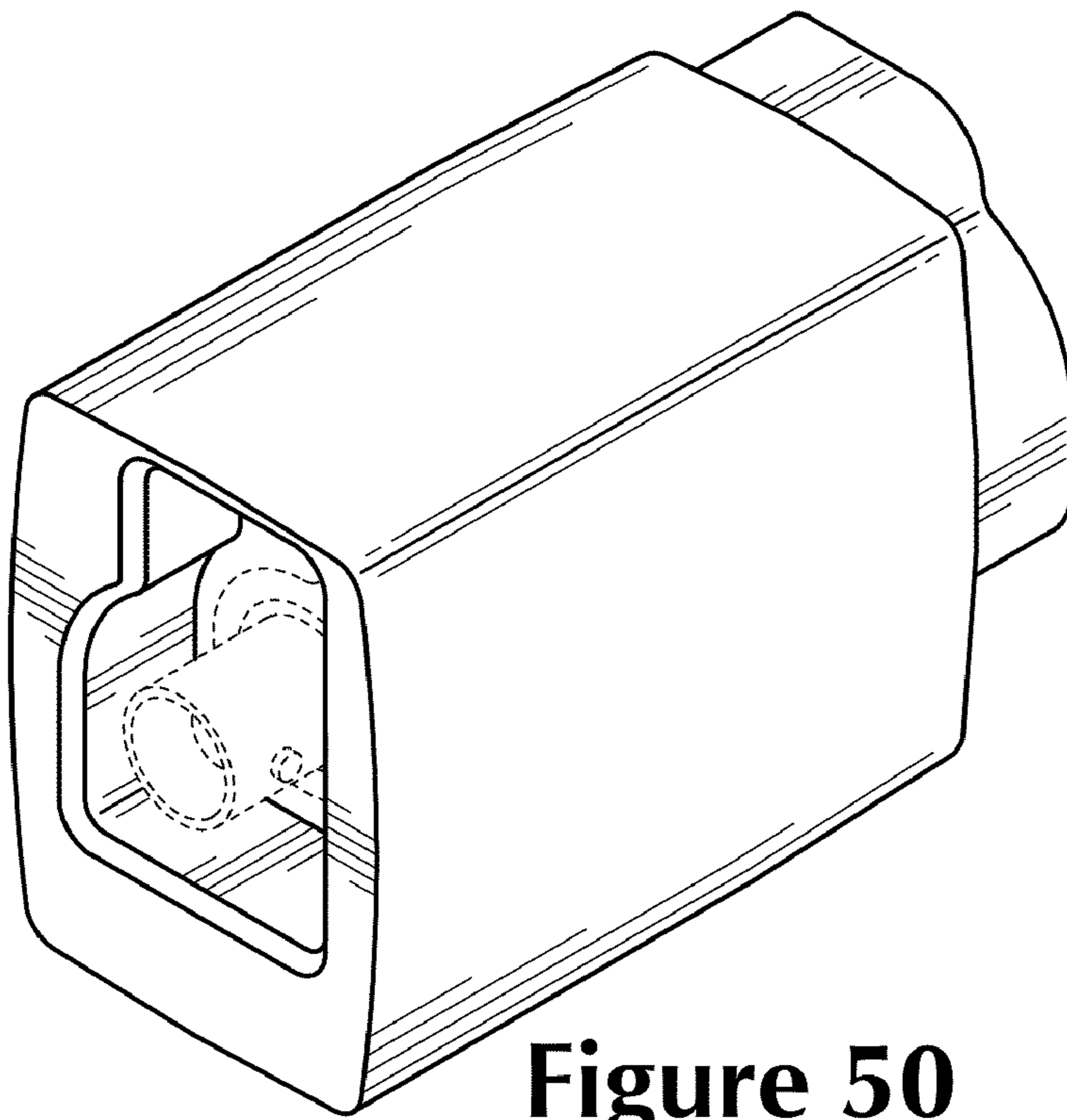


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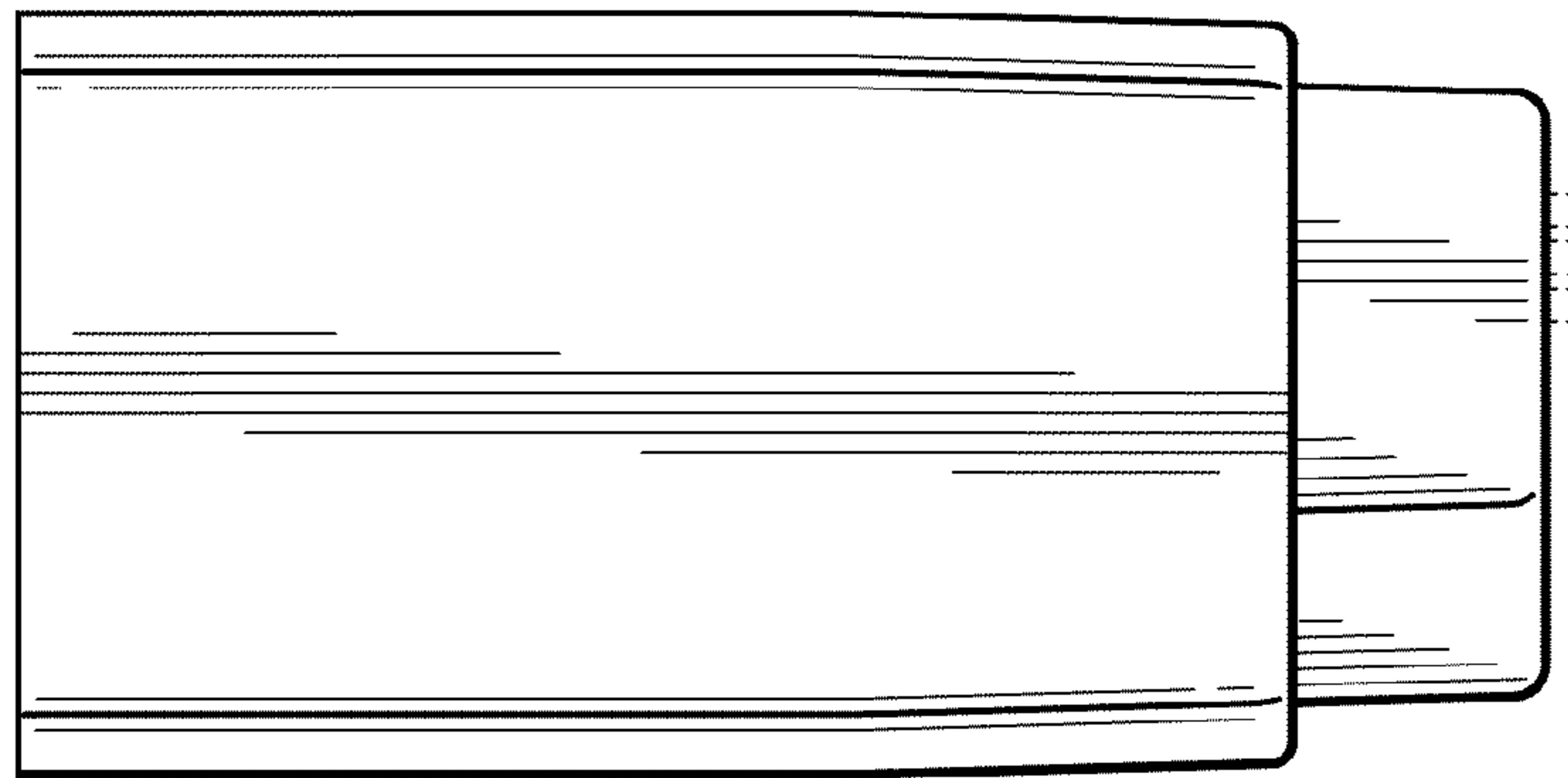


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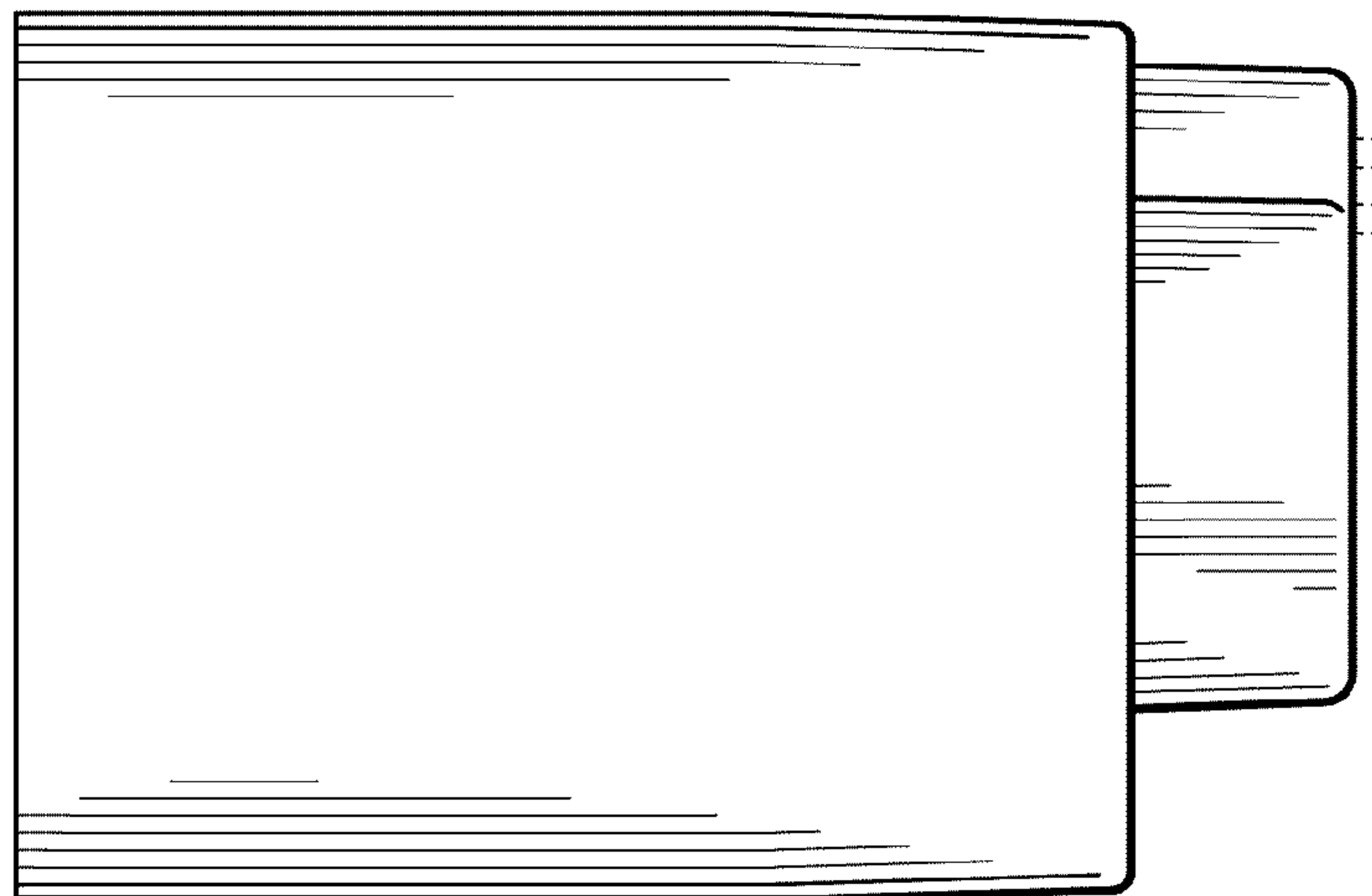


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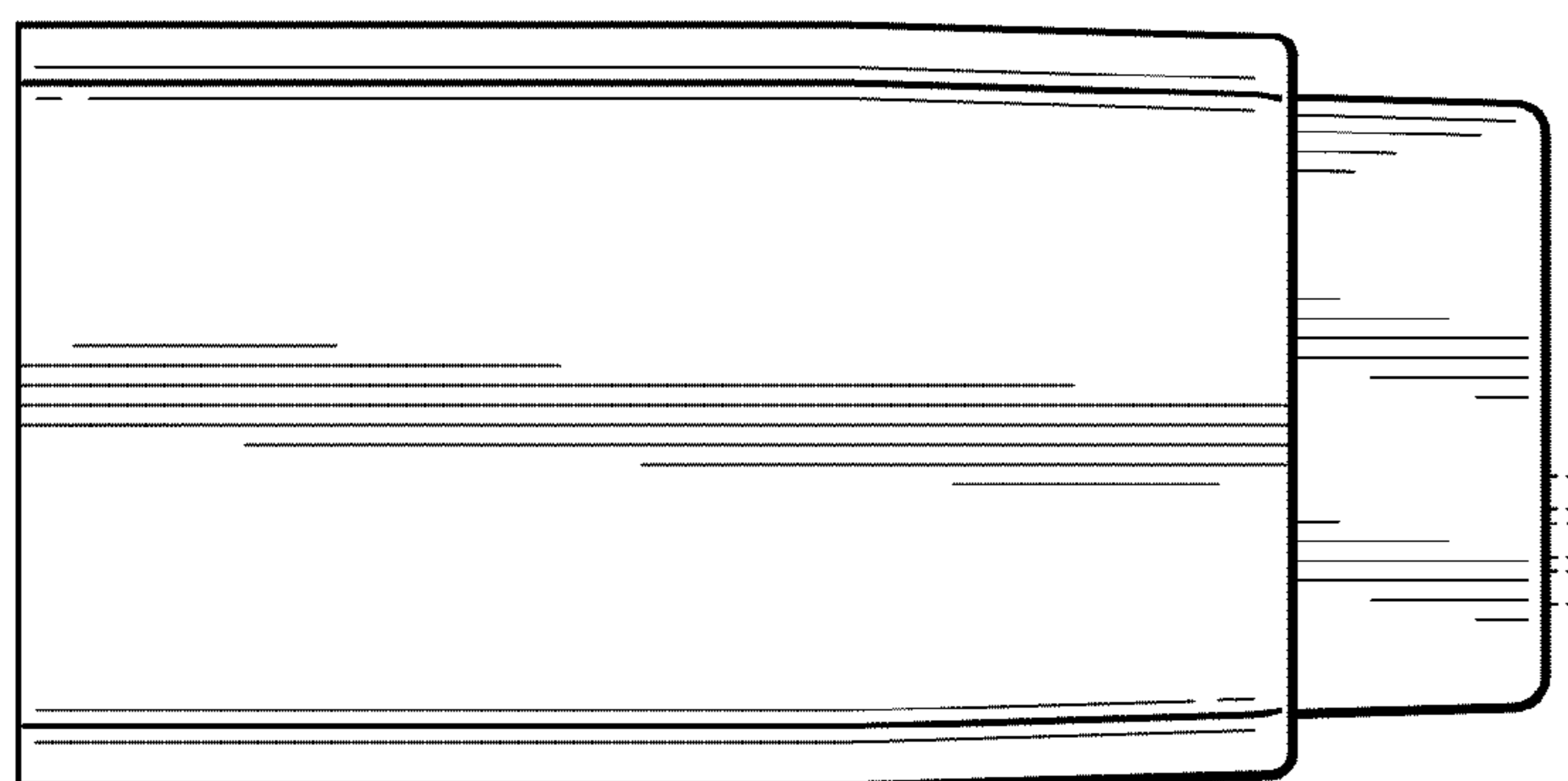


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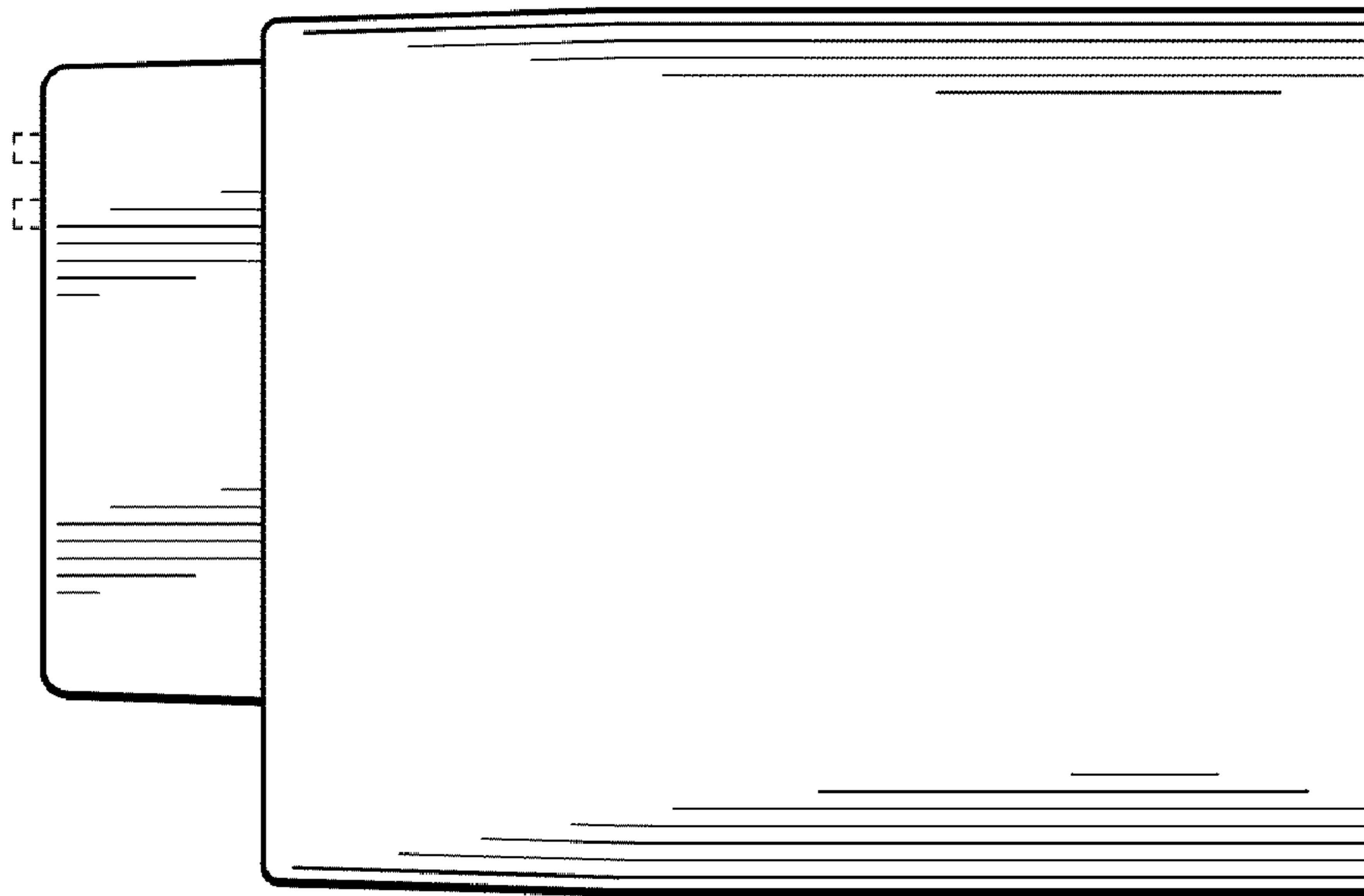


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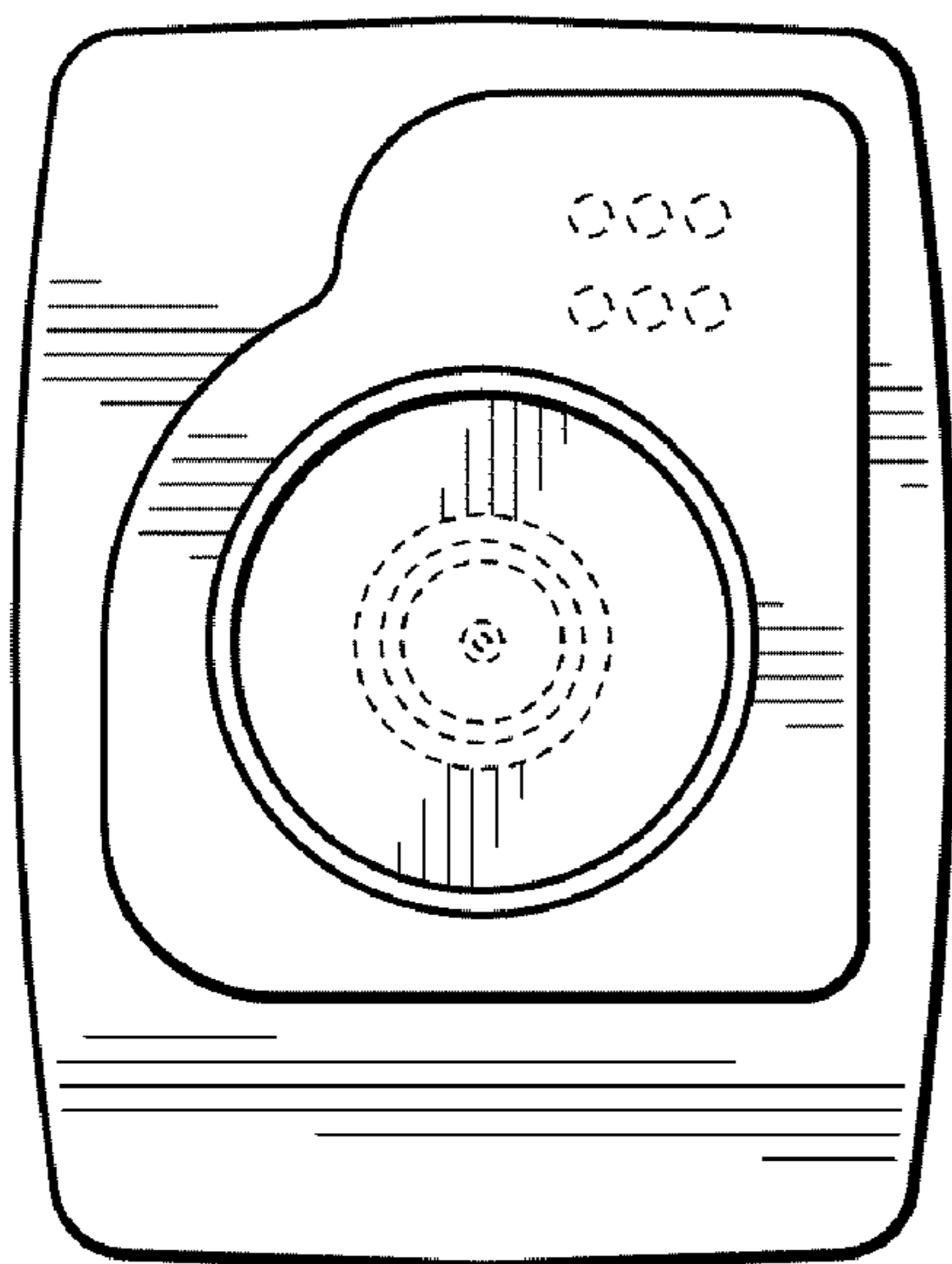


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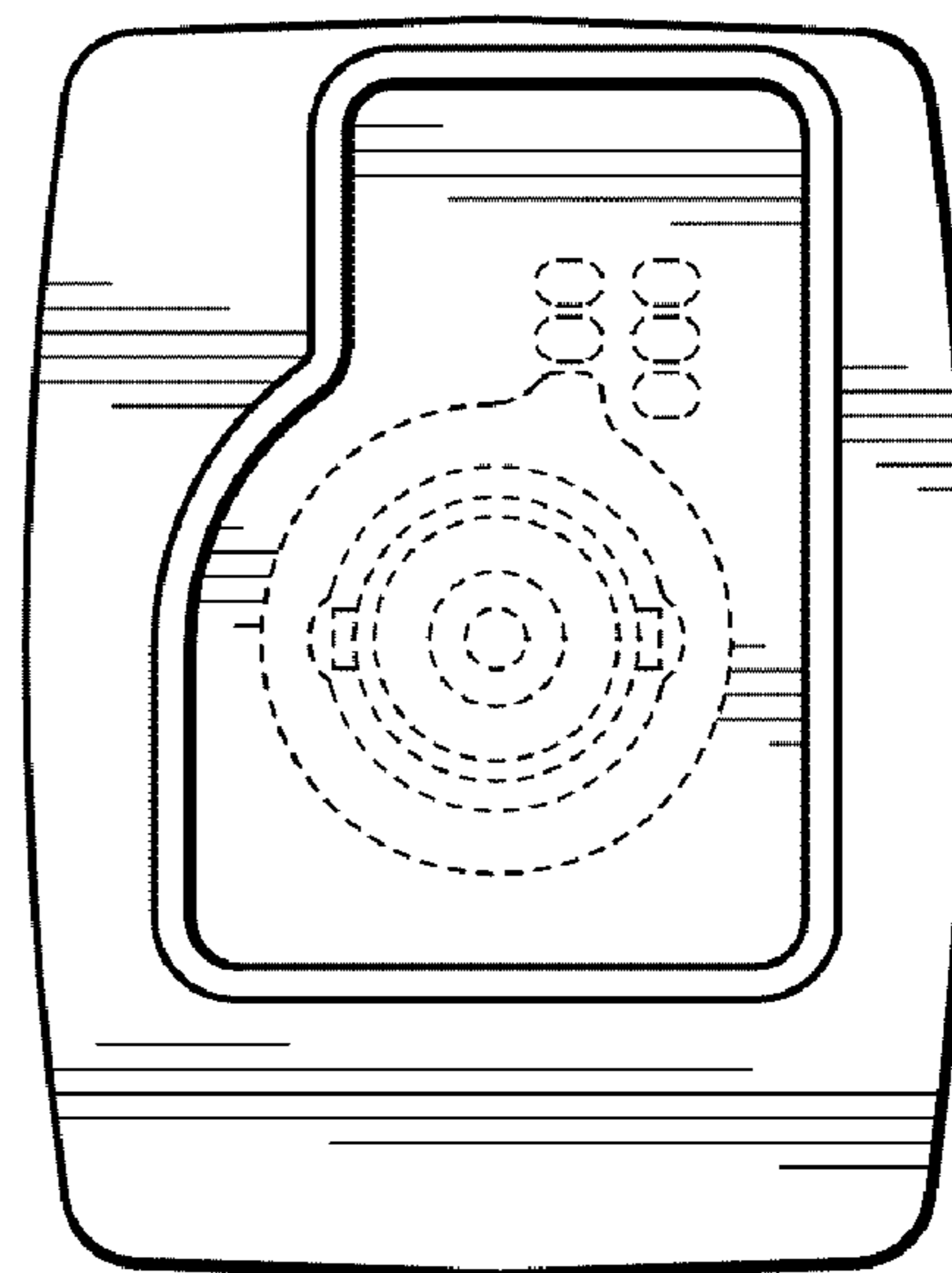


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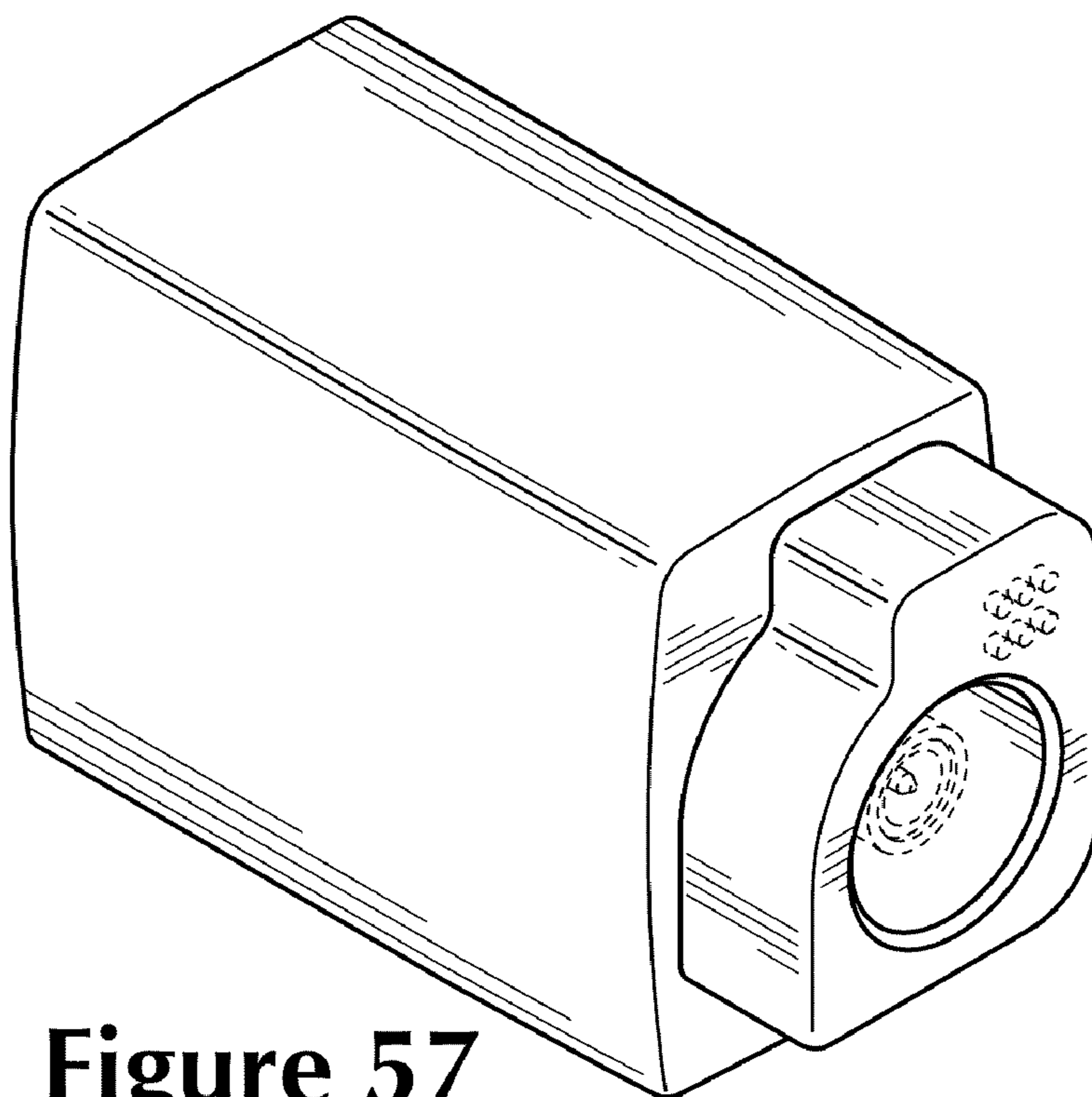


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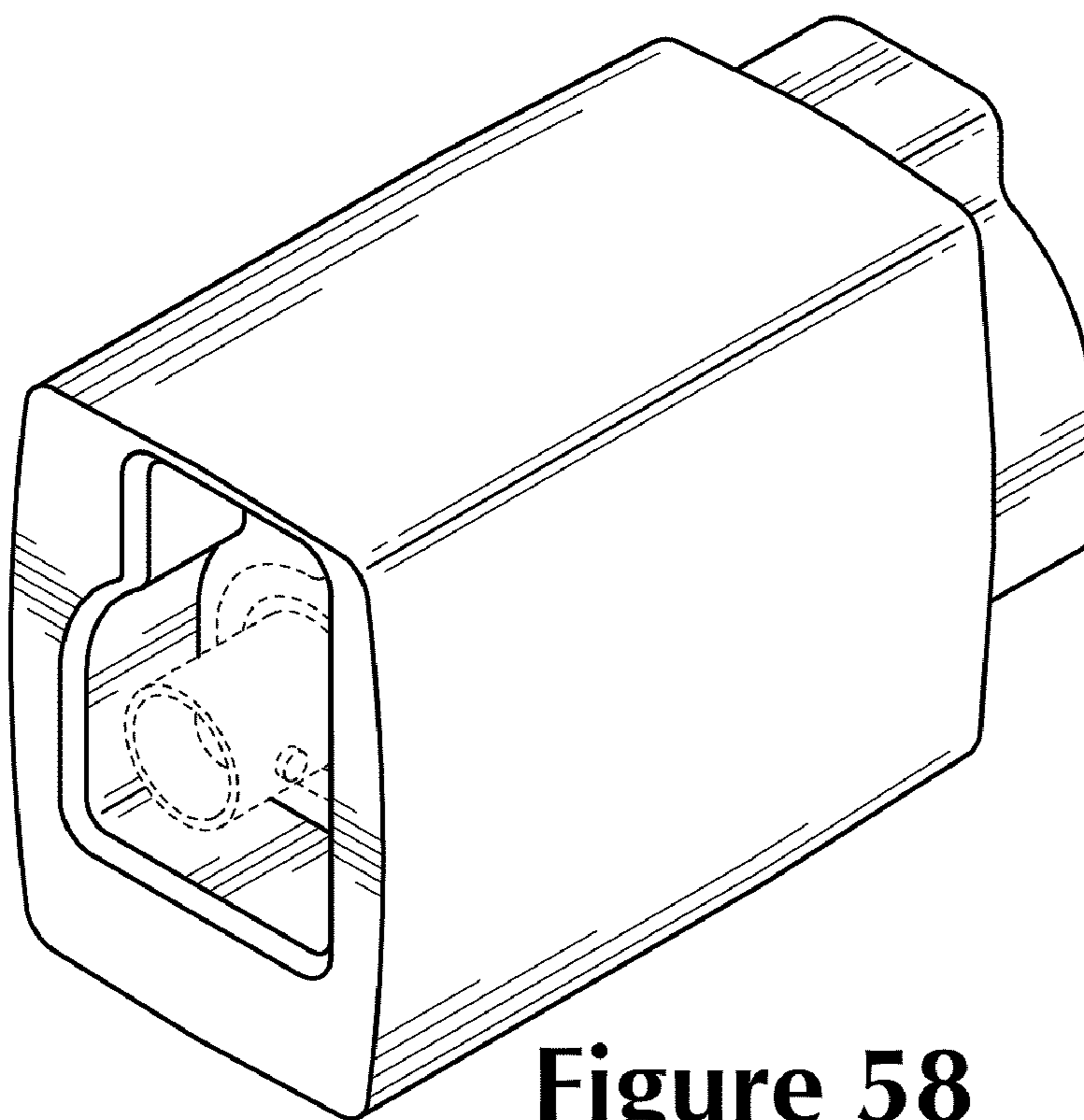


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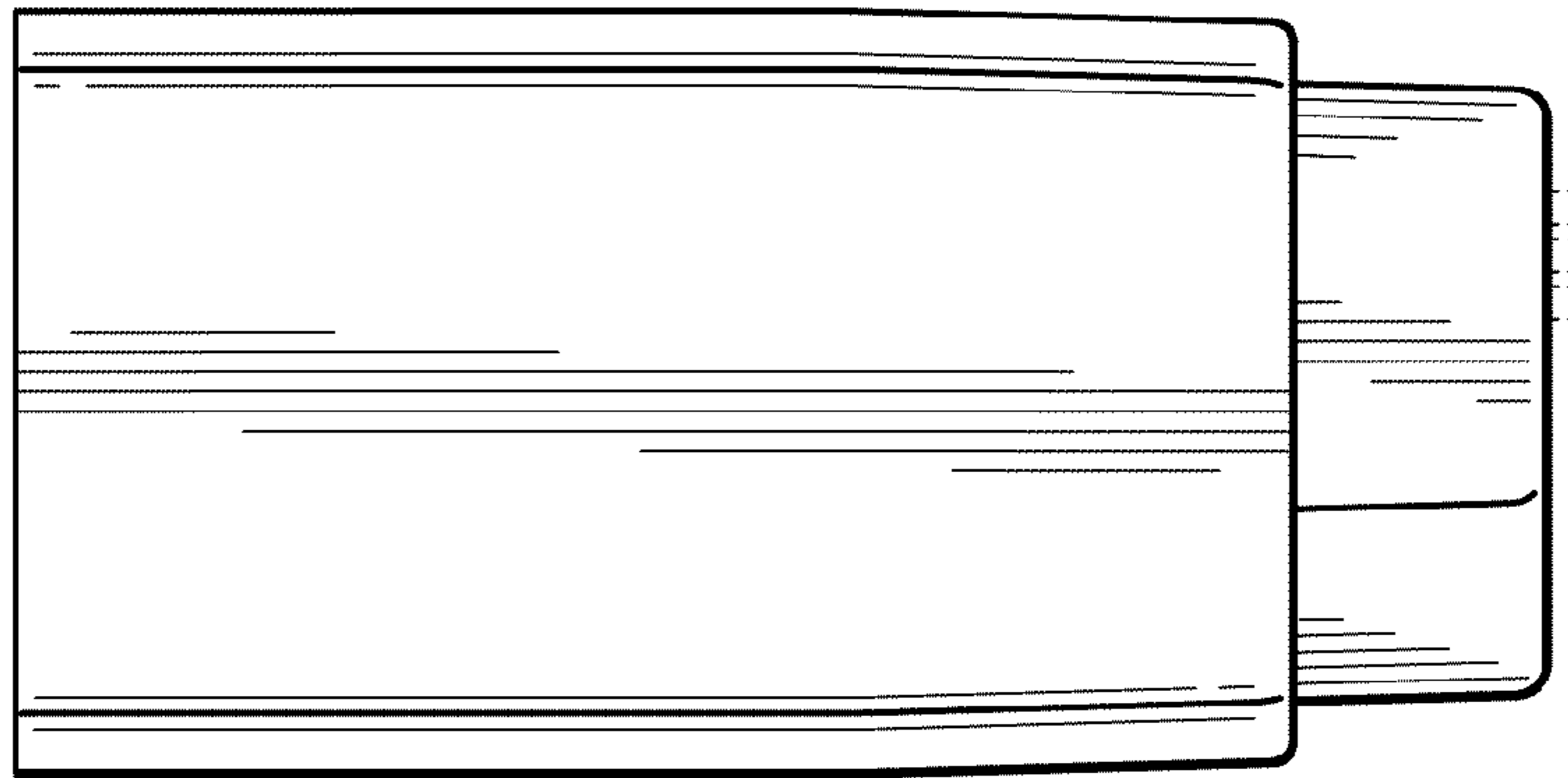


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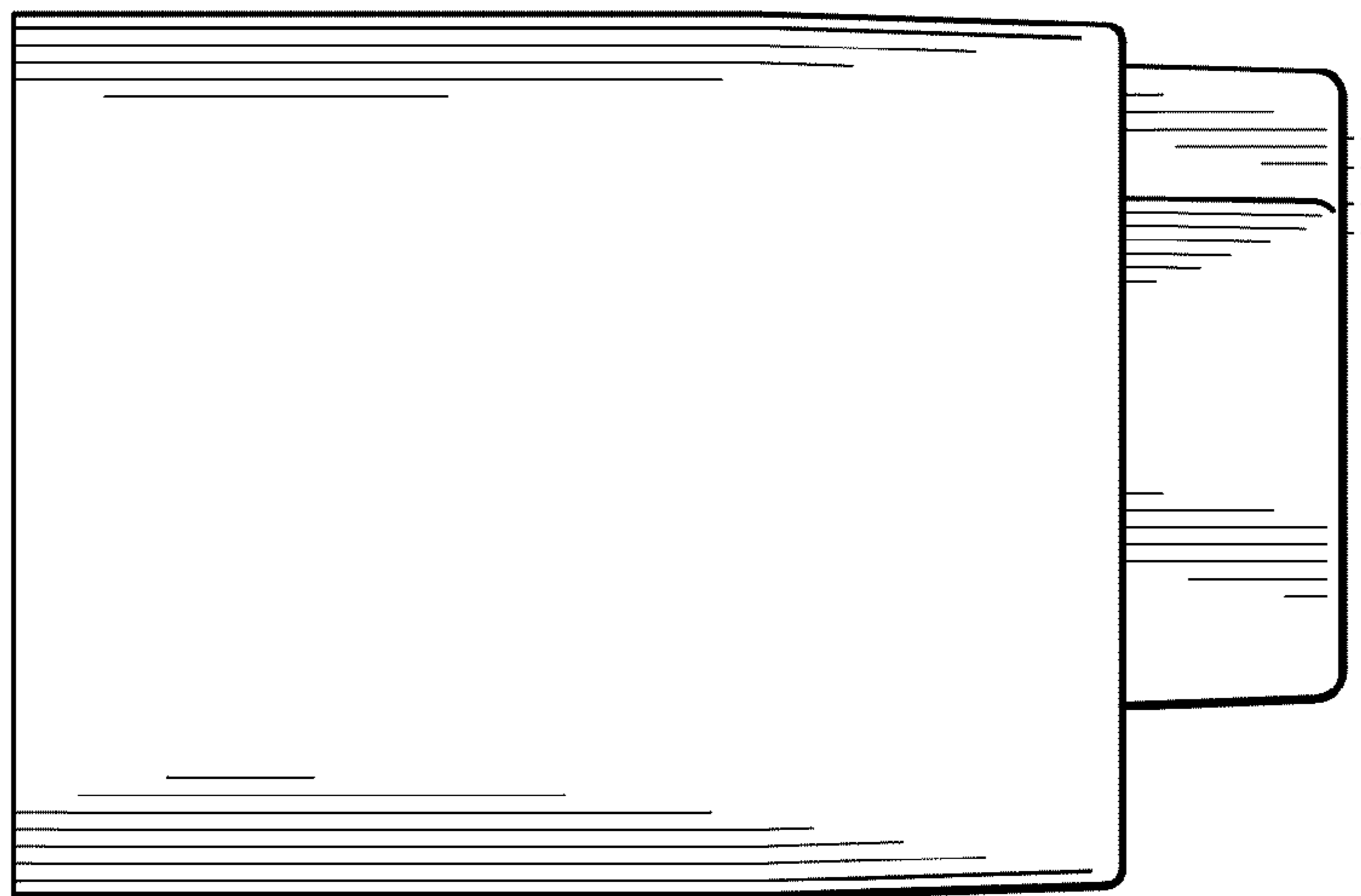


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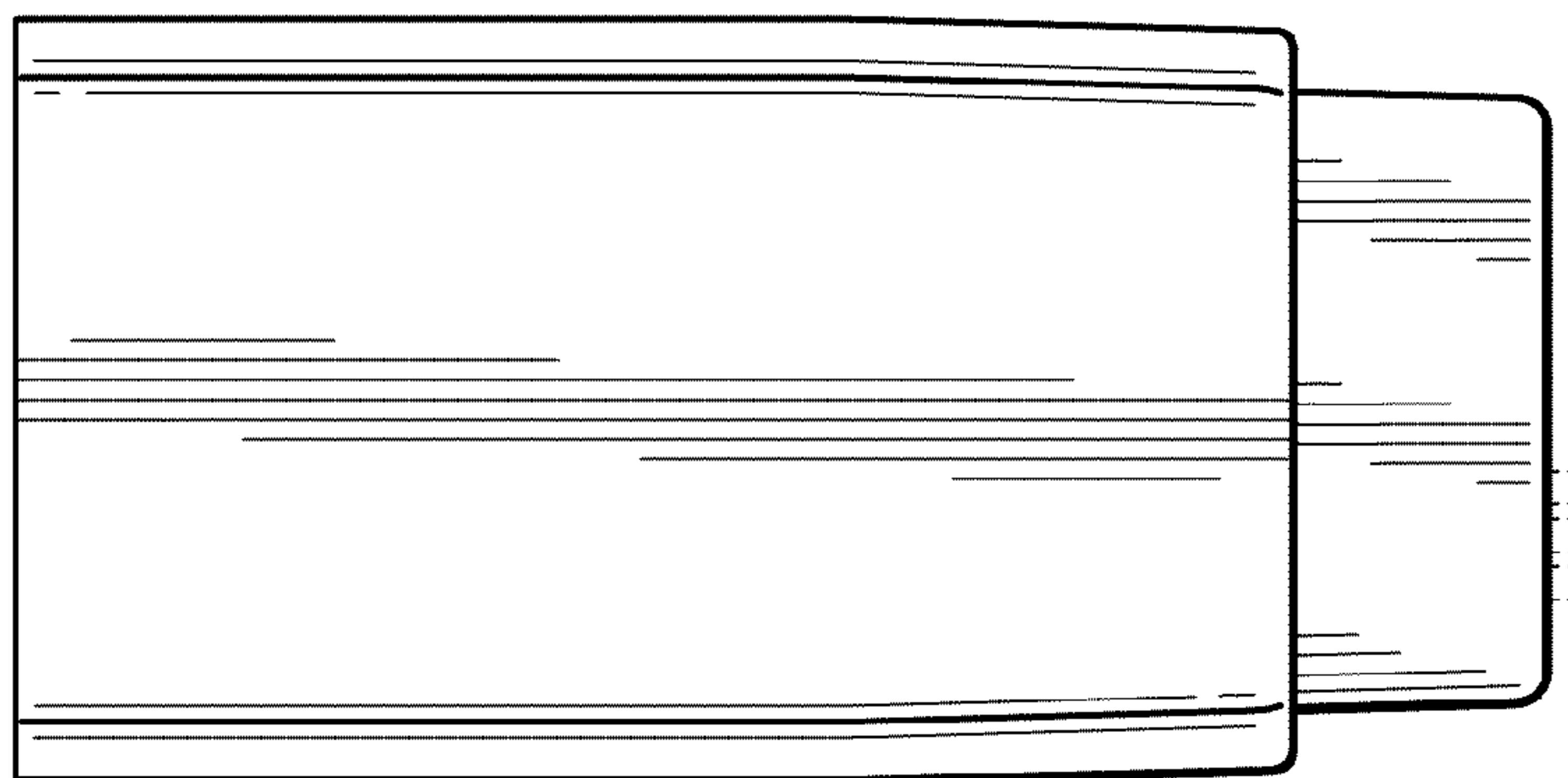


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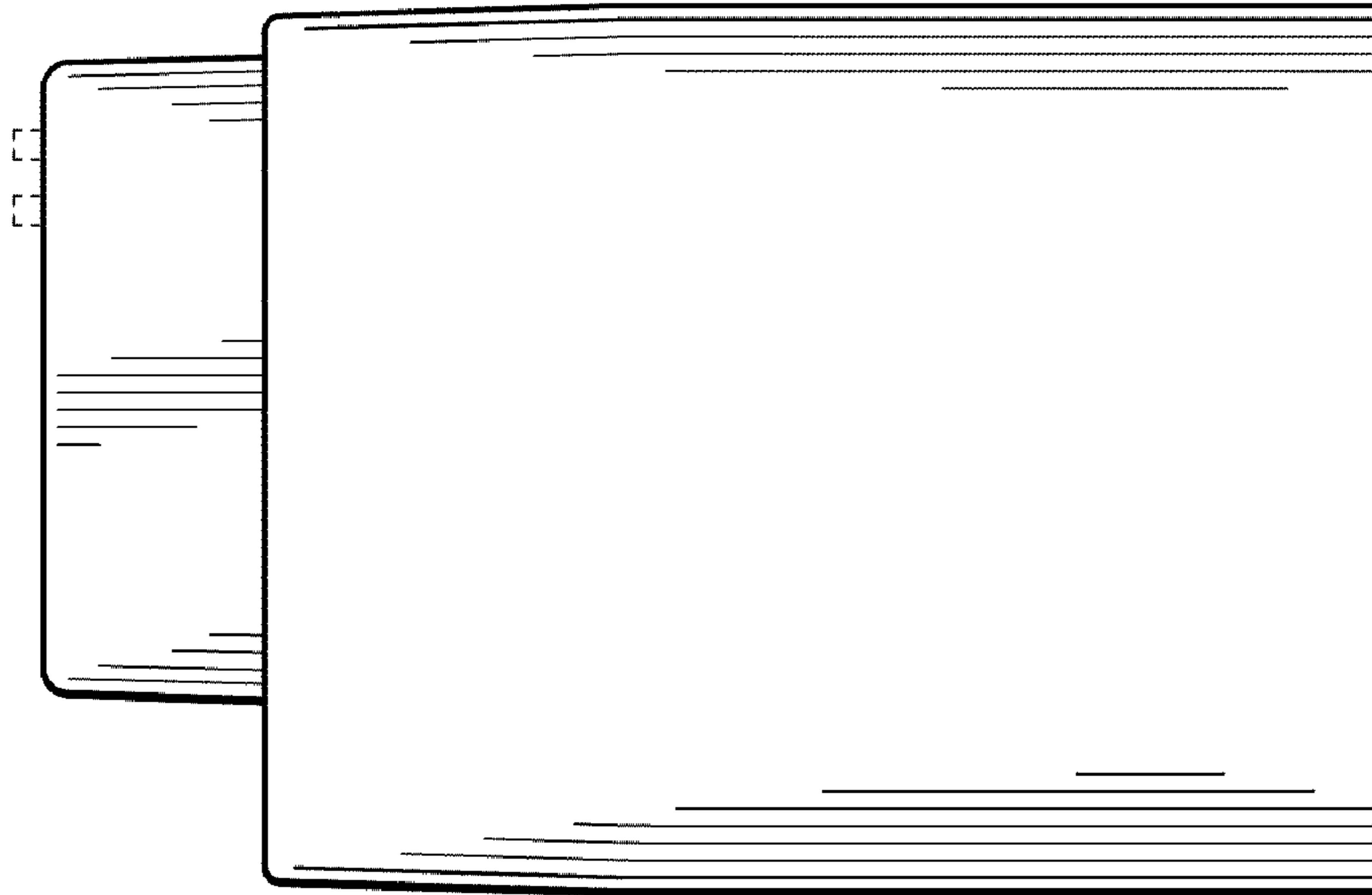


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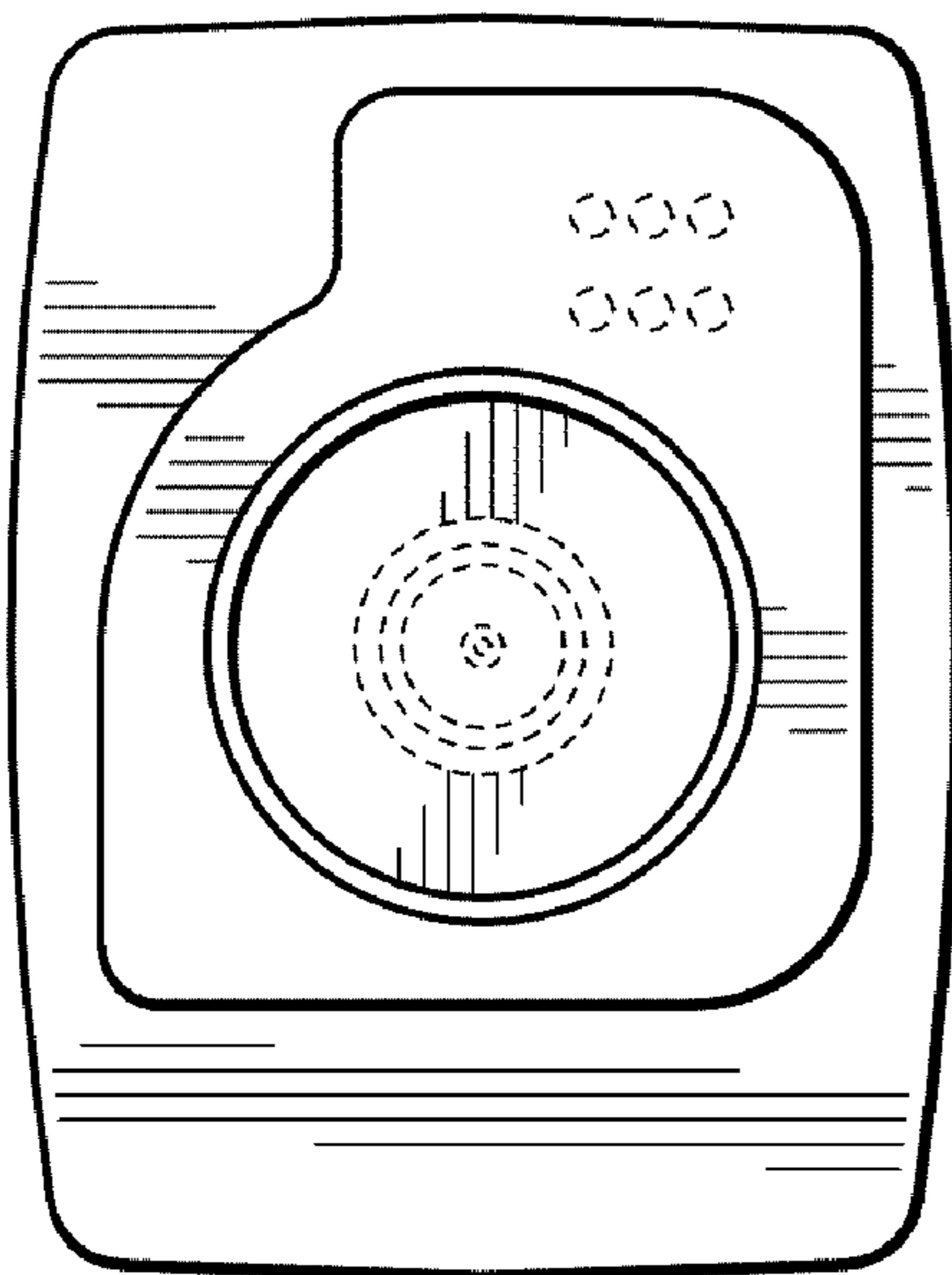


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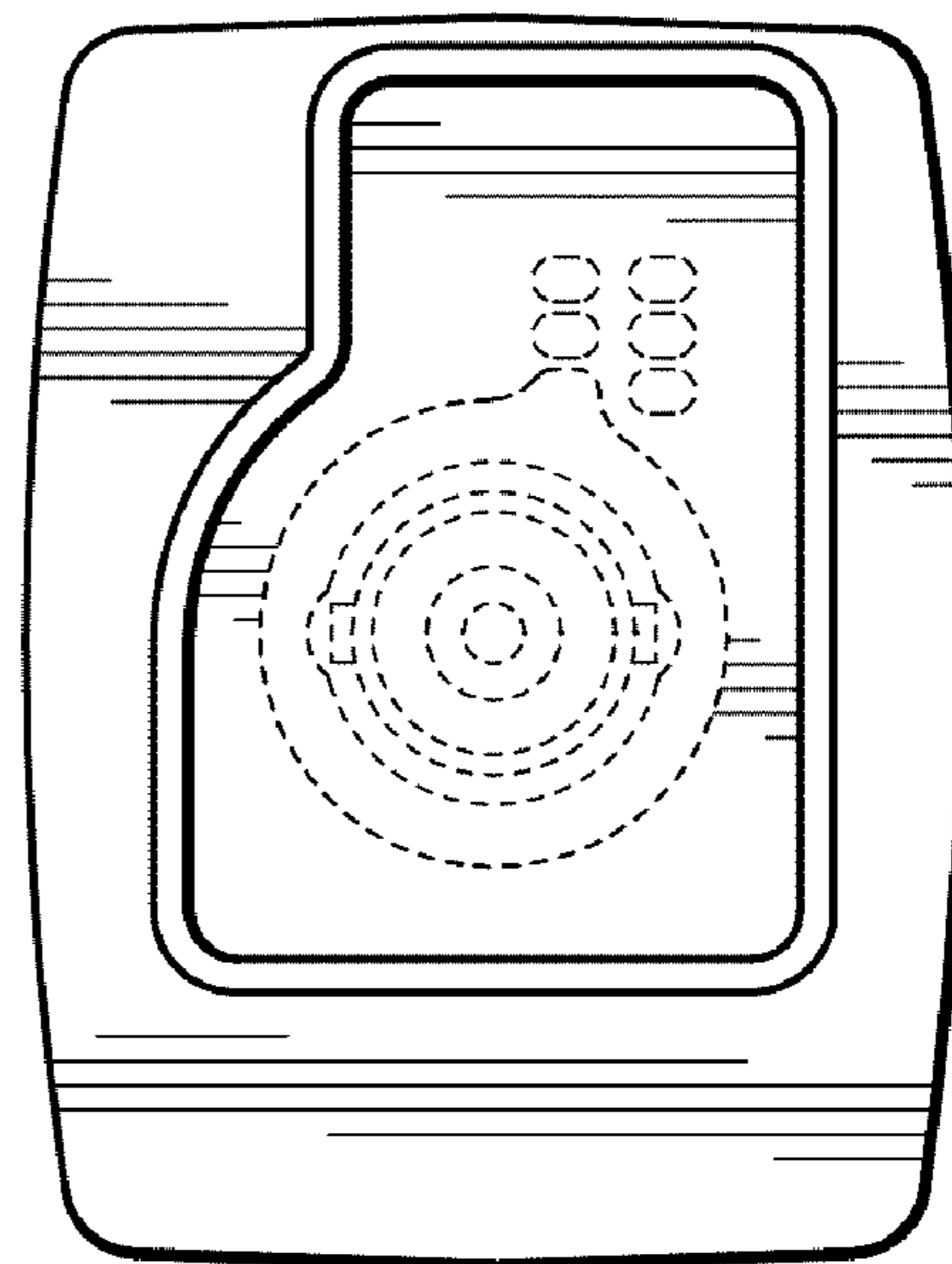


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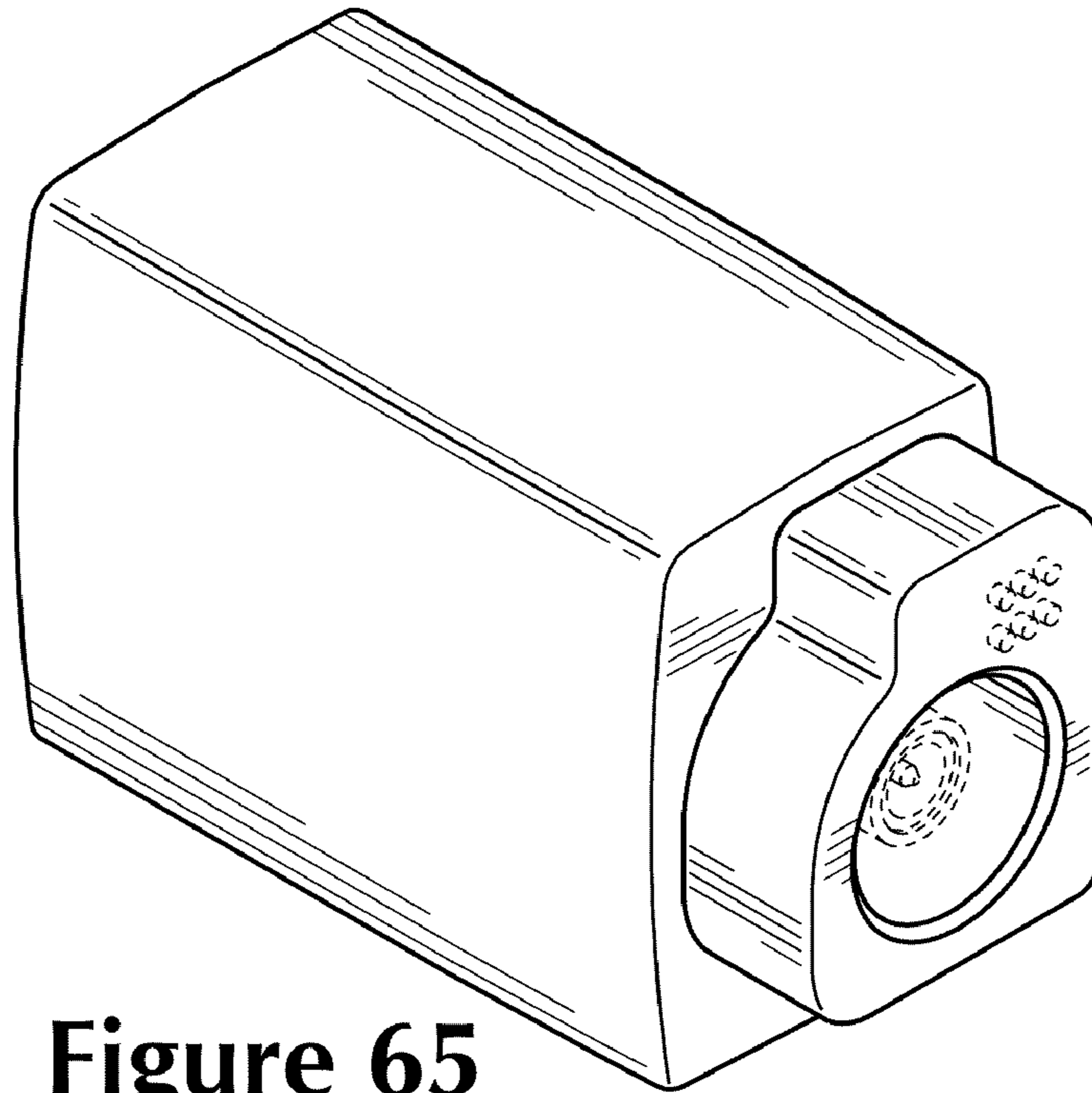


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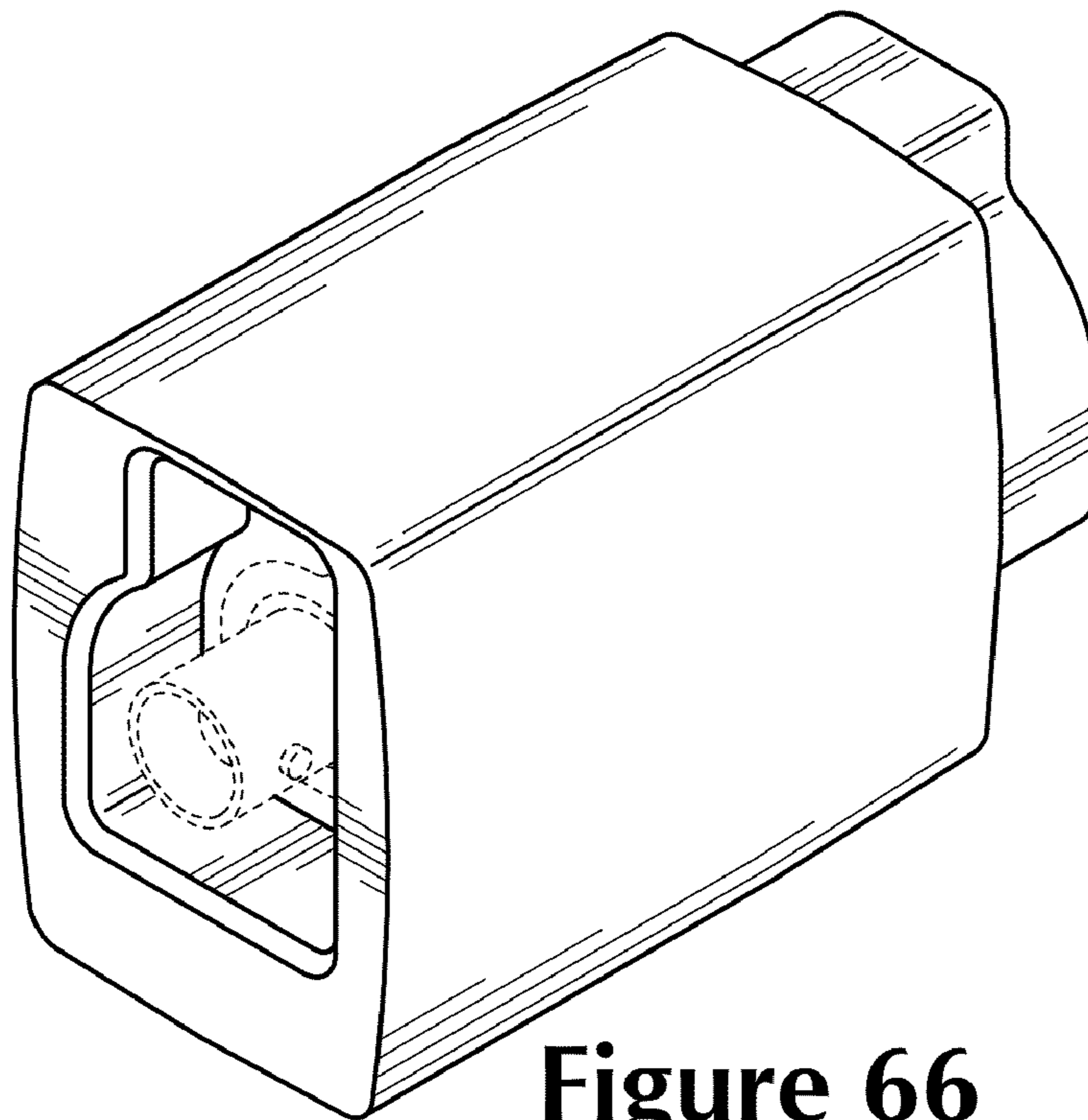


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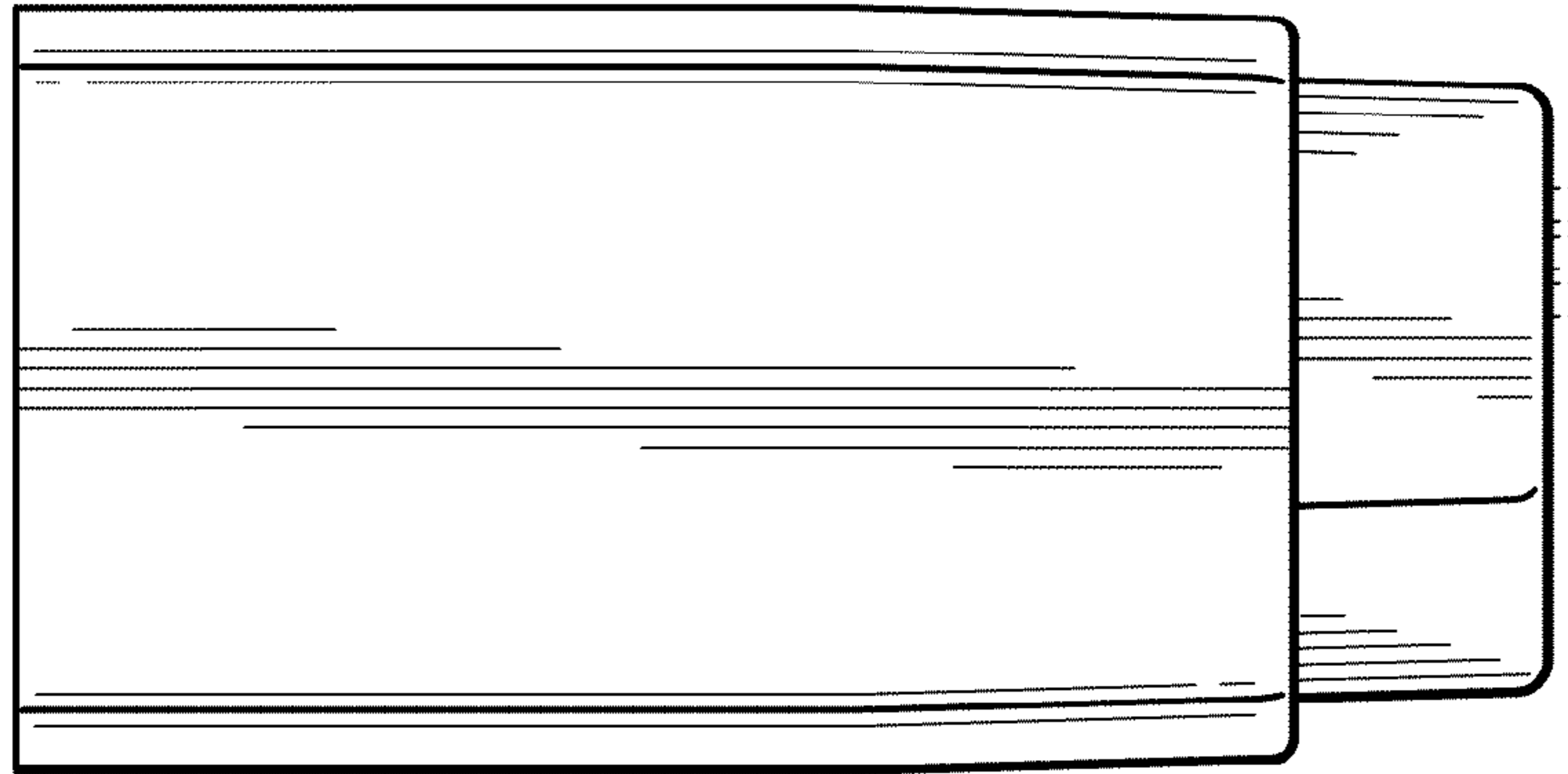


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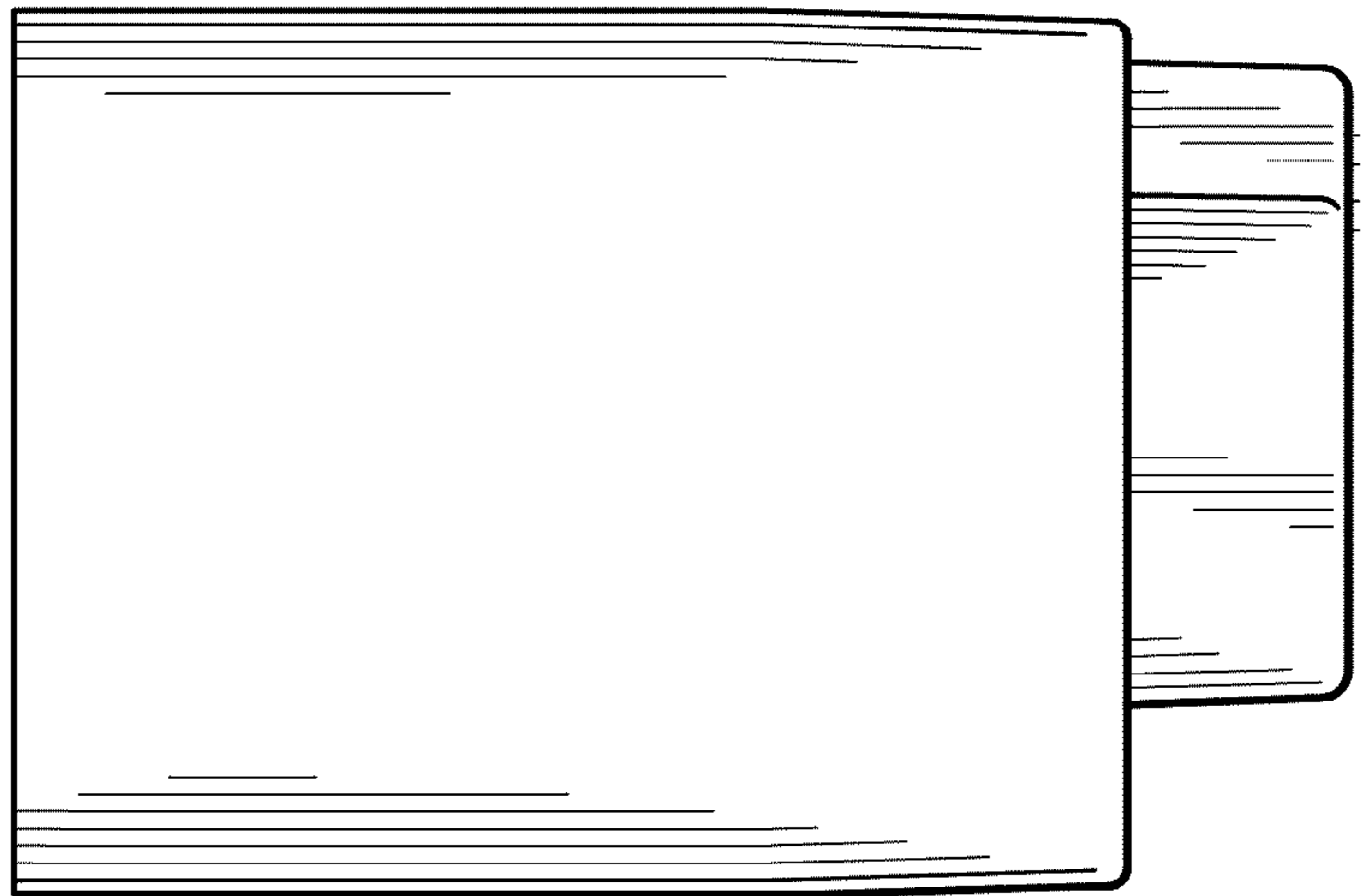


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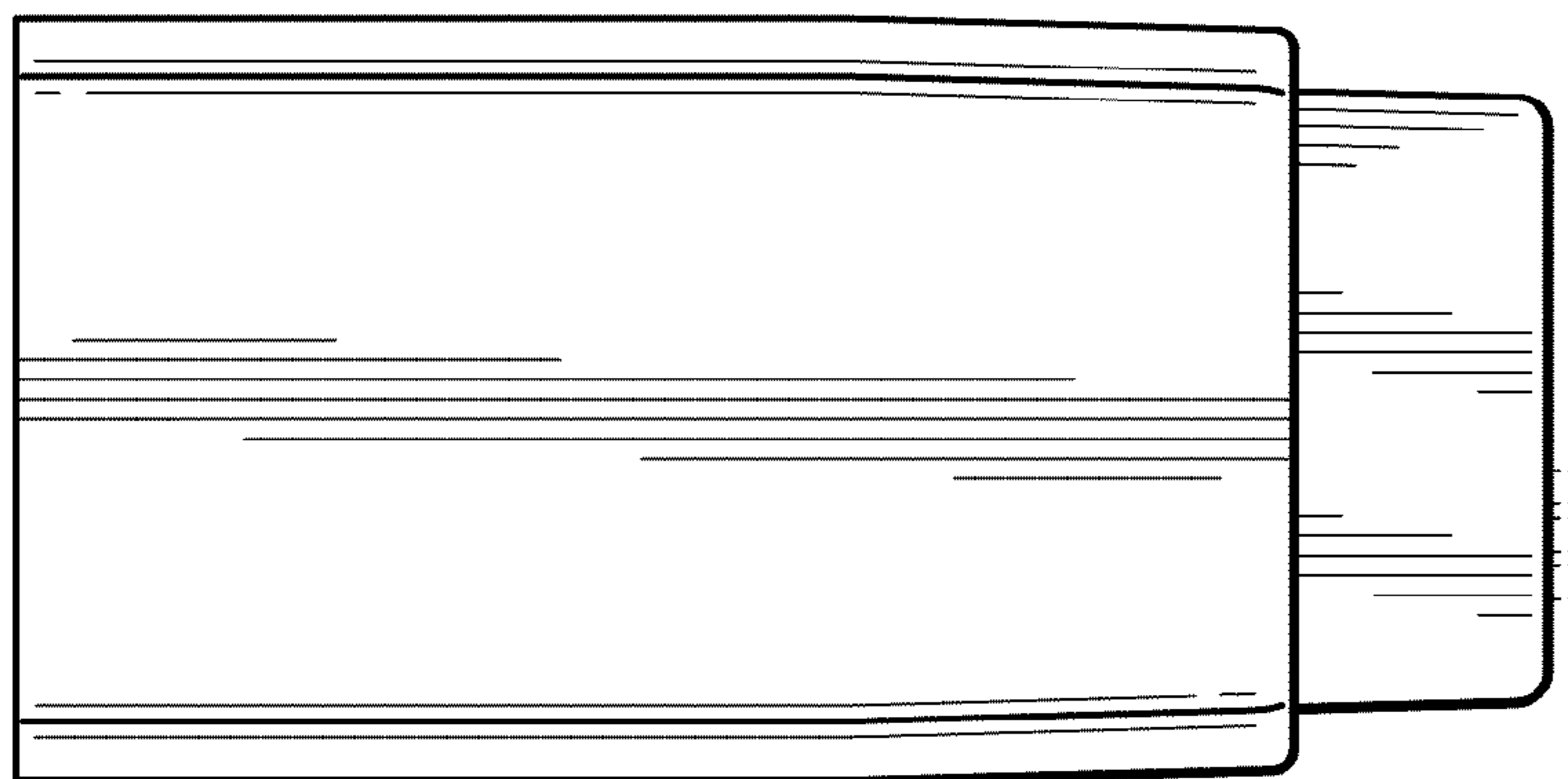


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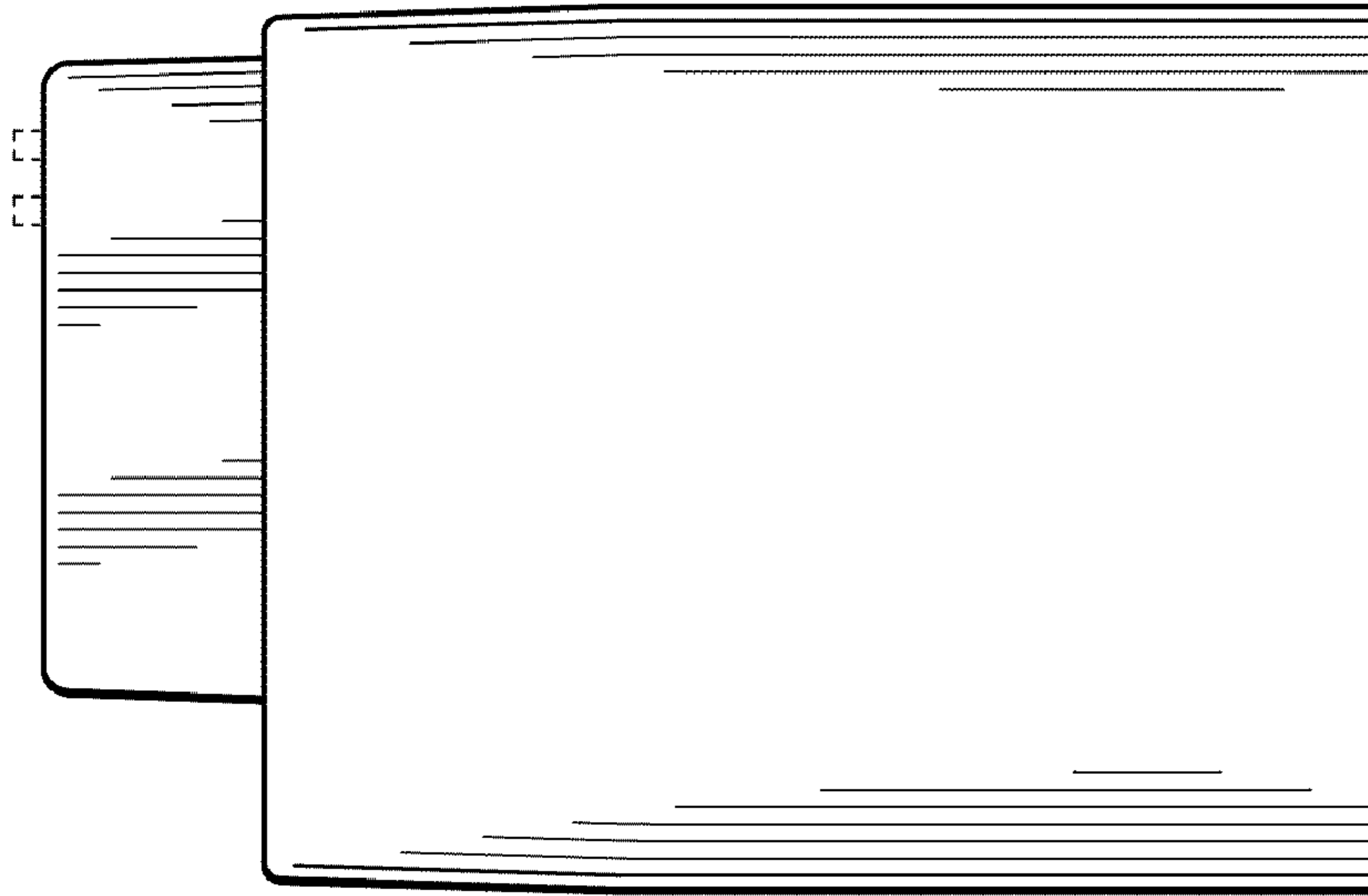


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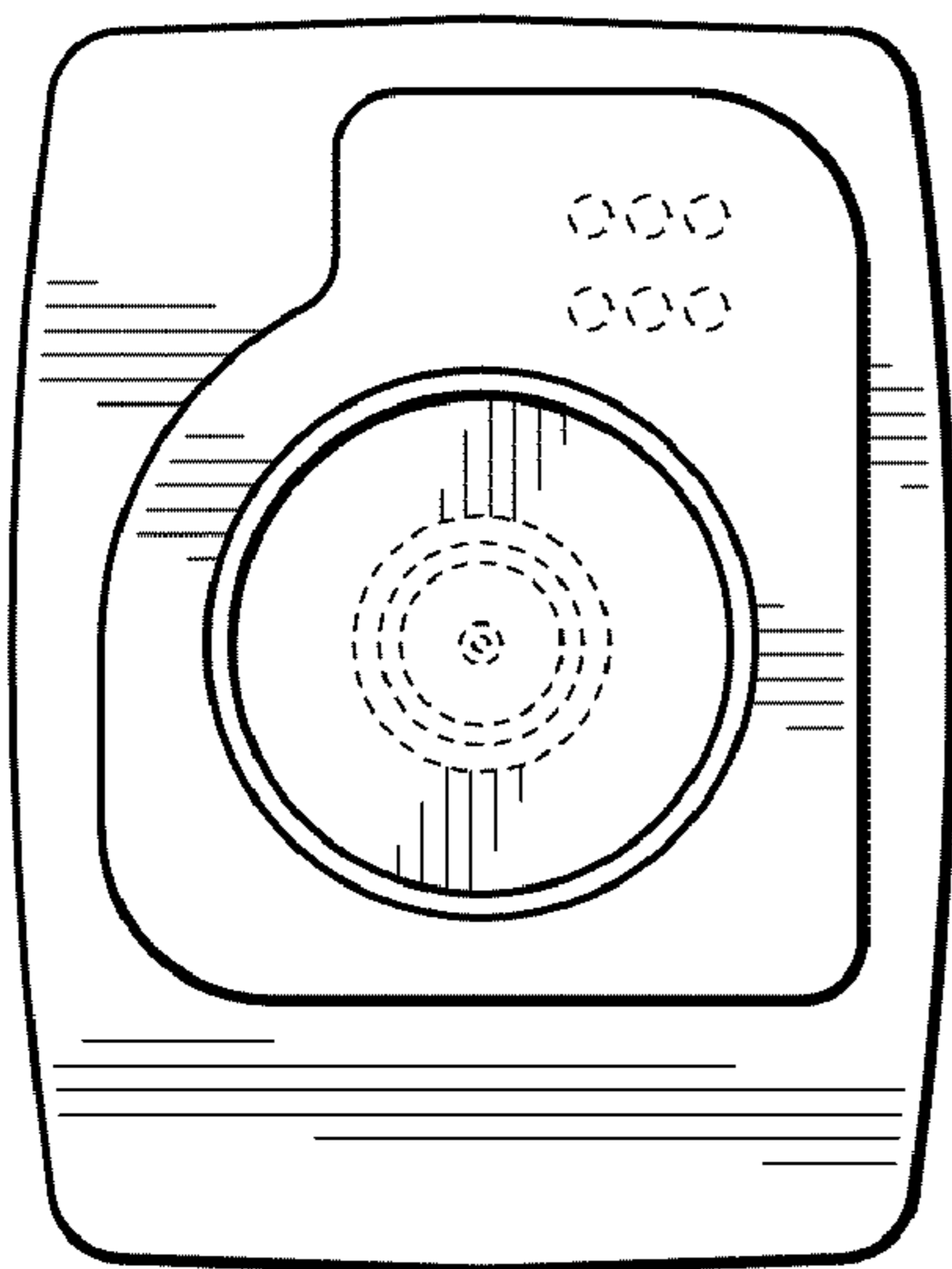


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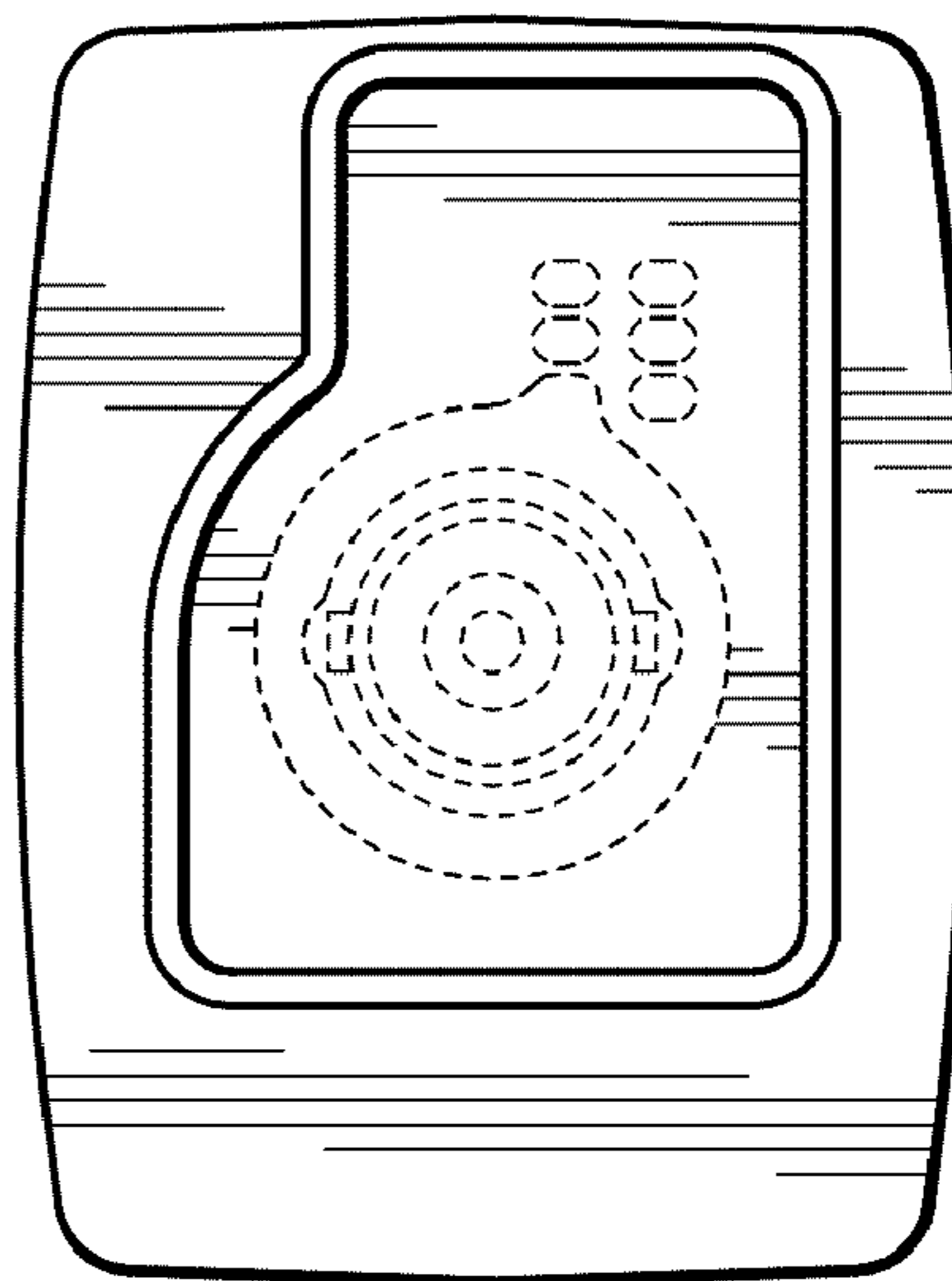


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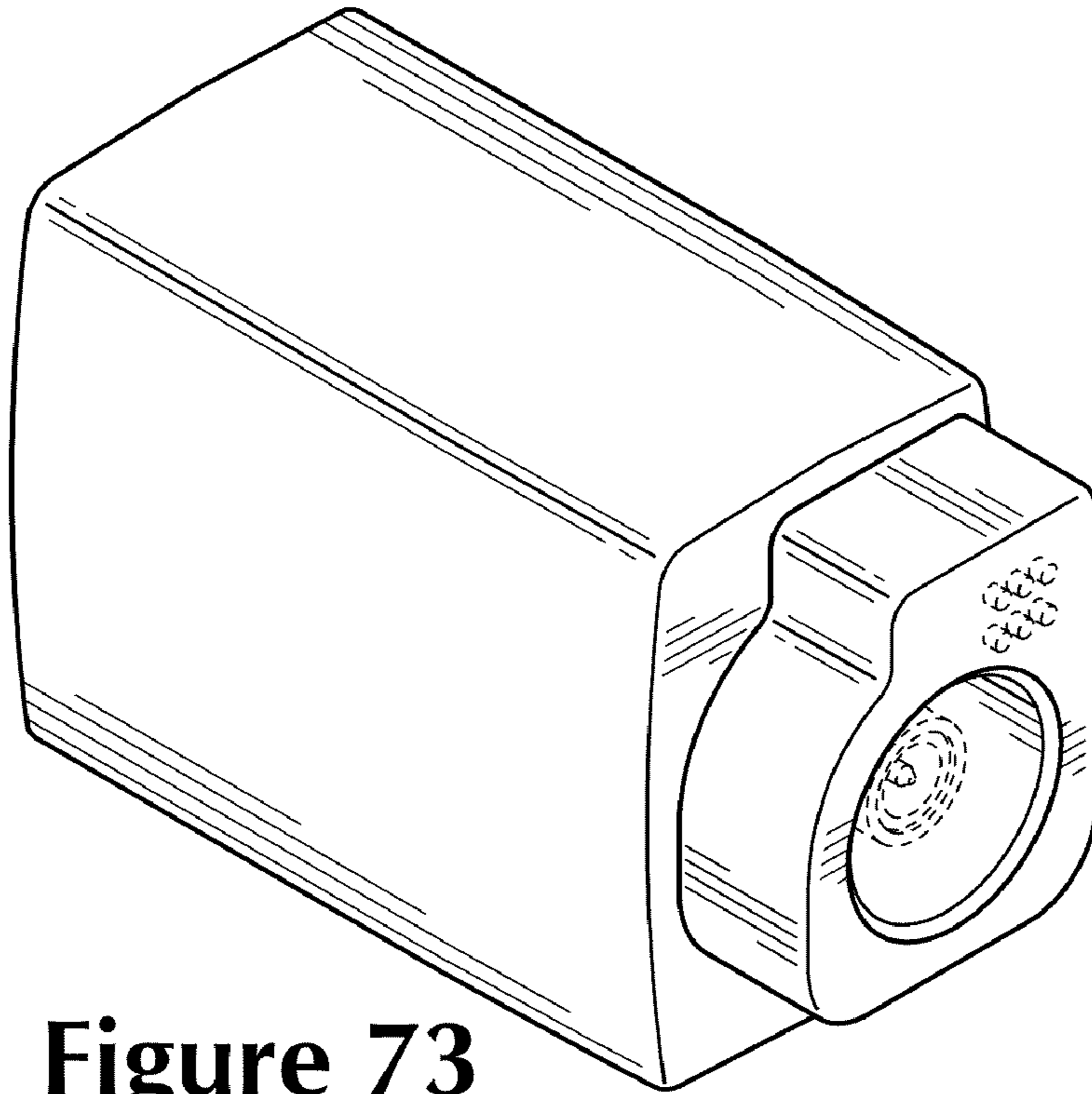


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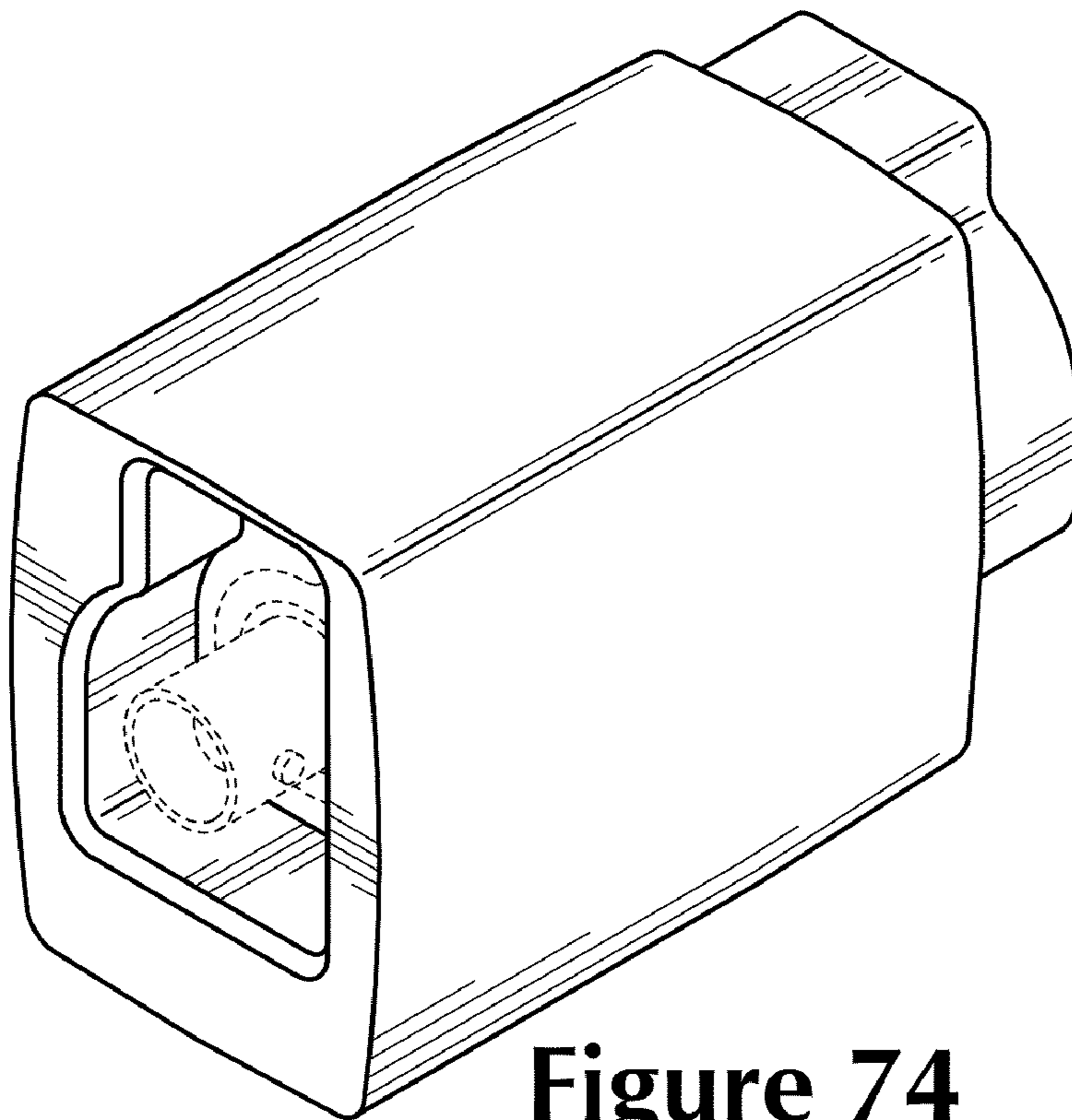


Figure 74

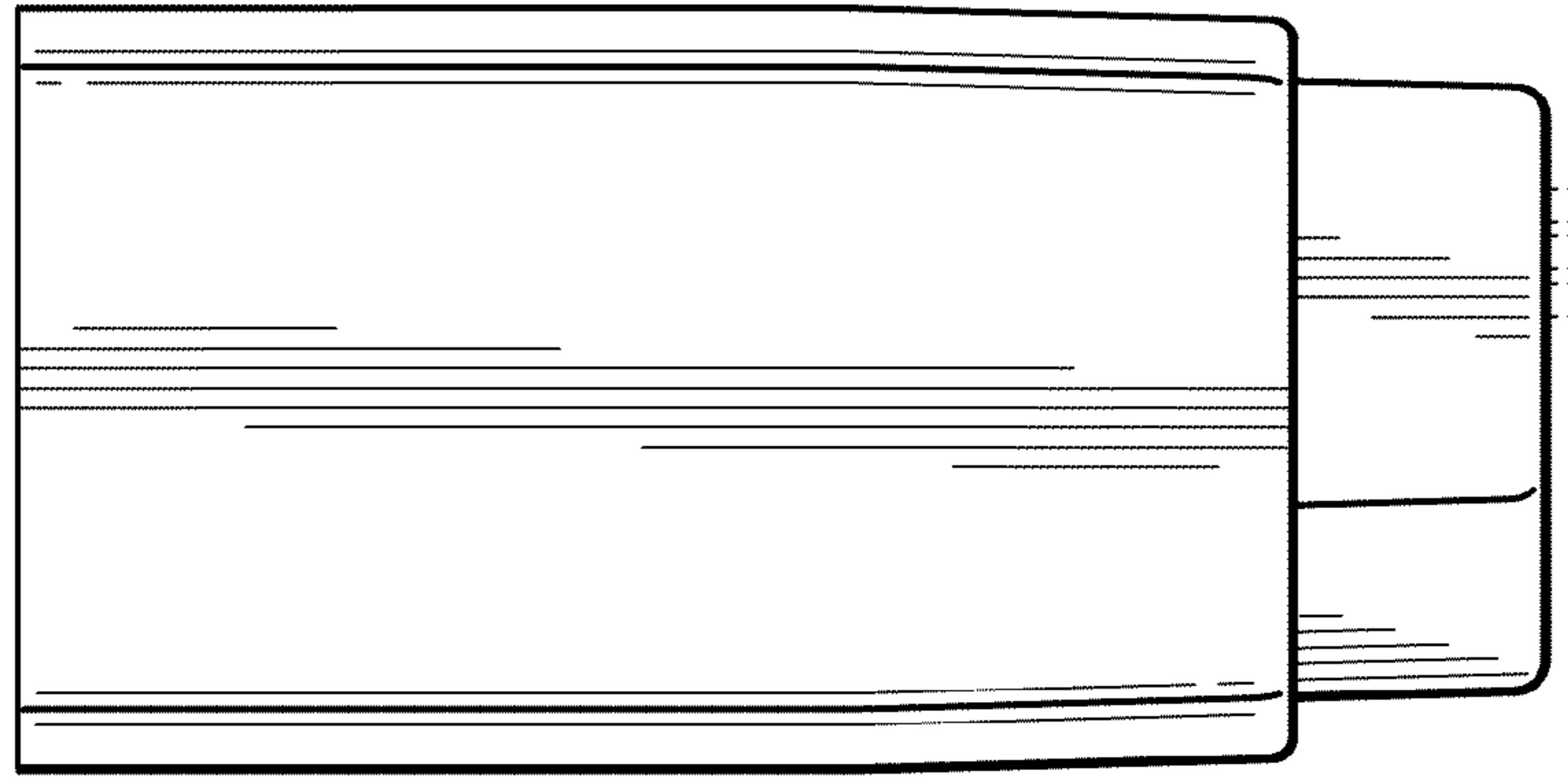


Figure 75

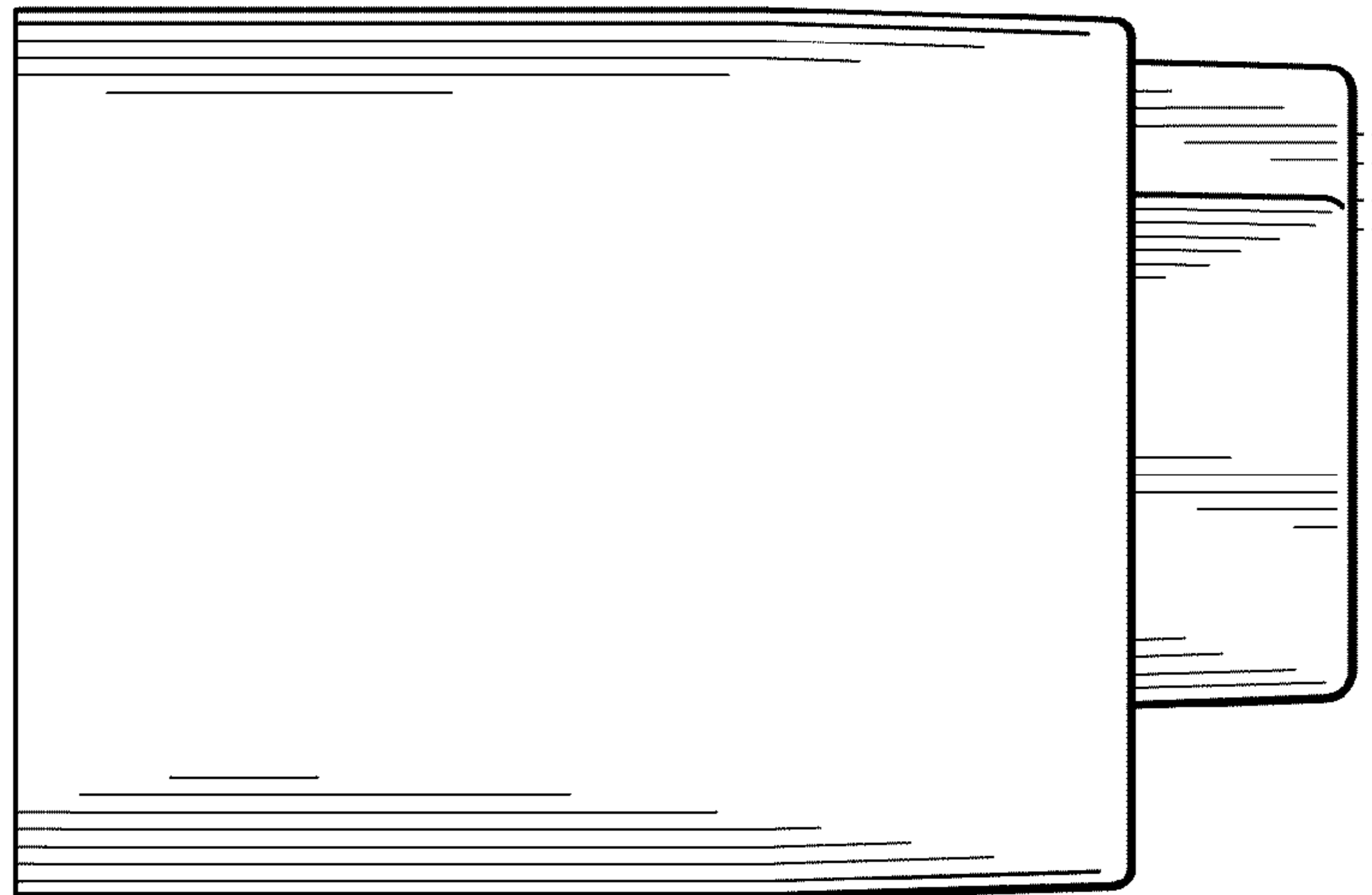


Figure 76

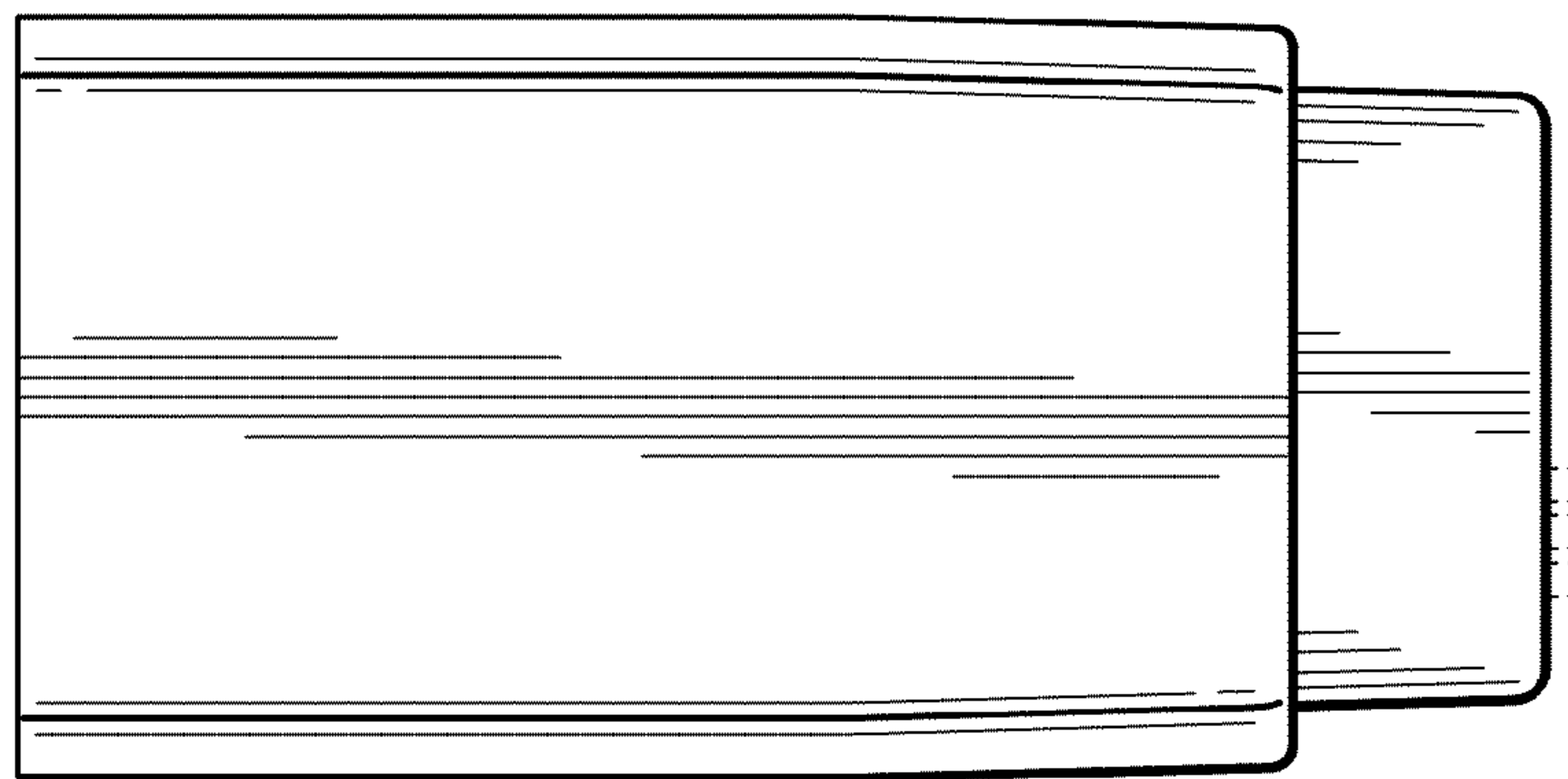


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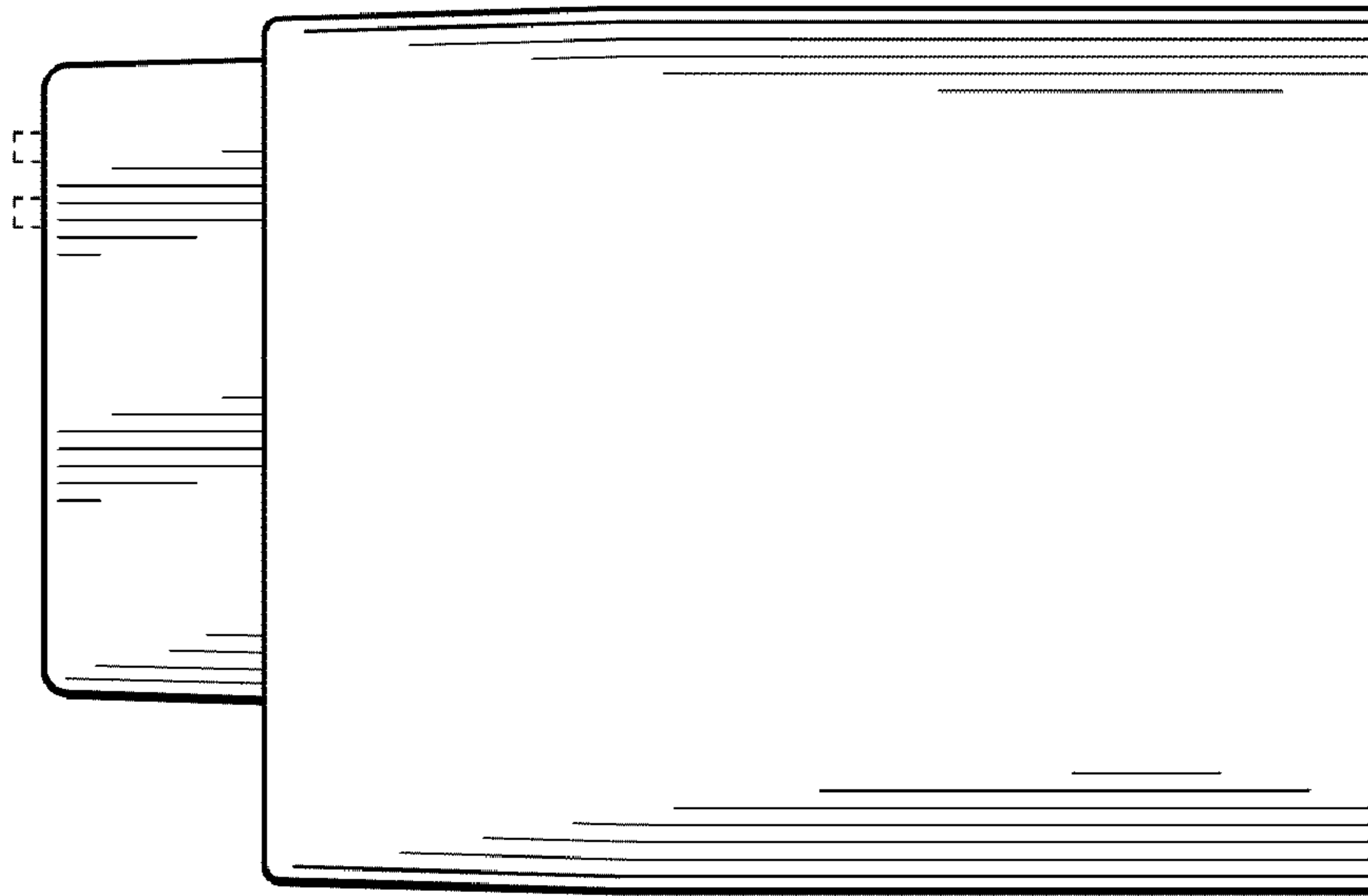


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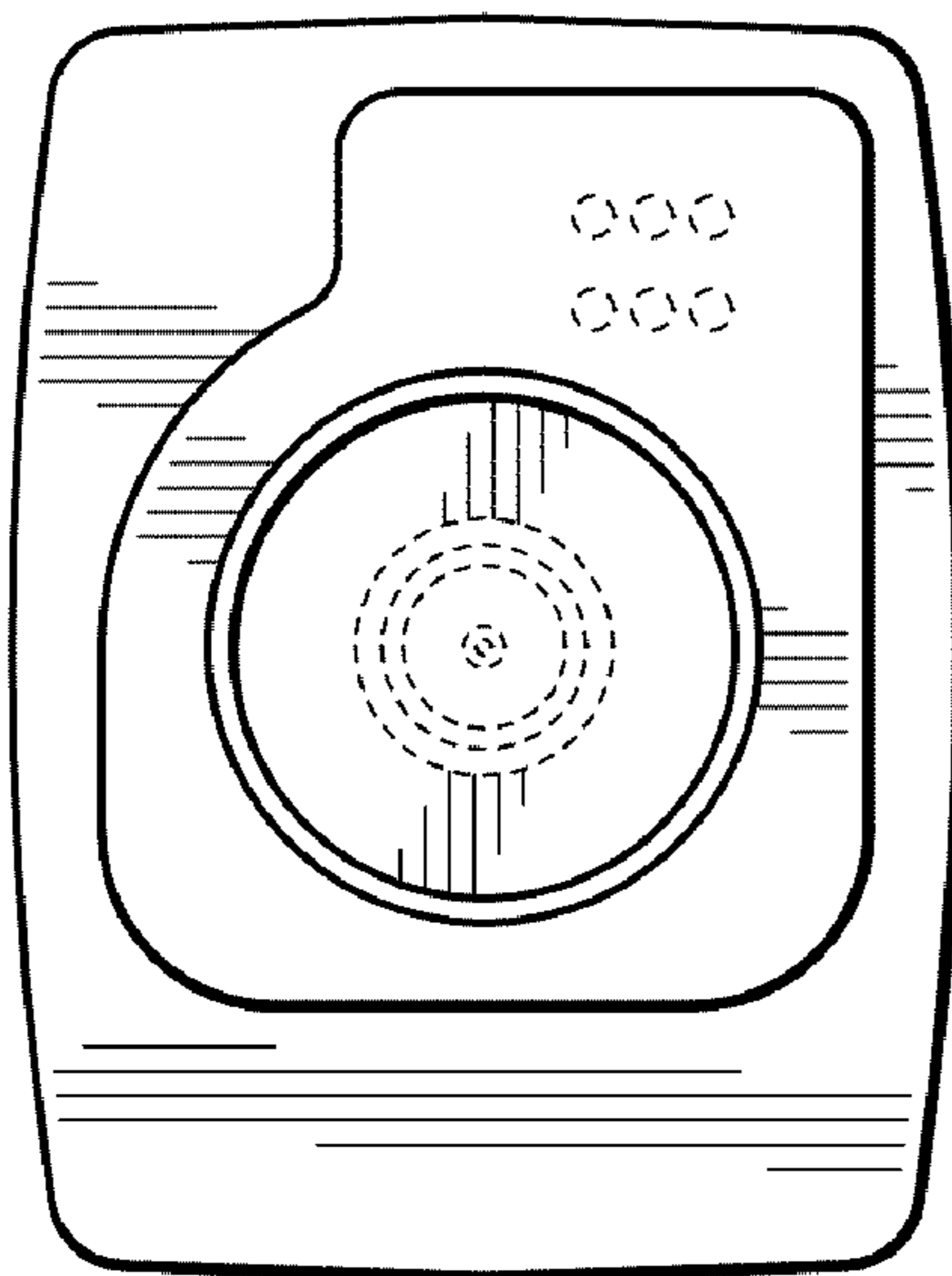


Figure 79

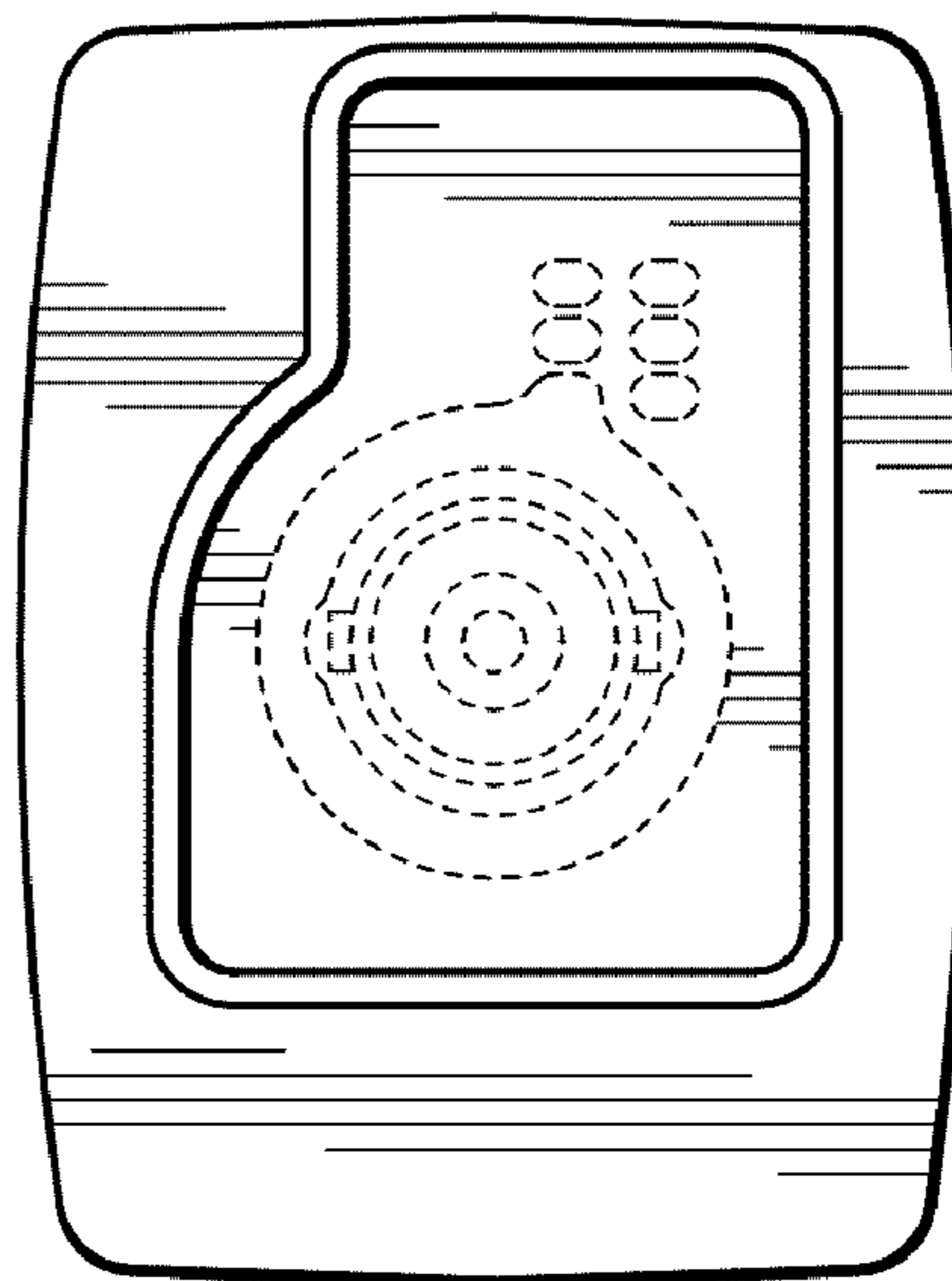


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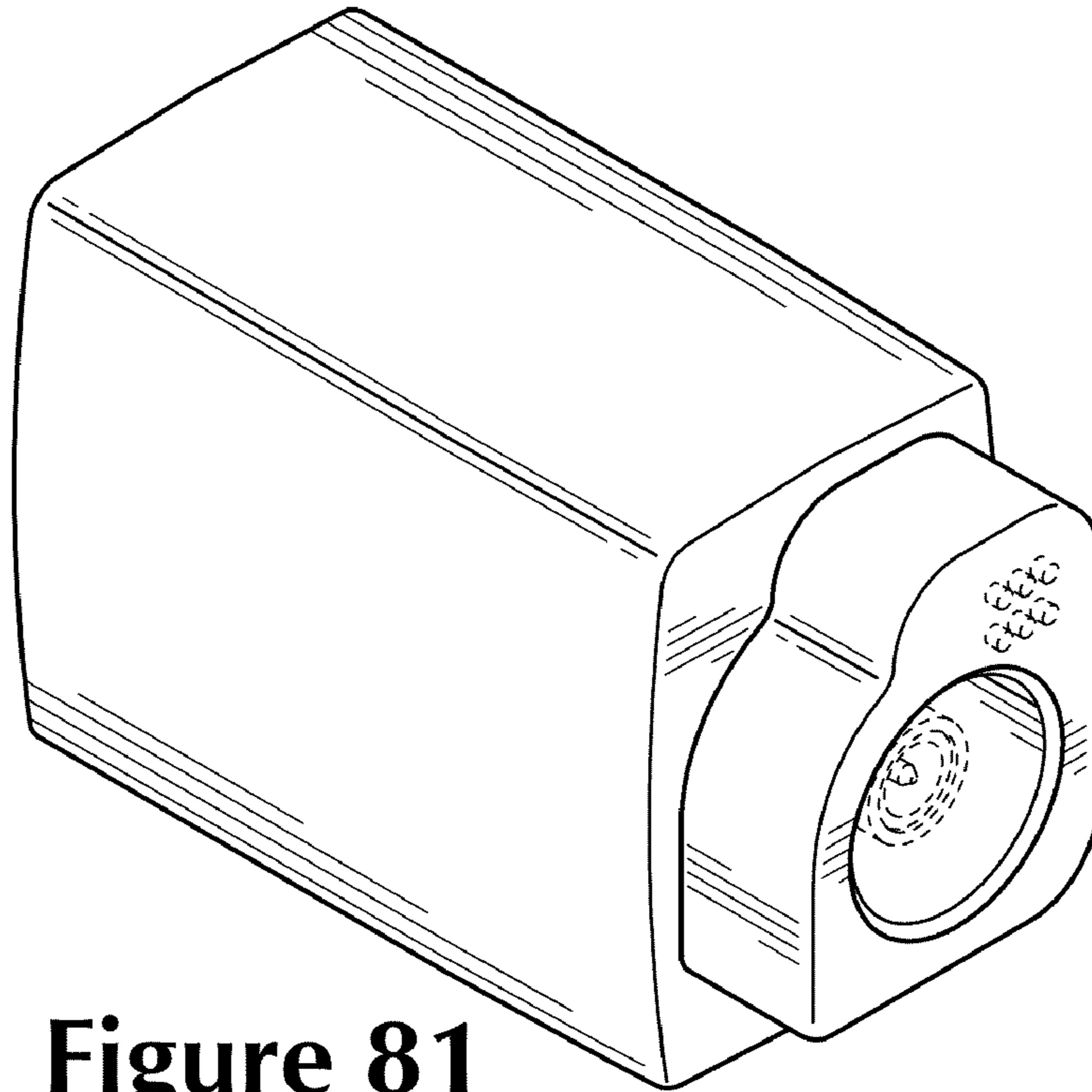


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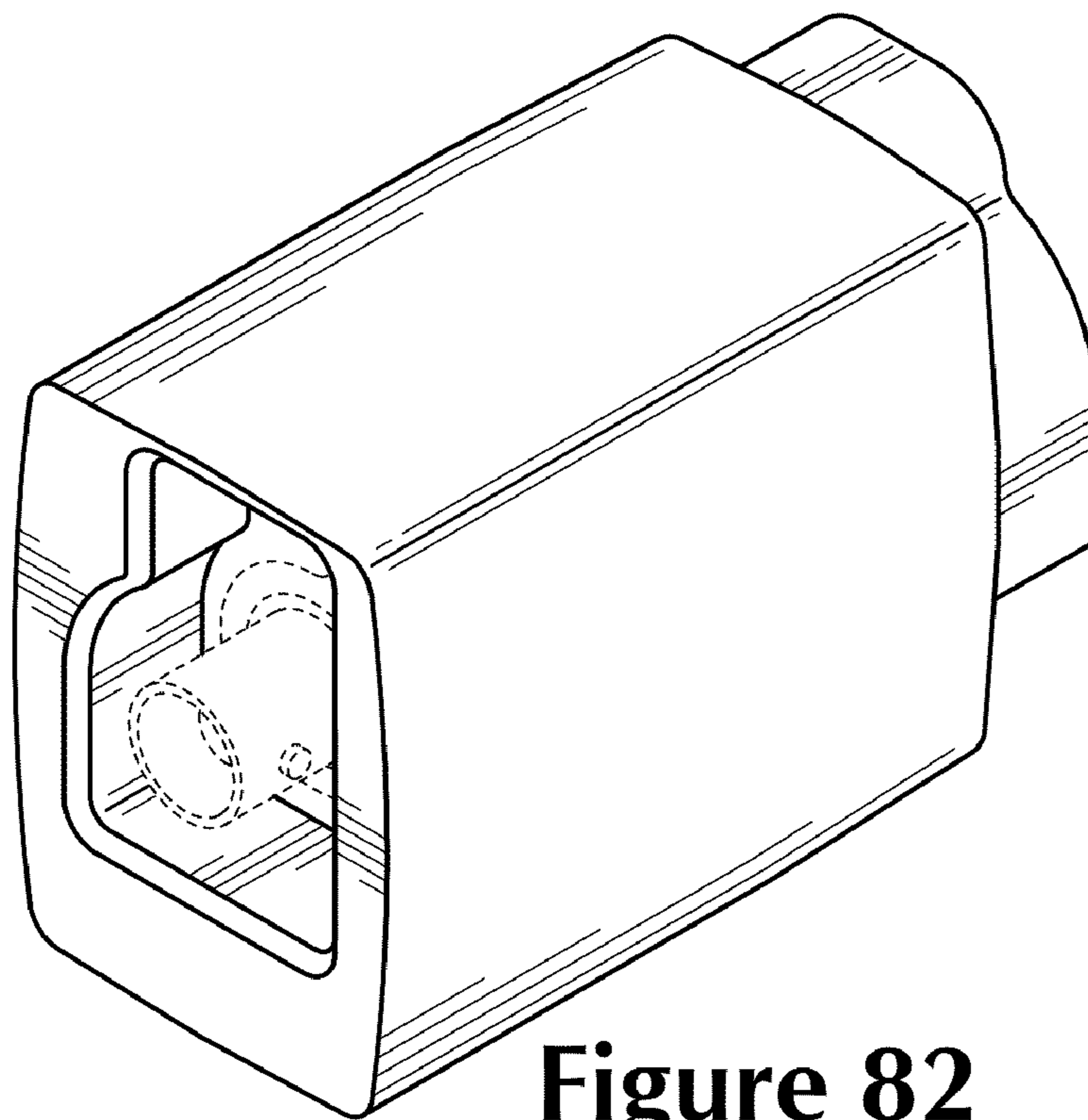


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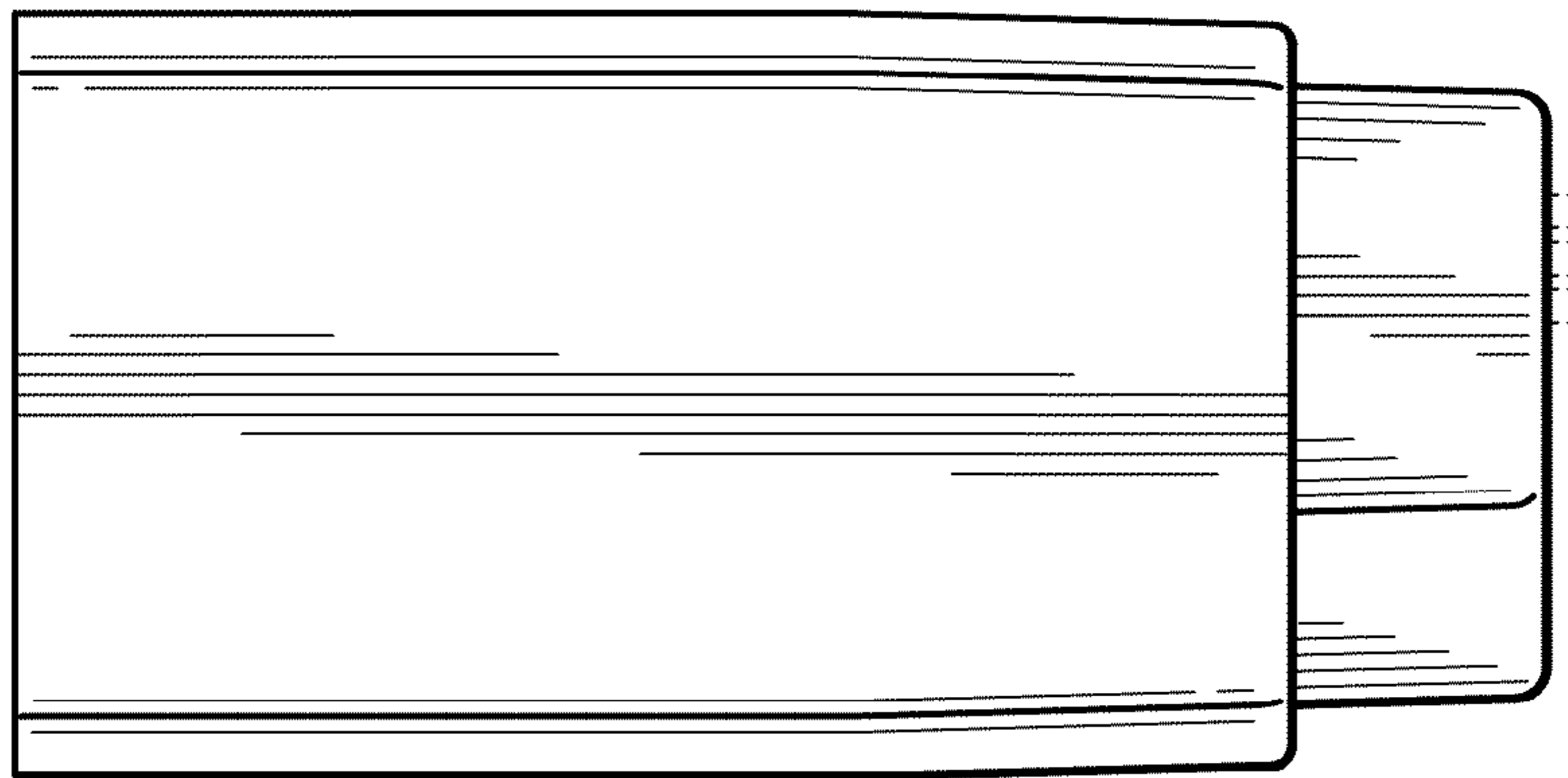


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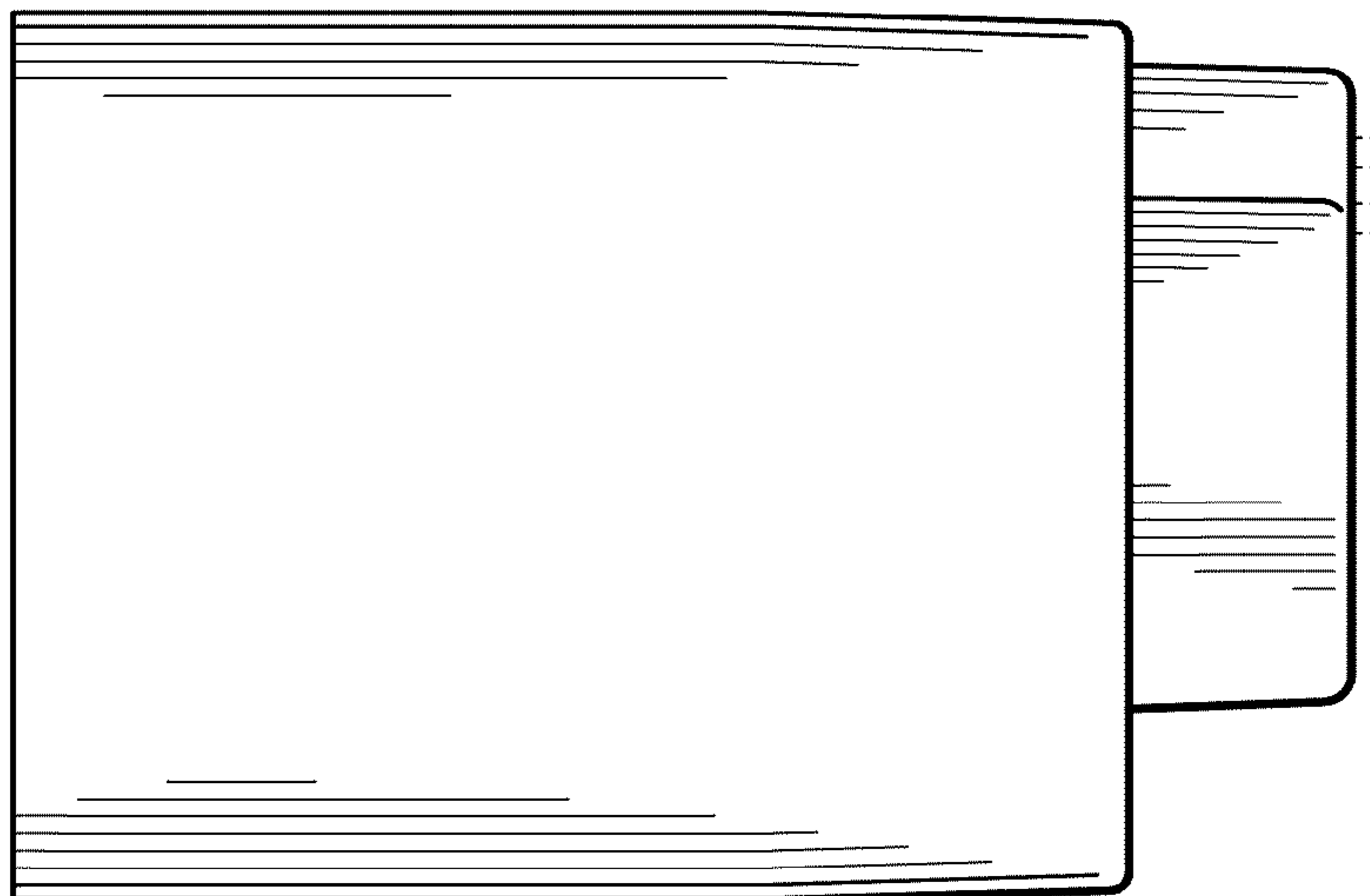


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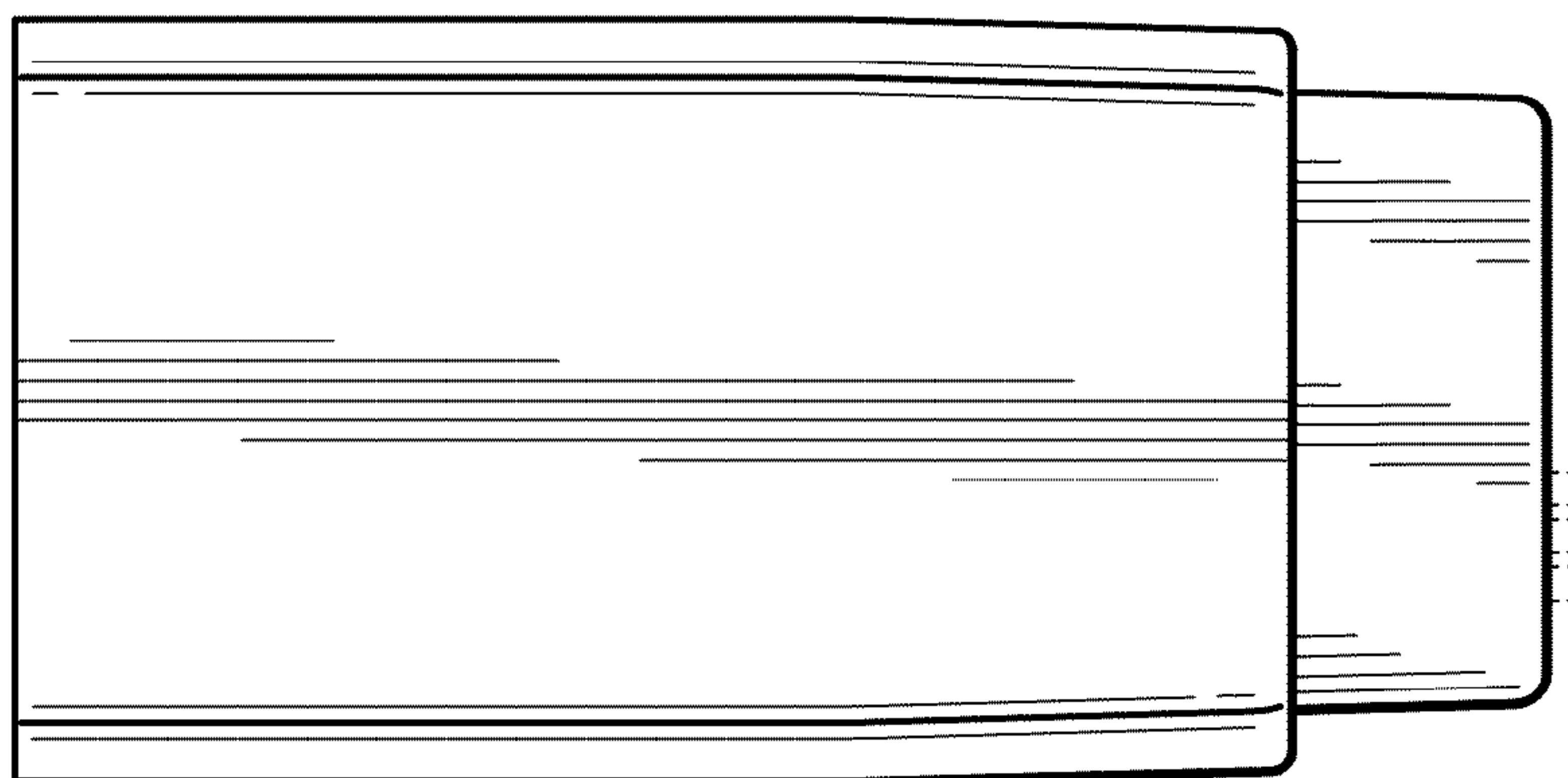


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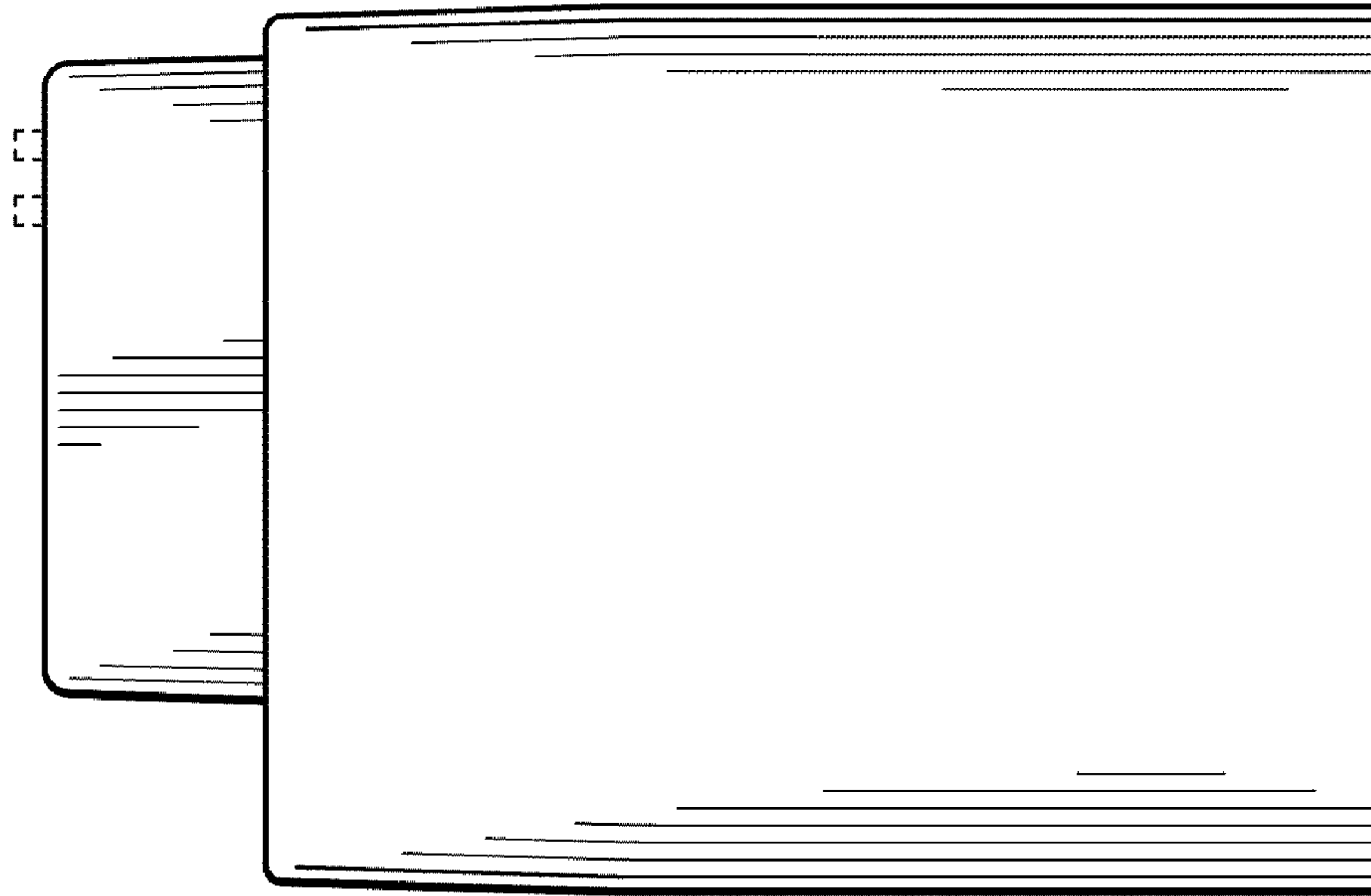


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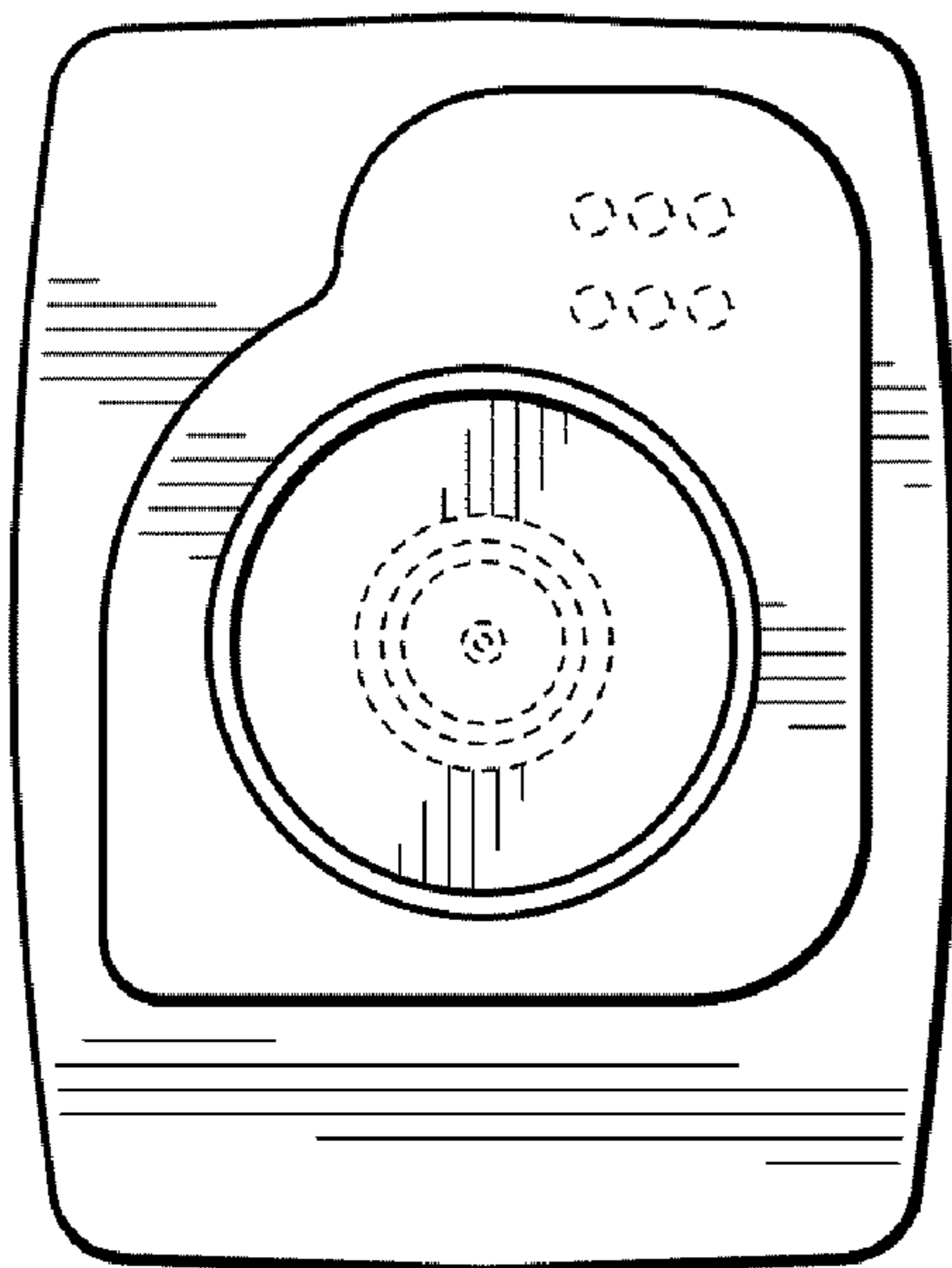


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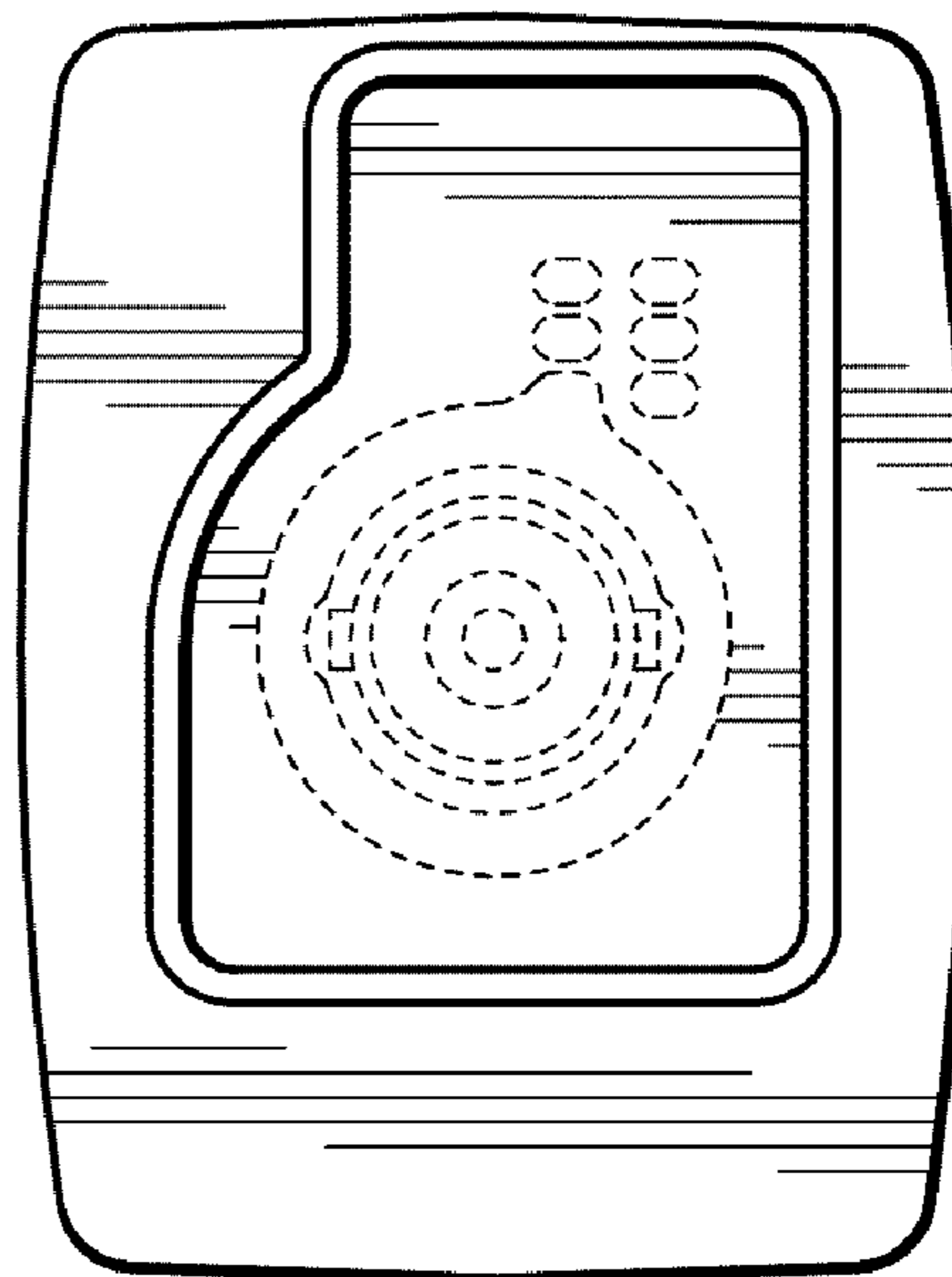


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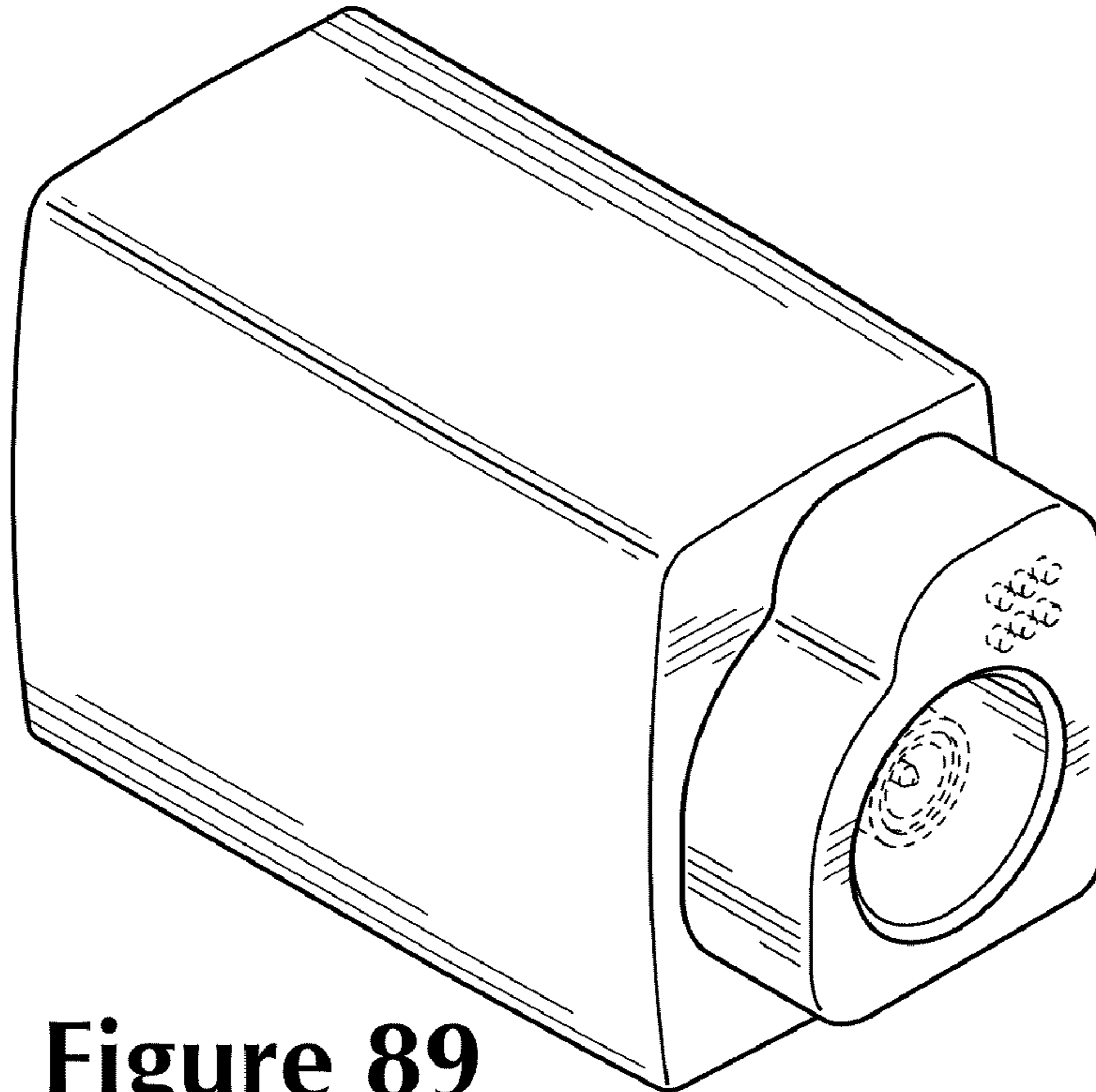


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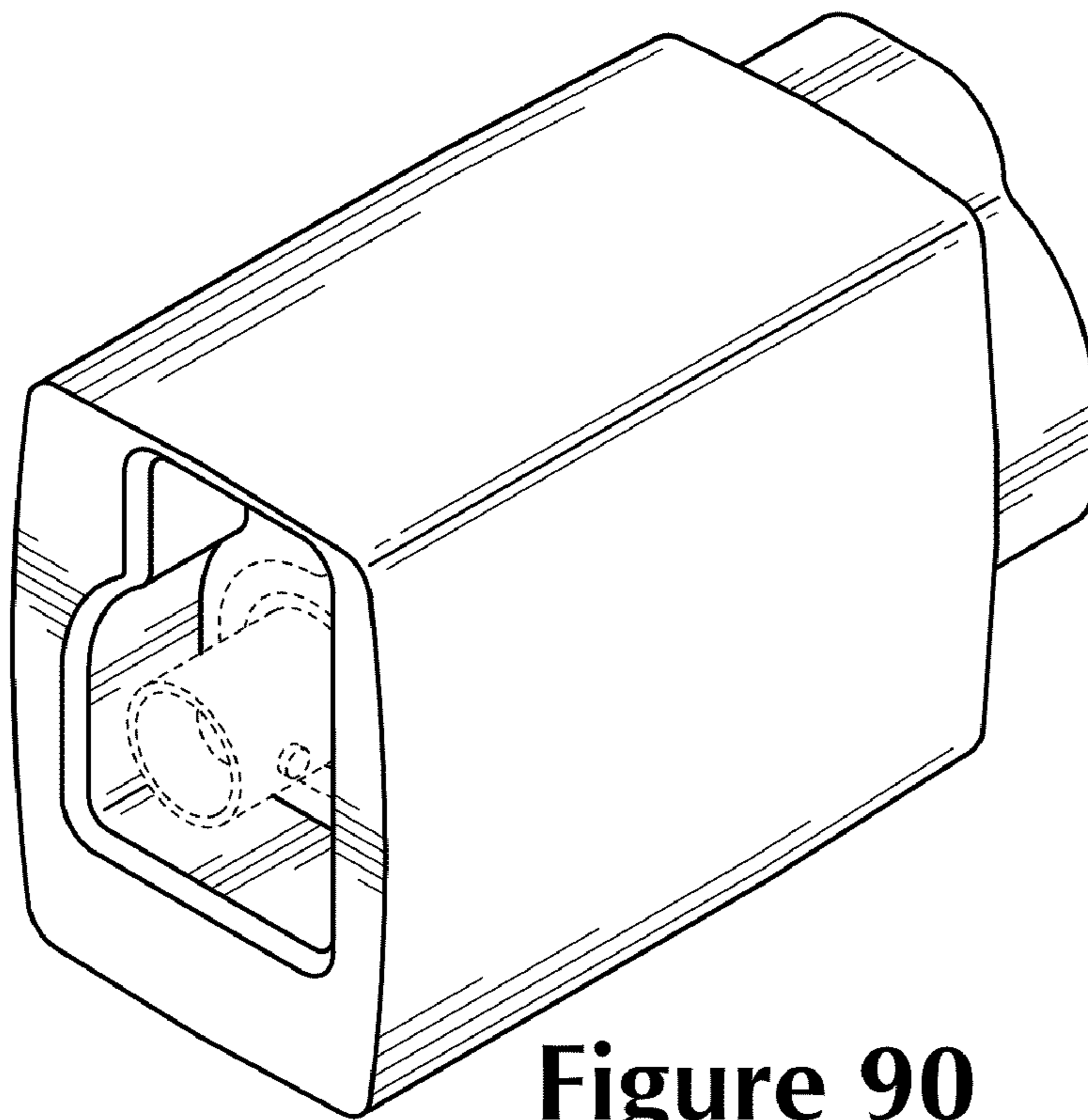


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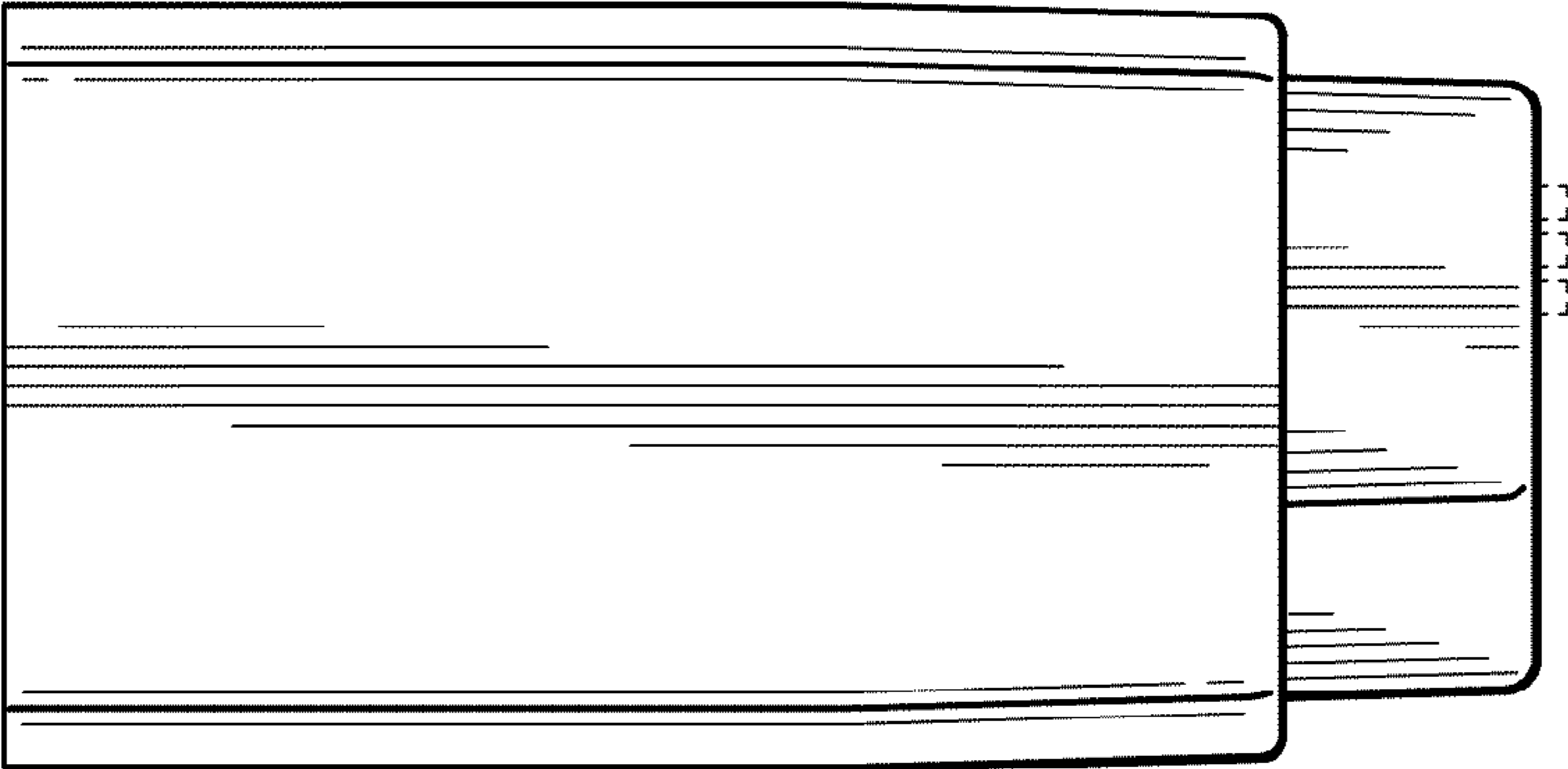


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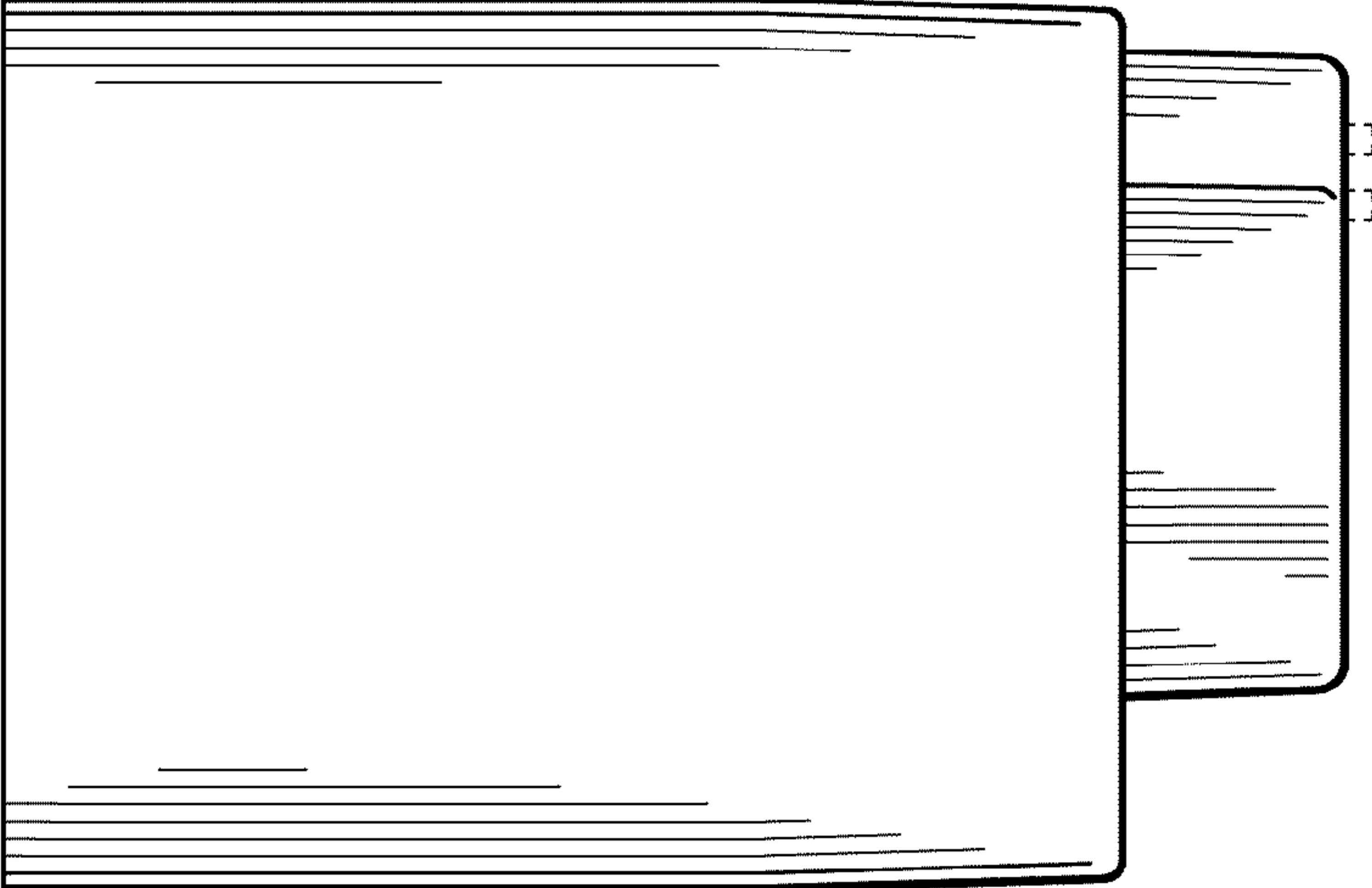


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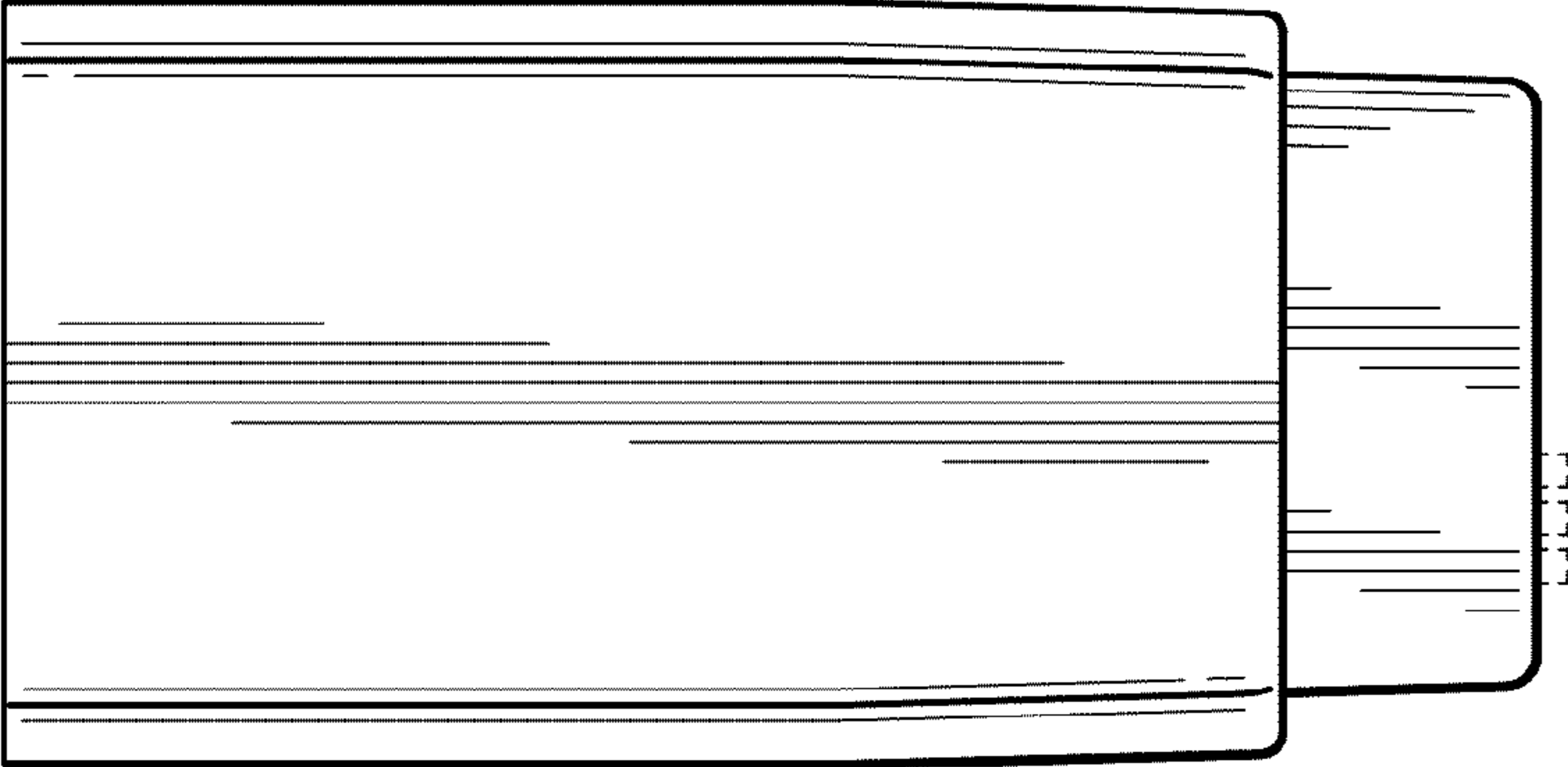


Figure 93

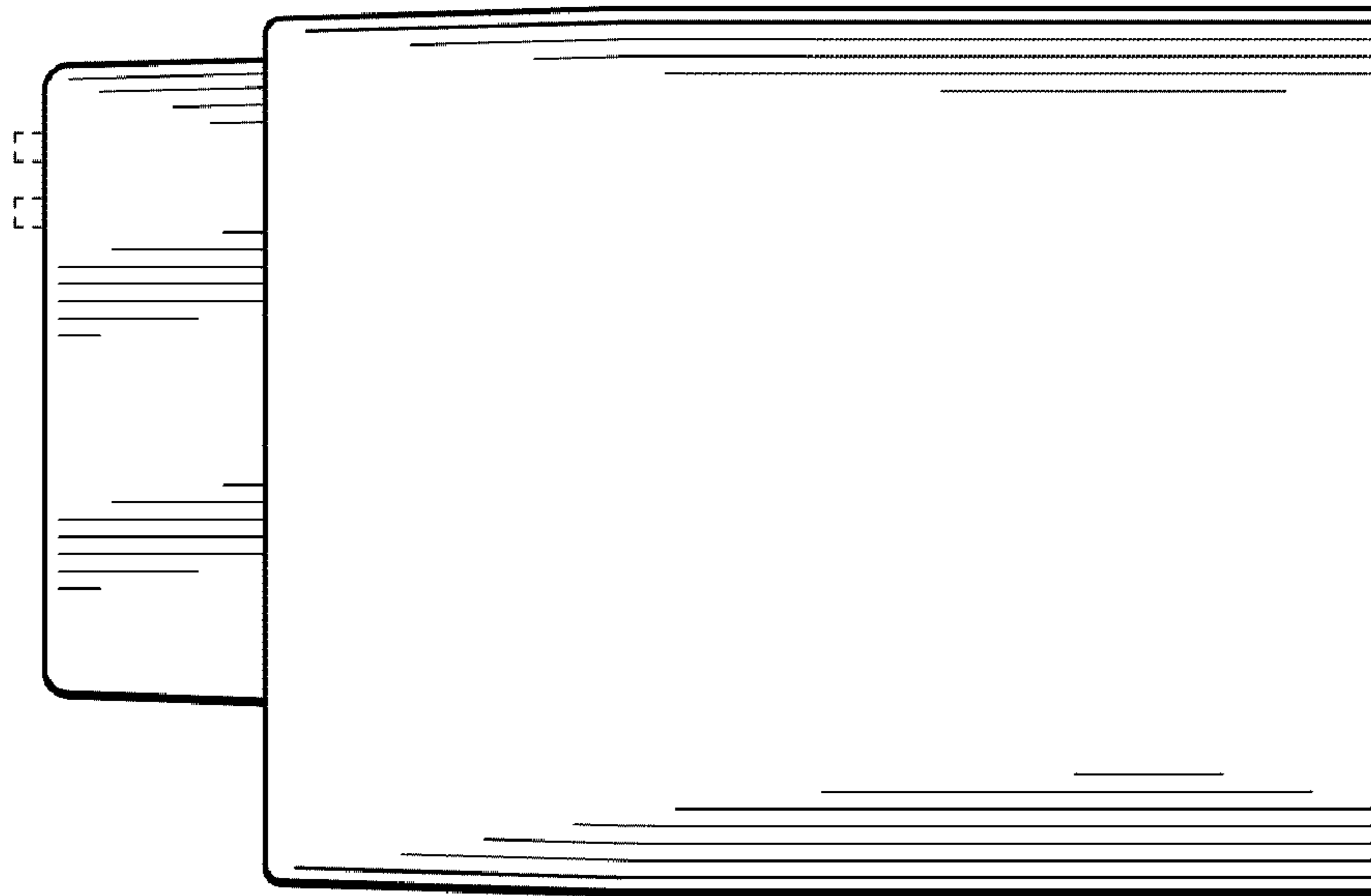


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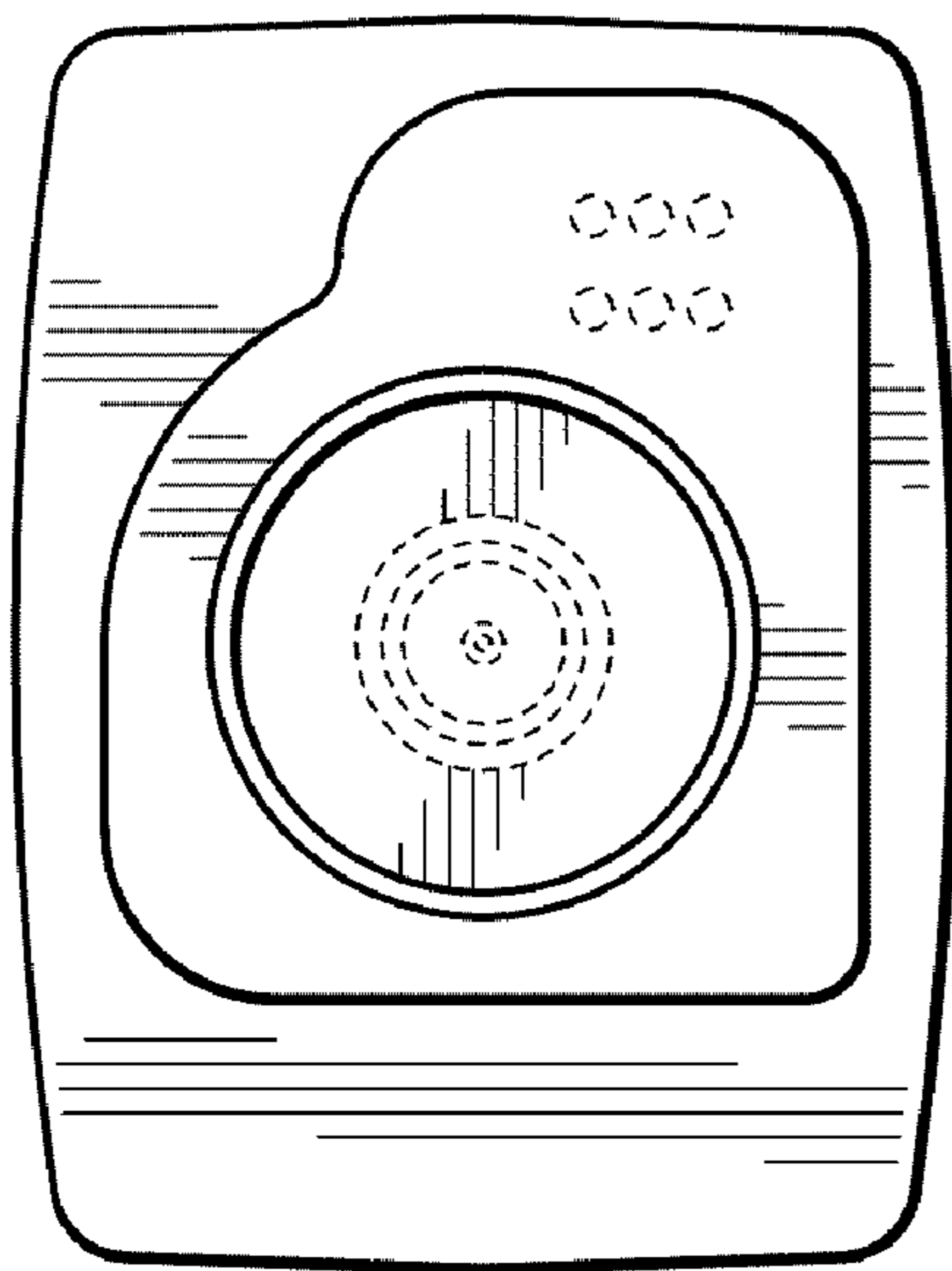


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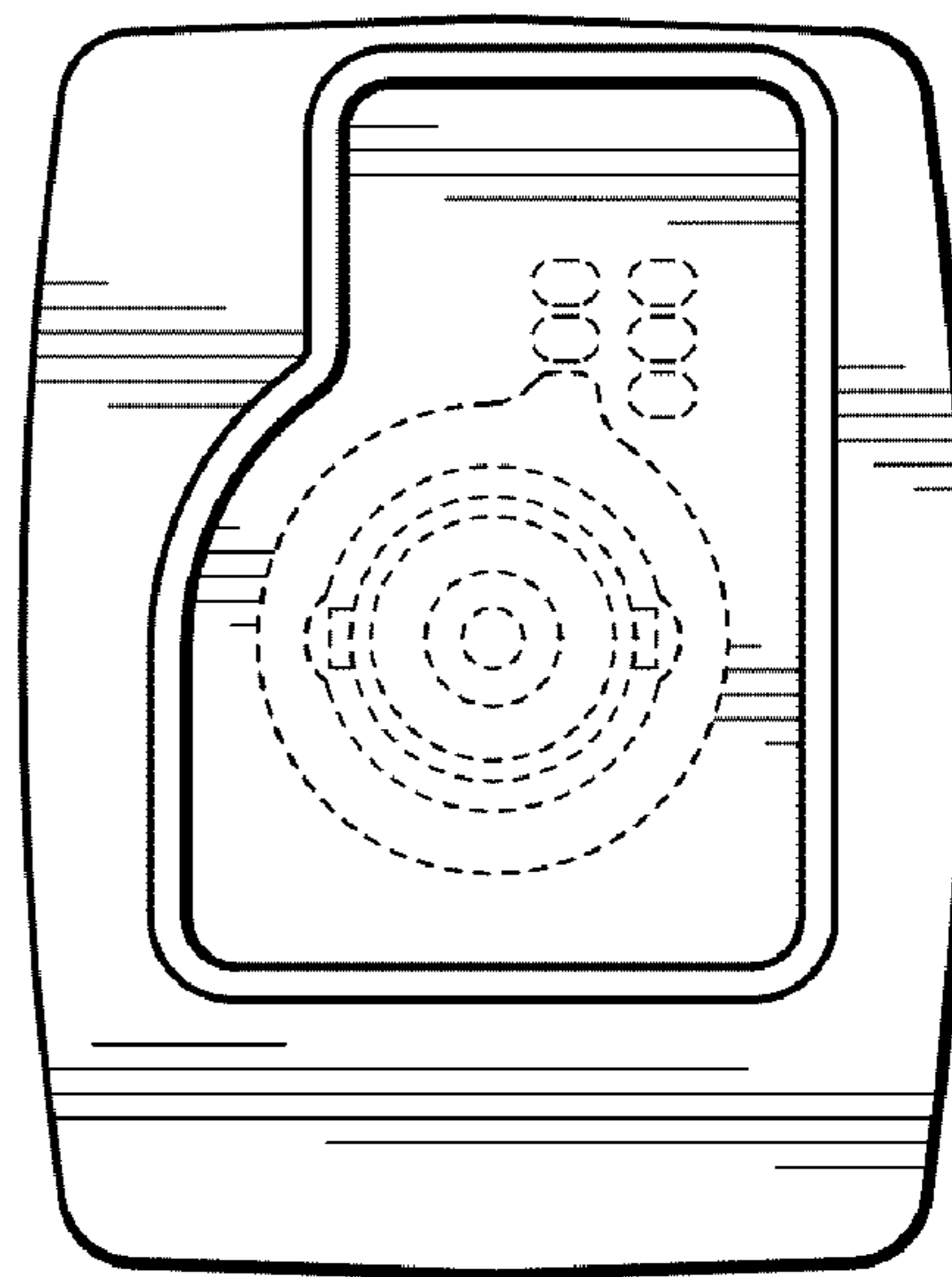


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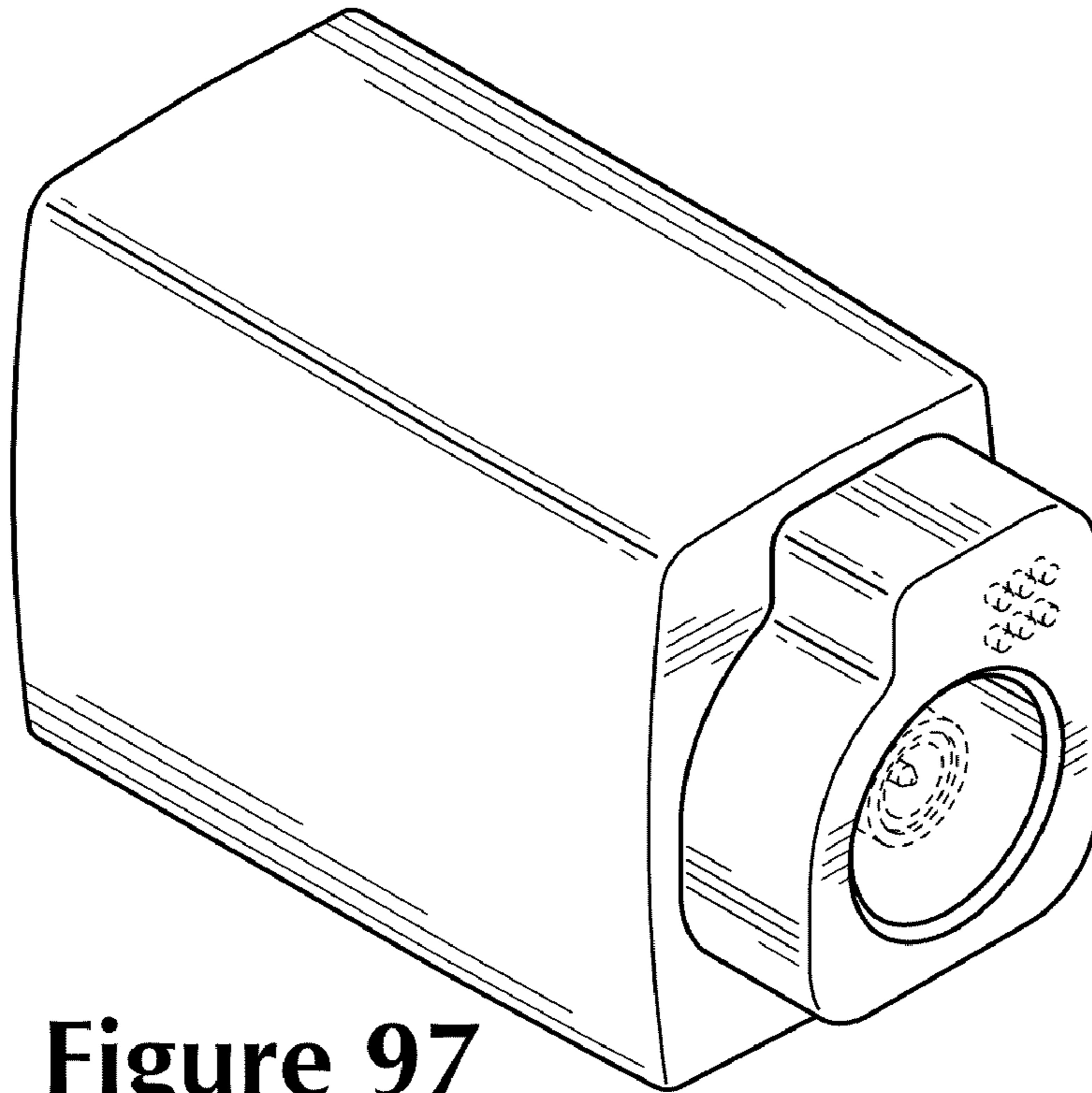


Figure 97

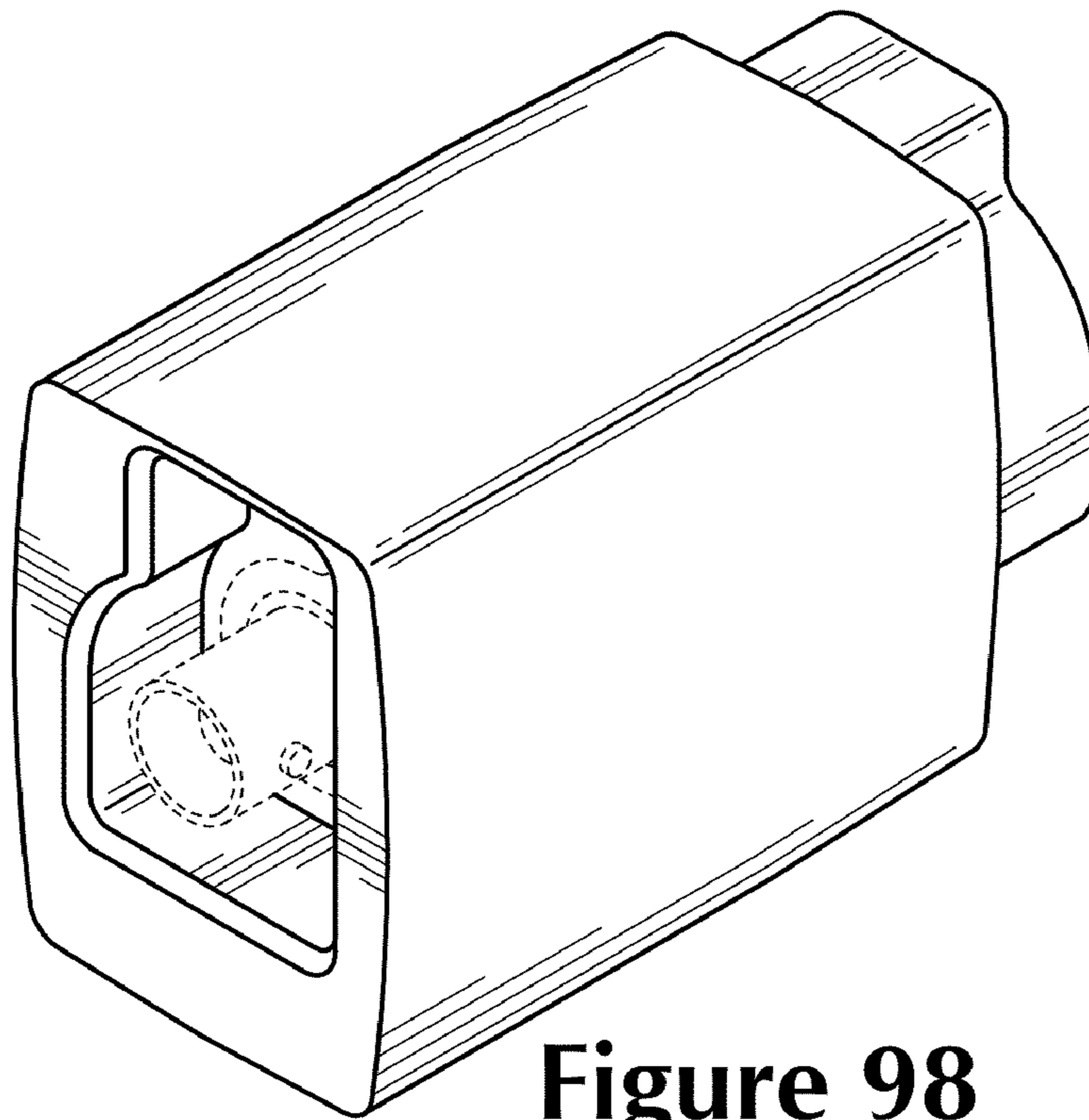


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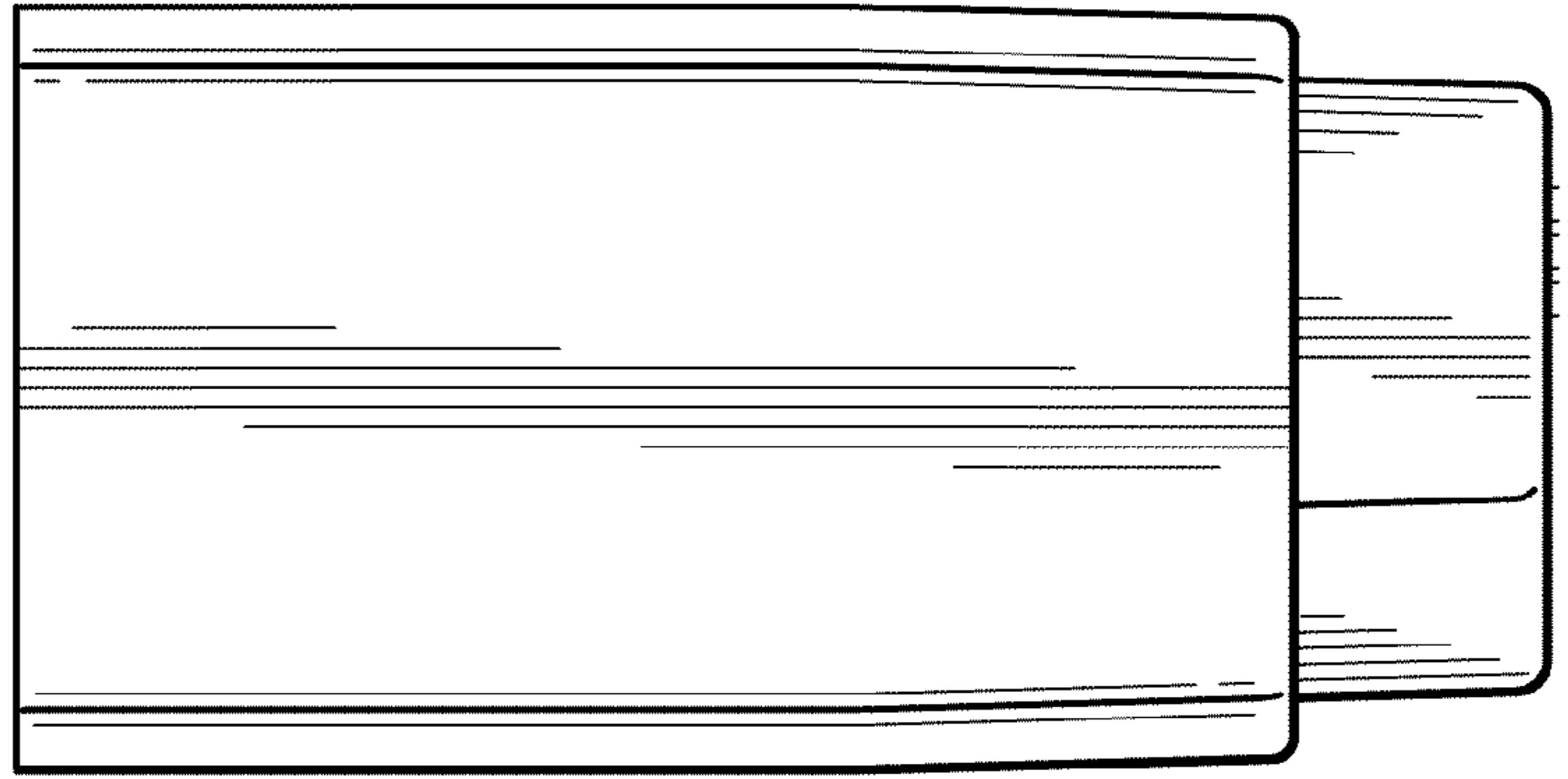


Figure 99

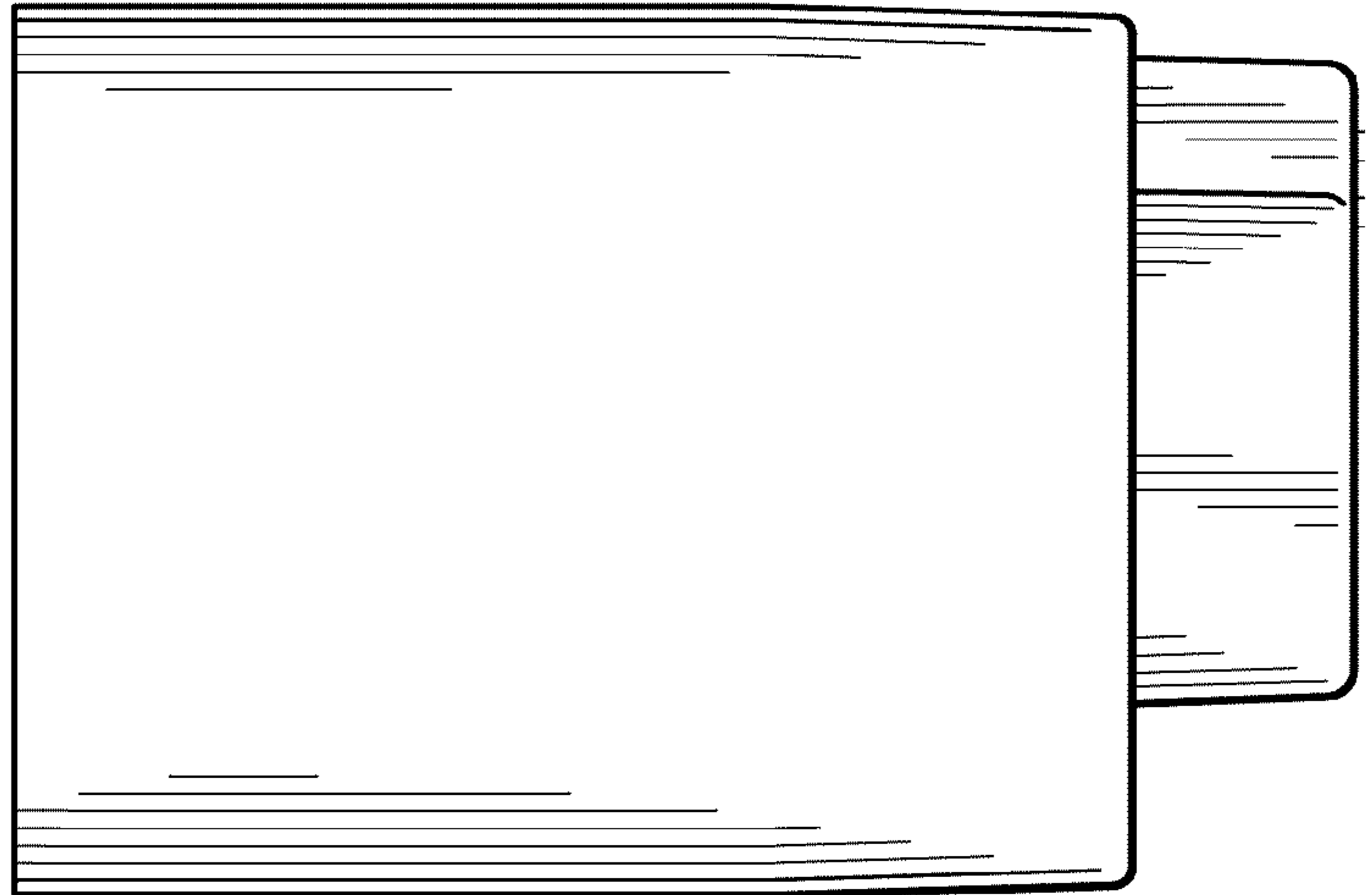


Figure 100

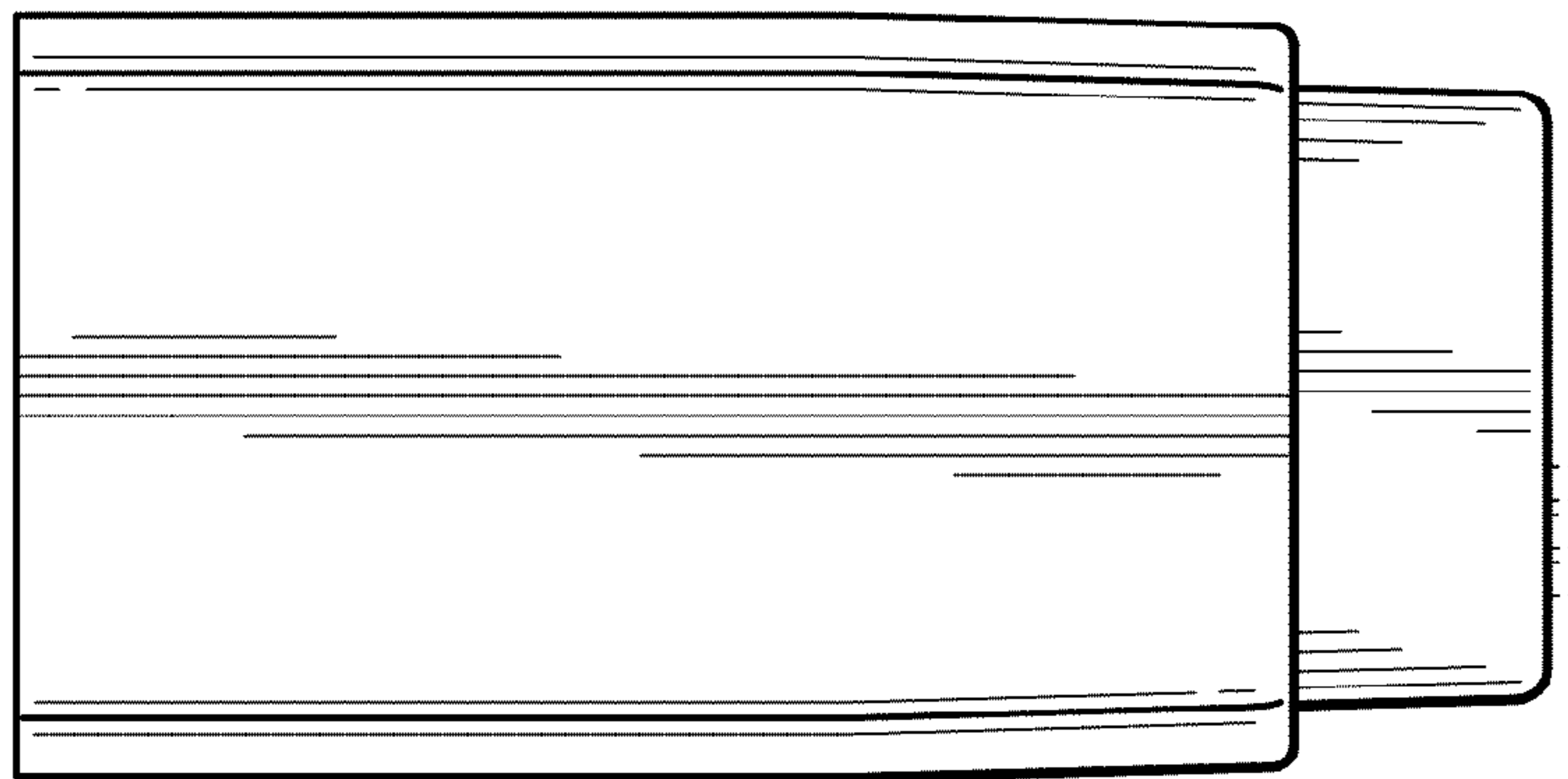


Figure 101

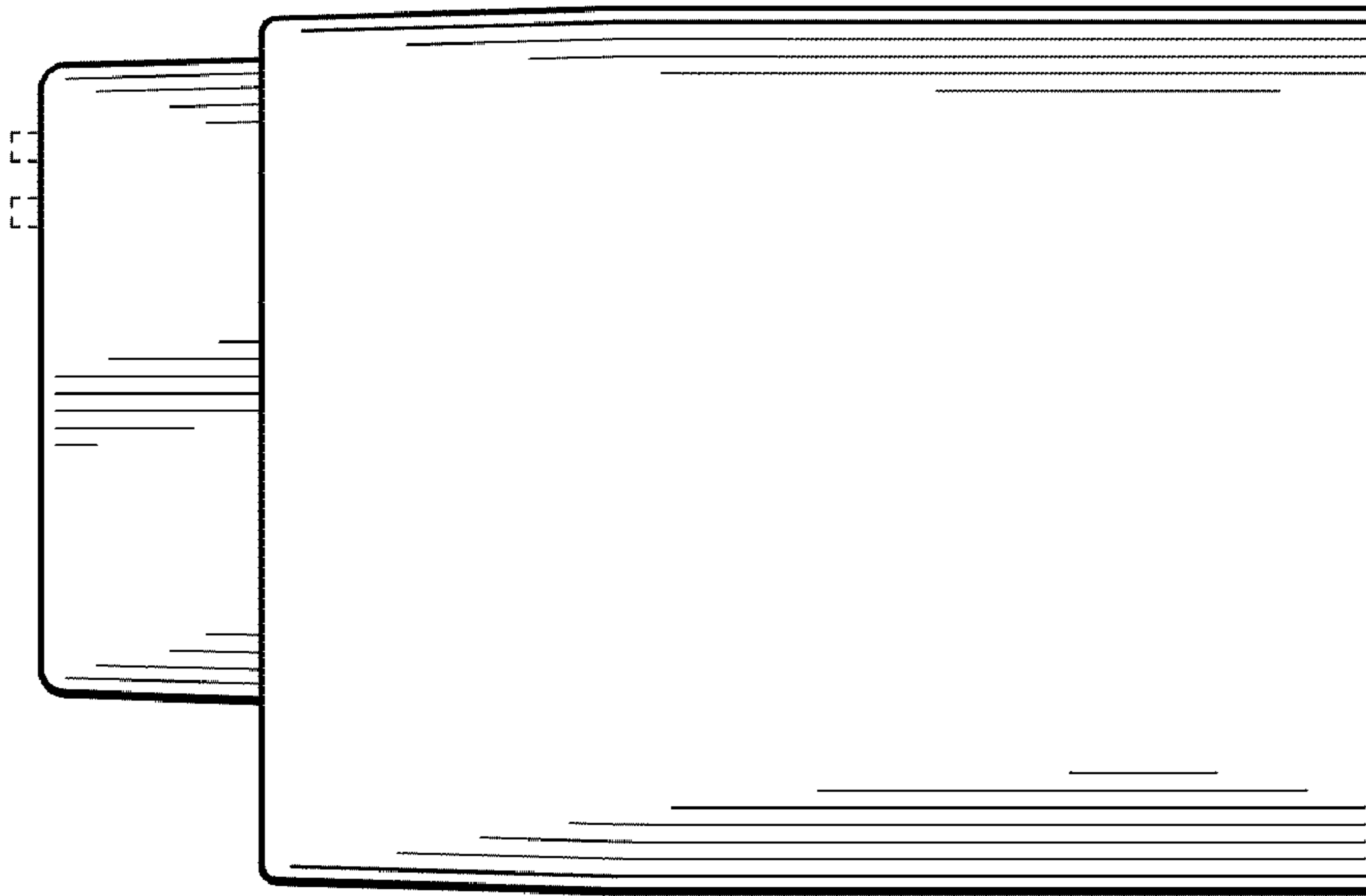


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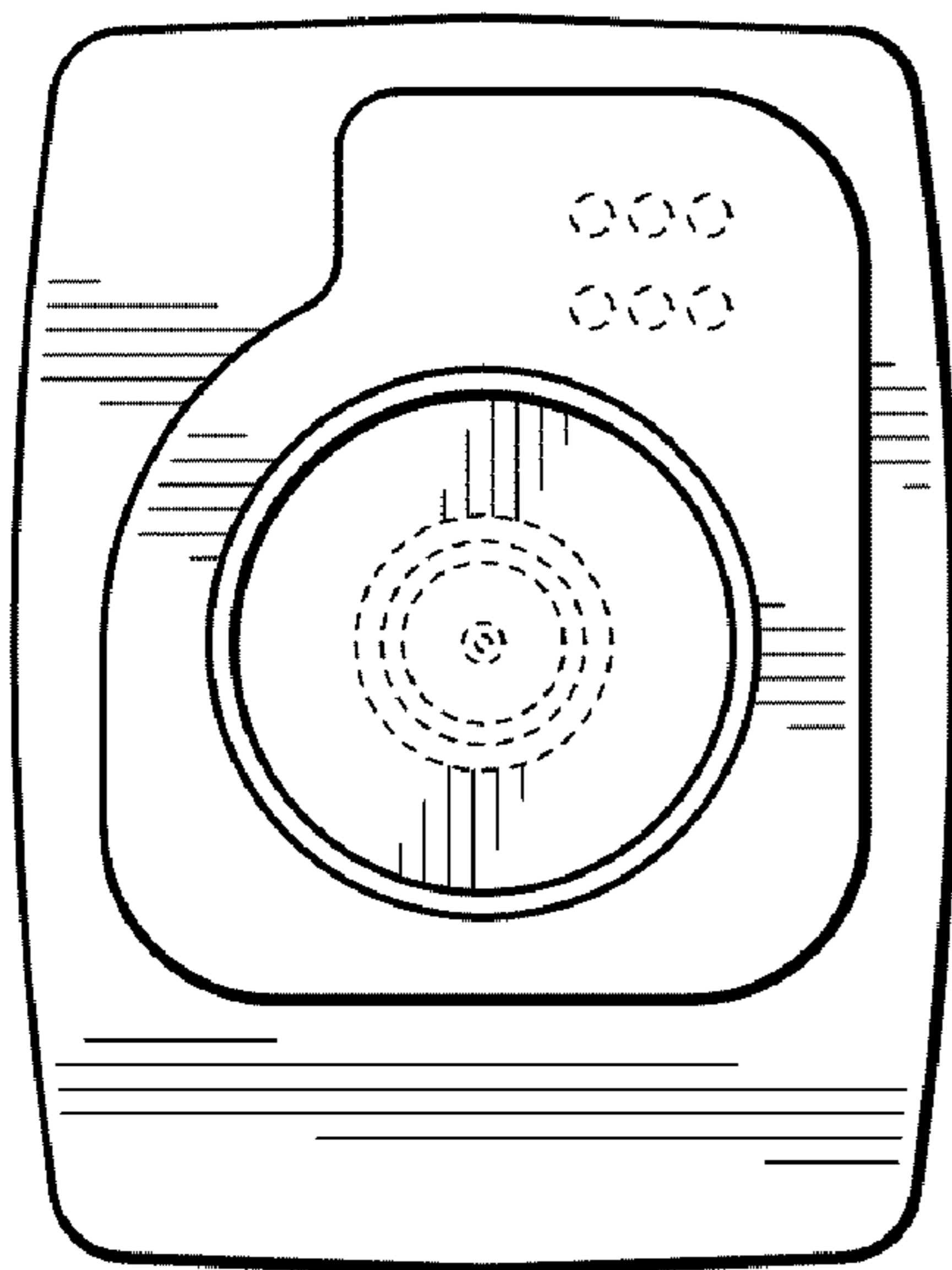


Figure 103

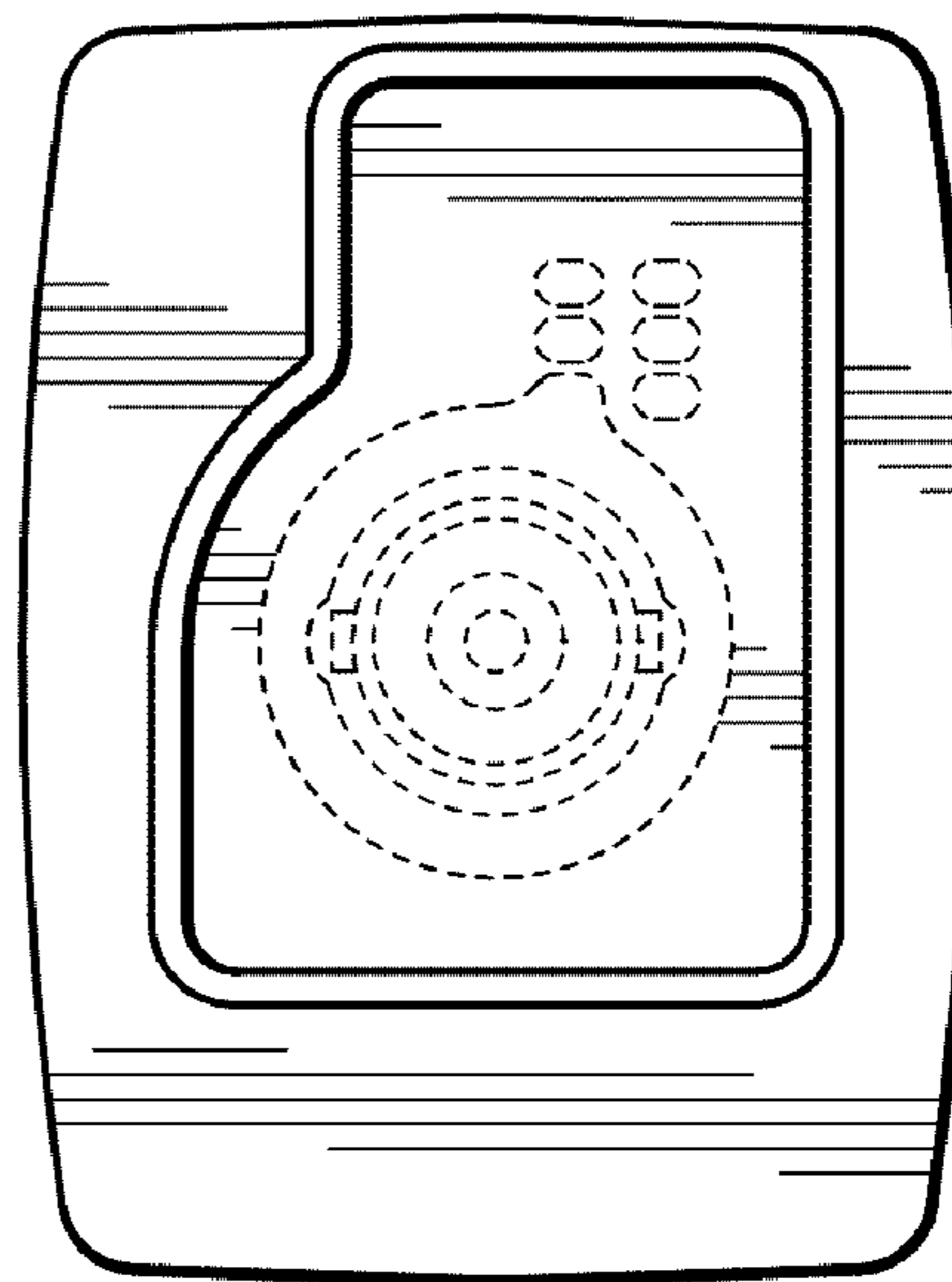


Figure 104

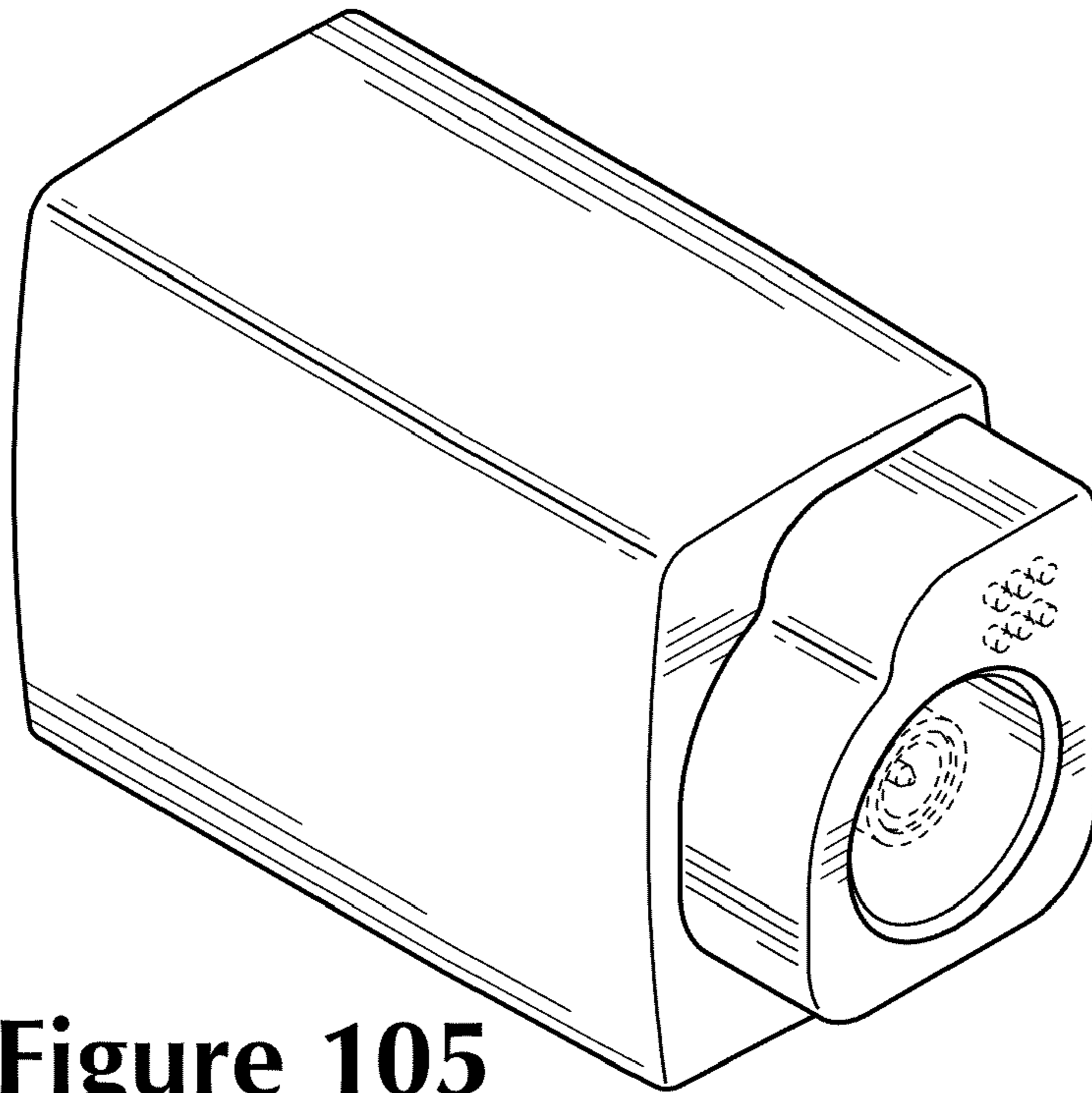


Figure 105

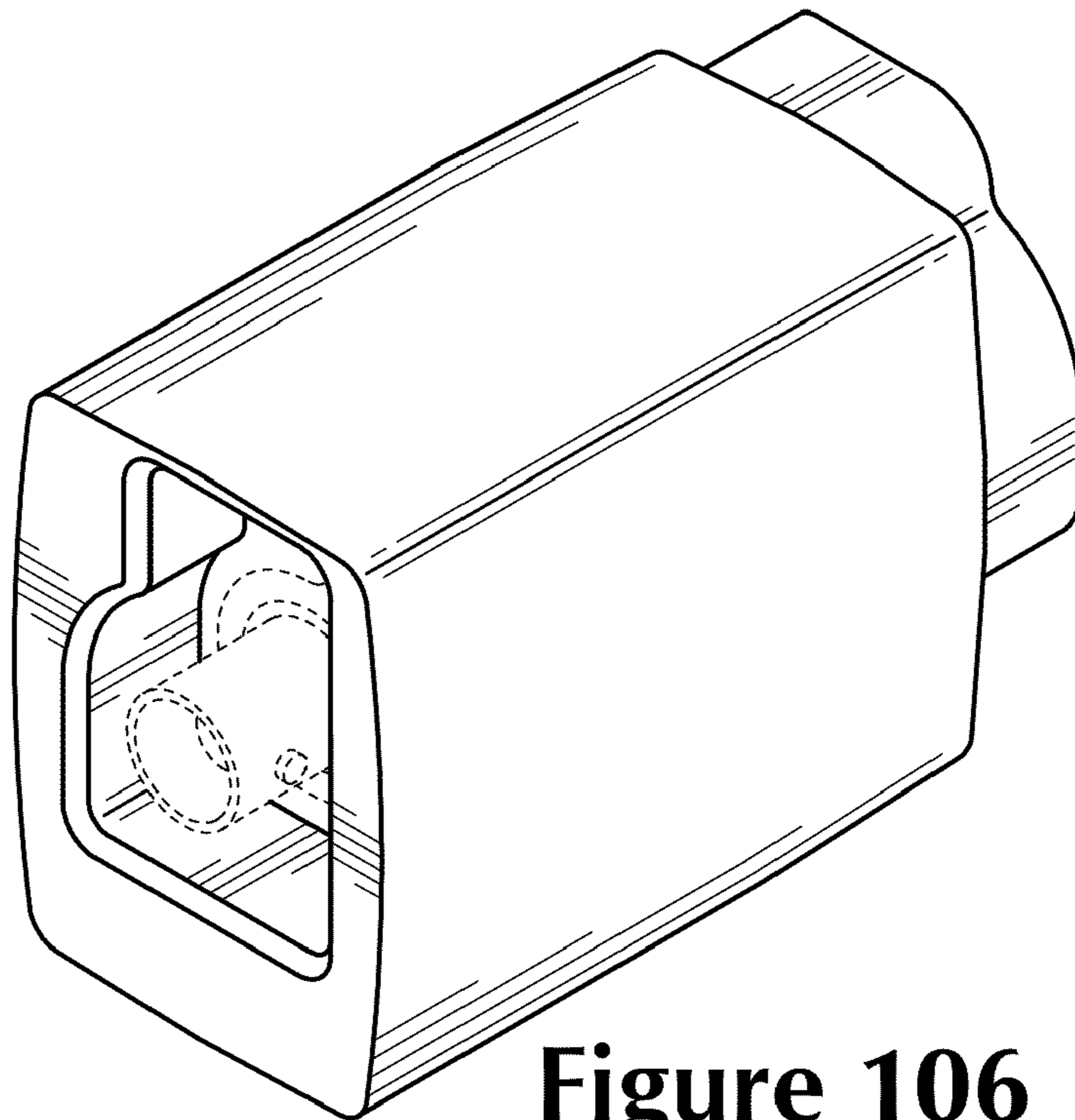


Figure 106

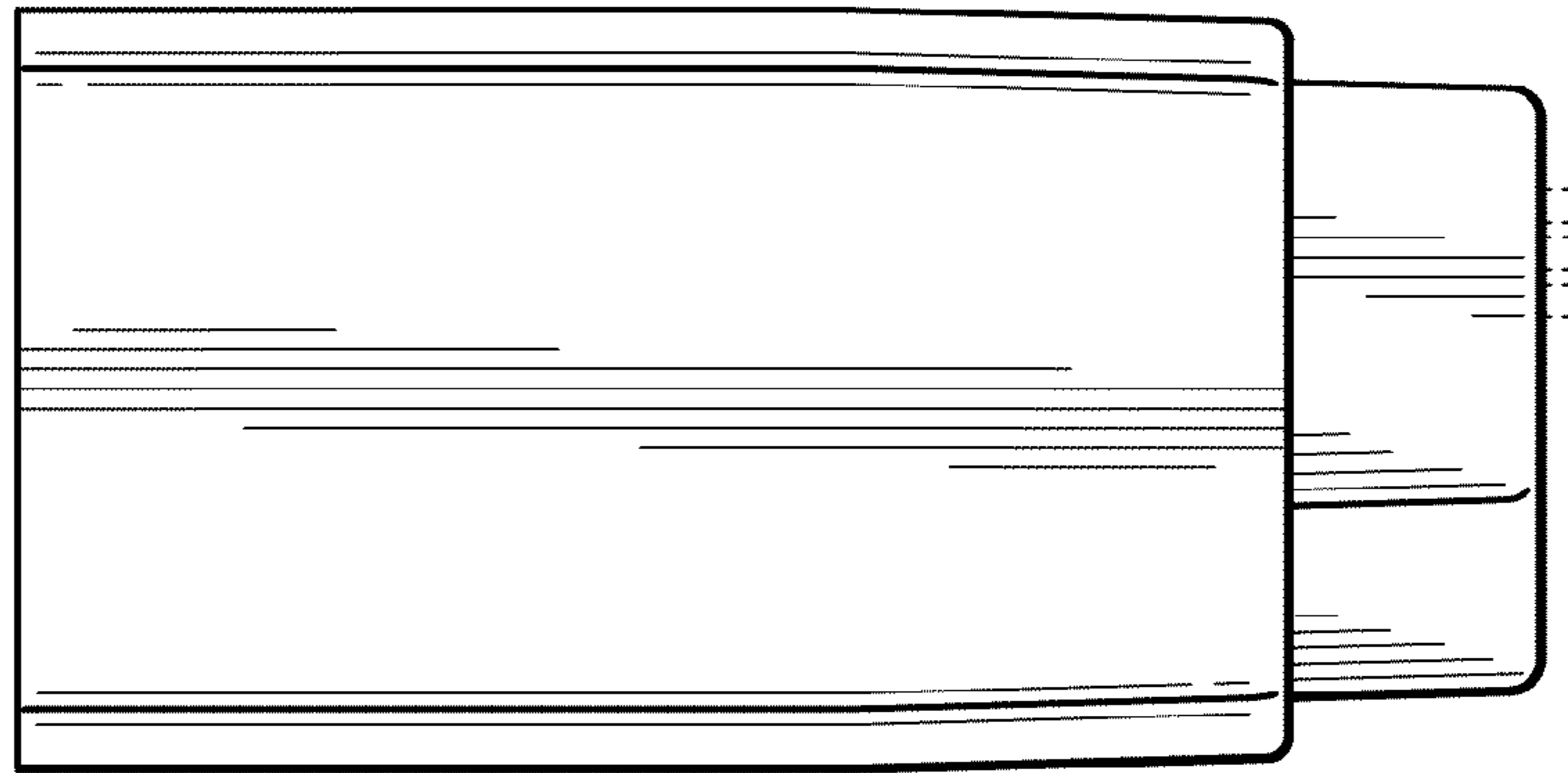


Figure 107

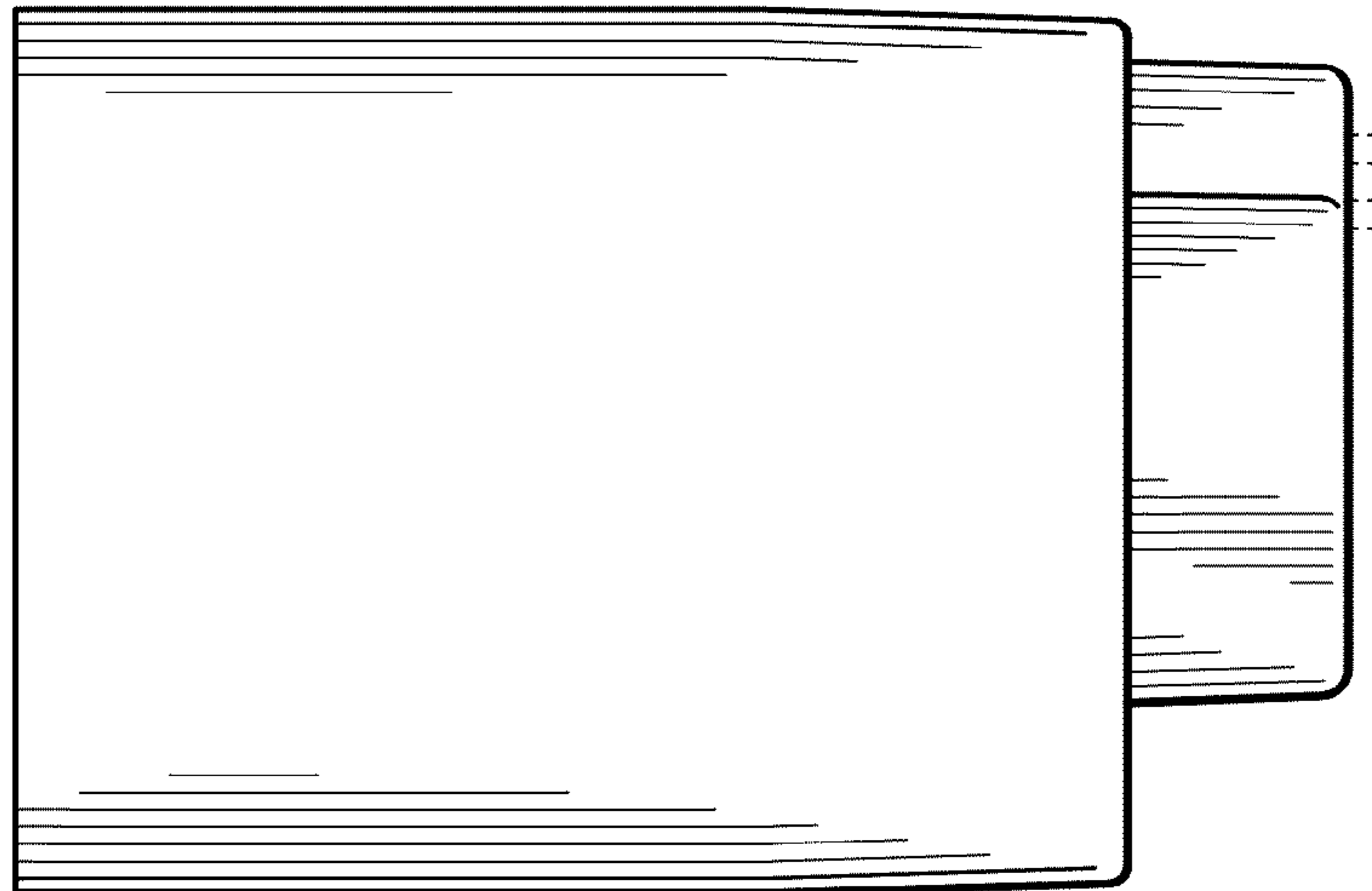


Figure 108

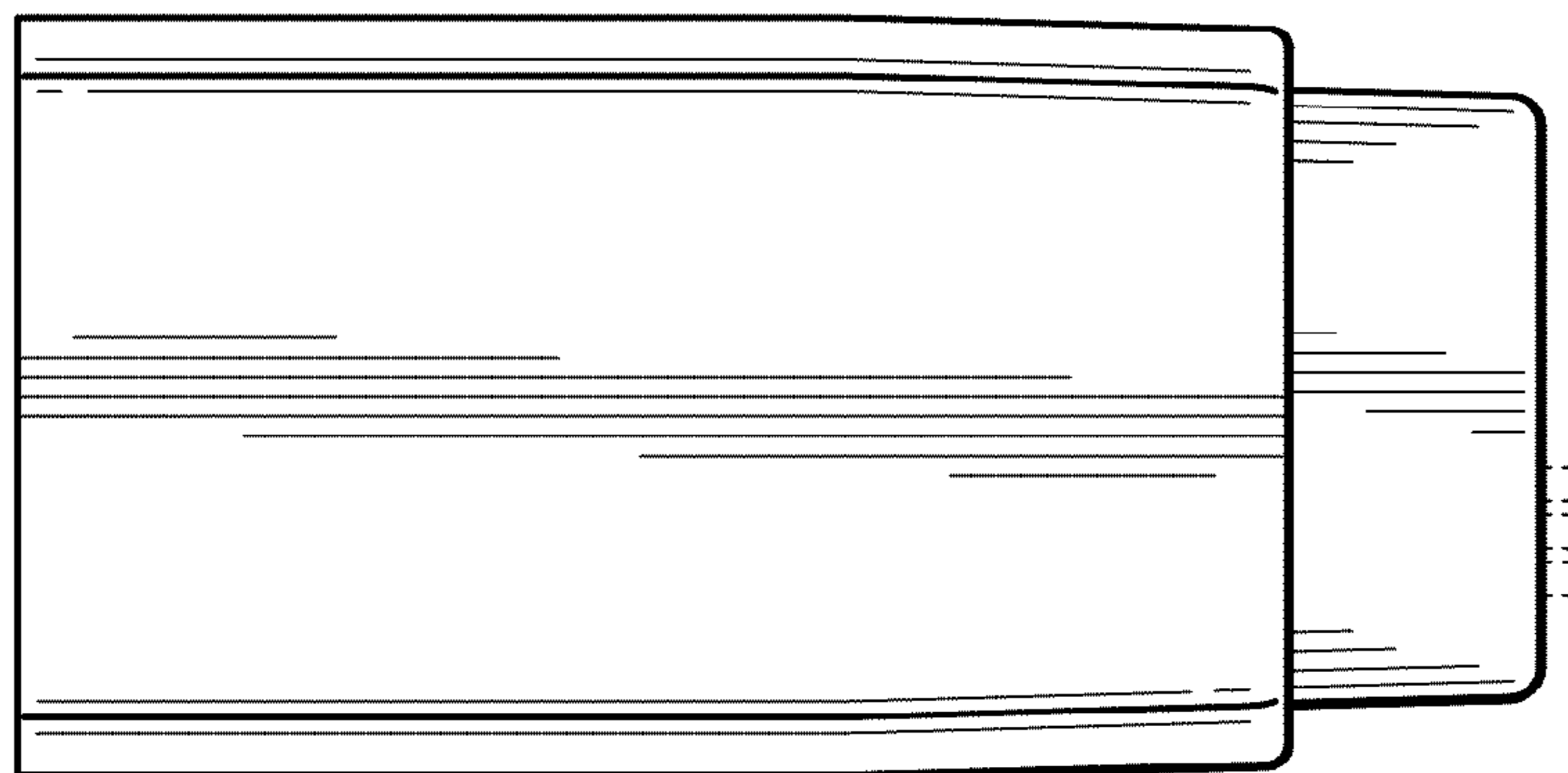


Figure 109

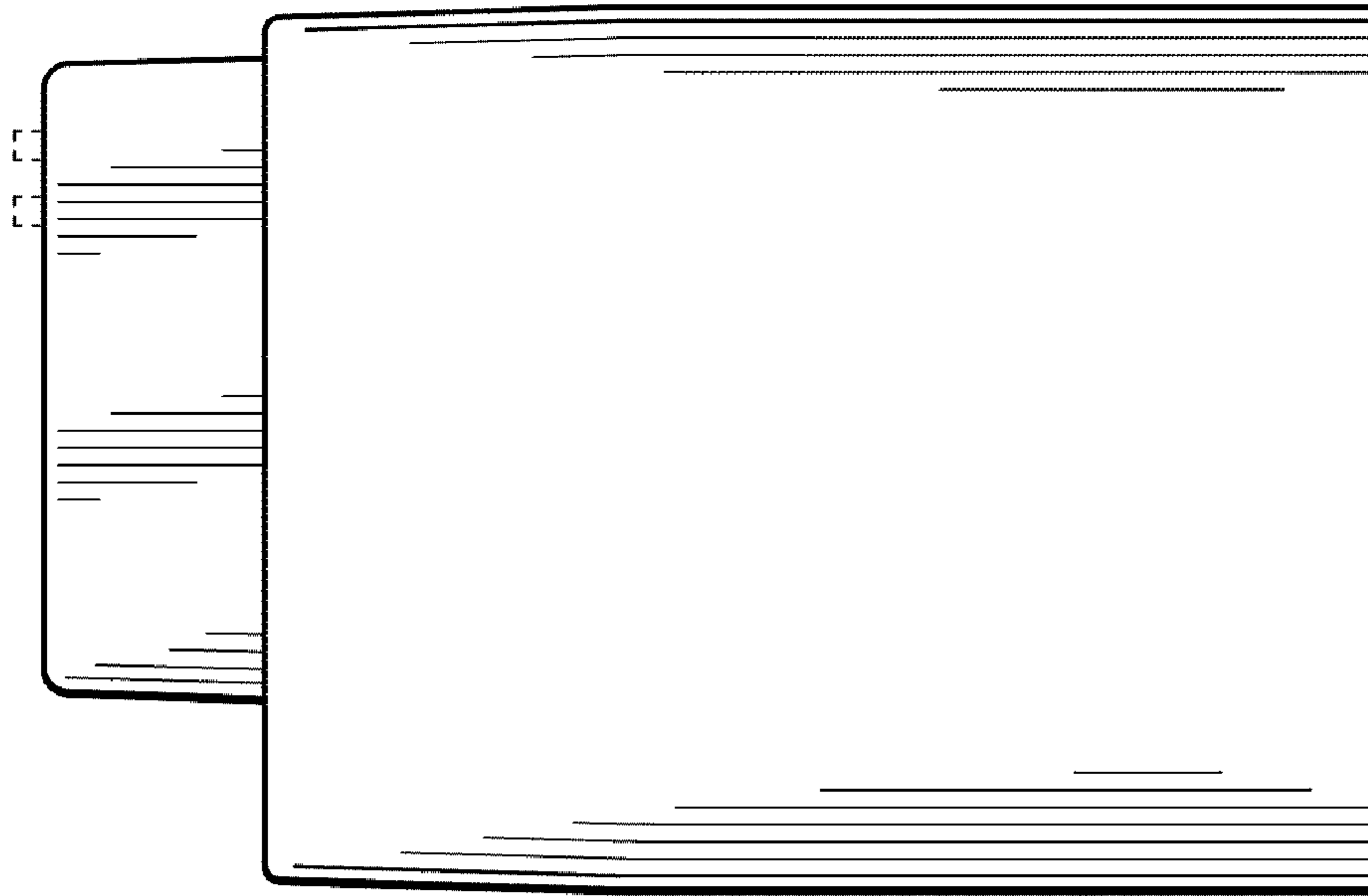


Figure 110

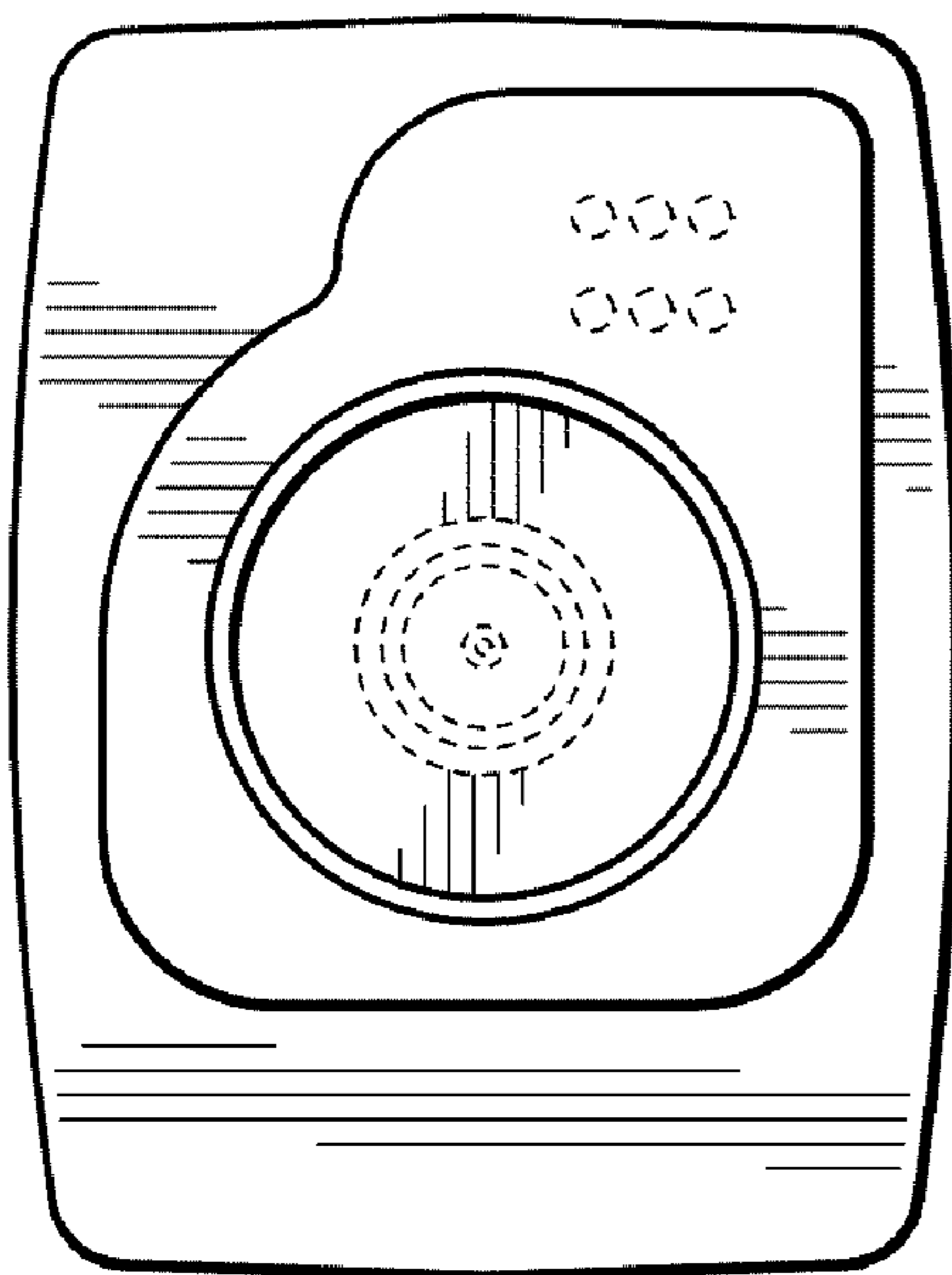


Figure 111

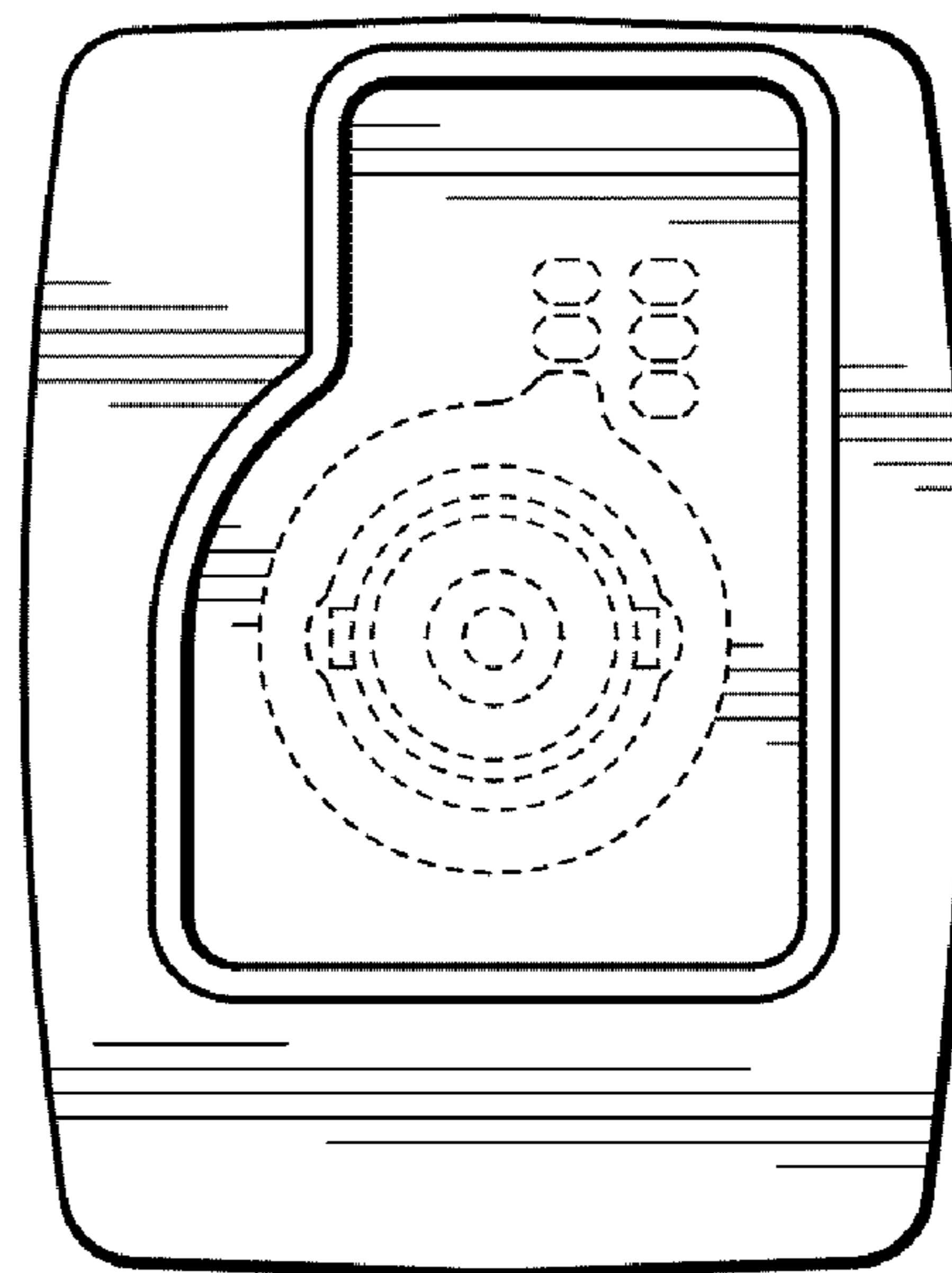


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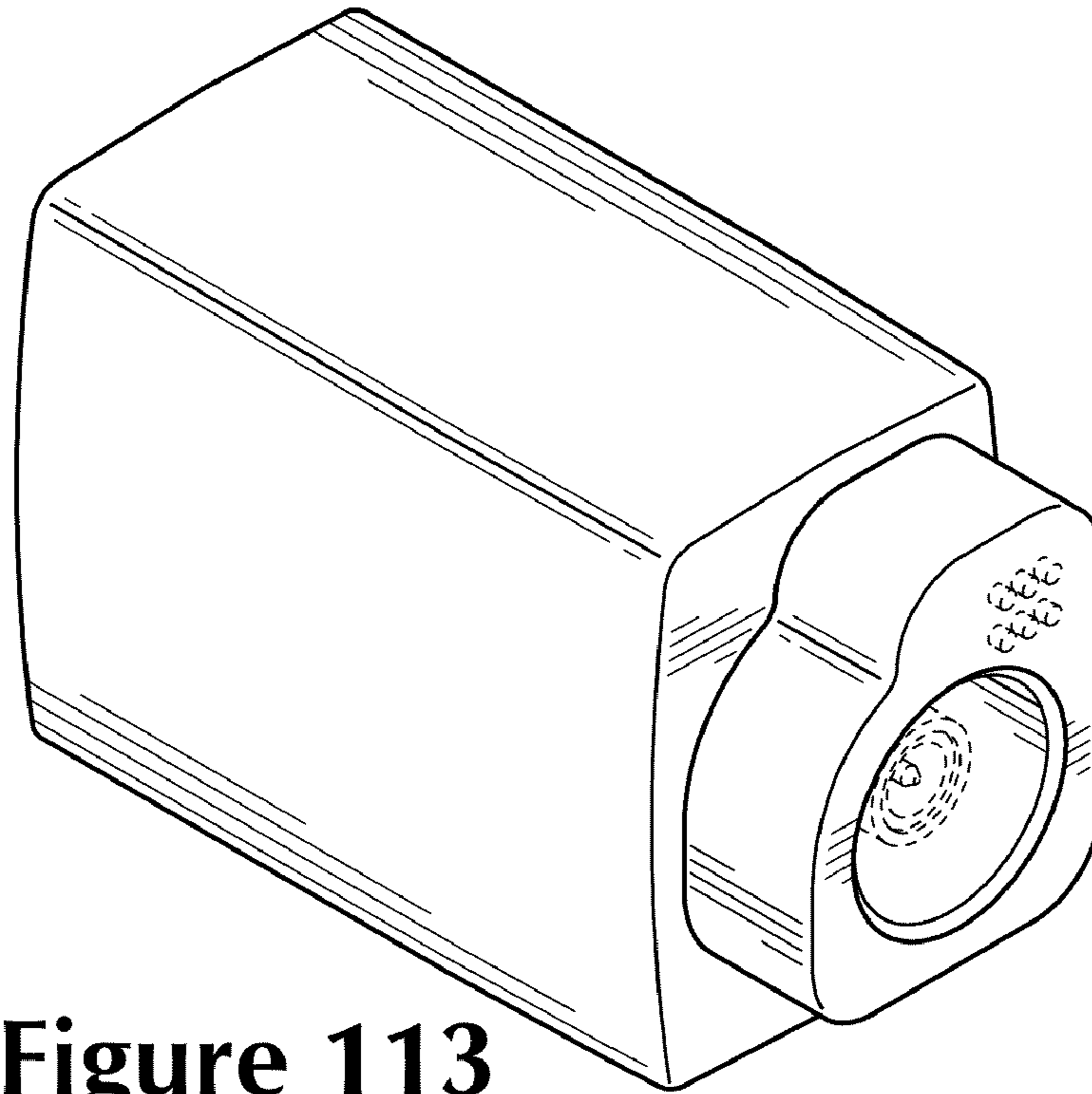


Figure 113

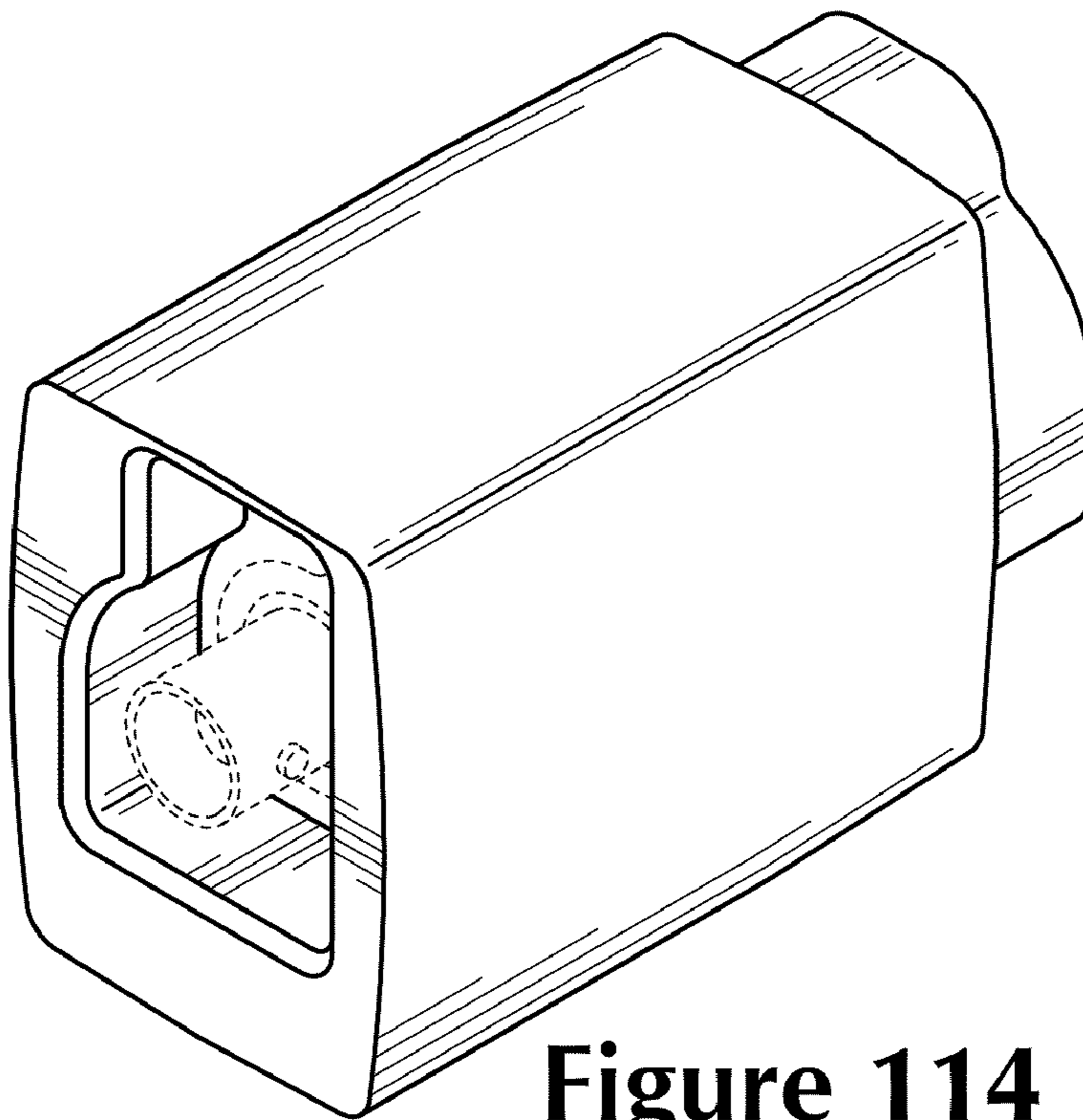


Figure 114

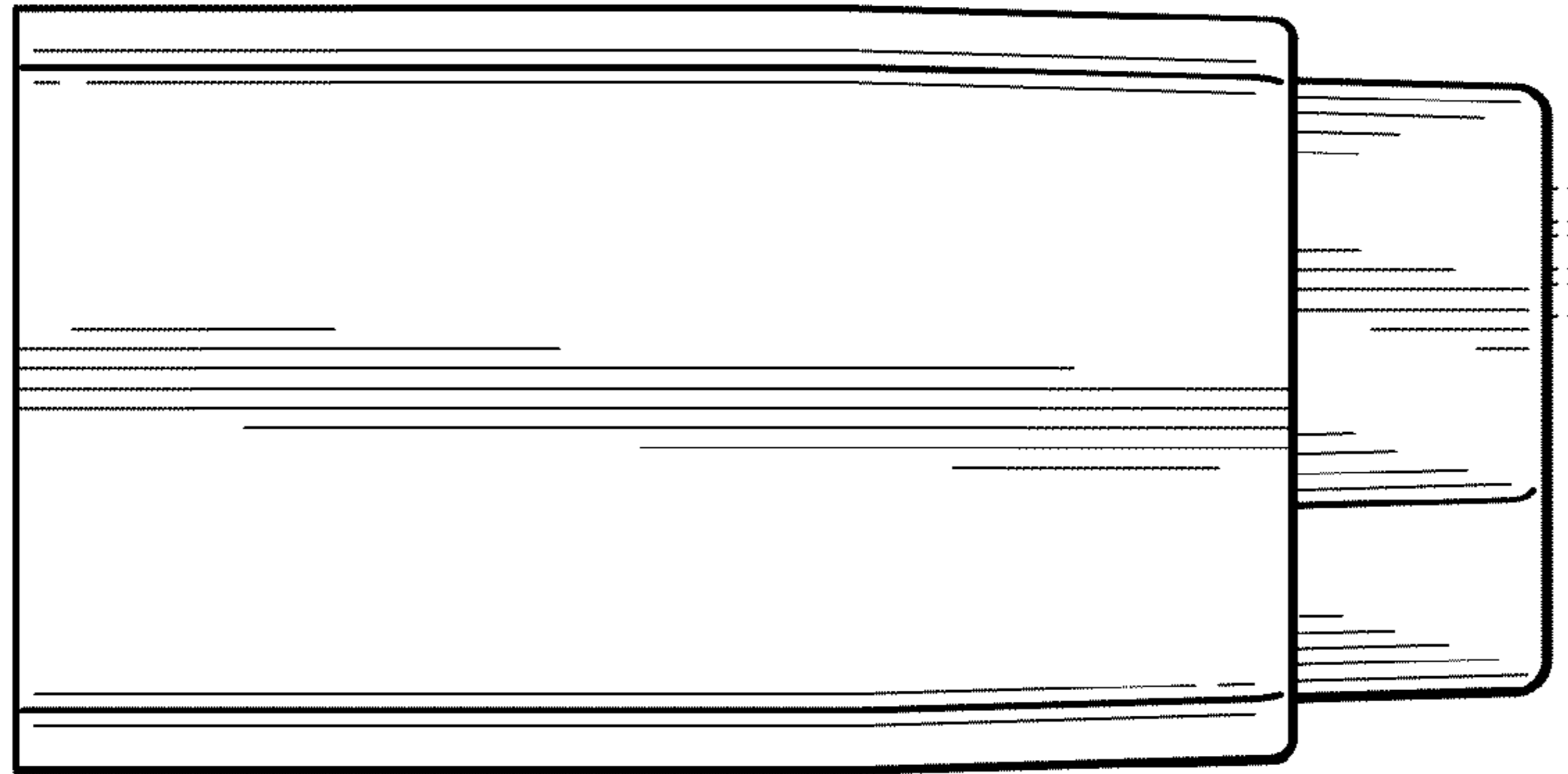


Figure 115

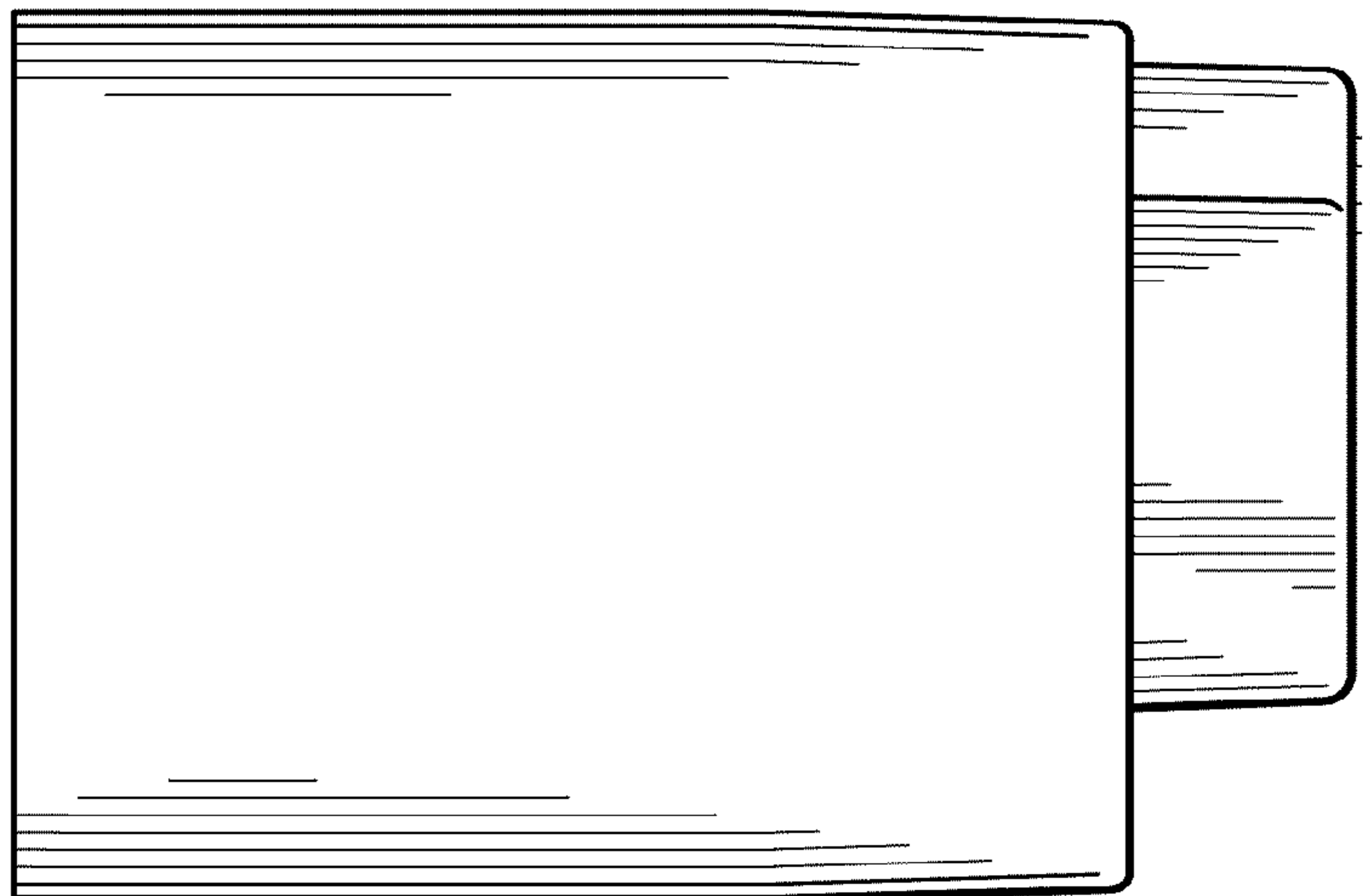


Figure 116

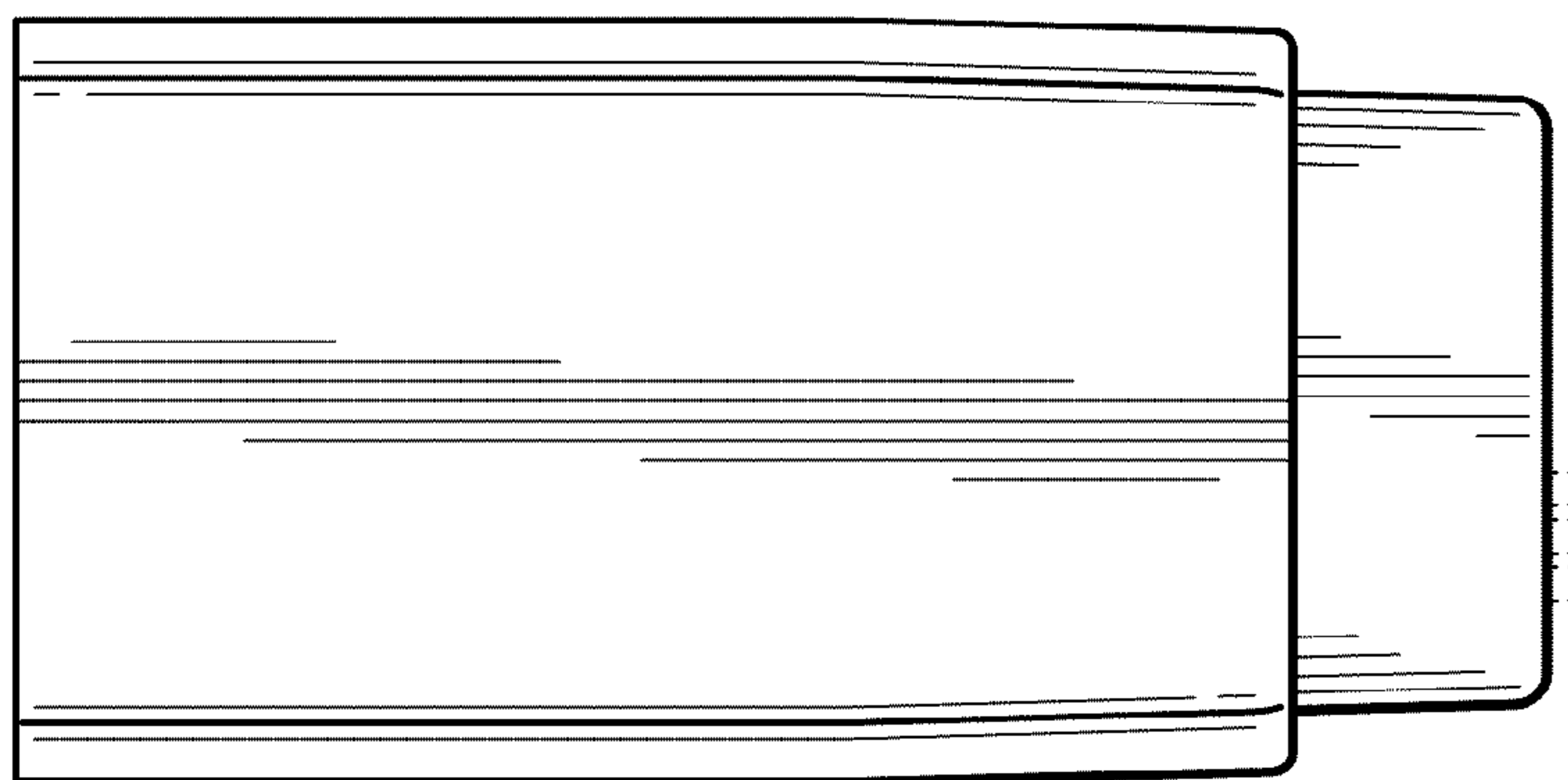


Figure 117

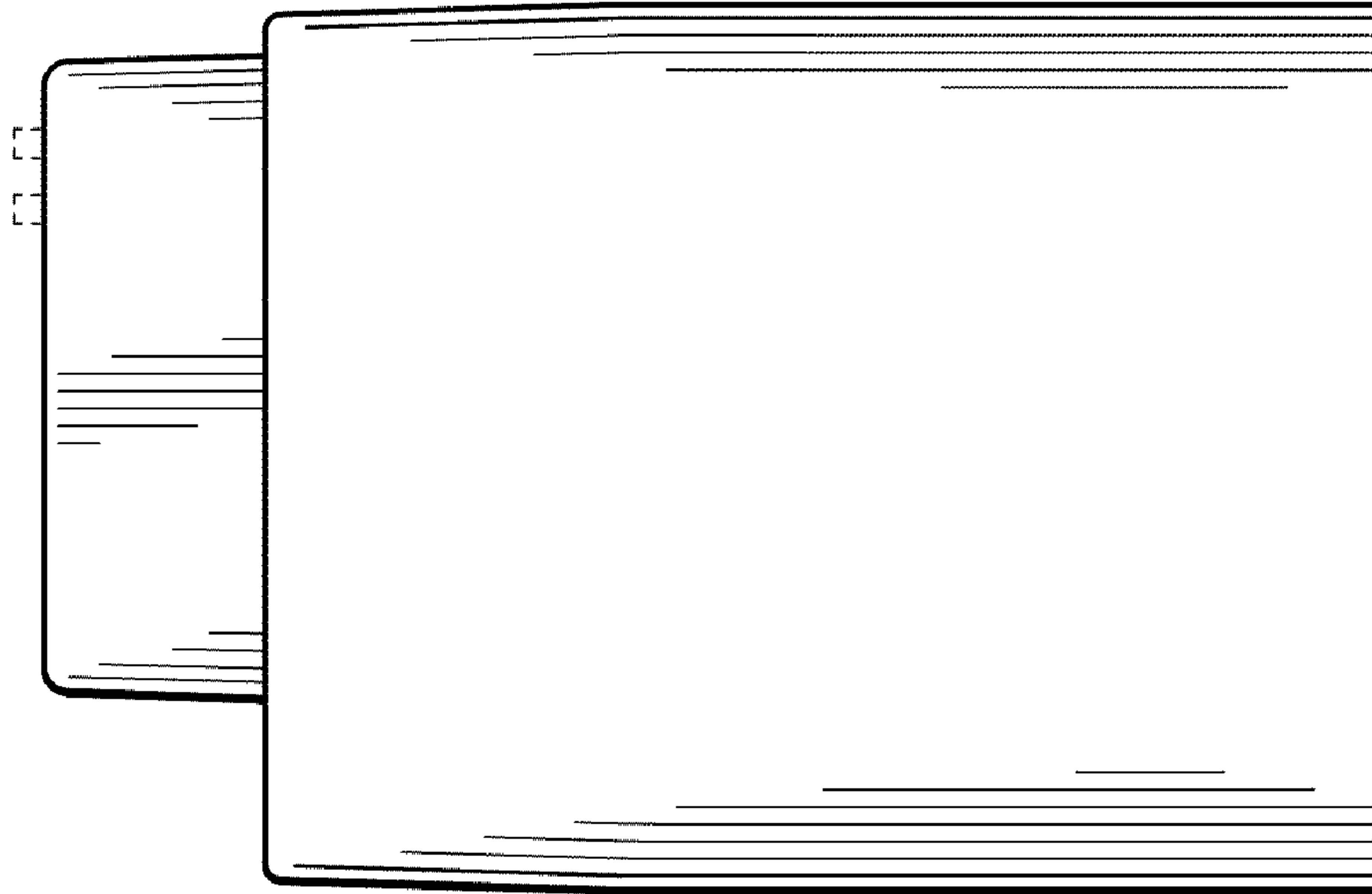


Figure 118

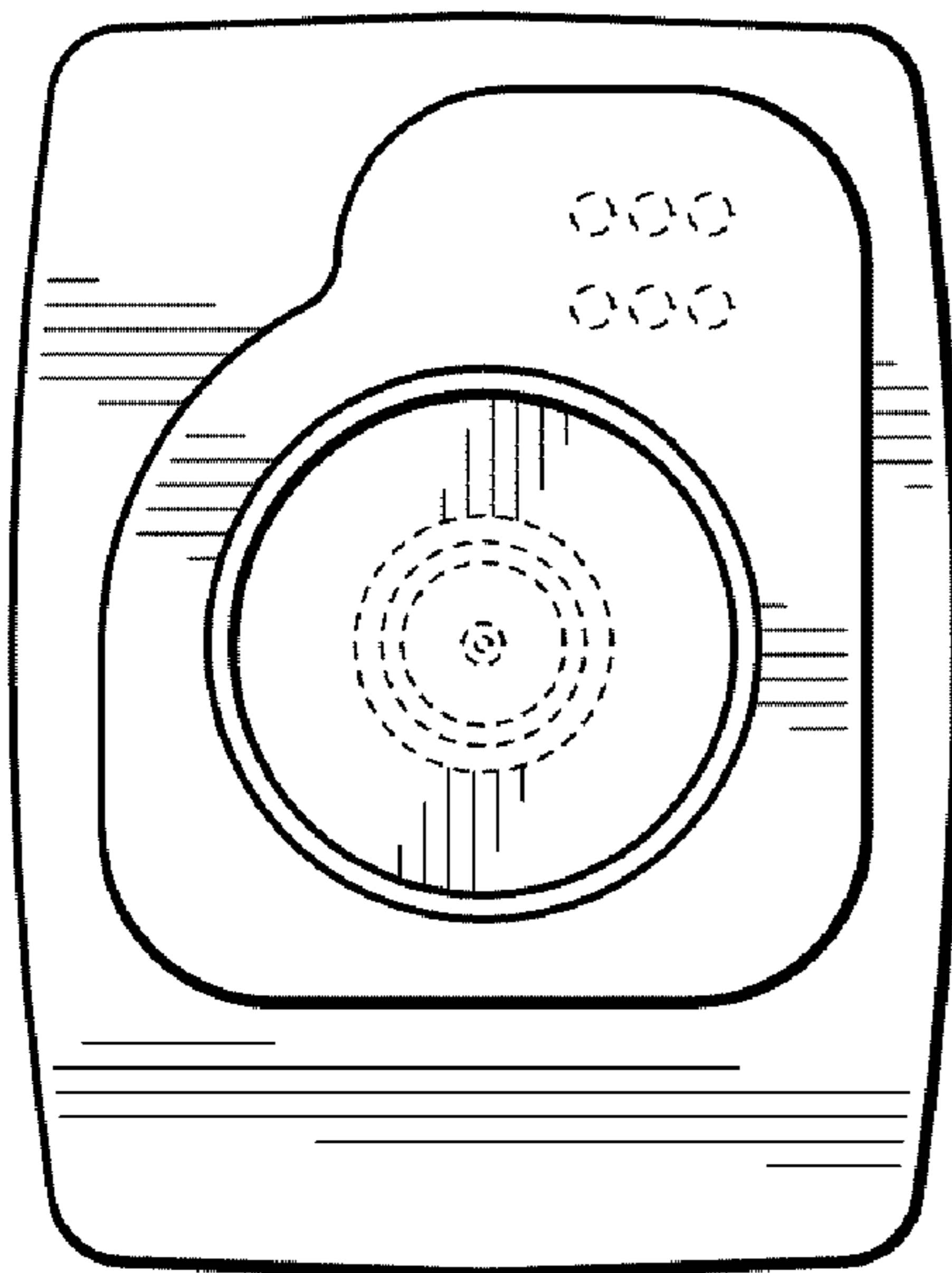


Figure 119

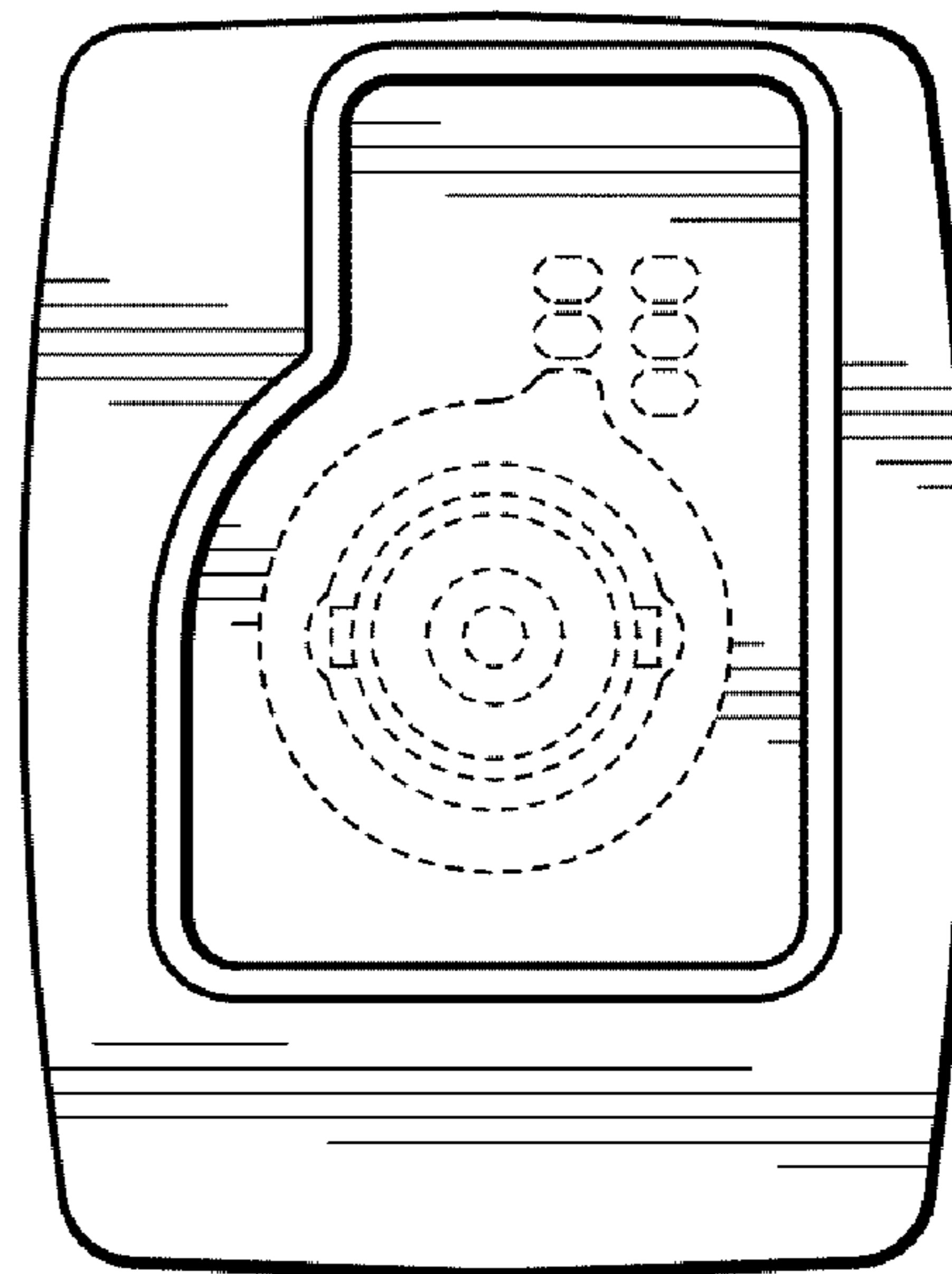


Figure 120