



US00D648507S

(12) **United States Design Patent**
Haigh

(10) **Patent No.:** **US D648,507 S**

(45) **Date of Patent:** **** Nov. 15, 2011**

(54) **WAFER**

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(**) **Term:** **14 Years**

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Related U.S. Application Data

(62) Division of application No. 29/335,472, filed on Apr. 16, 2009, now Pat. No. Des. 611,226.

(51) **LOC (9) Cl.** **01-01**

(52) **U.S. Cl.** **D1/128**

(58) **Field of Classification Search** D1/100-199;
D24/102; D28/8.2; 426/89, 94-95, 100-104,
426/293, 302, 305-307, 383, 438-439, 512,
426/516-518, 808, 138, 543, 549-550, 496
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a wafer, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a wafer embodying my new design;

FIG. 2 is a front view thereof;

FIG. 3 is a rear view thereof;

FIG. 4 is a left side view thereof;

FIG. 5 is a right side view thereof;

FIG. 6 is a top view thereof;

FIG. 7 is a bottom view thereof;

FIG. 8 is a side perspective view of a second embodiment of a wafer embodying my new design;

FIG. 9 is a front view thereof;

FIG. 10 is a rear view thereof;

FIG. 11 is a left side view thereof;

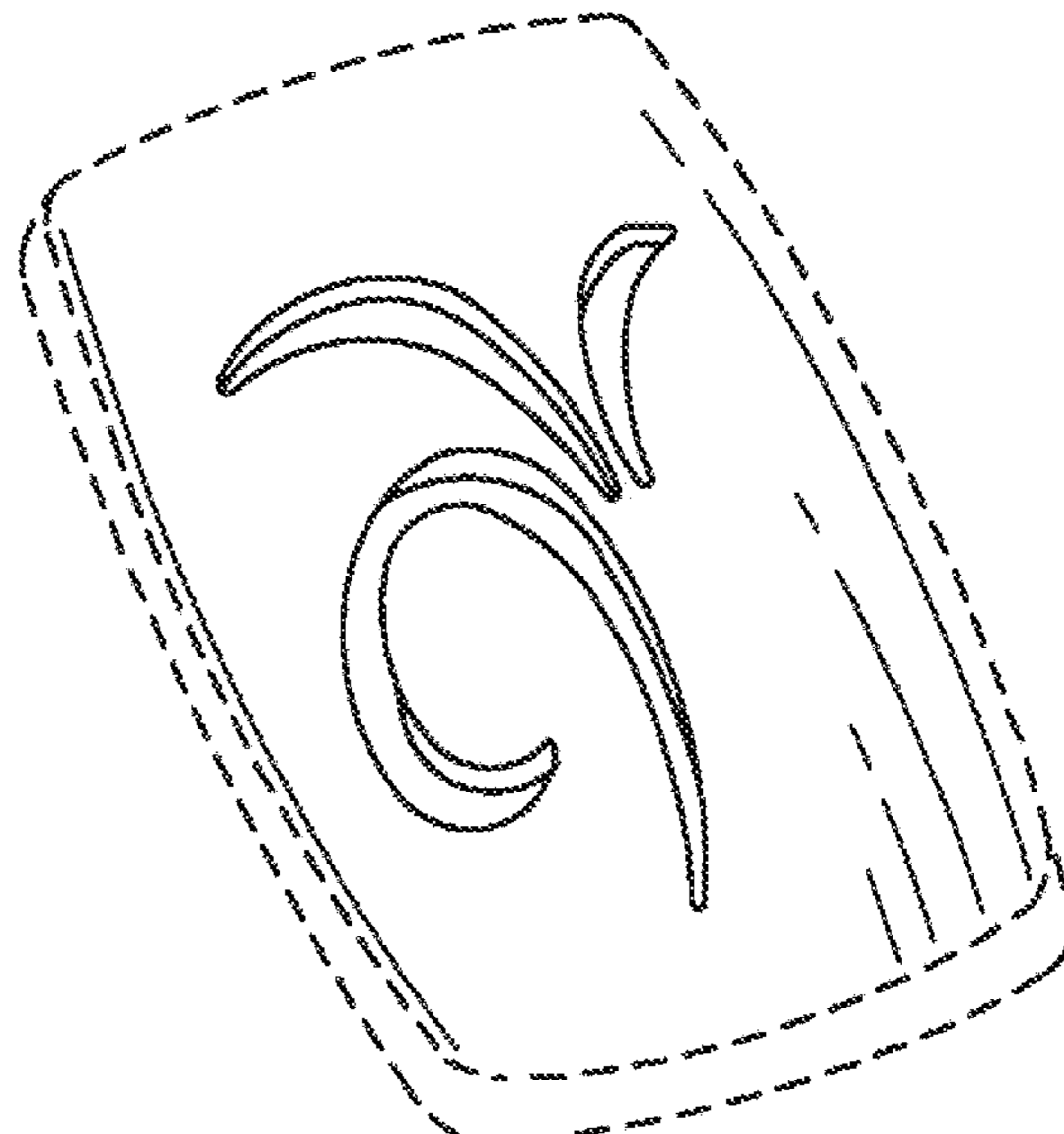
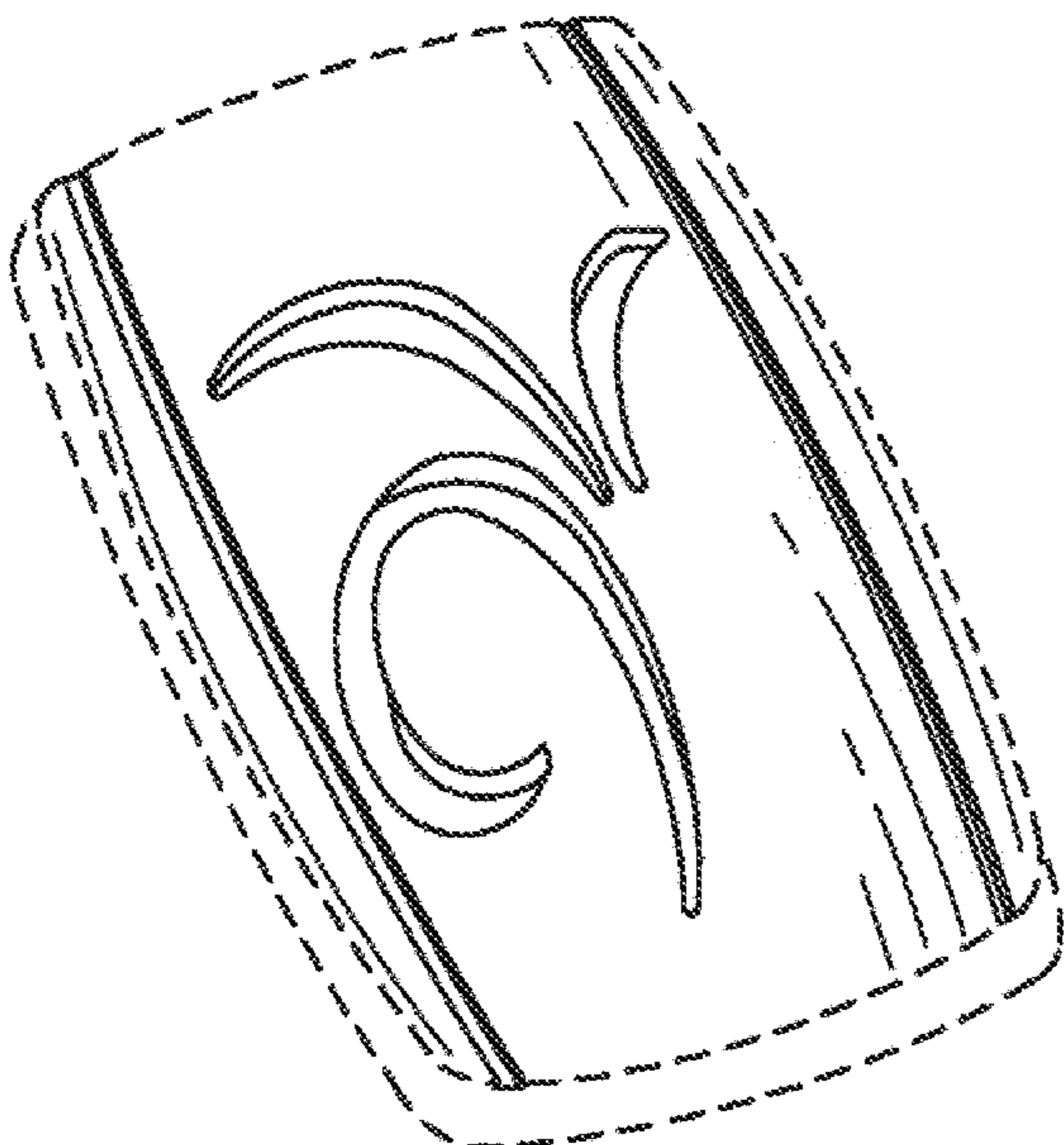
FIG. 12 is a right side view thereof;

FIG. 13 is a top view thereof; and,

FIG. 14 is a bottom view thereof.

The broken lines shown in the drawings are for the purpose of illustrating unclaimed portions of the wafer and form no part of the claimed design.

1 Claim, 4 Drawing Sheets



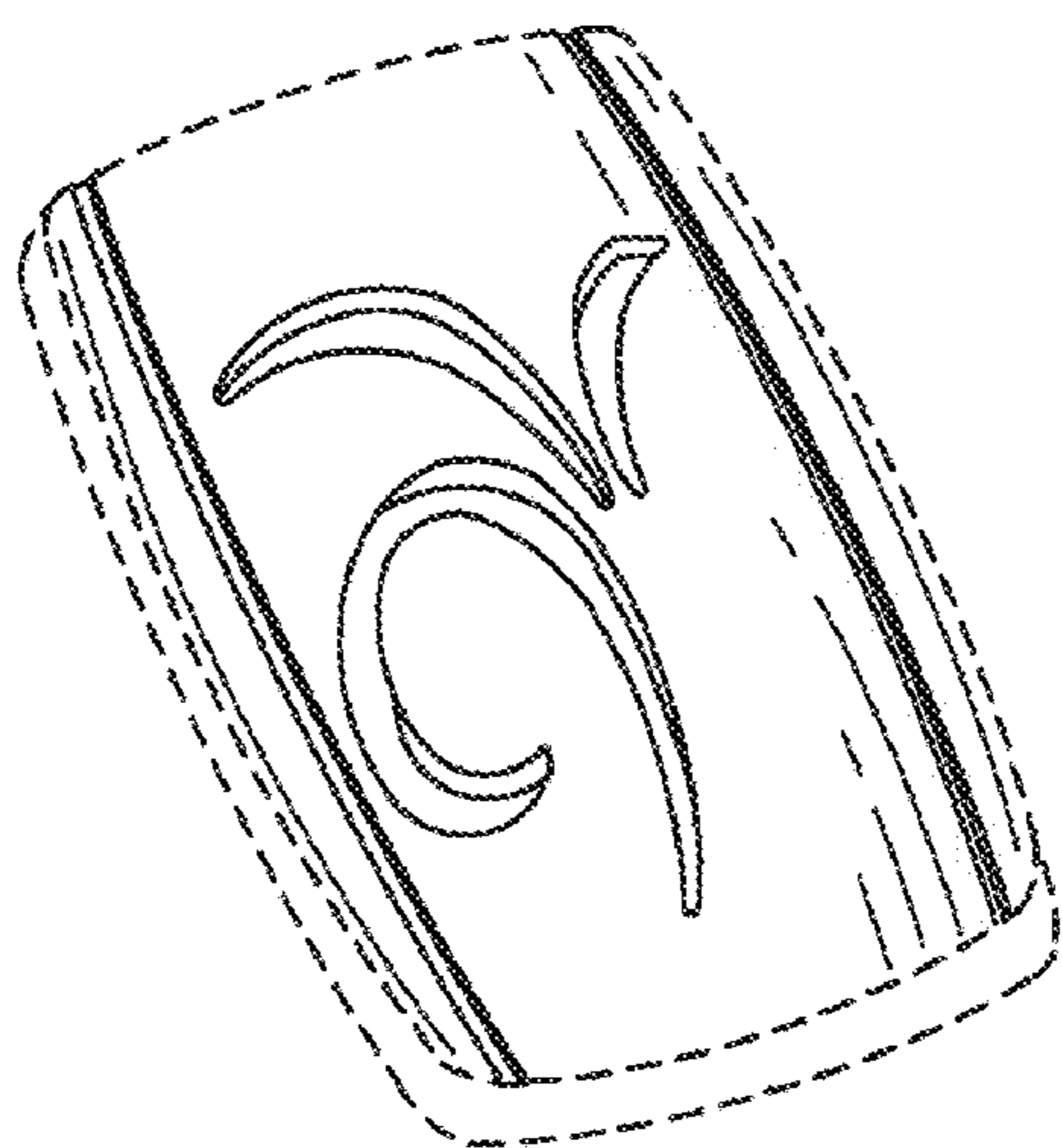


FIG. 1

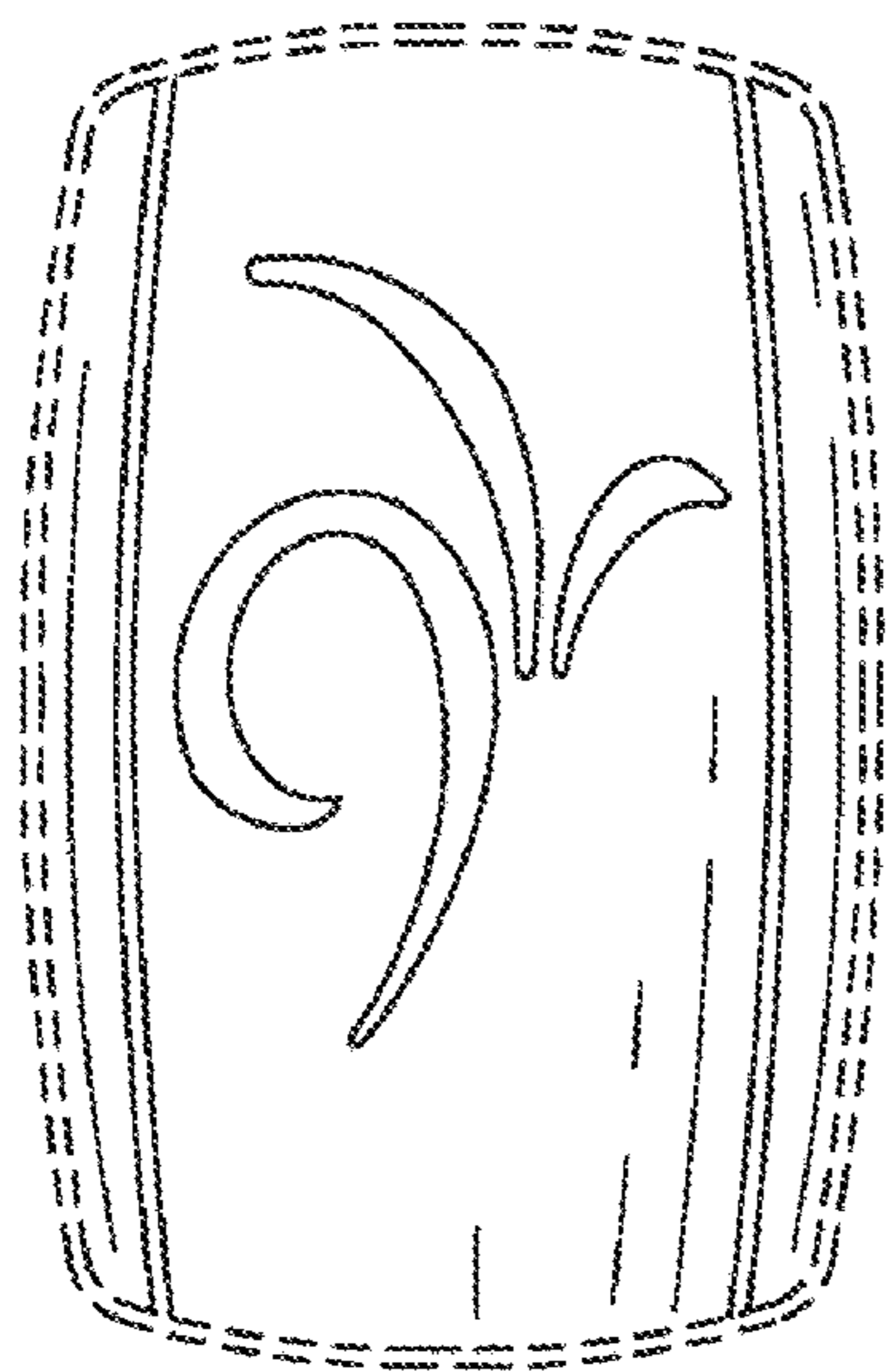


FIG. 2

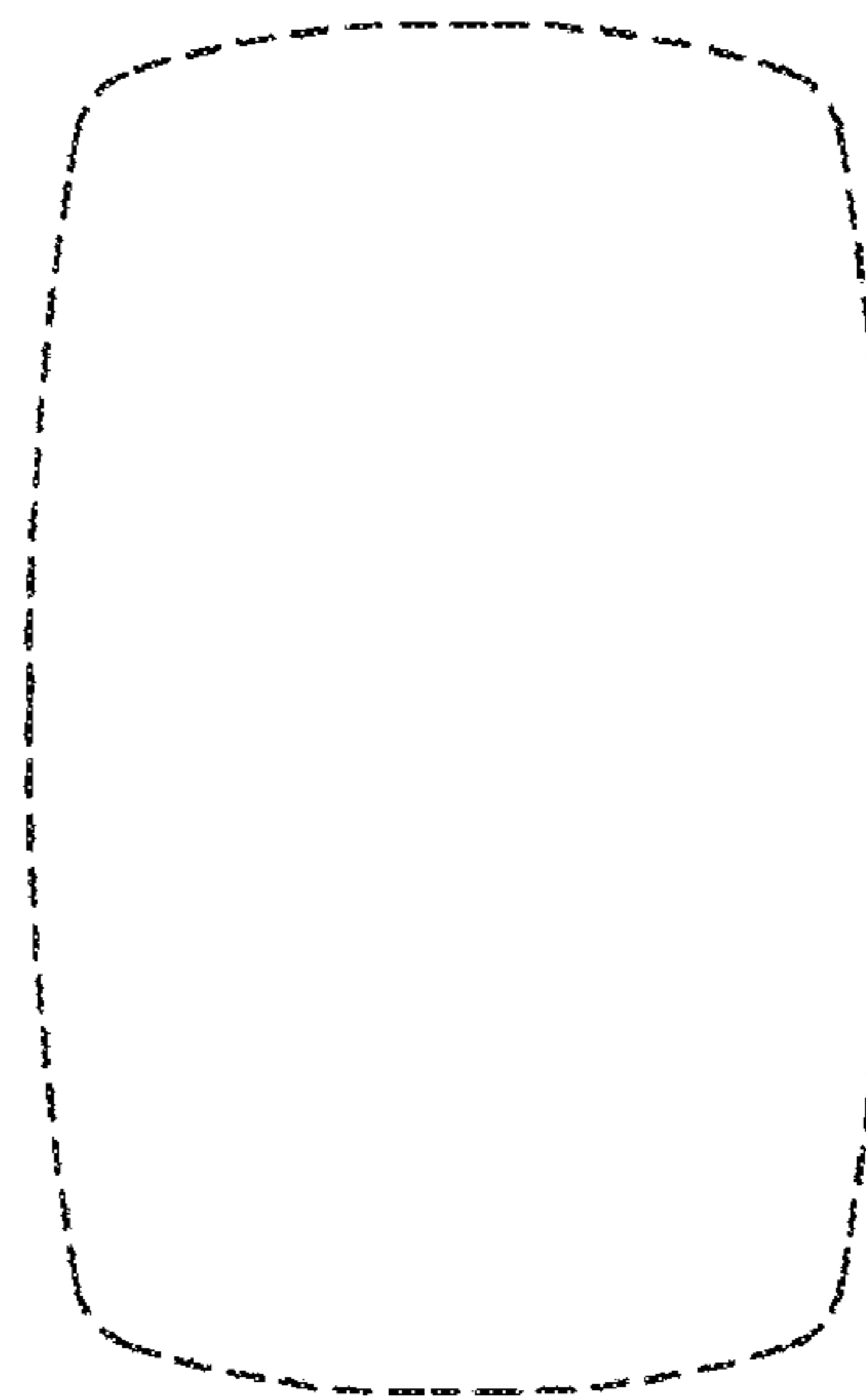


FIG. 3



FIG. 4

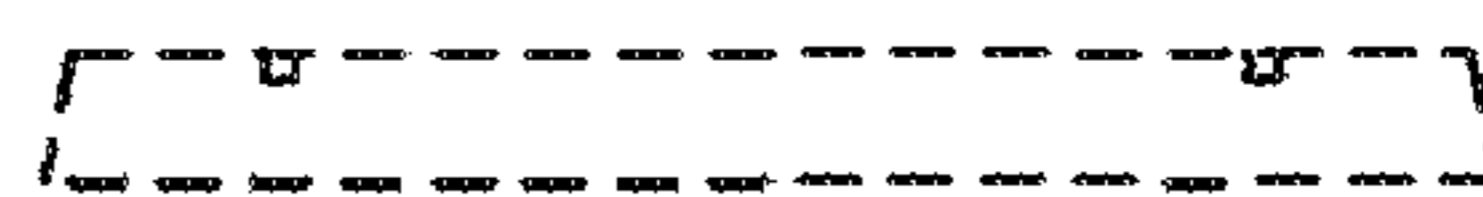


FIG. 5



FIG. 6



FIG. 7

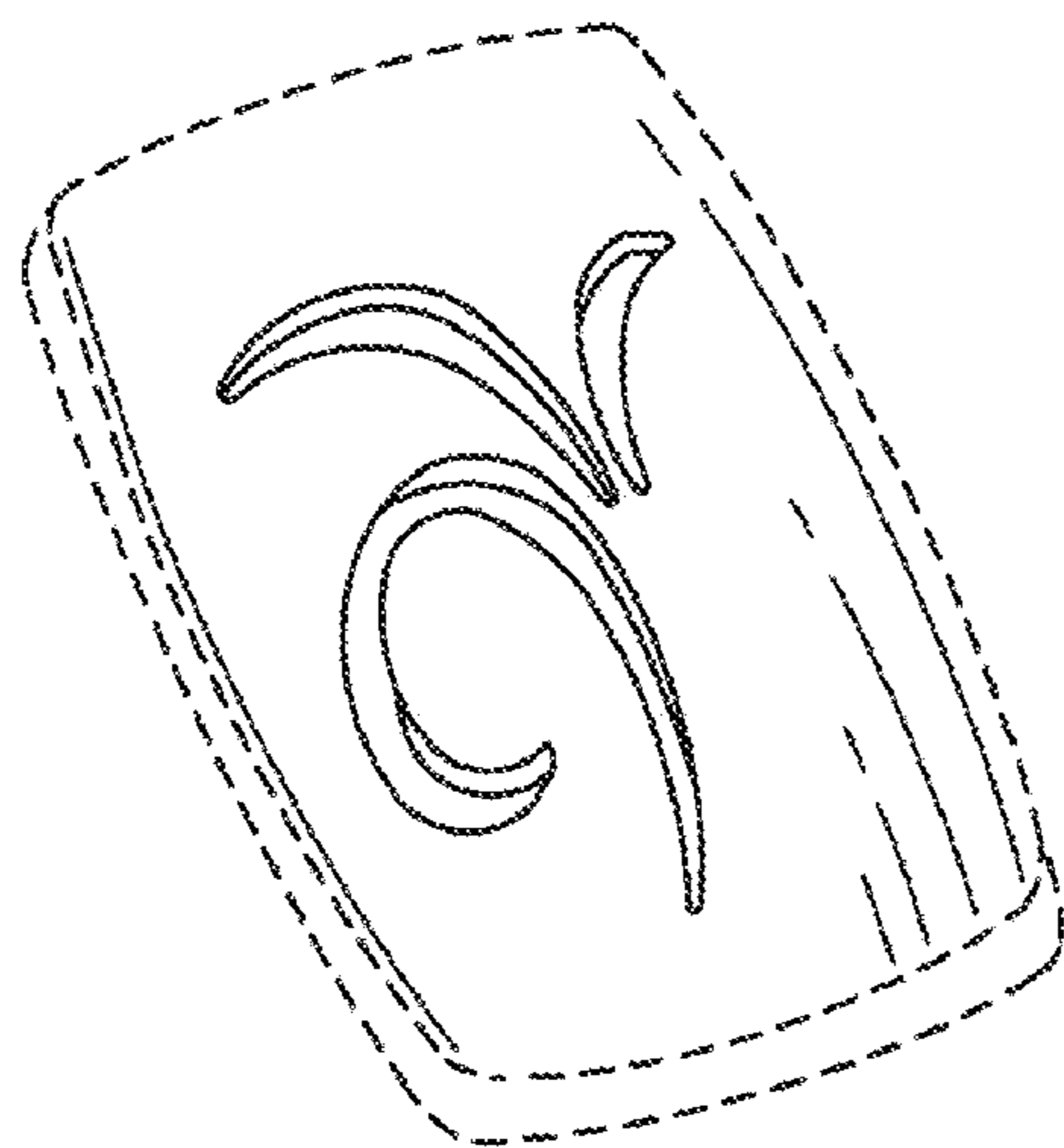


FIG. 8

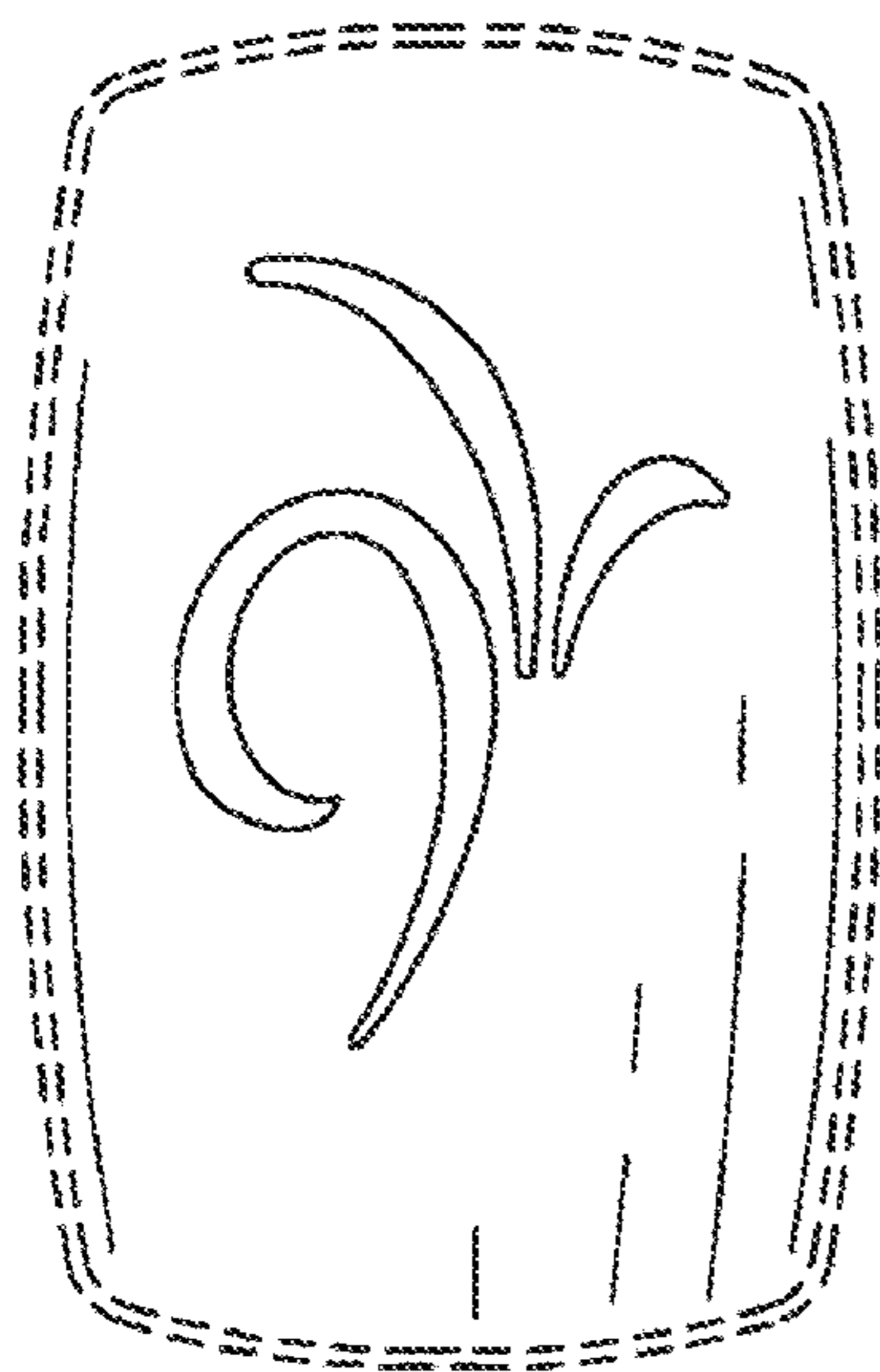


FIG. 9



FIG. 10



FIG. 11

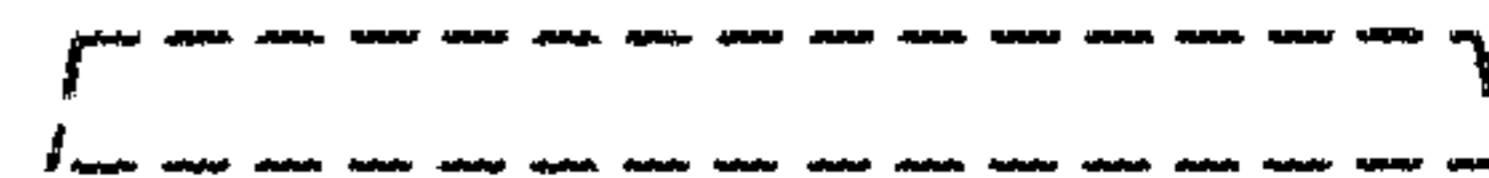


FIG. 12



FIG. 13

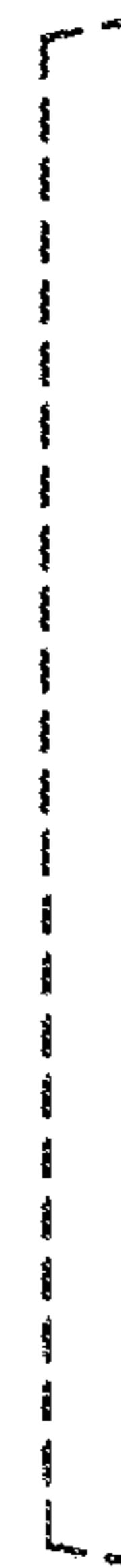


FIG. 14