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(12) **United States Design Patent**
Wada et al.

(10) **Patent No.:** **US D643,948 S**

(45) **Date of Patent:** **** Aug. 23, 2011**

(54) **LIGHT EMITTING DIODE LAMP**

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(73) Assignees: **Toshiba Lighting & Technology Corporation**, Kanagawa (JP); **Kabushiki Kaisha Toshiba**, Tokyo (JP)

(**) Term: **14 Years**

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(51) **LOC (9) Cl.** **26-04**

(52) **U.S. Cl.** **D26/2**

(58) **Field of Classification Search** D26/1-5,
D26/120, 138; 313/313, 315, 316, 317, 318,
313/493; 315/52, 53, 56, 57, 58; 362/290,
362/294, 309; D13/180, 110
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a light emitting diode lamp, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a light emitting diode lamp showing our new design;

FIG. 2 is another perspective view thereof;

FIG. 3 is a front view thereof;

FIG. 4 is a right side view thereof;

FIG. 5 is a top view thereof;

FIG. 6 is a bottom view thereof;

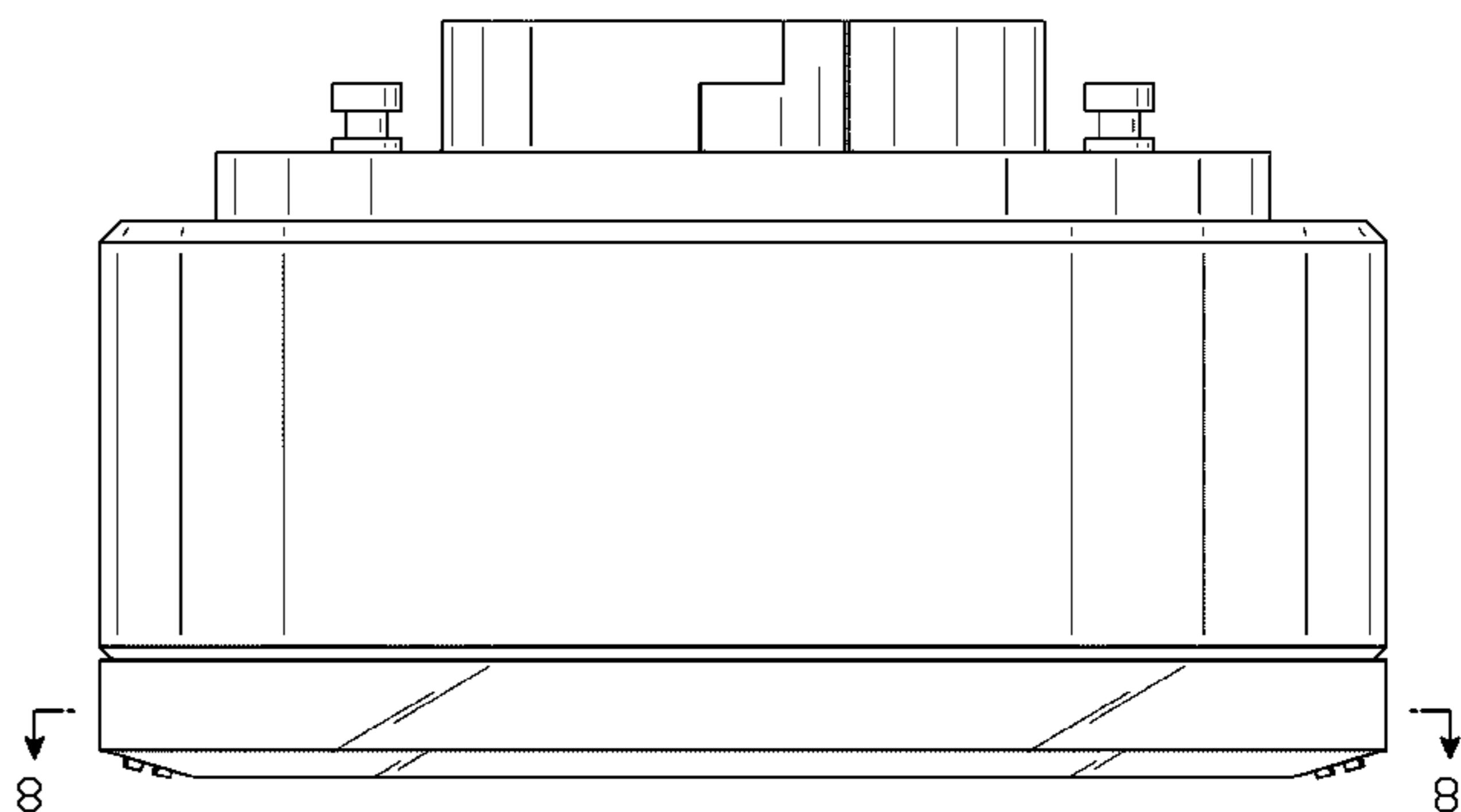
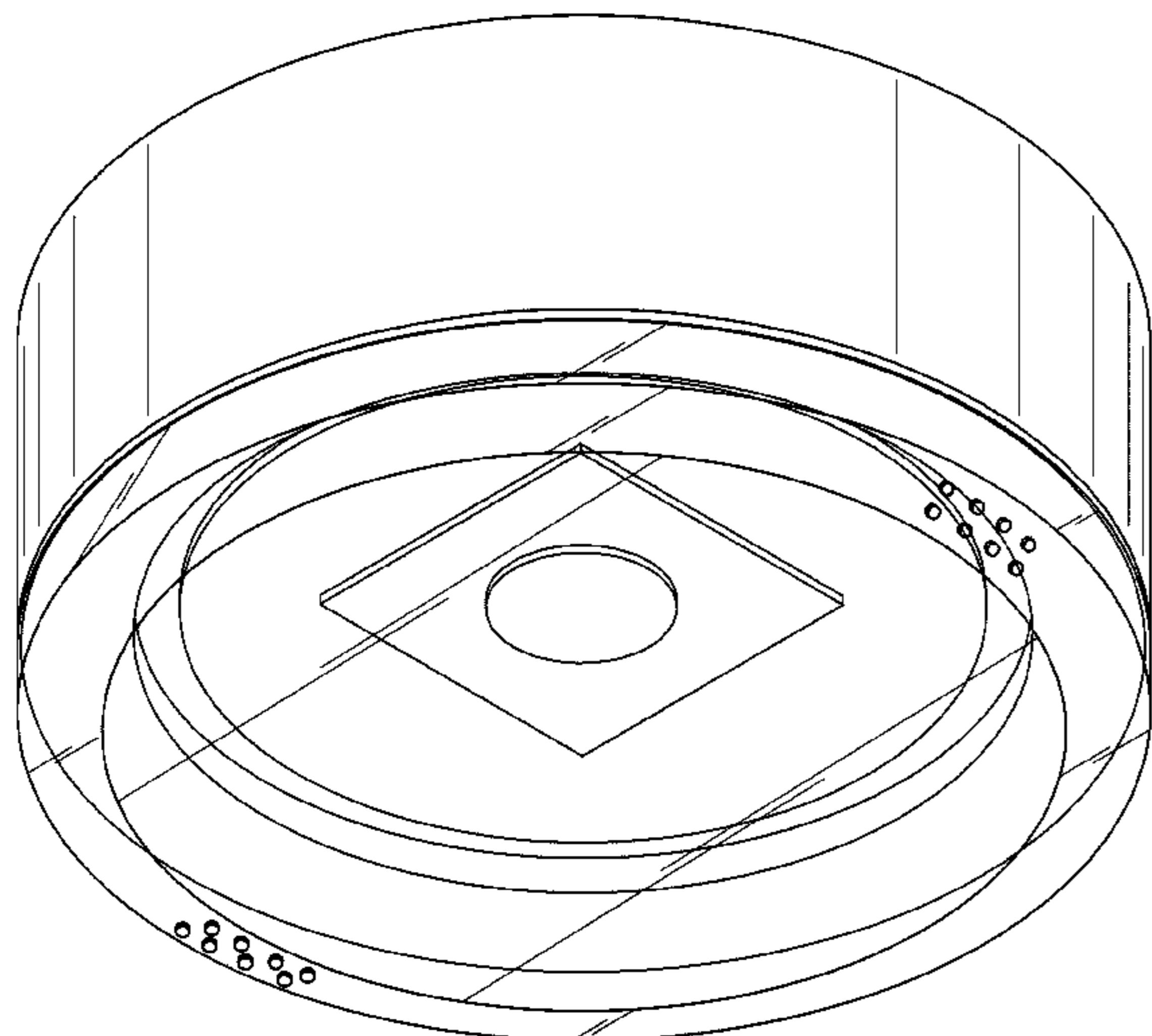
FIG. 7 is a sectional view of FIG. 4 along line 7-7 with internal structure omitted thereof; and,

FIG. 8 is a sectional view of FIG. 3 along line 8-8 with internal structure omitted thereof.

A left side view and a rear view of the light emitting diode lamp are omitted since the left side view and the rear view are respectively identical to the right side view and the front view.

In the figures, the portions with double hatching lines in the sectional view of FIG. 4 along line 7-7 with internal structure omitted and the sectional view of FIG. 3 along line 8-8 with internal structure omitted are transparent.

1 Claim, 8 Drawing Sheets



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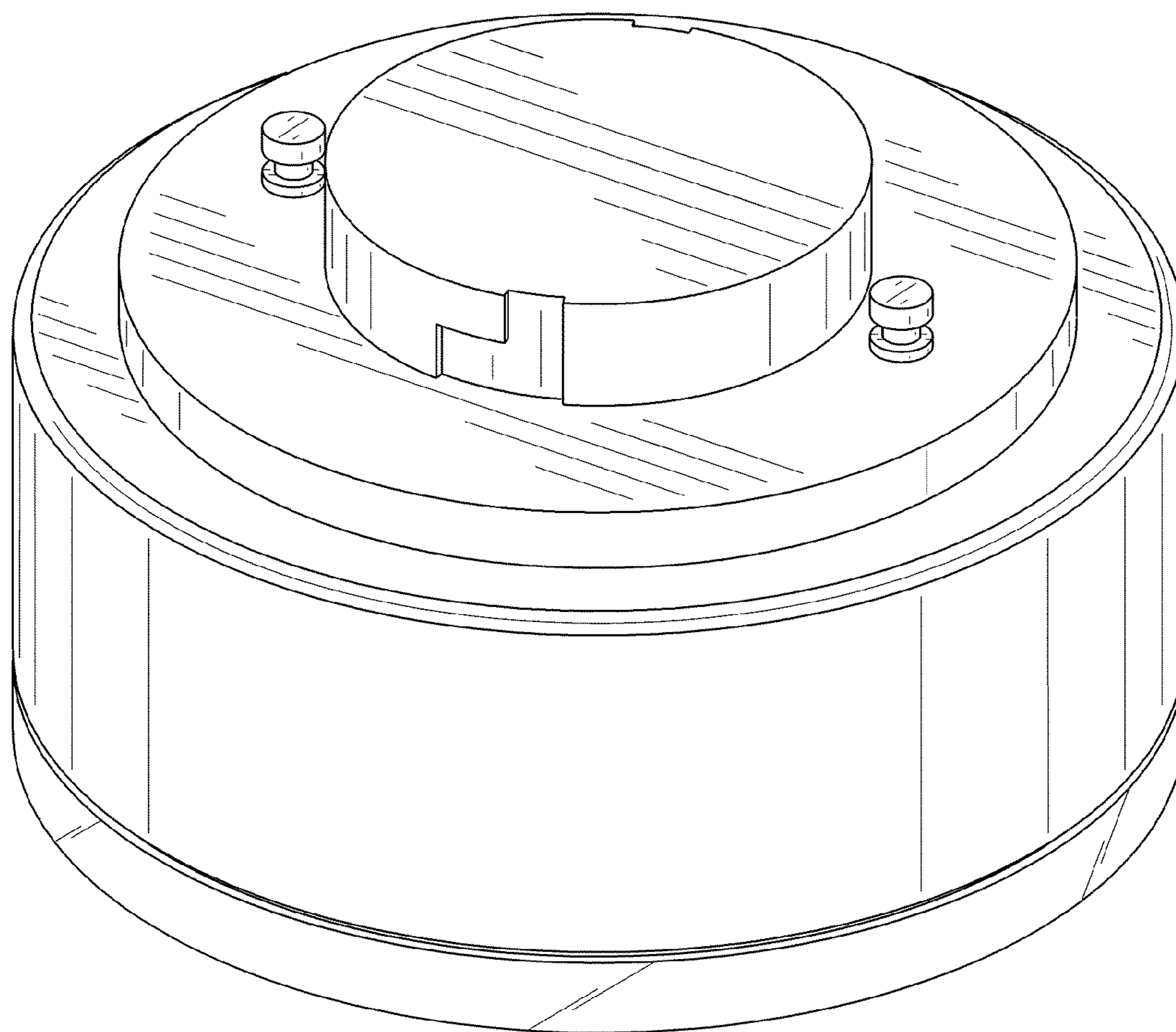


FIG. 1

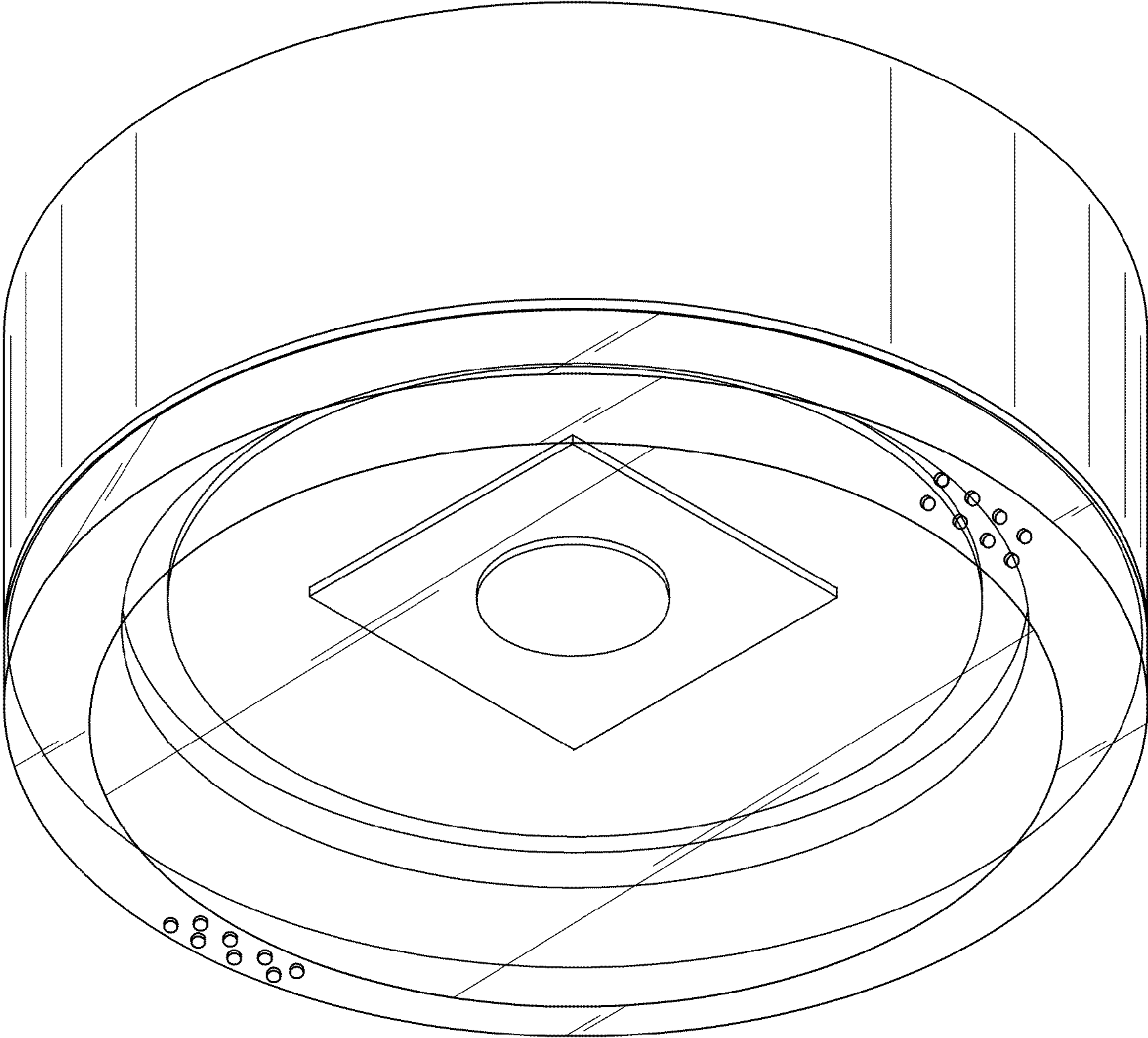


FIG. 2

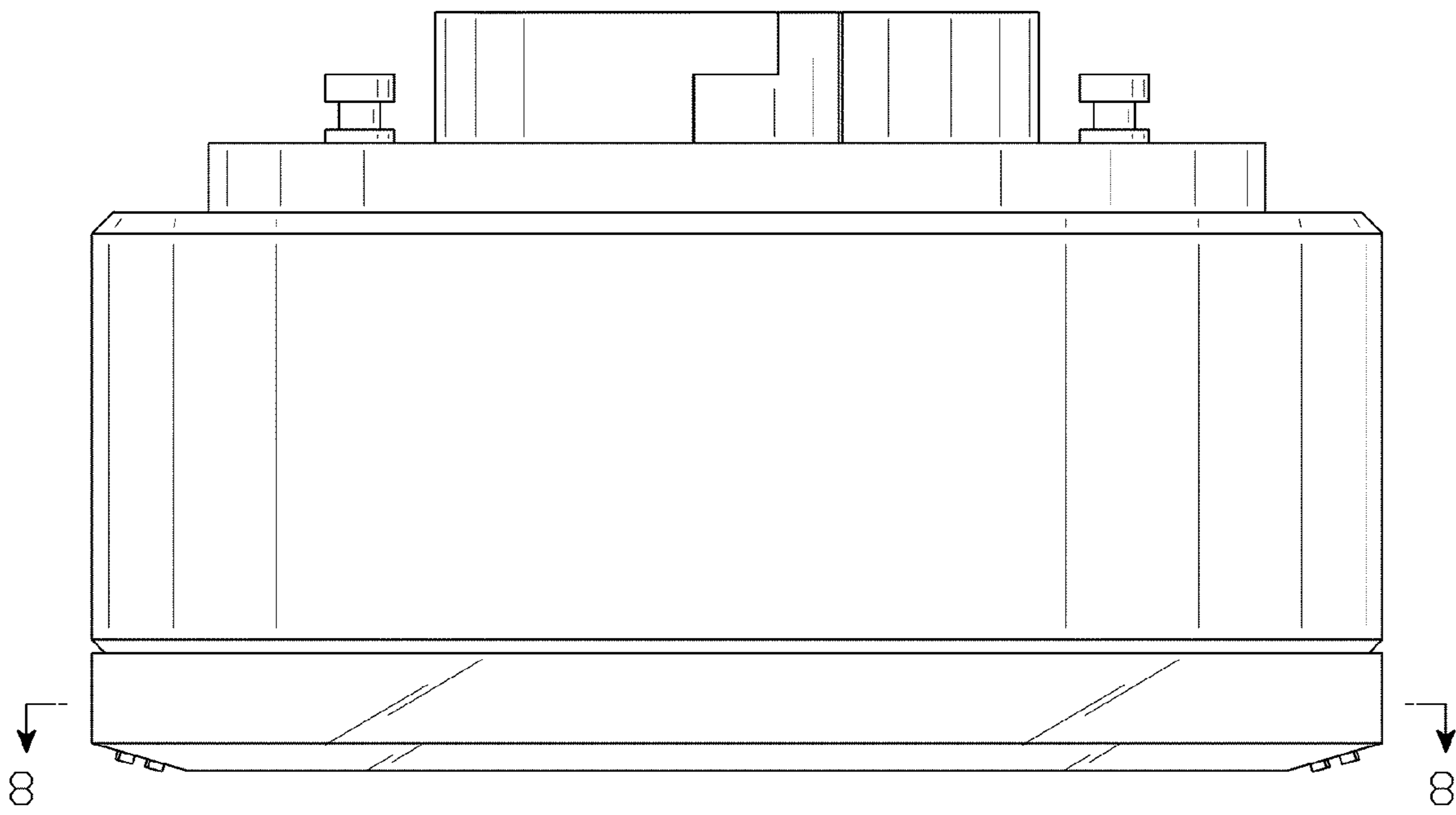


FIG. 3

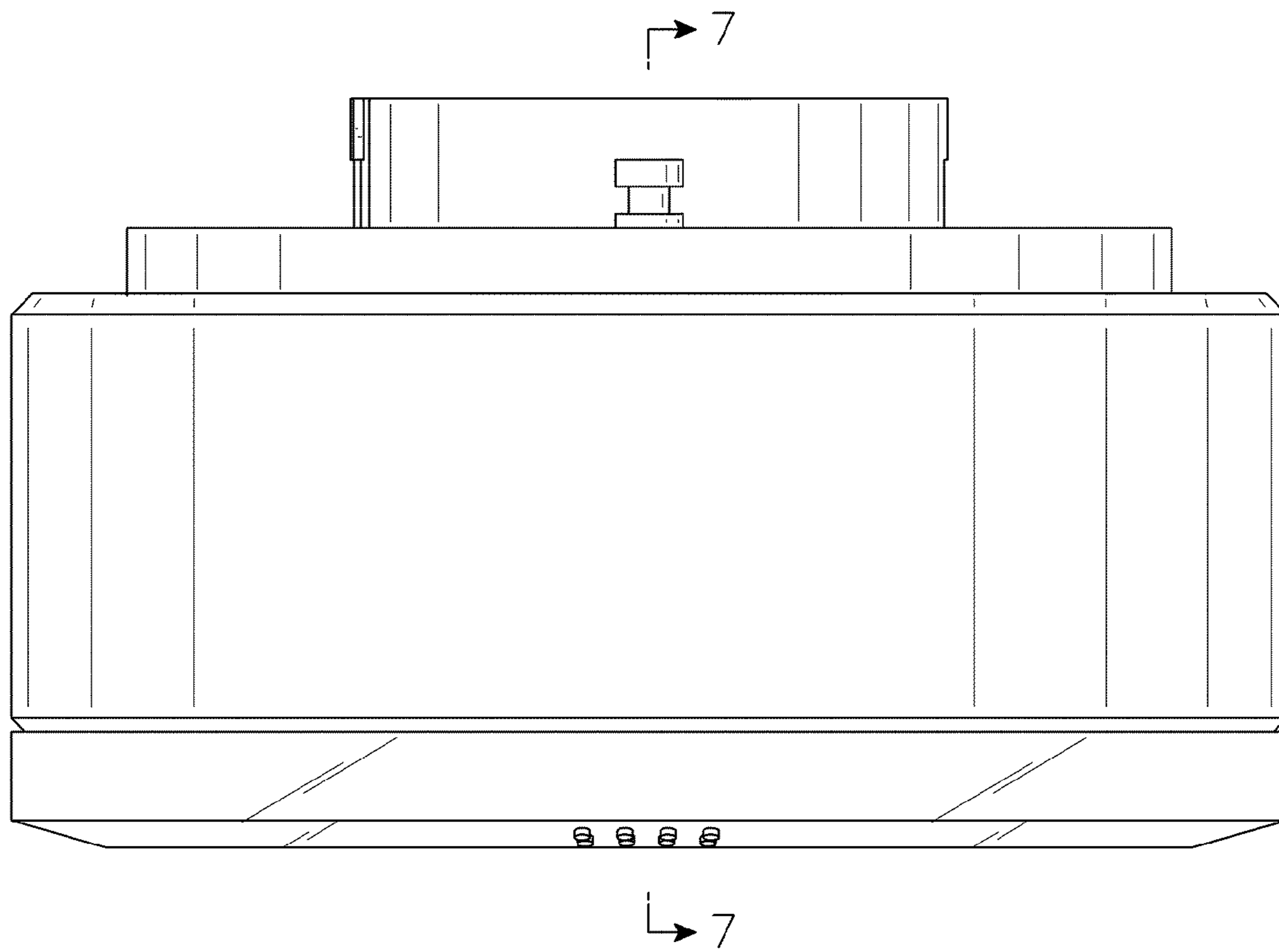


FIG. 4

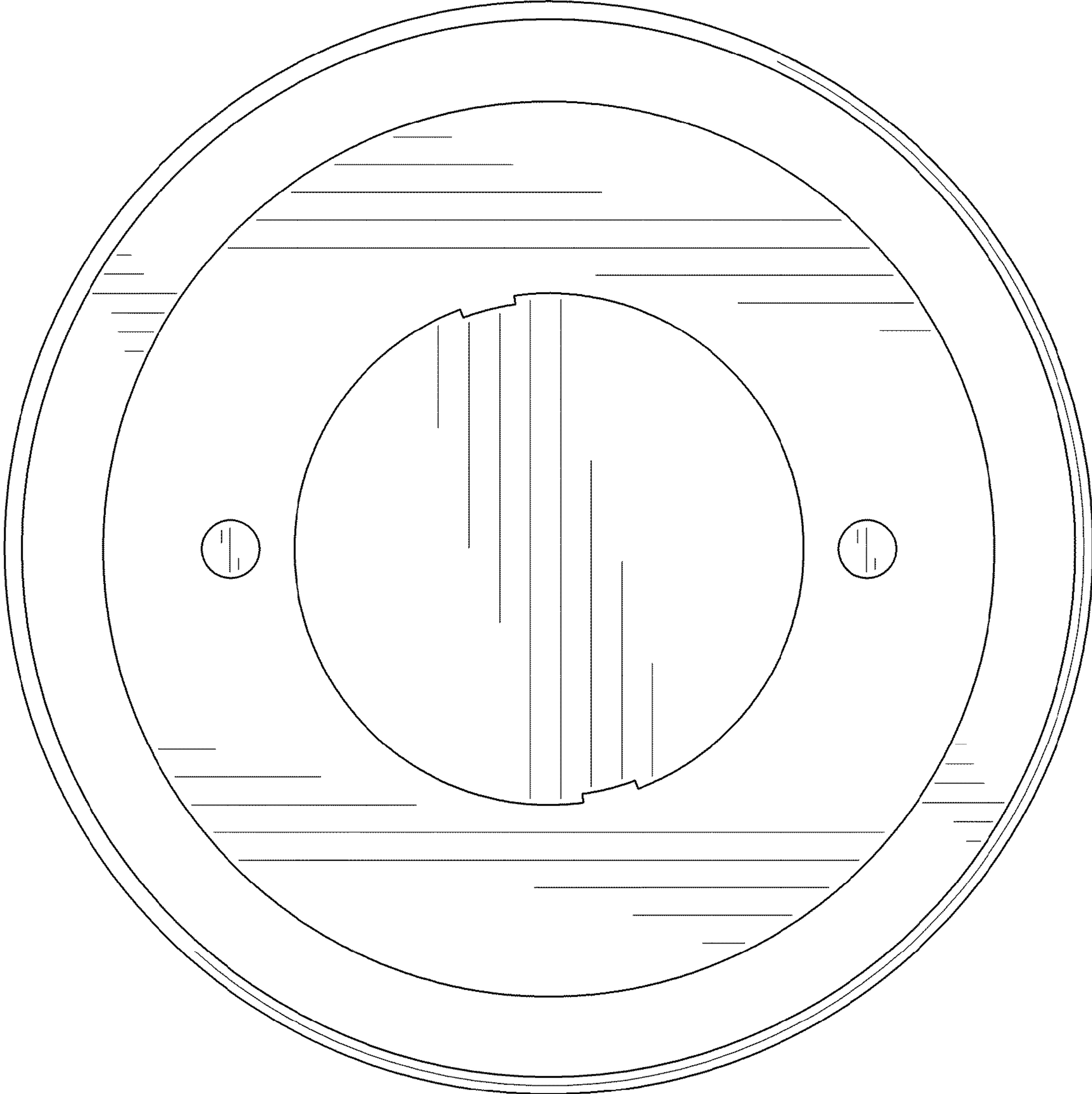


FIG. 5

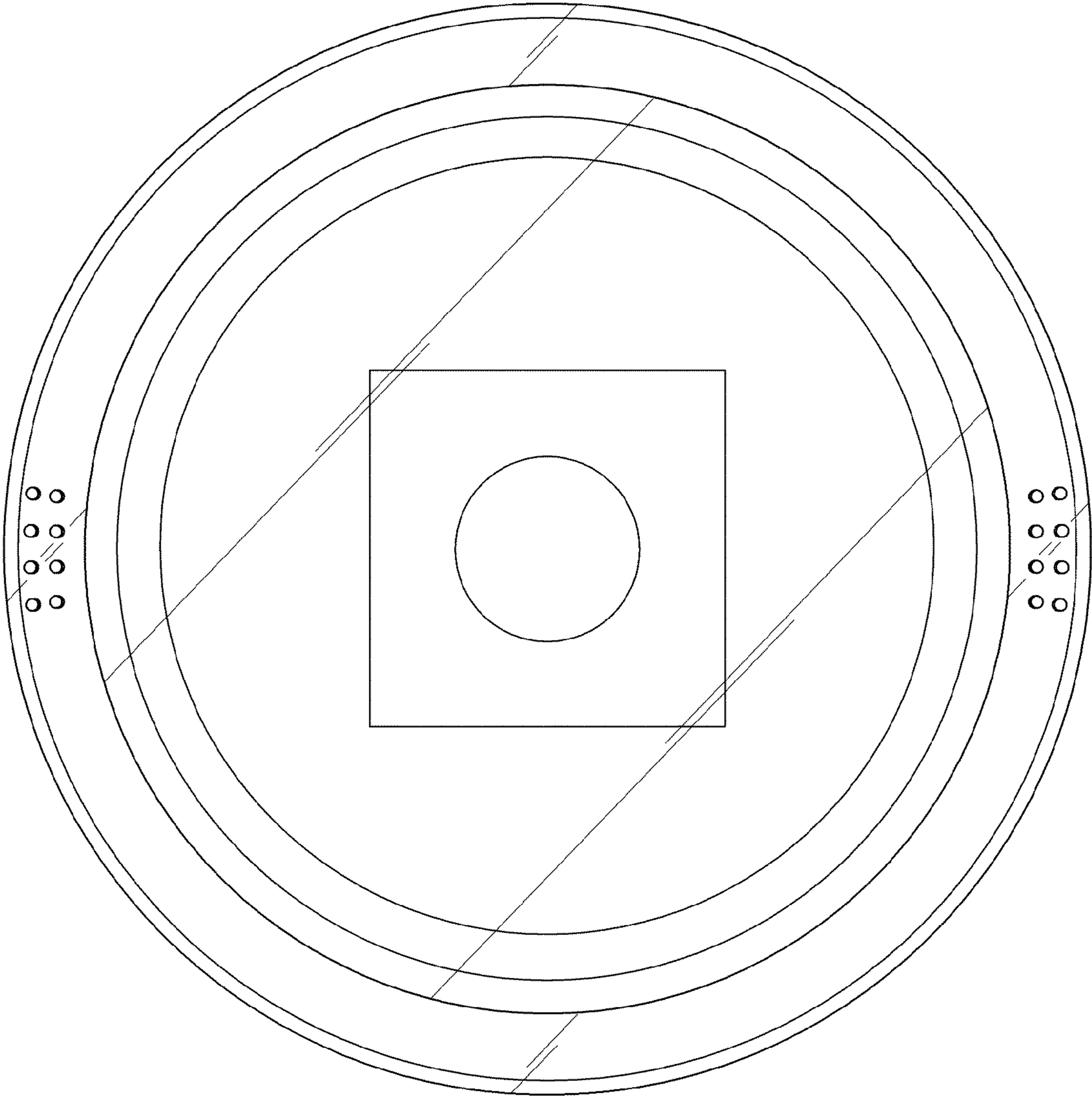


FIG. 6

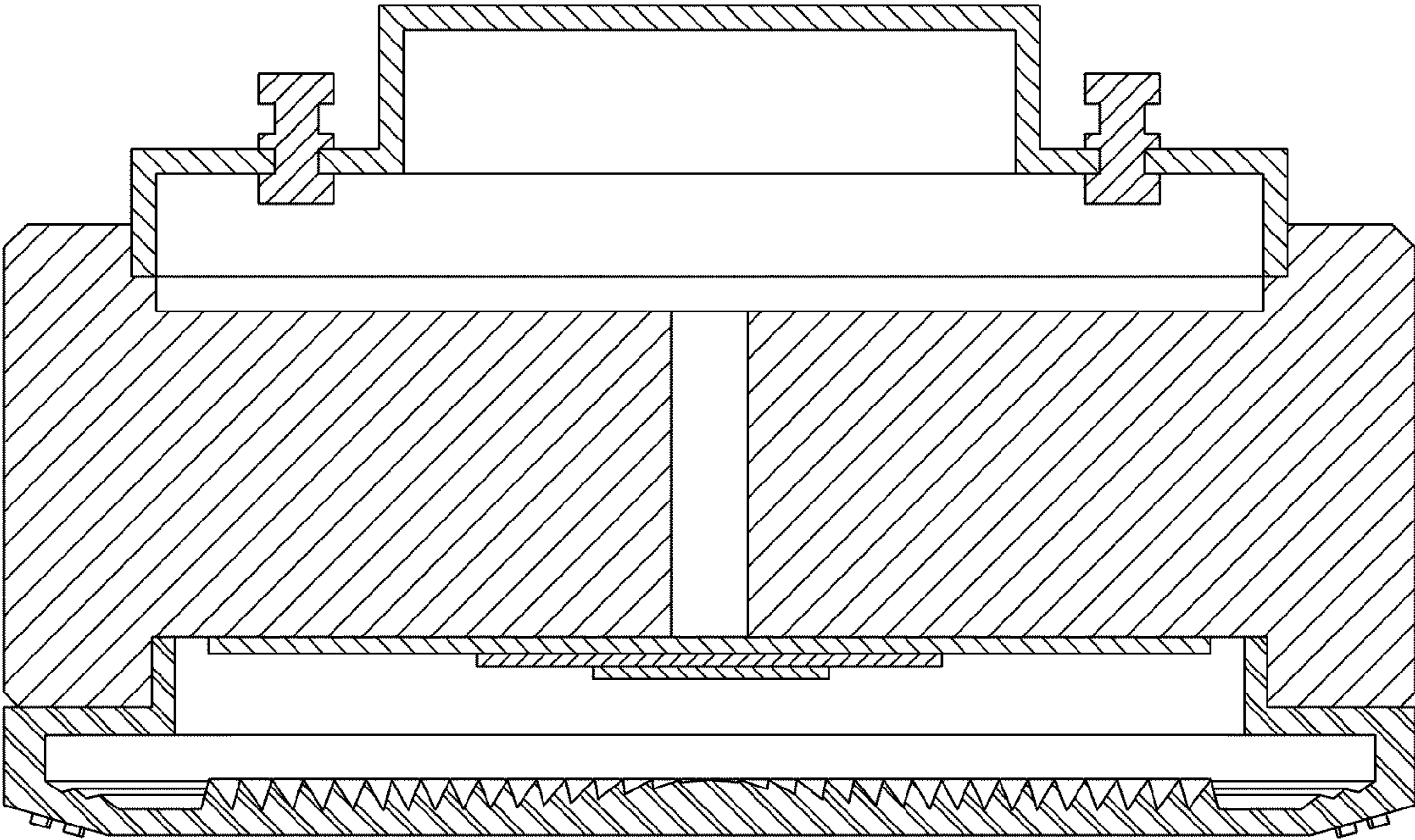


FIG. 7

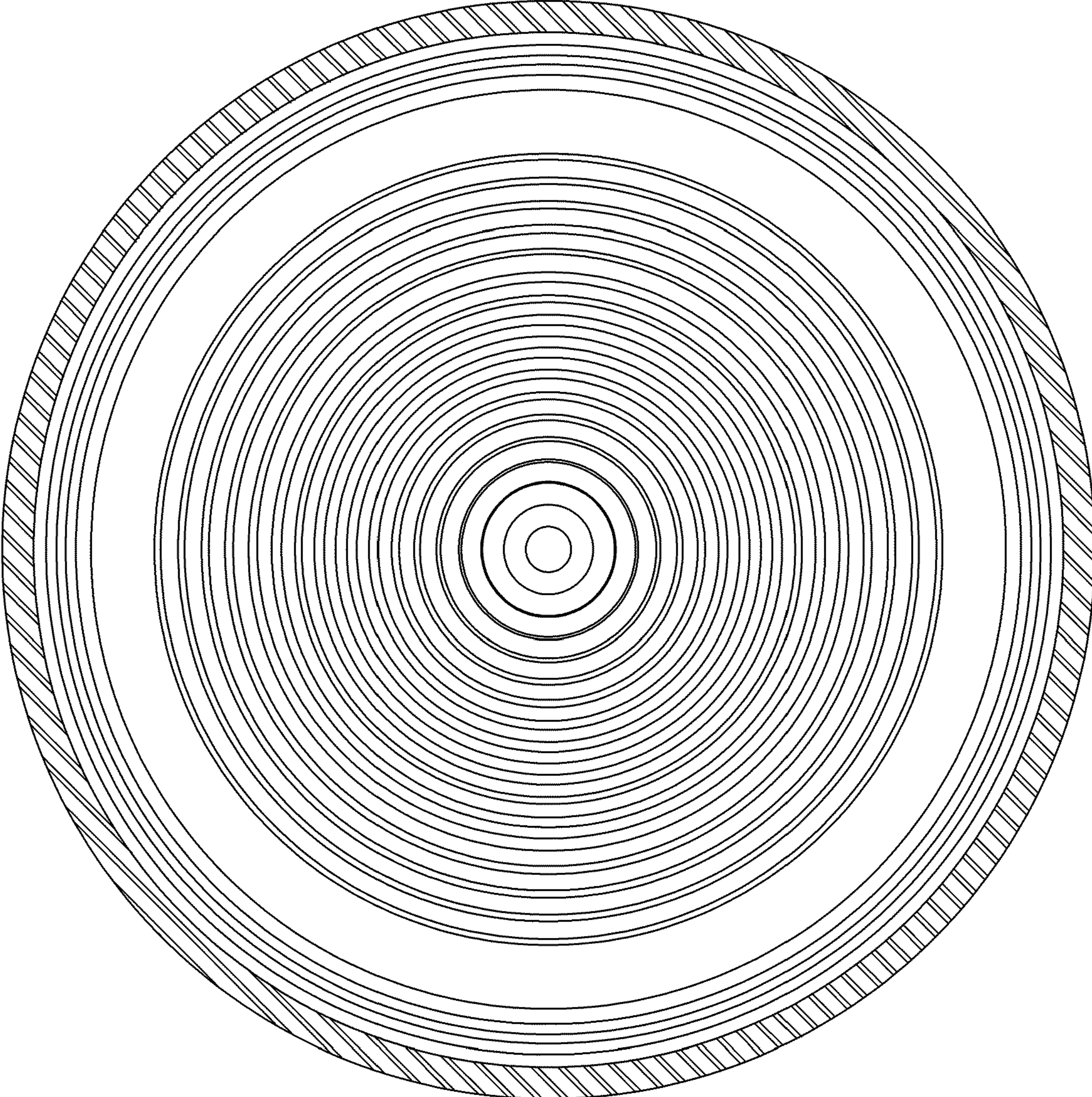


FIG. 8