



US00D639246S

(12) **United States Design Patent**  
**Ngo**

(10) **Patent No.:** **US D639,246 S**  
(45) **Date of Patent:** **\*\* \*Jun. 7, 2011**

(54) **ELECTRICAL CARD-EDGE CONNECTOR**

(75) Inventor: **Hung Viet Ngo**, Harrisburg, PA (US)

(73) Assignee: **FCI Americas Technology LLC**,  
Carson City, NV (US)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/363,269**

(22) Filed: **Jun. 7, 2010**

(51) **LOC (9) Cl.** ..... **13-03**

(52) **U.S. Cl.** ..... **D13/147**

(58) **Field of Classification Search** ..... D13/147,  
D13/154, 184; 439/188, 607.01, 607.04,  
439/607.05, 607.17, 607.25, 607.34, 607.41,  
439/607.53, 668, 939, 525, 526, 540.1, 541.5,  
439/607.07, 607.08, 607.09, 607.11, 607.13,  
439/607.14, 607.31, 607.32, 607.36, 607.4,  
439/604.46, 945, 947, 951

See application file for complete search history.

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U.S. Appl. No. 12/687,237, filed Jan. 14, 2010, Ngo.  
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(57) **CLAIM**

The ornamental design for an electrical card-edge connector,  
as shown and described.

**DESCRIPTION**

This application is related by subject matter to U.S. Design patent application Ser. No. 29/363,267 entitled “Electrical Card-Edge Connector” filed on even date herewith, and U.S. Design patent application Ser. No. 29/363,270 entitled “Electrical Card-Edge Connector” filed on even date herewith.

FIG. 1 is a bottom, left, front perspective view of an electrical card-edge connector according to my design;

FIG. 2 is a top, right, rear perspective view thereof;

FIG. 3 is a rear elevation view thereof;

FIG. 4 is a top plan view thereof;

FIG. 5 is a front elevation view thereof;

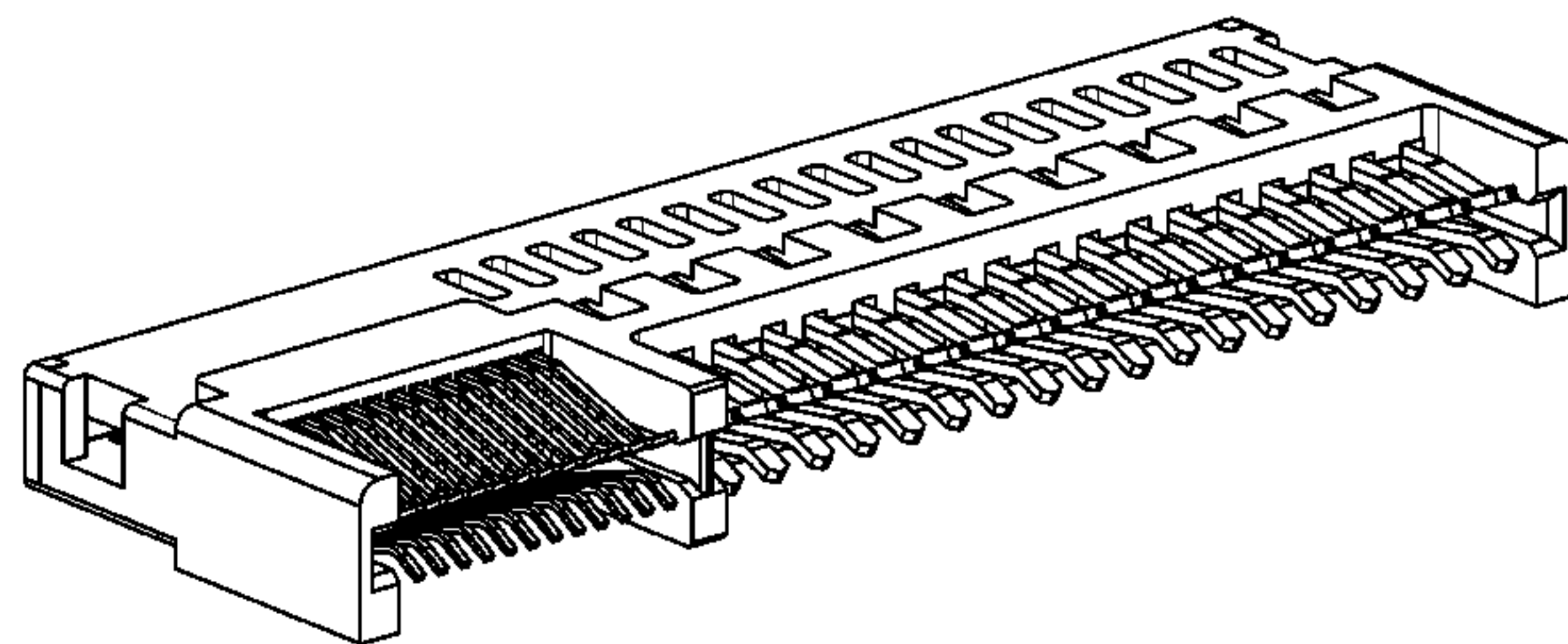
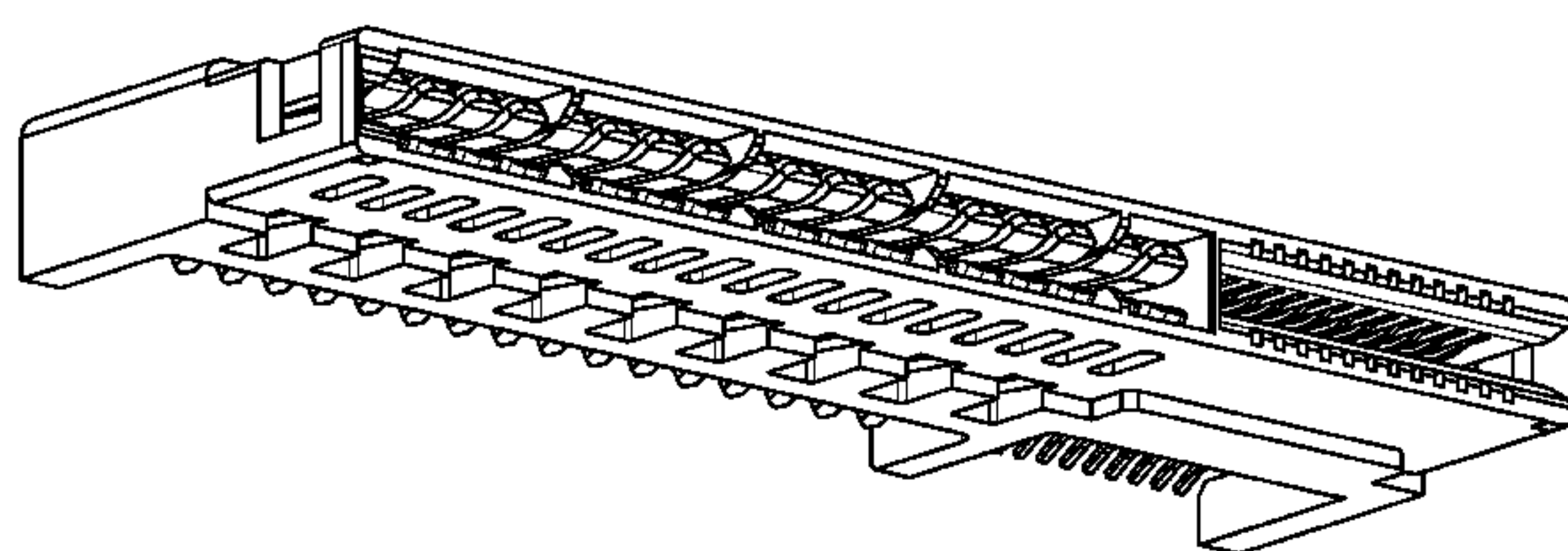
FIG. 6 is a bottom plan view thereof;

FIG. 7 is a left side elevation view thereof; and,

FIG. 8 is a right side elevation view thereof.

In a preferred embodiment, the nature of this product is an electrical card-edge connector in the form of an electrical connector housing that retains a plurality of electrical contacts, such that the electrical card-edge connector is configured to mount to a first circuit board and mate with a second circuit board.

**1 Claim, 4 Drawing Sheets**



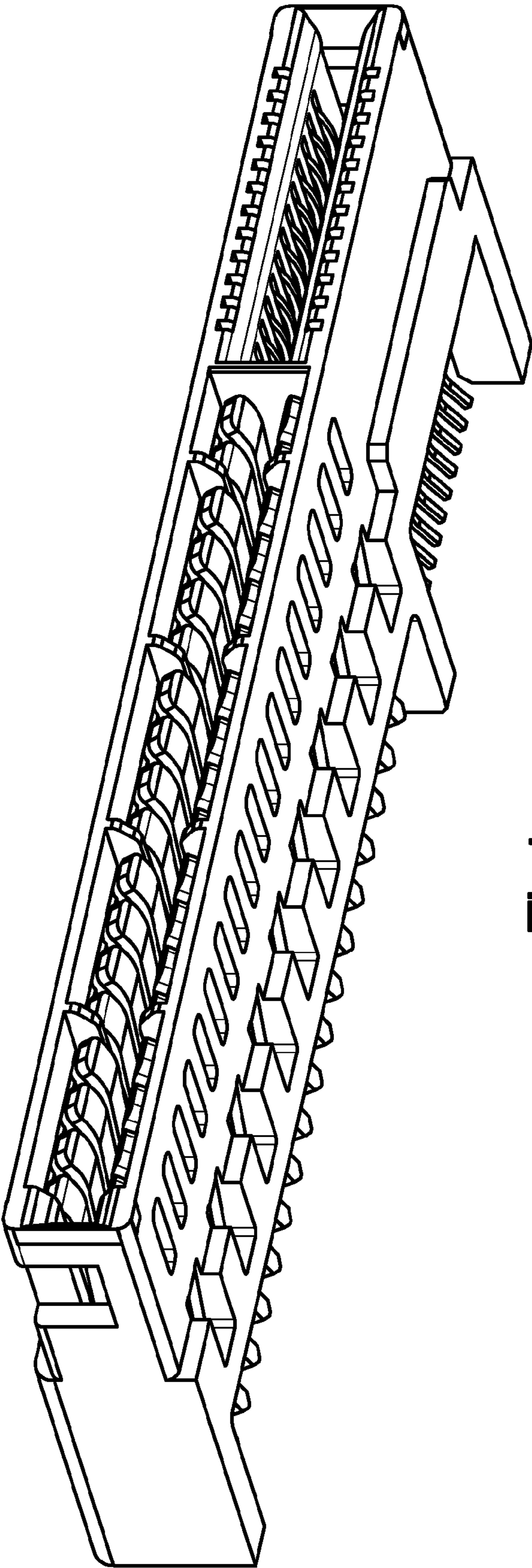


Fig.1

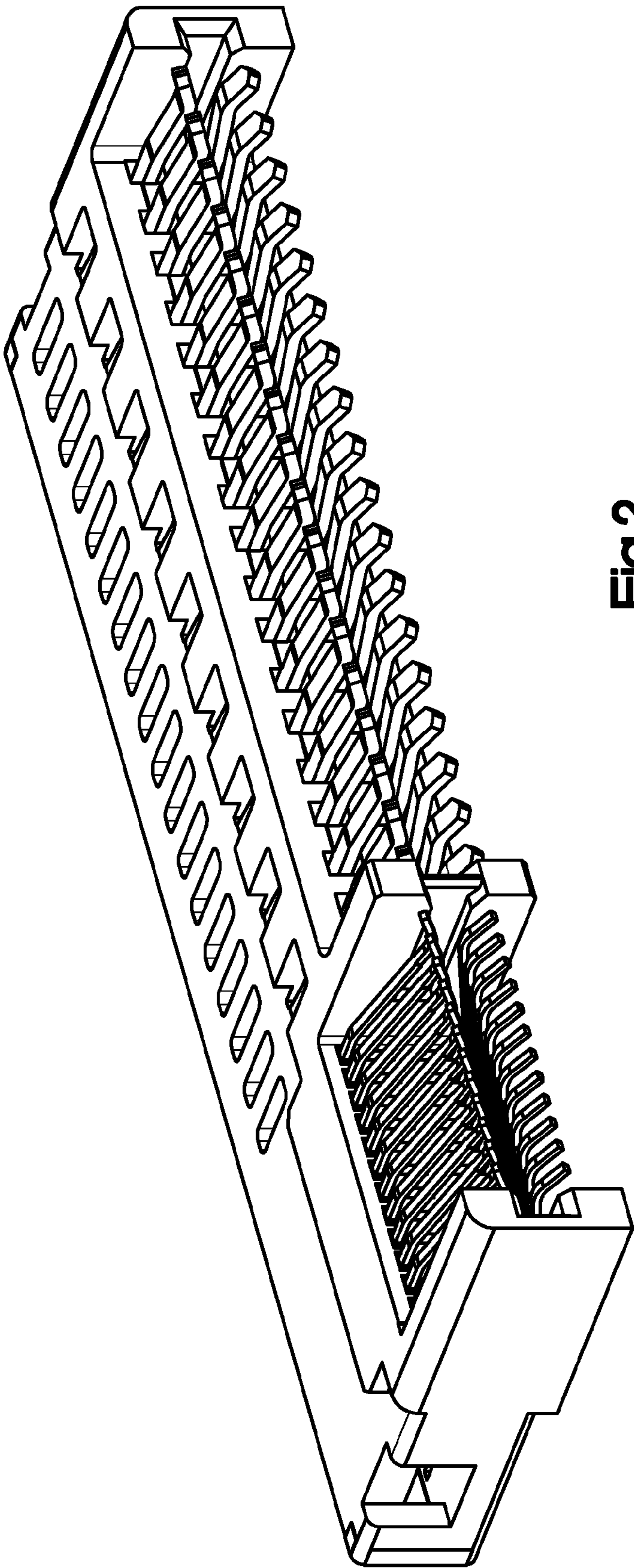


Fig.2

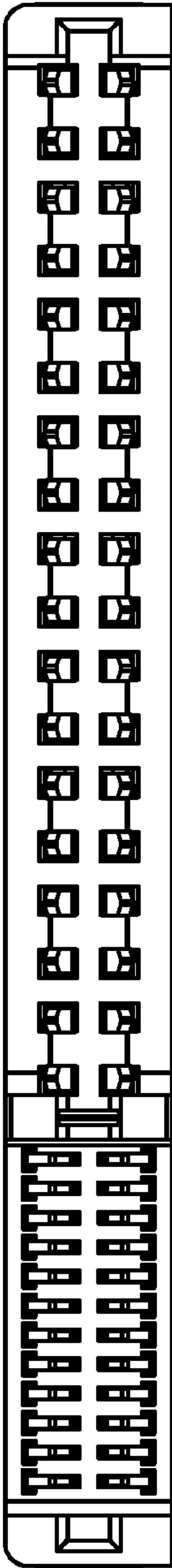


Fig.3

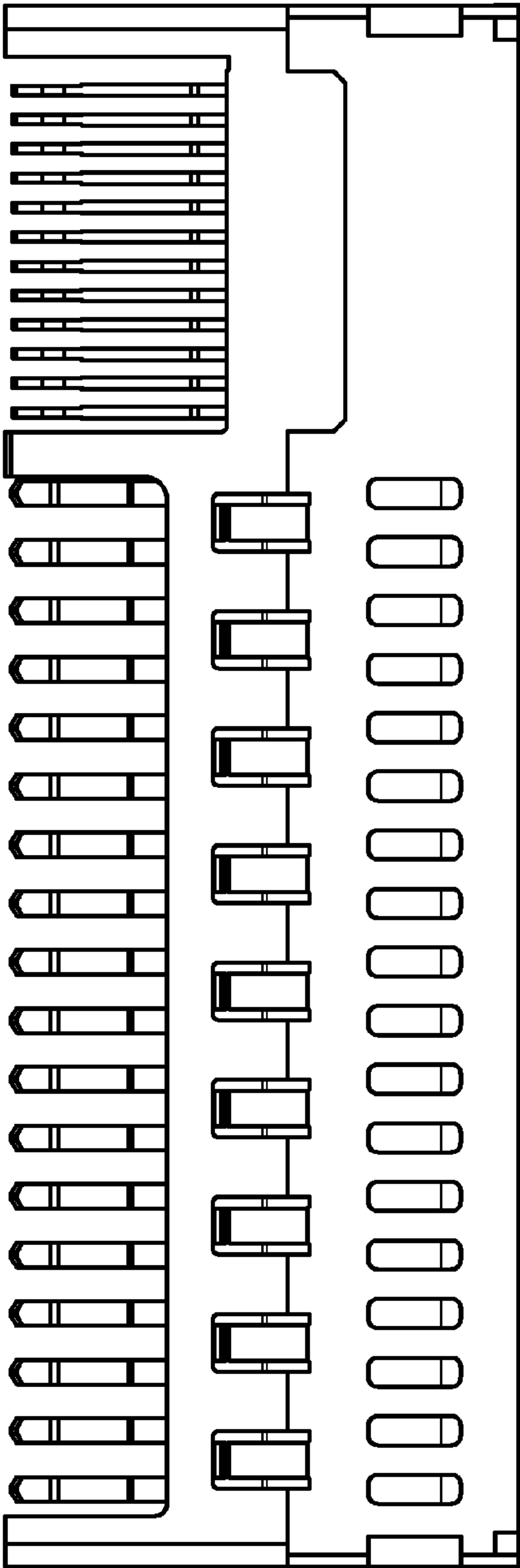


Fig.4

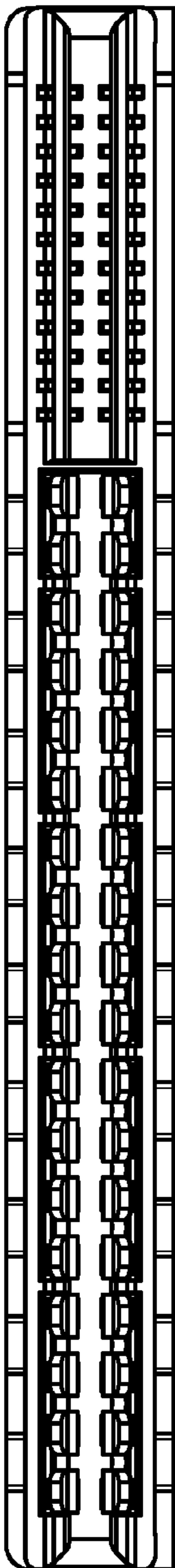


Fig.5



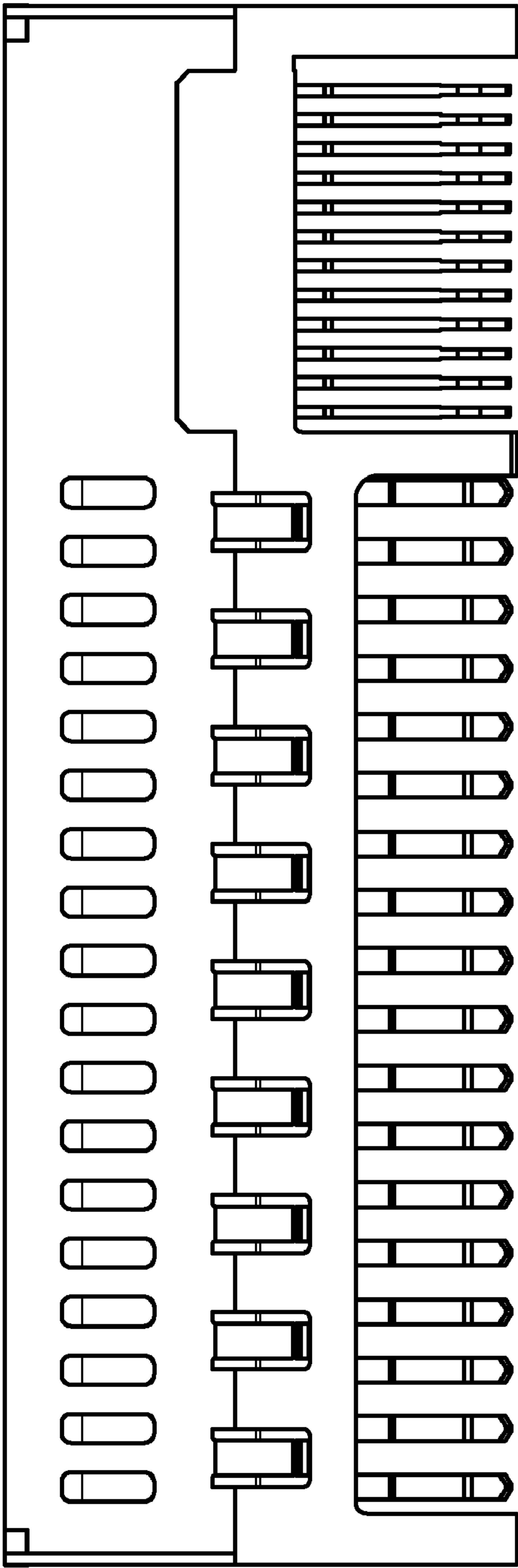


Fig. 6

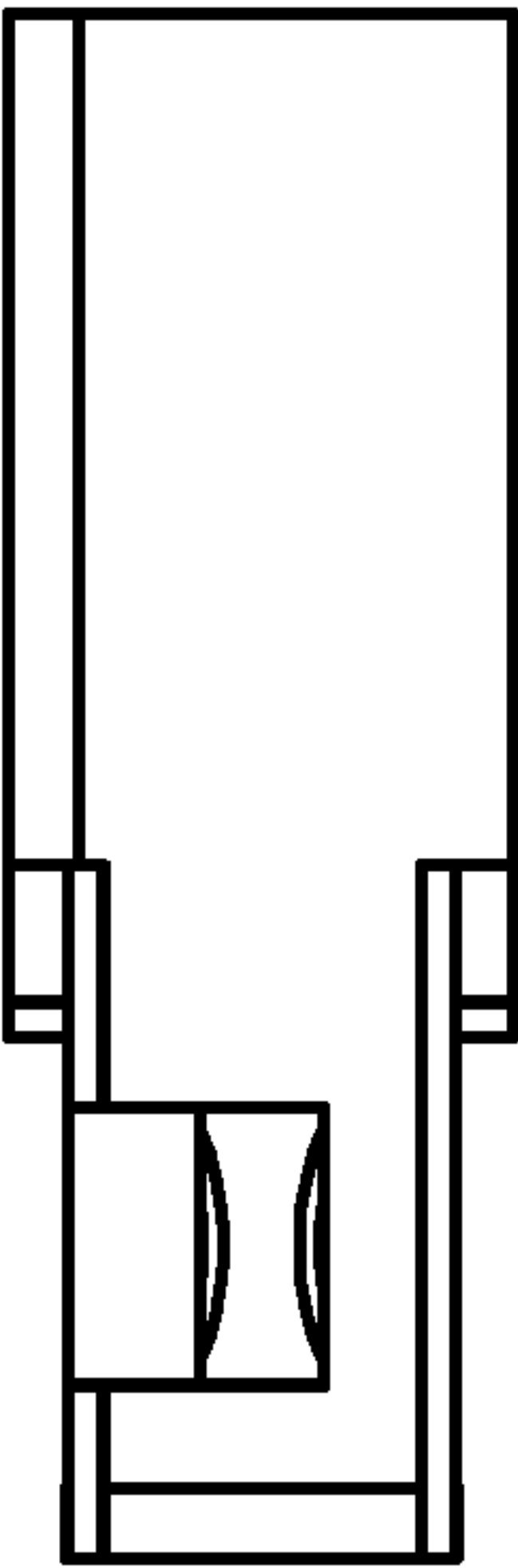


Fig. 8

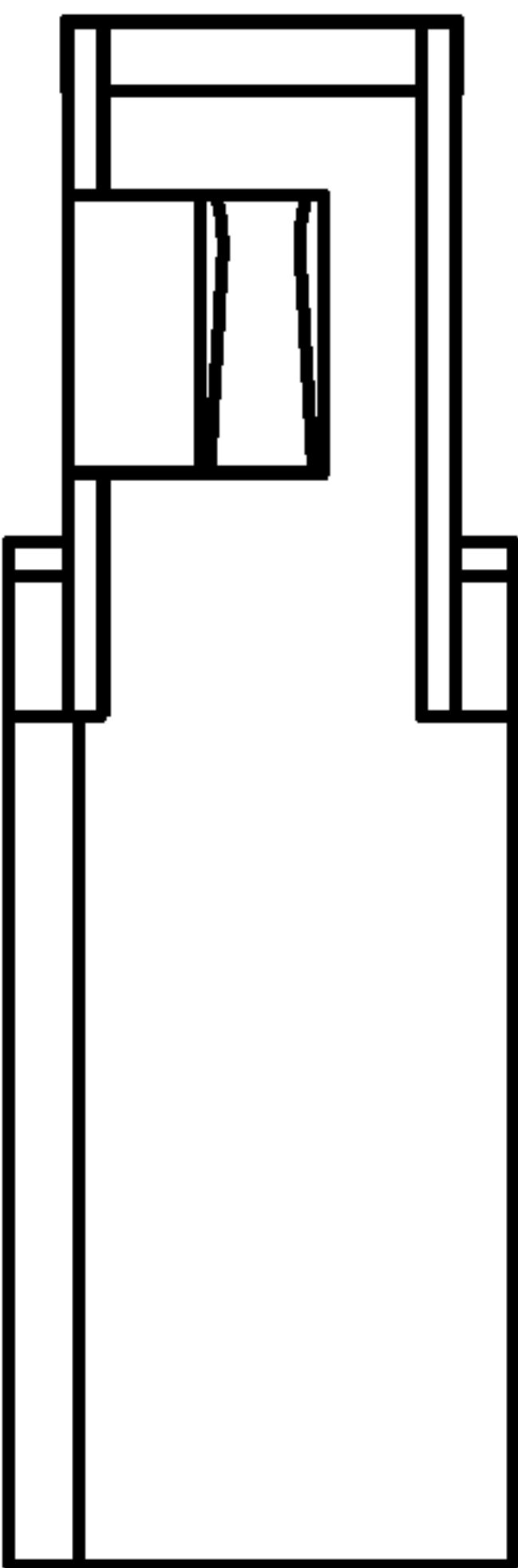


Fig. 7

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : D639,246 S  
APPLICATION NO. : 29/363269  
DATED : June 7, 2011  
INVENTOR(S) : Hung Viet Ngo

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page, at DESCRIPTION, the paragraph should read:

This application is related by subject matter to U.S. Provisional Patent Application No. 61/352,231, filed June 7, 2010, entitled "Electrical Card-Edge Connector", U.S. Design Patent Application No. 29/363,267, filed June 7, 2010, entitled "Electrical Card-Edge Connector", U.S. Provisional Patent Application No. 61/352,245, filed June 7, 2010, entitled "Electrical Card-Edge Connector", and U.S. Design Patent Application No. 29/363,270, filed June 7, 2010 entitled "Electrical Card-Edge Connector".

Signed and Sealed this  
Twenty-sixth Day of July, 2011

A handwritten signature in black ink, reading "David J. Kappos". The signature is written in a cursive, flowing style with a large initial "D".

David J. Kappos  
*Director of the United States Patent and Trademark Office*