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(12) **United States Design Patent**
Yamada

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(54) **LIGHT EMITTING DIODE MODULE**

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(73) Assignee: **Nichia Corporation**, Tokushima (JP)

(**) Term: **14 Years**

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Related U.S. Application Data

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(30) **Foreign Application Priority Data**

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Apr. 8, 2009 (JP) 2009-8102

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(52) **U.S. Cl.** **D13/180**

(58) **Field of Classification Search** D13/180;
D26/2; 257/79, 80, 81, 88, 89, 95, 98, 99,
257/100, E33.058; 313/483, 498, 500; 362/555,
362/800

See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a light emitting diode module, as shown and described.

DESCRIPTION

FIG. 1 is a front elevational view of a light emitting diode module in accordance with an embodiment of my new design; FIG. 2 is a top plan view of the light emitting diode module in accordance with the embodiment of my new design; FIG. 3 is a bottom plan view of the light emitting diode module in accordance with the embodiment of my new design;

FIG. 4 is a left side elevational view of the light emitting diode module in accordance with the embodiment of my new design; and,

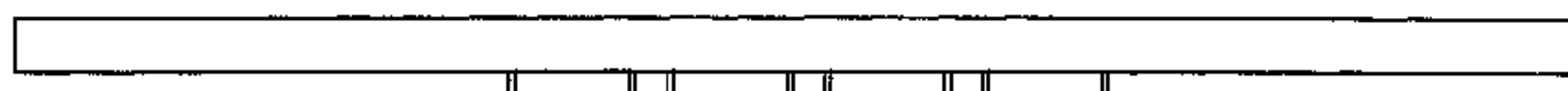
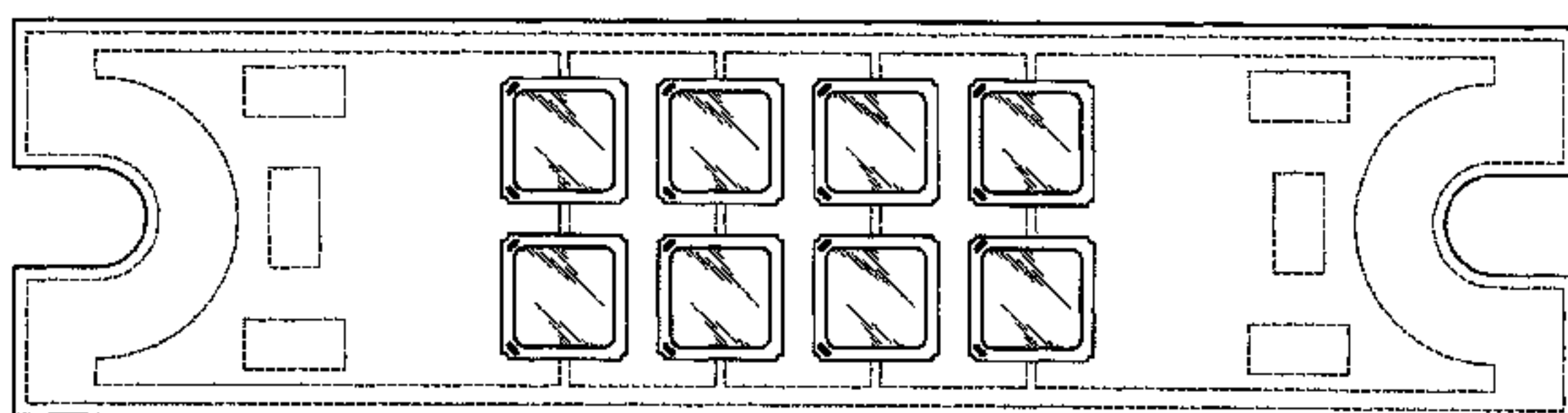
FIG. 5 is a right side elevational view of the light emitting diode module in accordance with the embodiment of my new design.

The broken line showing of environment (the remaining structure of the light emitting diode module) in FIGS. 1, 4 and 5 is for illustrative purposes only and forms no part of the claimed design.

The diagonal line shading illustrates a transparent portion of the light emitting diode module.

The rear elevation view of the light emitting diode module is omitted because the rear surface of the light emitting diode module is flat and devoid of surface ornamentation.

1 Claim, 3 Drawing Sheets



US D636,356 S

Page 2

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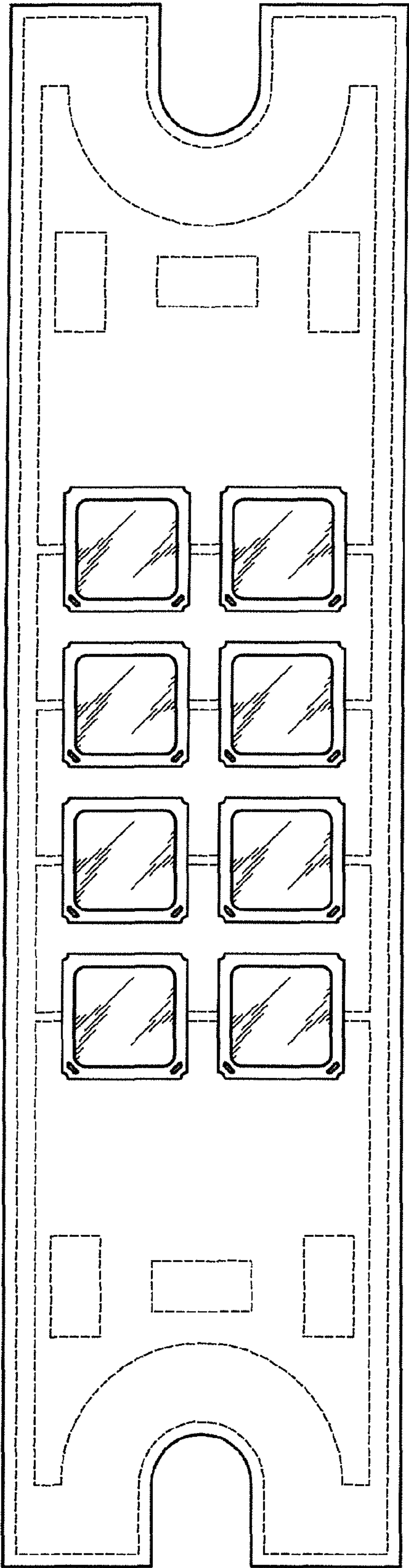


FIG. 1

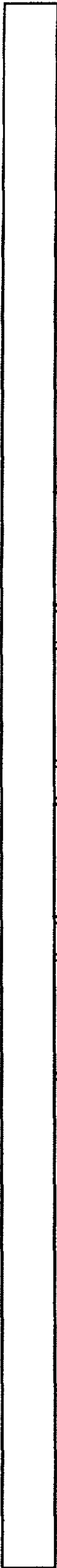


FIG. 2



FIG. 3



FIG. 4



FIG. 5