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(12) **United States Design Patent**
DePaula

(10) **Patent No.:** **US D632,298 S**

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(54) **ELECTRONIC MEMORY DEVICE**

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(57) **CLAIM**

(**) **Term:** **14 Years**

I claim the ornamental design for an electronic memory device, as shown and described.

(21) **Appl. No.:** **29/326,751**

(22) **Filed:** **Oct. 23, 2008**

DESCRIPTION

(51) **LOC (9) Cl.** **14-02**

FIG. 1 is an isometric view of a top surface of an electronic memory device.

(52) **U.S. Cl.** **D14/436**

FIG. 2 is an isometric view of a bottom surface of the electronic memory device.

(58) **Field of Classification Search** D14/435–438, D14/478–480; 235/441, 443, 487, 492, 493, 235/495, 380; 361/737, 679, 752, 736, 728, 361/796, 797, 729, 730, 725–727; 257/678, 257/679, 693, E23.064; 439/946, 948, 159; 174/250, 260, 52.1, 52.2; 710/300–303; 438/121

FIG. 3 is a plan view of a top surface of the electronic memory device.

See application file for complete search history.

FIG. 4 is a plan view of a bottom surface of the electronic memory device.

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FIG. 5 is a side elevational view of the electronic memory device.

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FIG. 5A is an enlarged view of a first portion of the side elevational view of FIG. 5.

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FIG. 5B is an enlarged view of a second portion of the side elevational view of FIG. 5.

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FIG. 6 is a side elevational view of the electronic memory device.

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FIG. 6A is an enlarged view of a first portion of the side elevational view of FIG. 6.

FIG. 6B is an enlarged view of a second portion of the side elevational view of FIG. 6.

FIG. 7 is a rear elevational view of the electronic memory device; and,

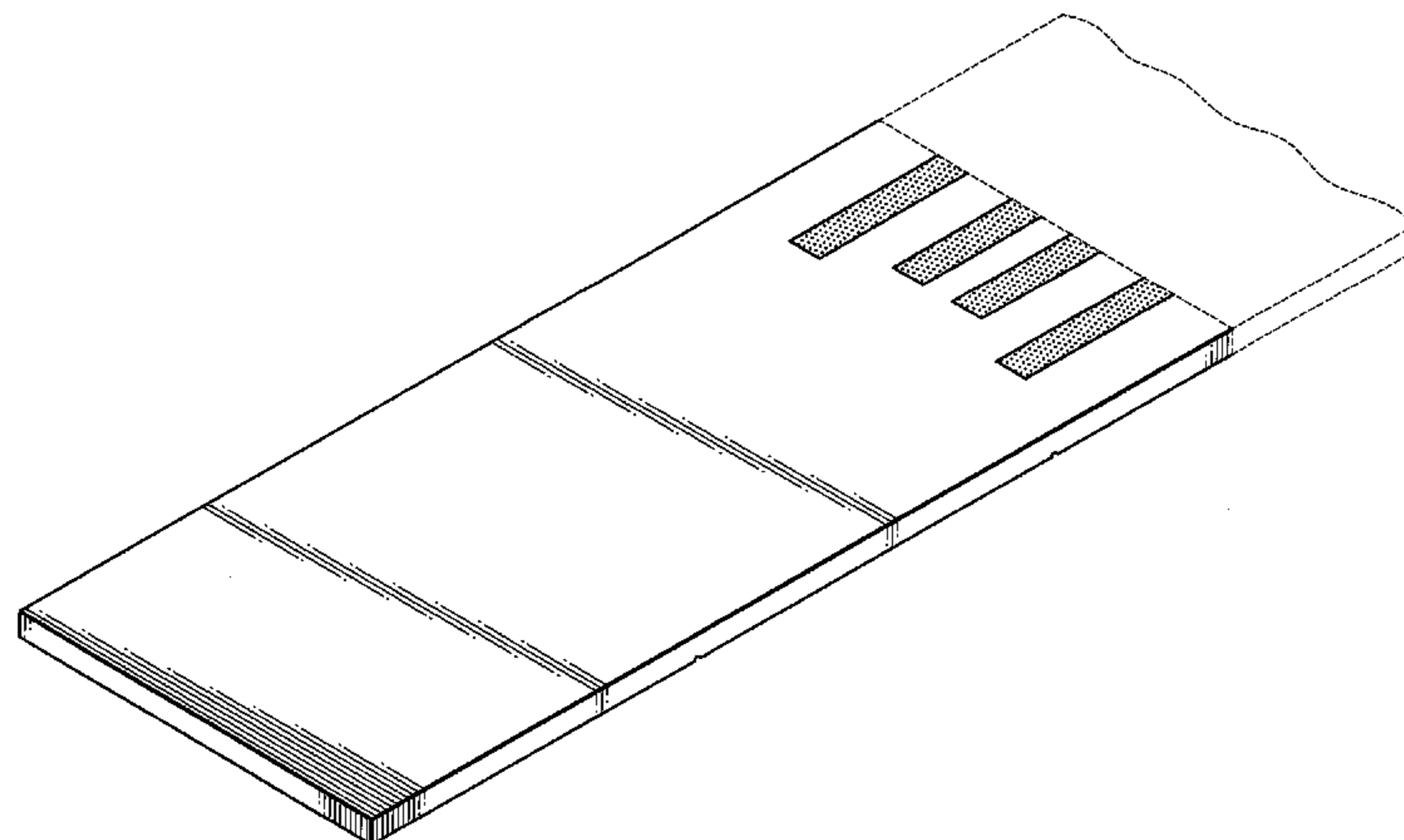
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FIG. 8 is a front elevational view of the electronic memory device.

Throughout the figures, portions of the electronic memory device shown in broken lines form no part of the claimed design.

Primary Examiner—Susan Moon Lee

1 Claim, 7 Drawing Sheets



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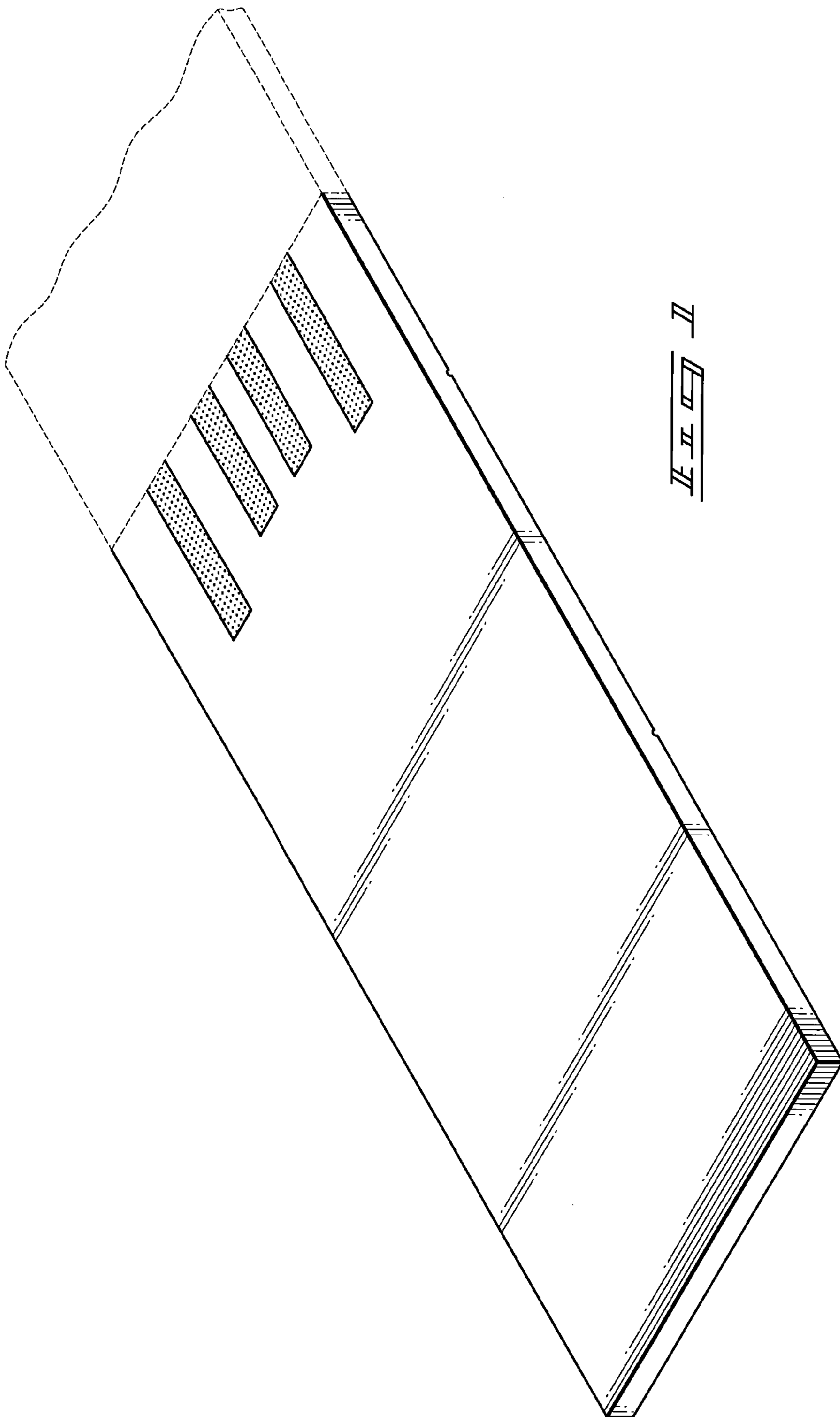
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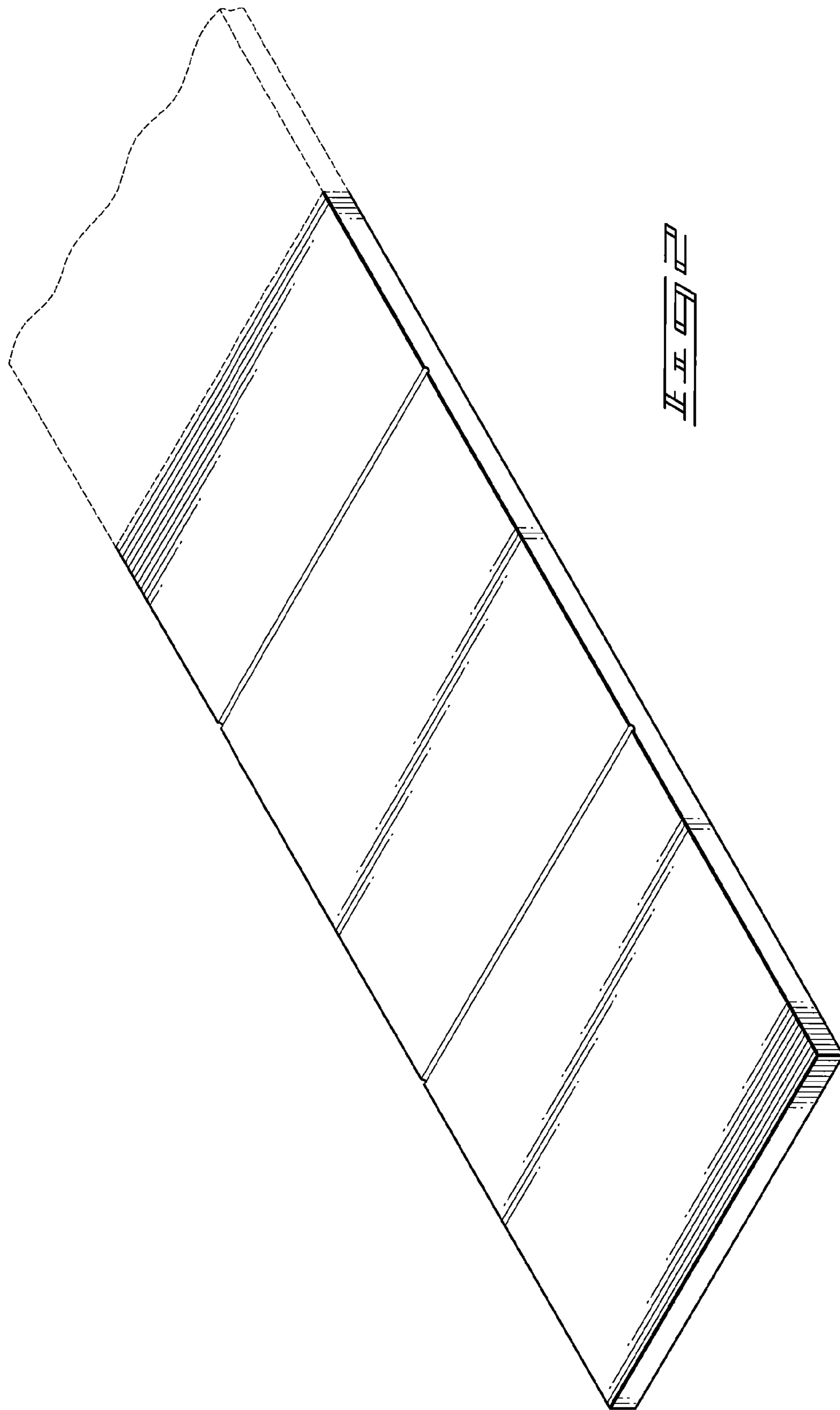


FIG. 2

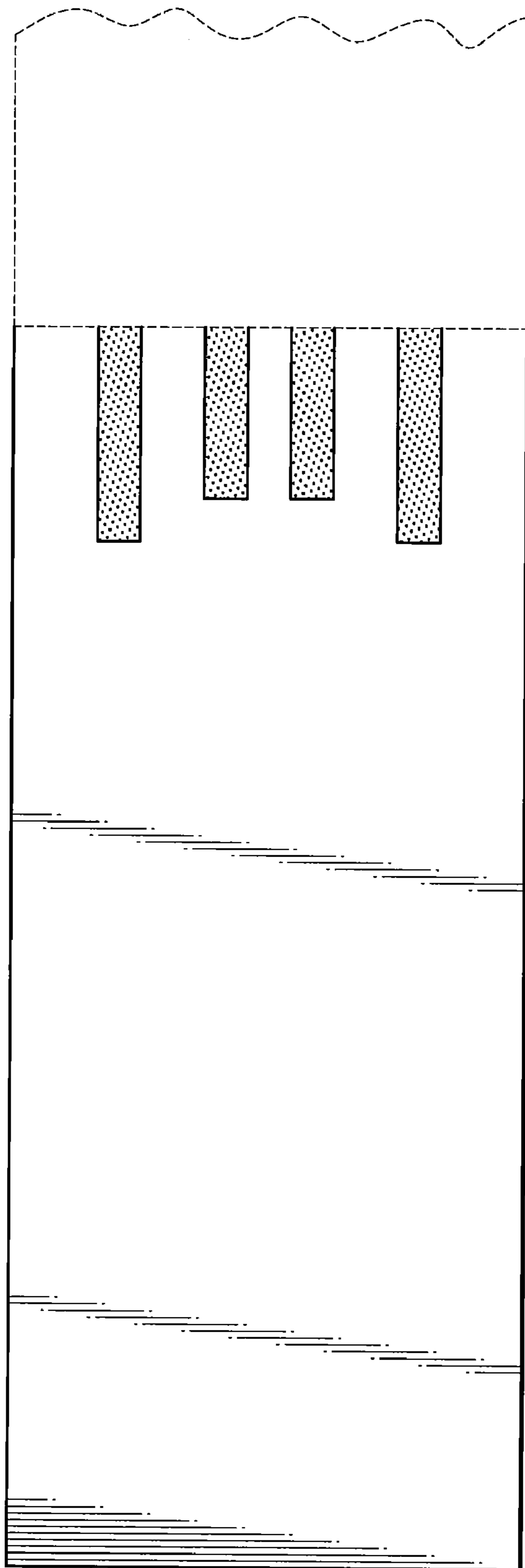


FIG. 3

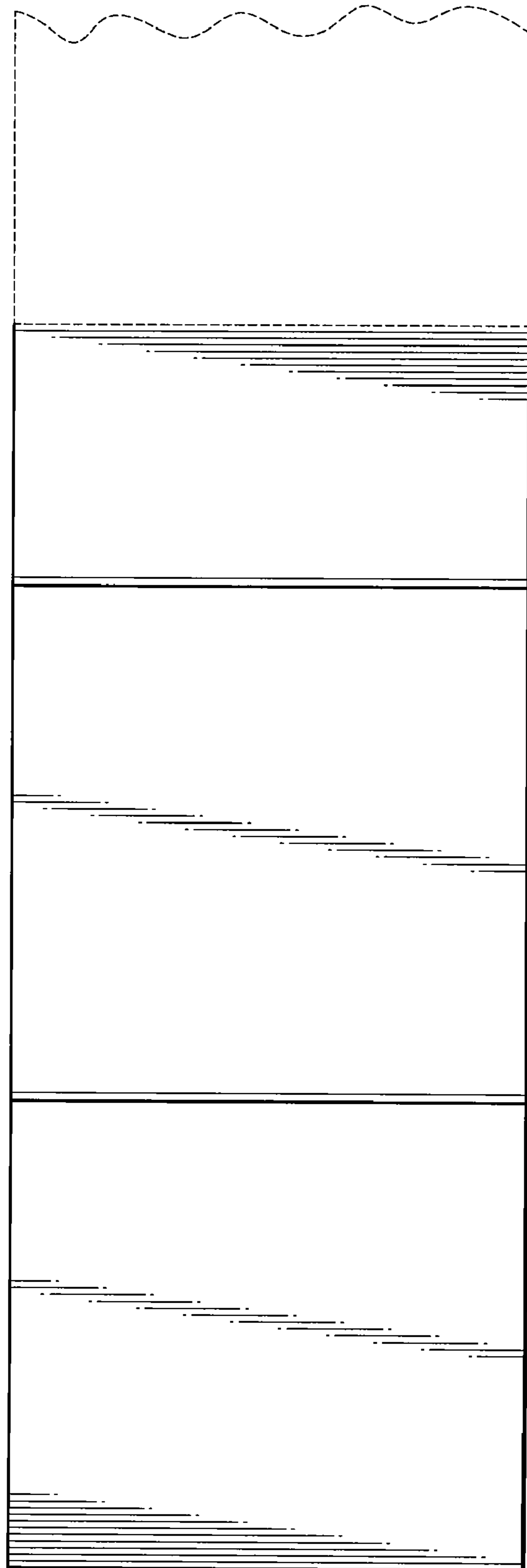
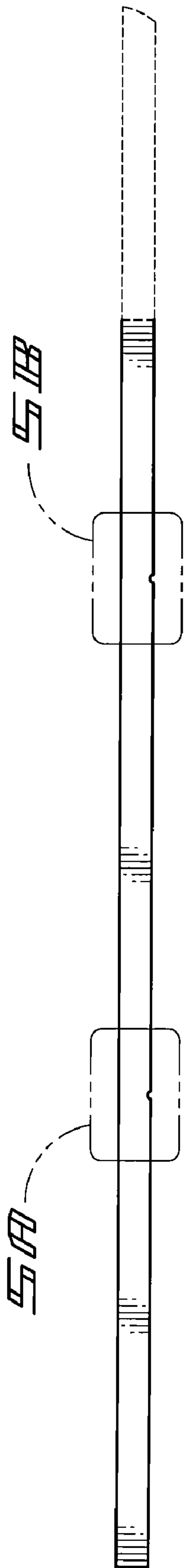
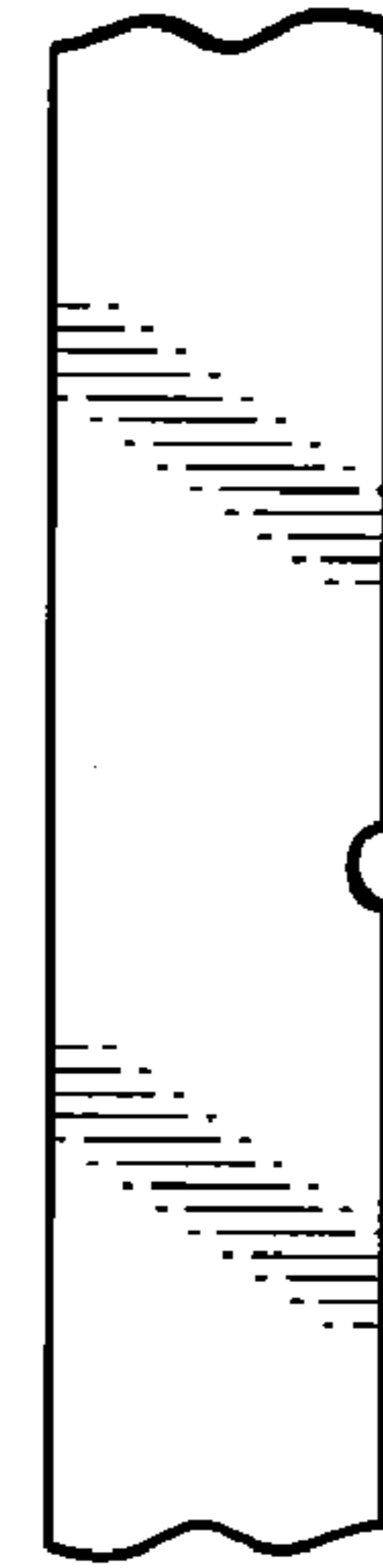


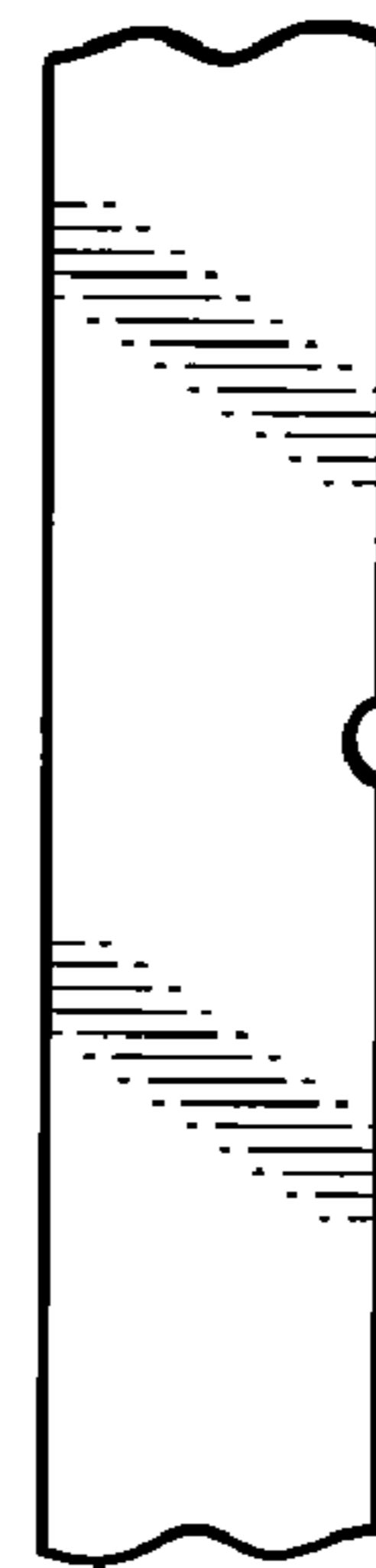
FIG. 4



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