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(12) **United States Design Patent**  
**Laursen et al.**

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(54) **EQUIPMENT RACK PANEL**

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(US)

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(73) Assignee: **Leviton Manufacturing Co., Inc.**, Little  
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THOMASNET.COM; Patch Panel and Panel Switch support Cat-6  
Switch; Open-Source Storage Press Release; Release date Feb. 10,  
2006.

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/284,404**

(Continued)

(22) Filed: **Sep. 7, 2007**

*Primary Examiner*—T. Chase Nelson  
*Assistant Examiner*—Eric L Goodman

(51) **LOC (9) Cl.** ..... **13-99**

(52) **U.S. Cl.** ..... **D13/199**

(58) **Field of Classification Search** ..... D13/199,  
D13/133, 158; D14/313, 439; 361/733,  
361/731, 735; D25/122, 124, 126

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See application file for complete search history.

(57) **CLAIM**

The ornamental design for an equipment rack panel, as shown  
and described.

(56) **References Cited**

**DESCRIPTION**

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FIG. 1 is a top front perspective view of showing our new  
design.

FIG. 2 is a front elevational view thereof.

FIG. 3 is a rear elevational view thereof.

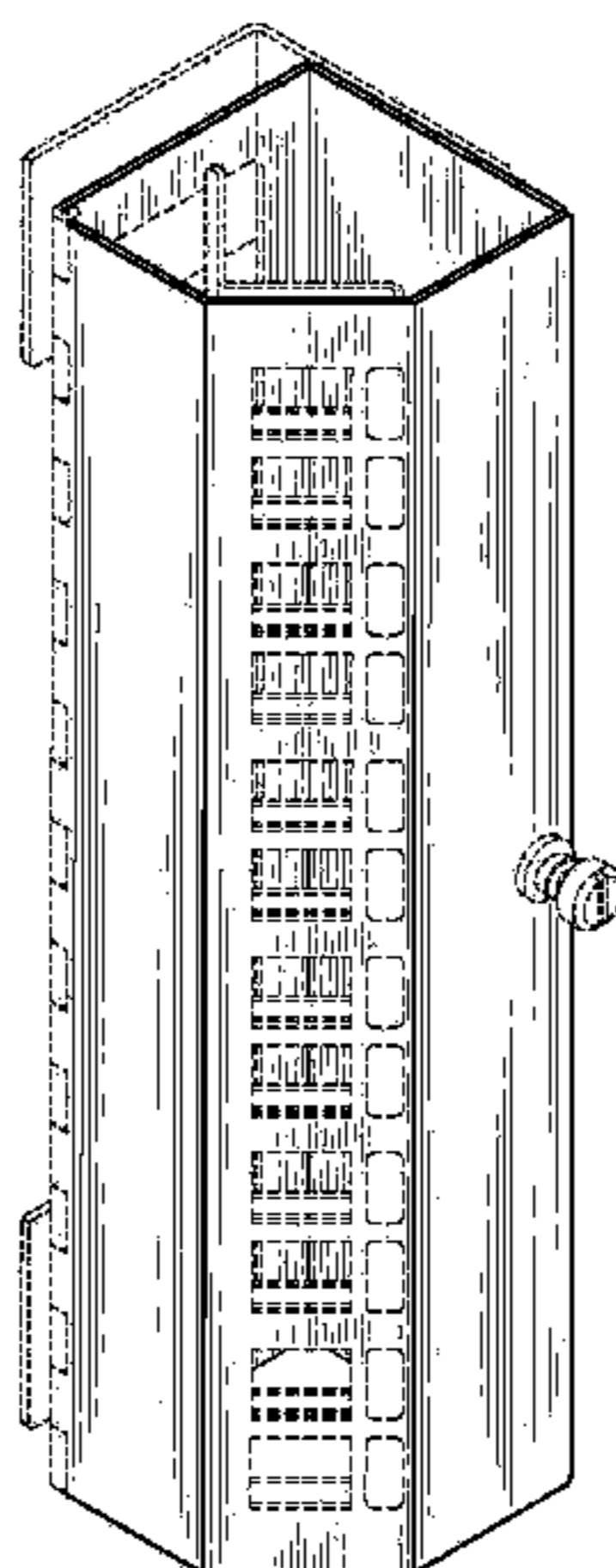
FIG. 4 is a left side elevational view thereof.

FIG. 5 is a right side elevational view thereof; and,

FIG. 6 is a top plan view thereof, the bottom plan view being  
the mirror image.

The broken lines depict environmental structure that forms no  
part of the claimed design, and in locations where the broken  
lines define openings in the claimed surface, they depict  
boundaries of the claimed design. The broken lines form no  
part of the claimed design.

**1 Claim, 6 Drawing Sheets**



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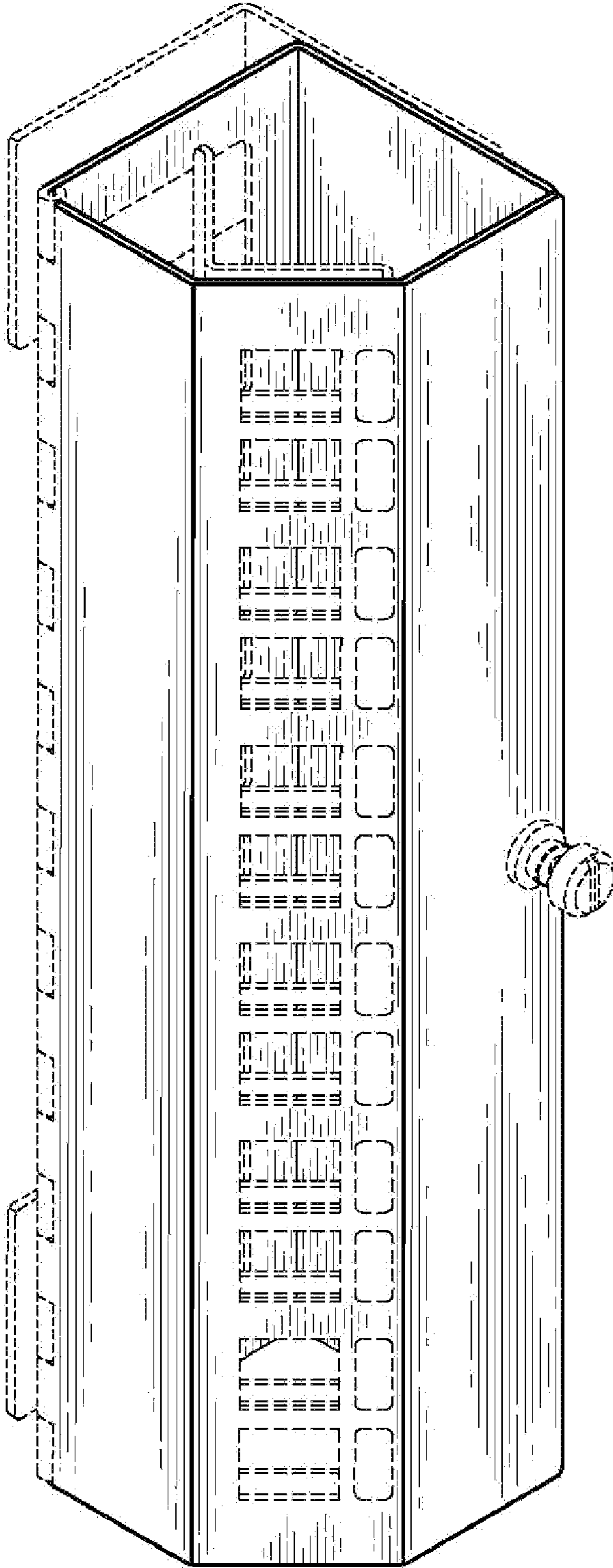
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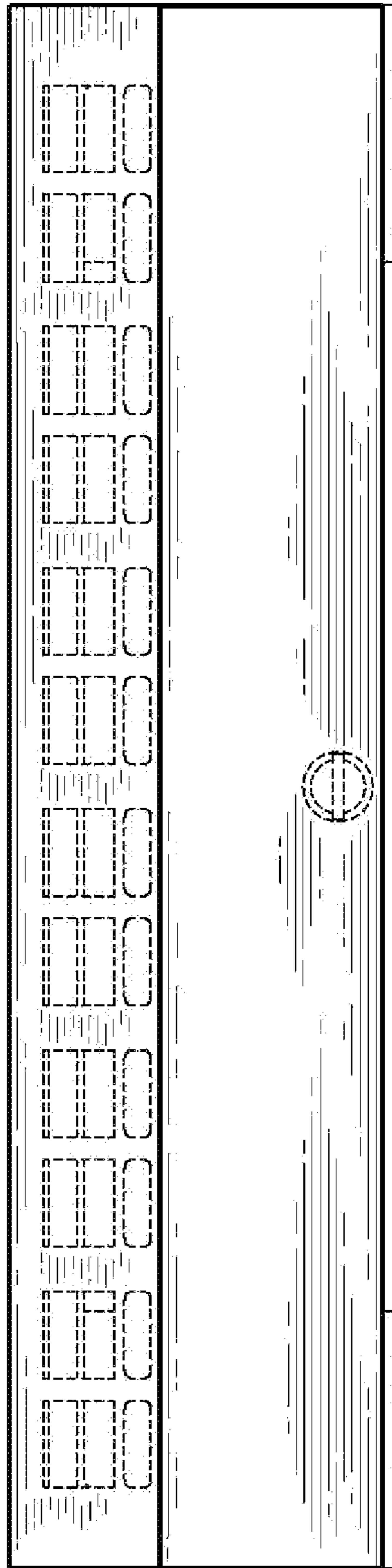
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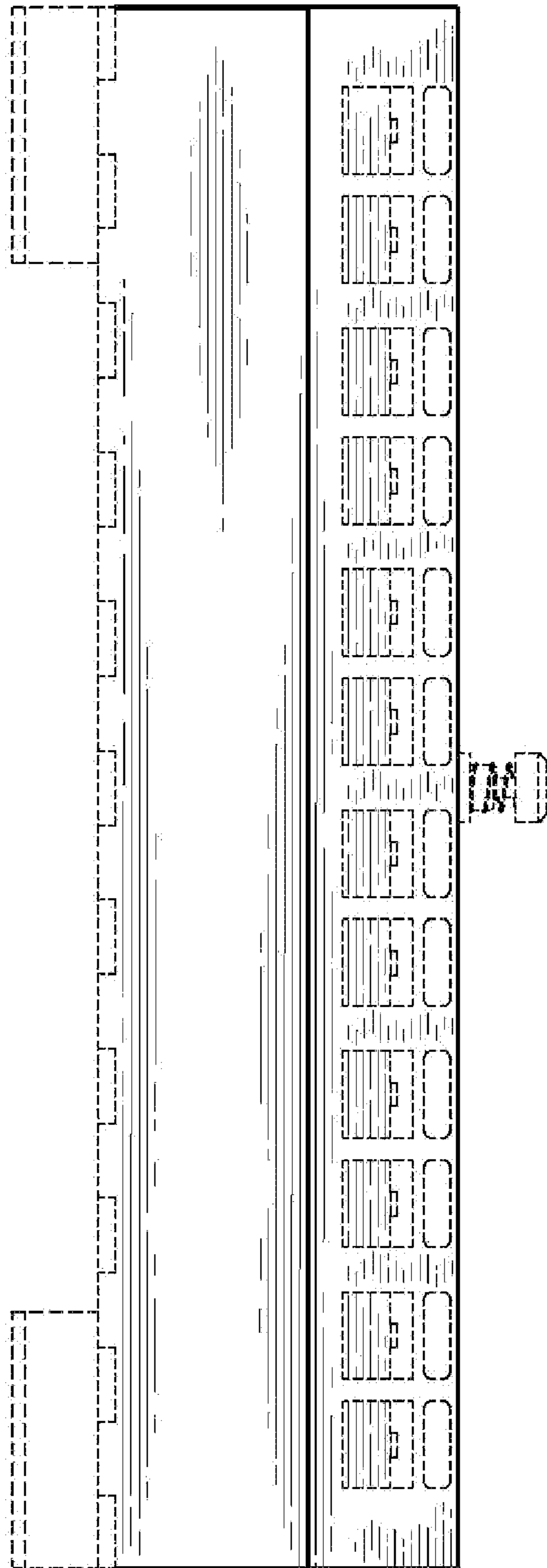
*Fig. 1*



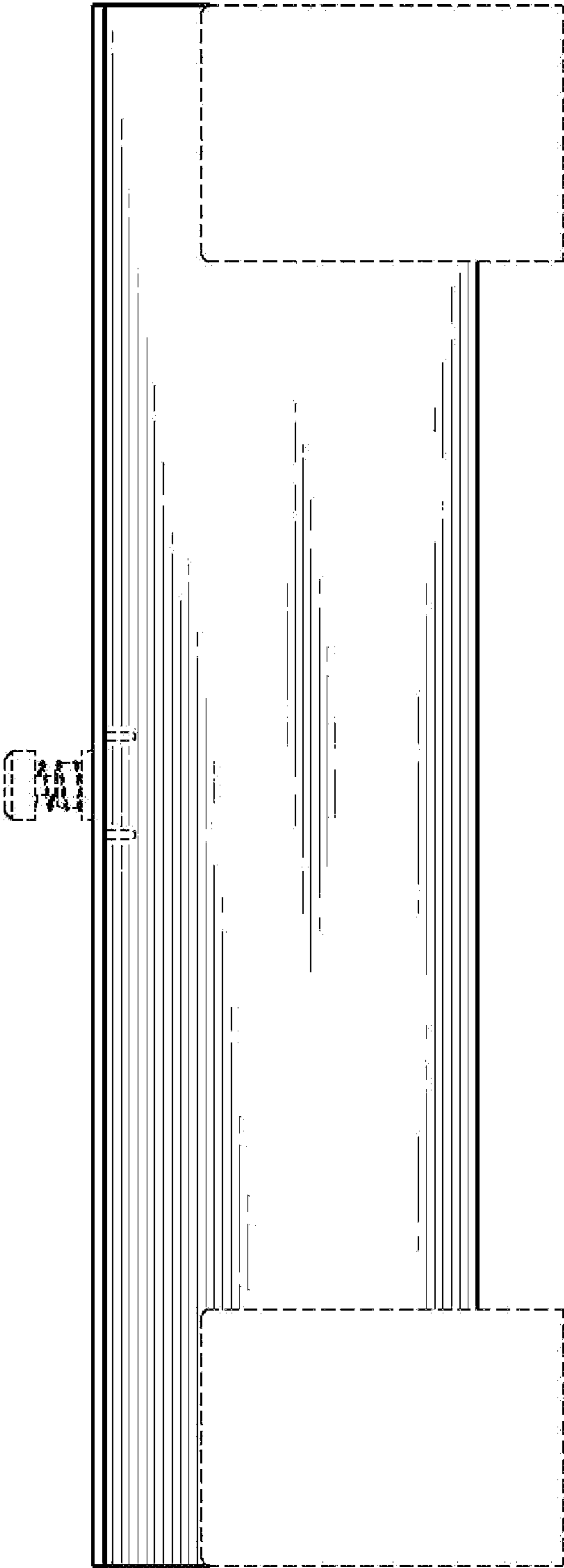
*Fig. 2*



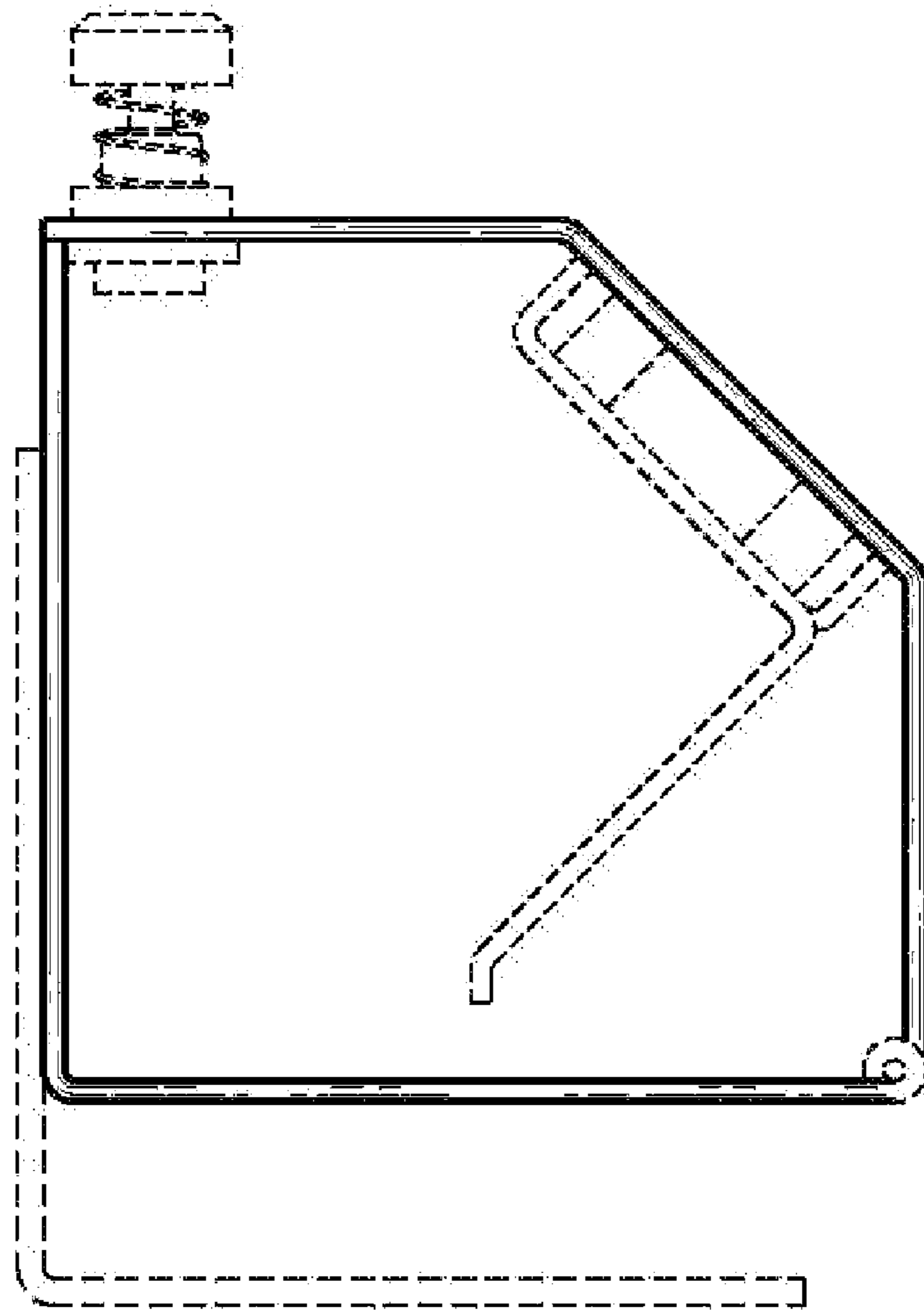
*Fig. 3*



*Fig. 4*



*Fig. 5*



*Fig. 6*