

US00D631021S

(12) **United States Design Patent**
Ni et al.

(10) **Patent No.:** **US D631,021 S**
(45) **Date of Patent:** **** Jan. 18, 2011**

(54) **LIGHT EMITTING DIODE**

(75) Inventors: **Chun-Yao Ni**, Tucheng (TW);
Chih-Chia Tsai, Tucheng (TW)

(73) Assignee: **Everlight Electronics Co., Ltd.**,
Tucheng, Taipei County (TW)

(**) Term: **14 Years**

(21) Appl. No.: **29/363,817**

(22) Filed: **Jun. 15, 2010**

Related U.S. Application Data

(63) Continuation of application No. 29/345,478, filed on
Oct. 16, 2009, now Pat. No. Des. 619,977.

(51) **LOC (9) Cl.** **13-03**

(52) **U.S. Cl.** **D13/180**

(58) **Field of Classification Search** D13/180;
D26/2; 257/79, 80, 81, 88, 89, 95, 98, 99,
257/100, E33.058; 313/483, 498, 500; 362/555,
362/800

See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

6,614,056	B1	9/2003	Tarsa et al.	
6,713,868	B2	3/2004	Ueno	
D566,665	S	4/2008	Liao et al.	
D575,245	S	8/2008	Yen	
D584,248	S	1/2009	Sung et al.	
D597,968	S	8/2009	Kobayakawa et al.	
D597,971	S	8/2009	Kobayakawa et al.	
D602,450	S	10/2009	Edmond et al.	
D619,977	S *	7/2010	Ni et al.	D13/180
2006/0220050	A1	10/2006	Higaki et al.	

FOREIGN PATENT DOCUMENTS

JP D1078478 7/2000

JP	D1309411	9/2007
JP	D1325977	3/2008
JP	D1346959	12/2008
JP	D1349554	1/2009
TW	584491	4/2004
TW	D108928	1/2006
TW	D112092	7/2006

OTHER PUBLICATIONS

Taiwan Office Action mailed Aug. 16, 2010.

* cited by examiner

Primary Examiner—Selina Sikder

(74) *Attorney, Agent, or Firm*—Thomas, Kayden,
Horstemeyer & Risley, LLP

(57) **CLAIM**

The ornamental design for a light emitting diode, as shown
and described.

DESCRIPTION

FIG. 1 is a front perspective view of a light emitting diode
showing the claimed design;

FIG. 2 is a rear perspective view thereof;

FIG. 3 is a front elevational view thereof;

FIG. 4 is a rear elevational view thereof;

FIG. 5 is a left side elevational view thereof;

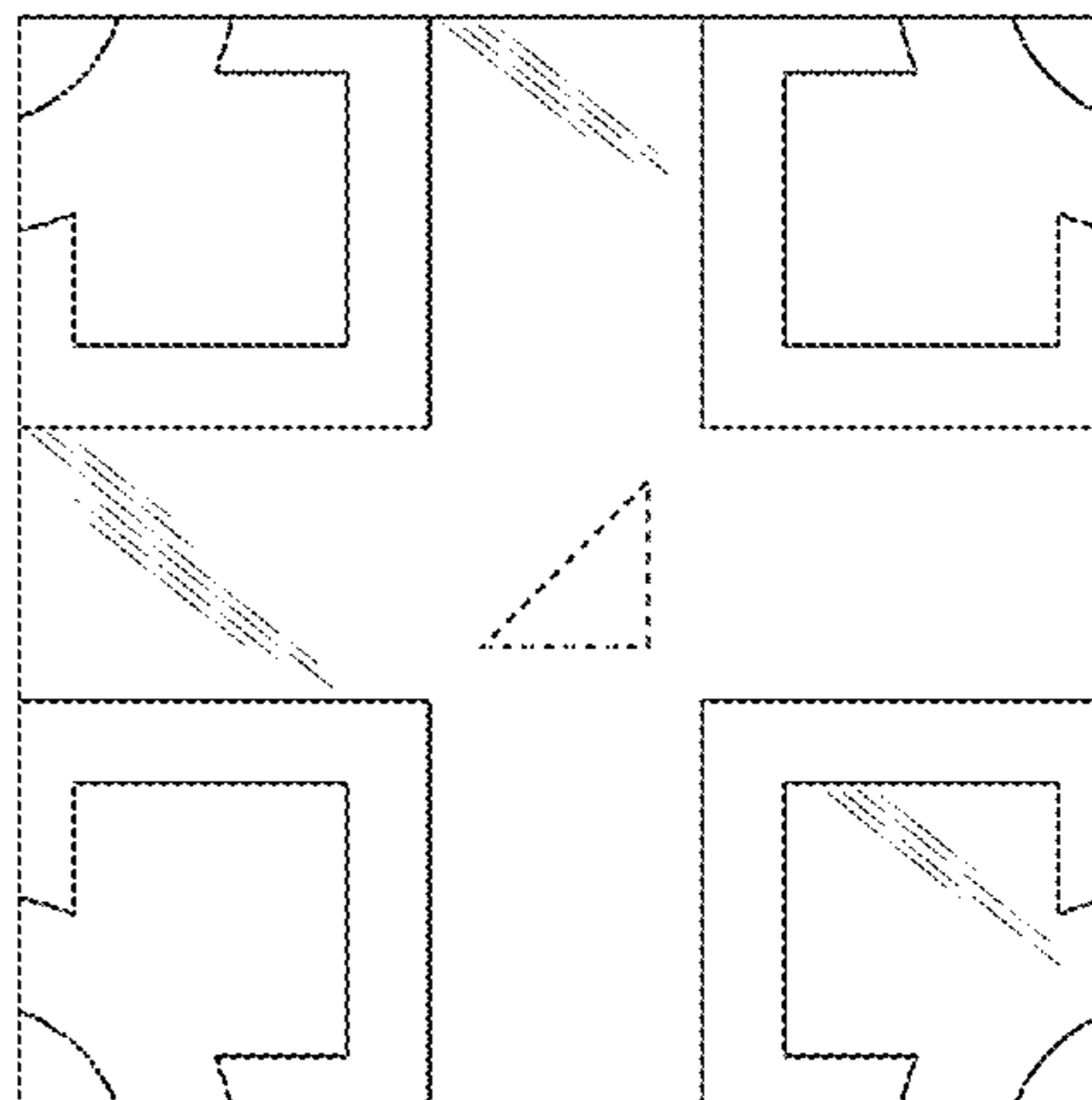
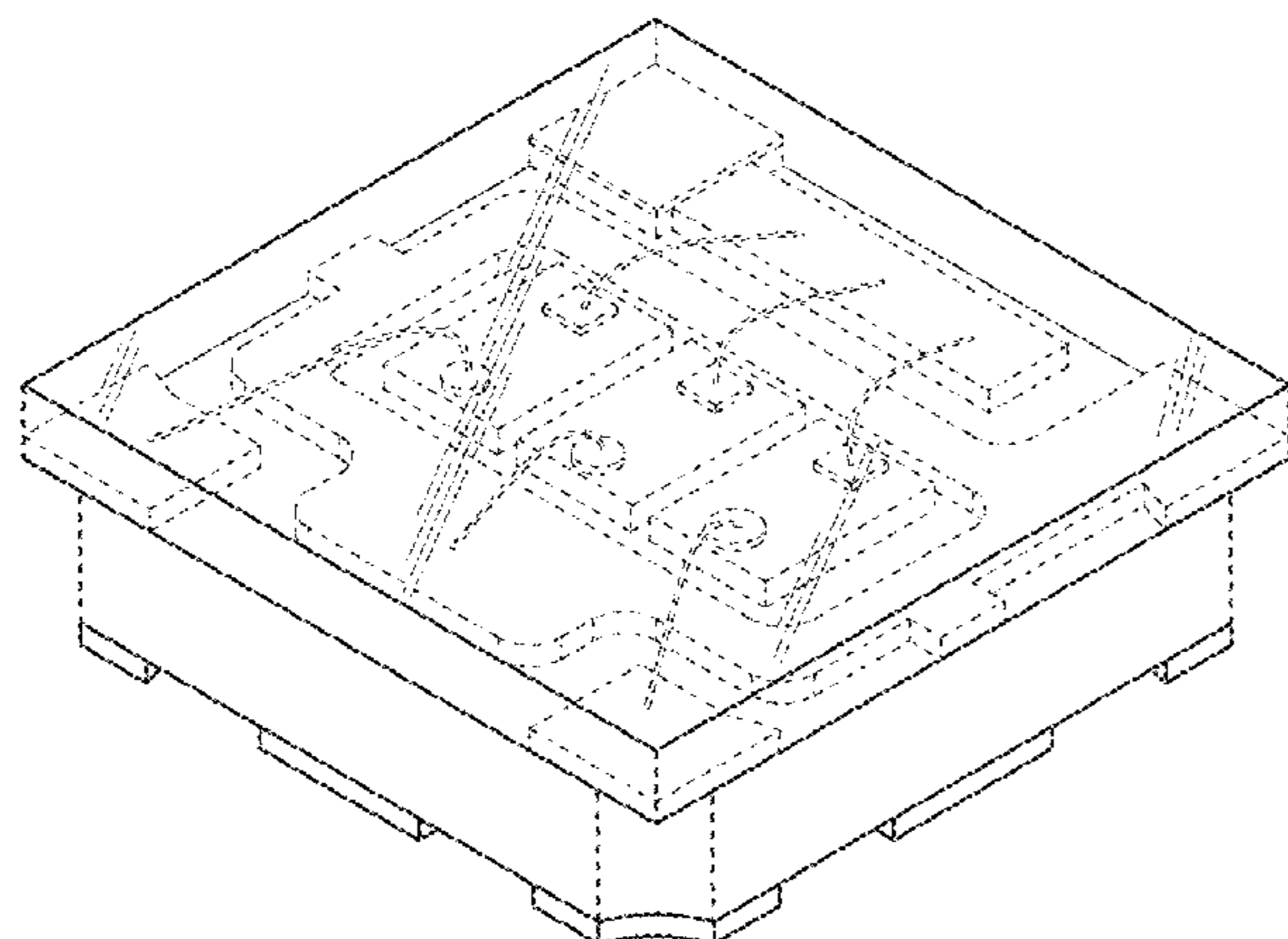
FIG. 6 is a right side elevational view thereof;

FIG. 7 is a top plan view thereof; and,

FIG. 8 is a bottom plan view thereof.

Several of the broken lines represent the bounds of the
claimed design while the other broken lines at the inner por-
tion and the bottom portions are directed to environment and
are for illustrative purposes only; the broken lines form no
part of the claimed design. Also, the top layer is substantially
transparent or translucent.

1 Claim, 5 Drawing Sheets



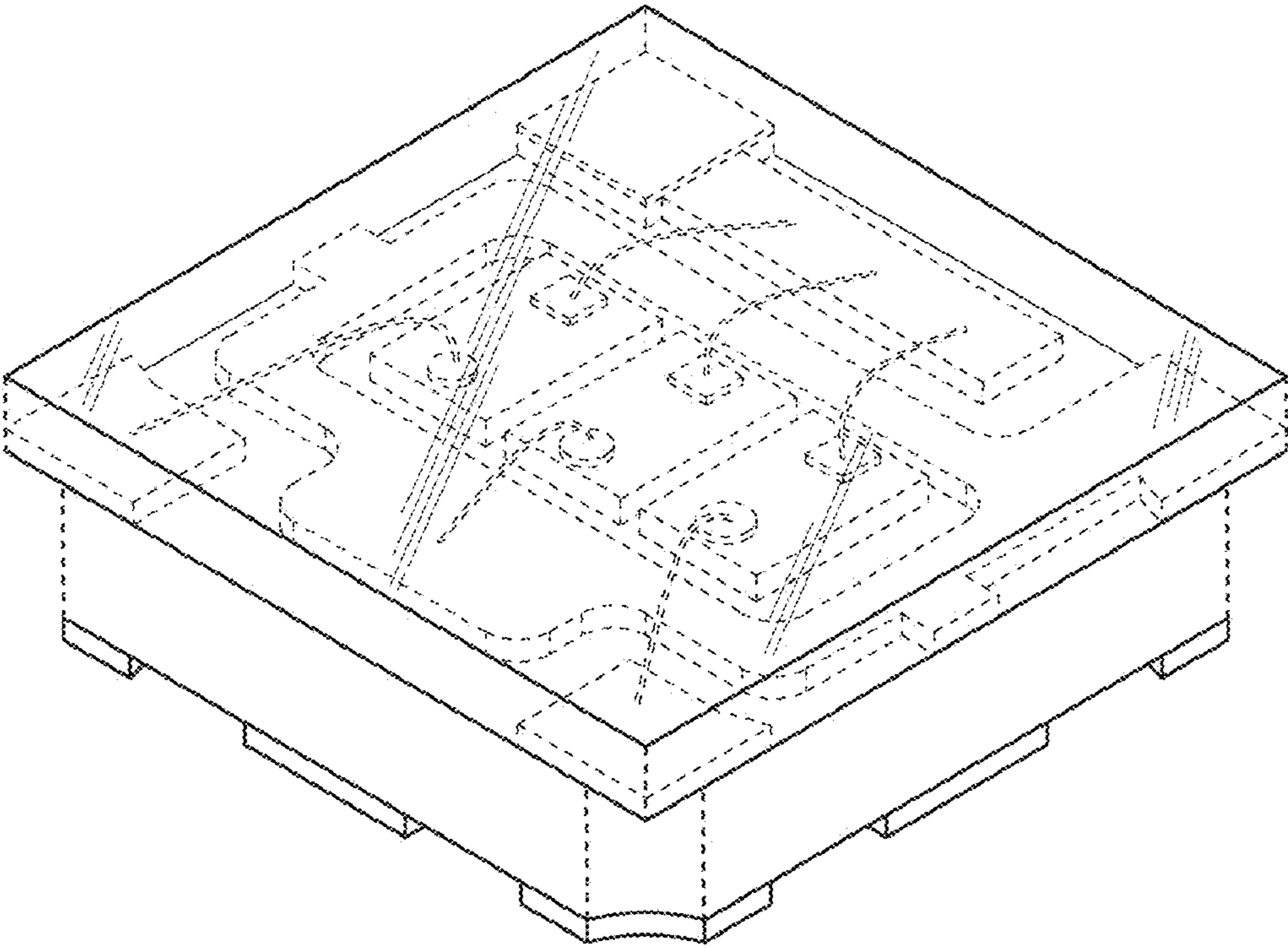


FIG. 1

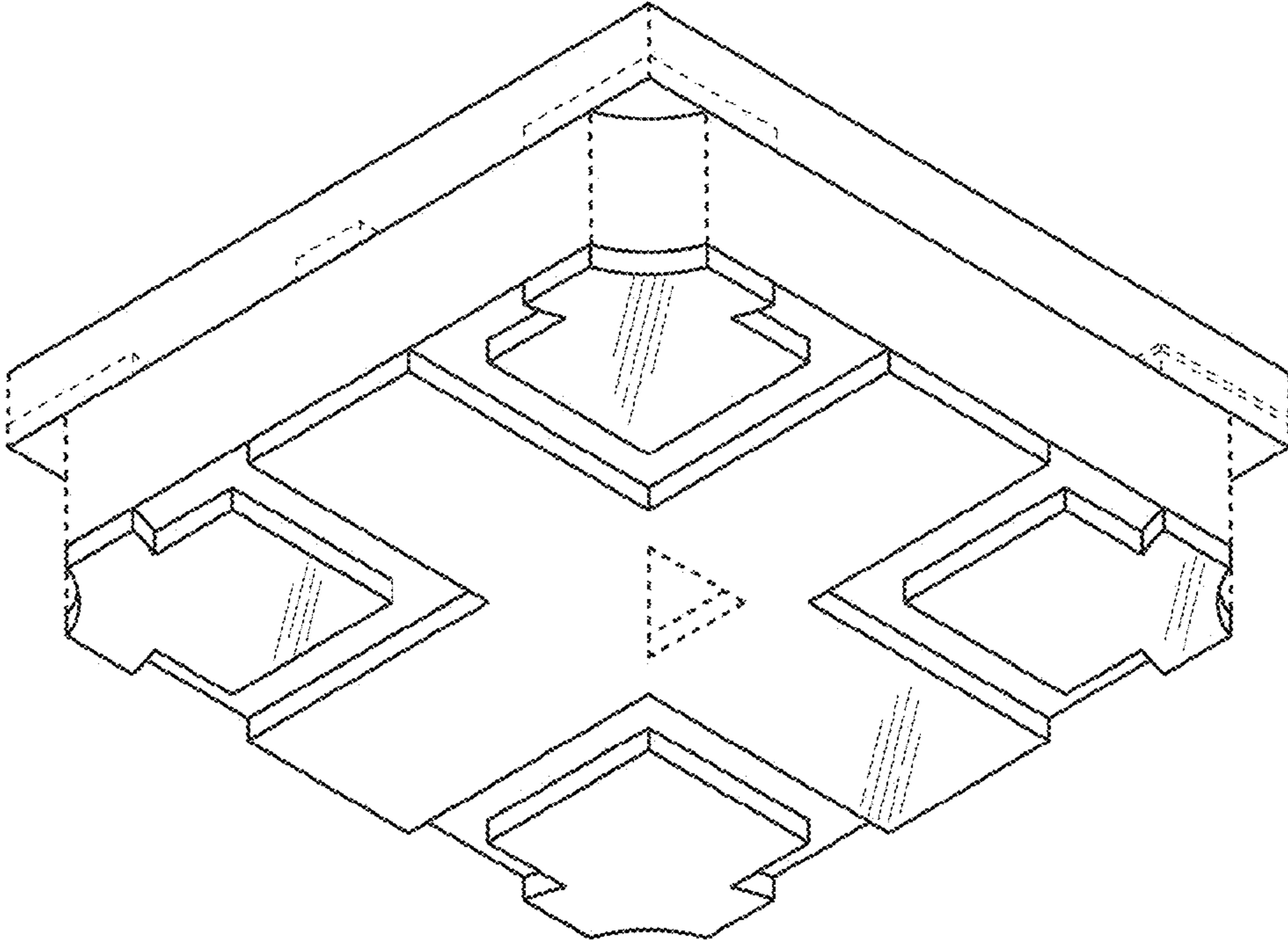


FIG. 2

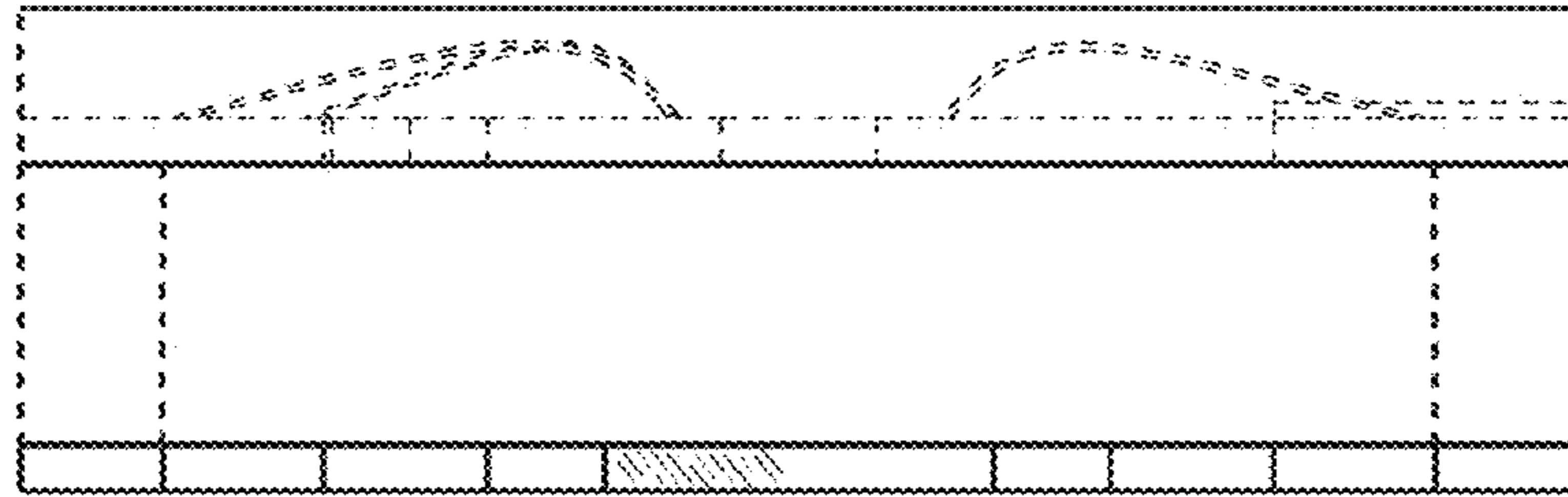


FIG. 3

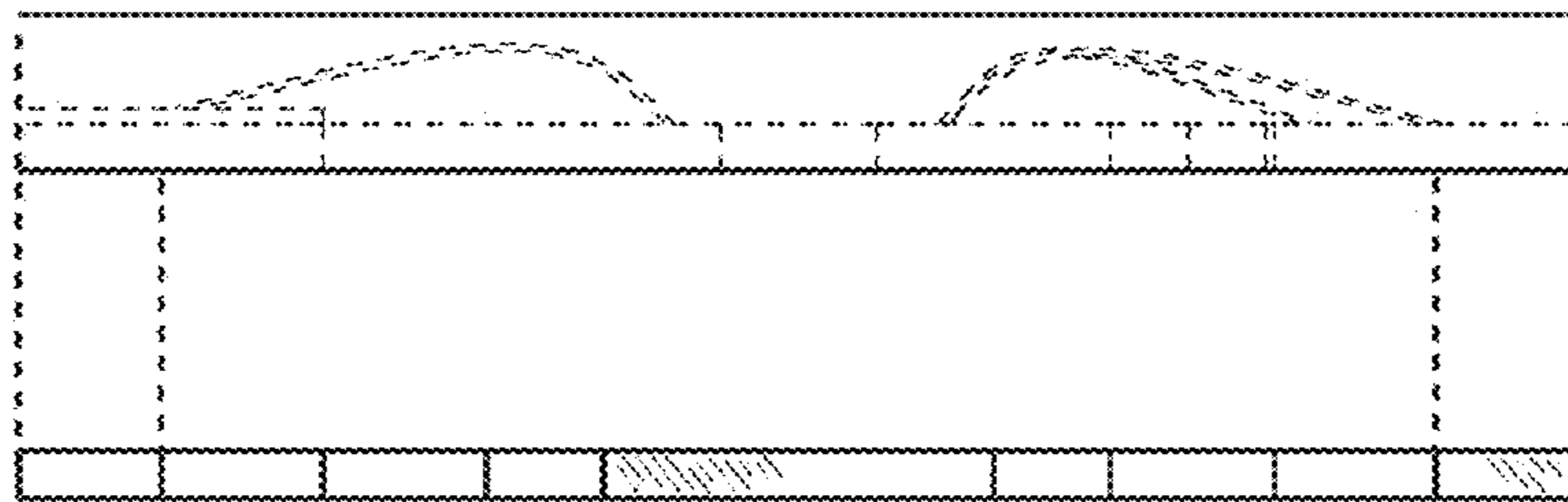


FIG. 4

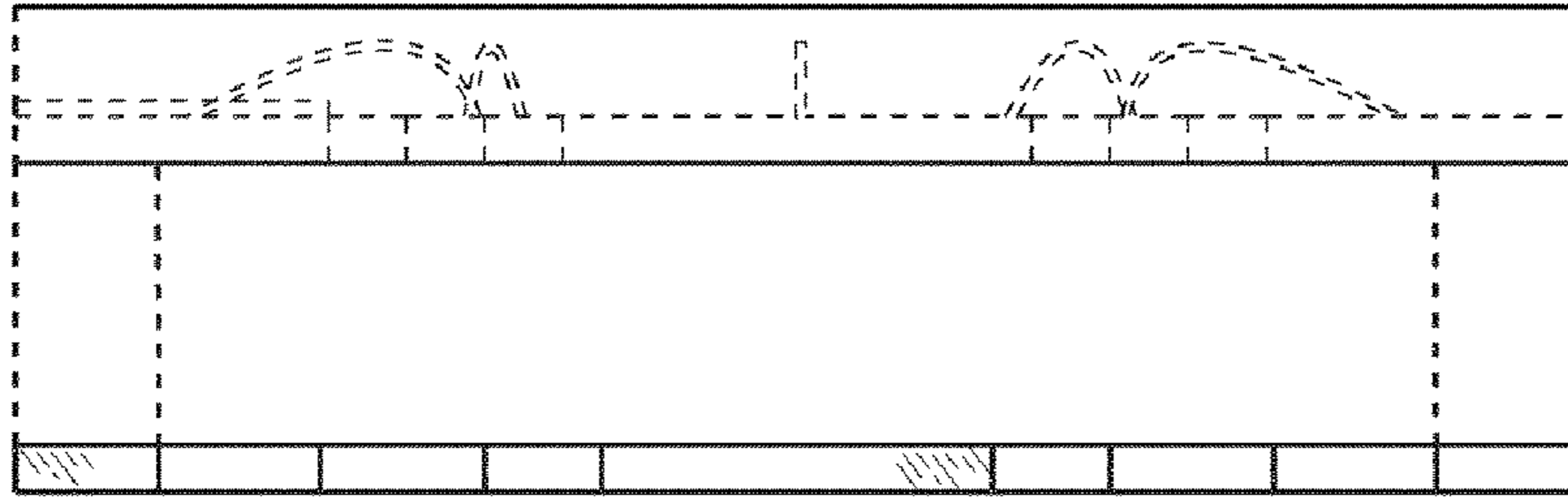


FIG. 5

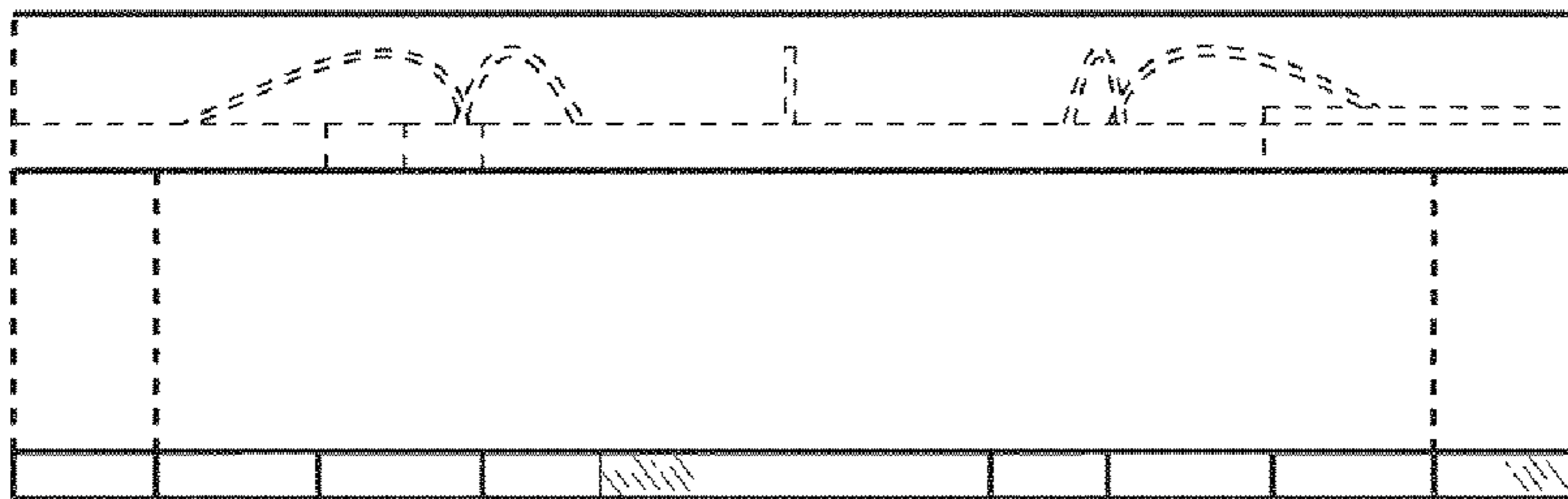


FIG. 6

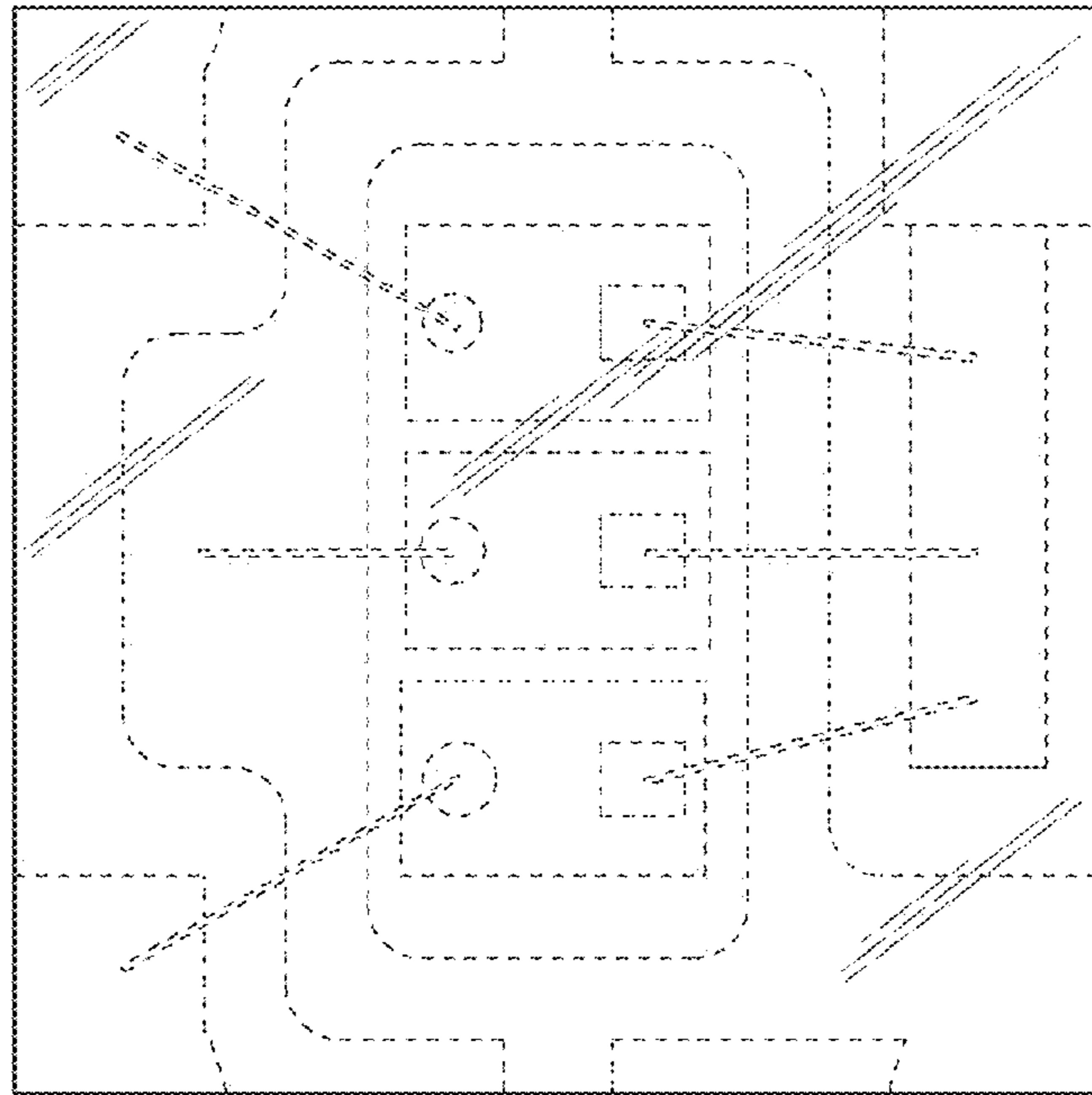


FIG. 7

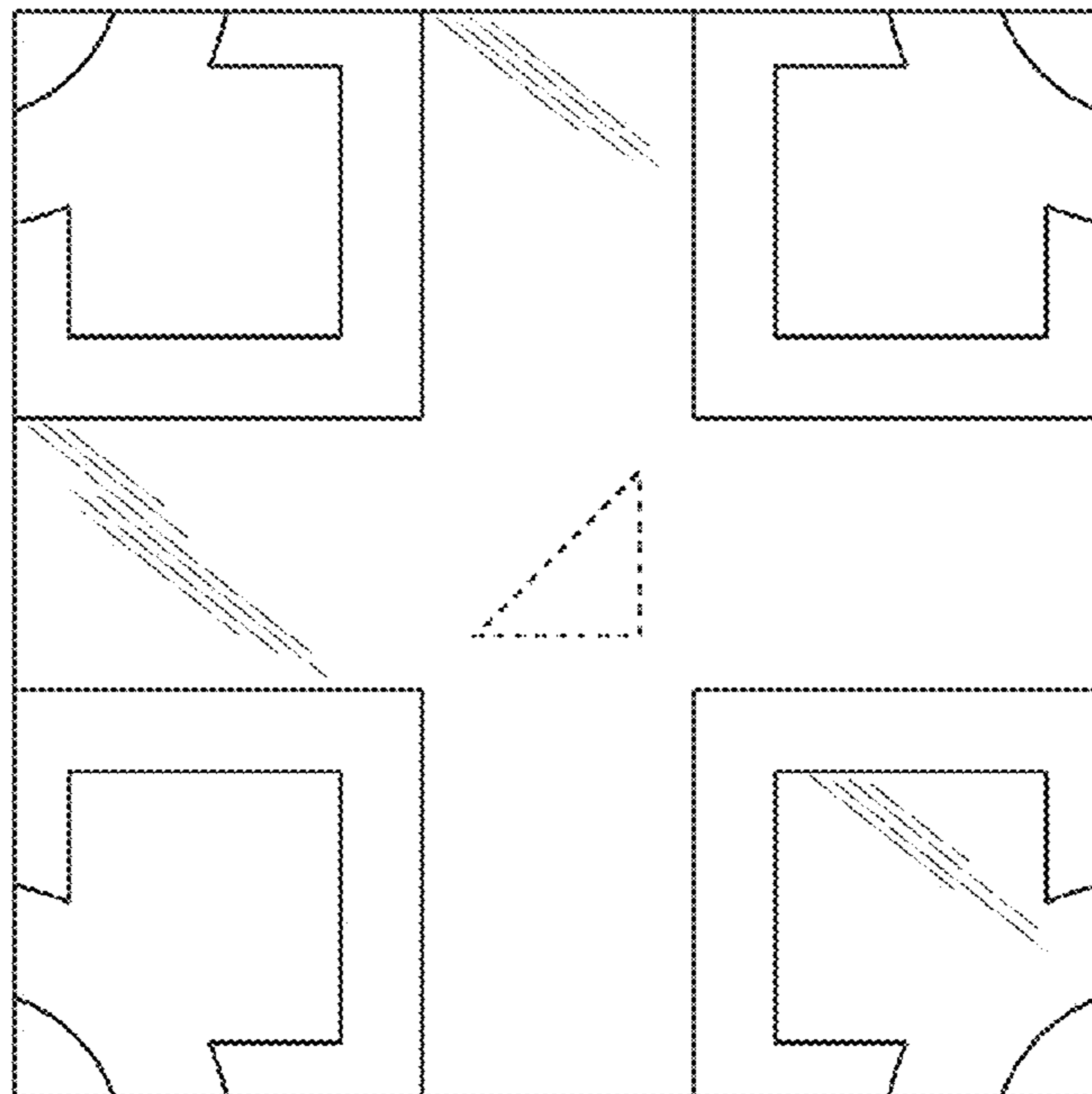


FIG. 8