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Lindsey et al.

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(54) **INTERFACE ON AN ELECTRONICS CONNECTOR**

D395,284 S * 6/1998 Downing et al. D13/184
D401,913 S * 12/1998 Beck et al. D13/184
D404,711 S * 1/1999 Lai et al. D13/147
D465,207 S * 11/2002 Williams et al. D13/182

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* cited by examiner

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(**) Term: **14 Years**

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(21) Appl. No.: **29/327,293**

(57) **CLAIM**

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The ornamental design for an interface on an electronics connector, as shown and described.

(51) **LOC (9) Cl.** **13-03**

(52) **U.S. Cl.** **D13/154**

(58) **Field of Classification Search** D13/147,
D13/154, 184; 439/500, 607–610, 883, 892,
439/894, 901, 943

See application file for complete search history.

DESCRIPTION

The sole FIGURE is a top plan view of an interface on an electronics connector showing our new design.

The broken line portion of the FIGURE drawing is included to show unclaimed subject matter only and forms no part of the claimed design.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D394,845 S * 6/1998 Hirsbrunner et al. D13/184

1 Claim, 1 Drawing Sheet



