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(12) **United States Design Patent**  
**Beland et al.**

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(54) **ELECTRONIC TIMER HOUSING**

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(51) **LOC (9) Cl.** ..... **10-03**

(52) **U.S. Cl.** ..... **D10/40; D10/123**

(58) **Field of Classification Search** ..... D10/40,  
D10/122, 123, 124; D13/138.1, 137.1, 137.3,  
D13/139.8, 139.6, 162, 162.1; 368/107-123;  
307/141, 141.4; 200/38 R, 33 R, 38 D, 38 A,  
200/38 F

See application file for complete search history.

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(57) **CLAIM**

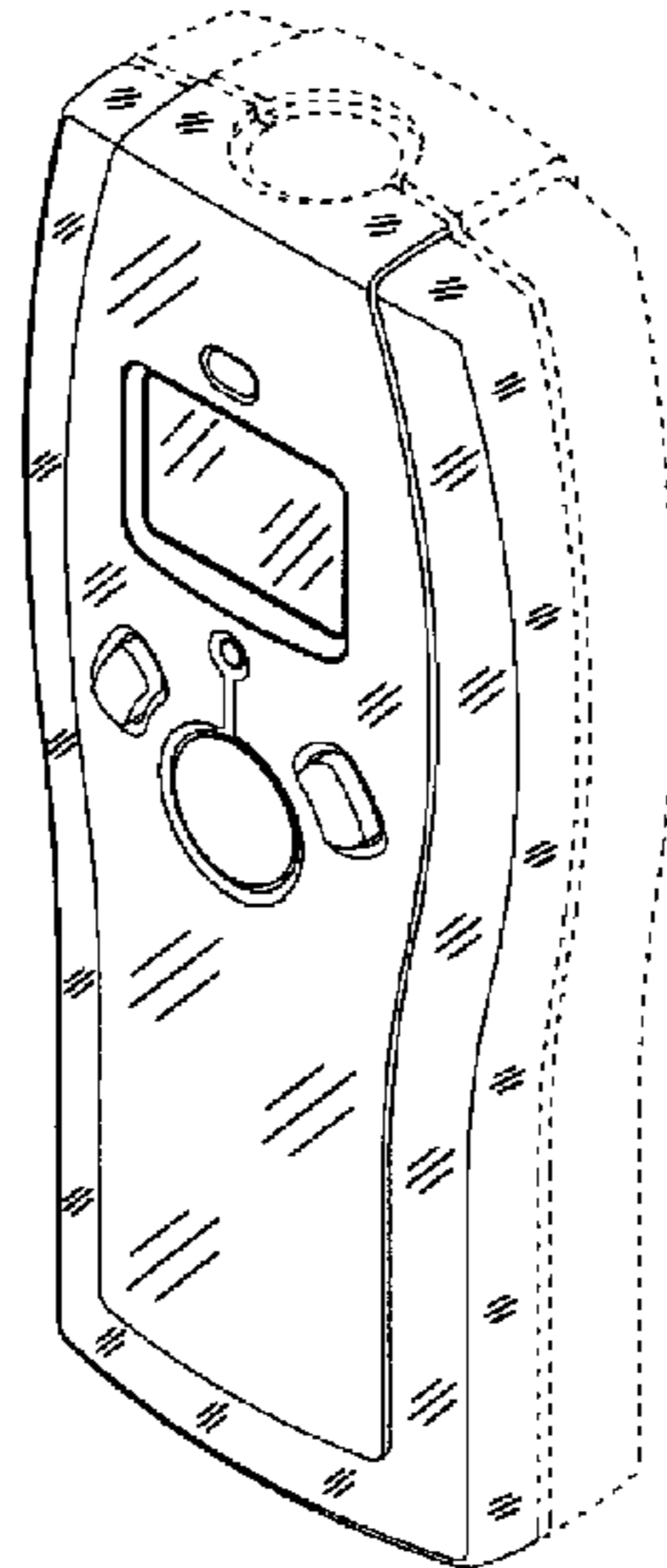
The ornamental design for an electronic timer housing, as shown and described.

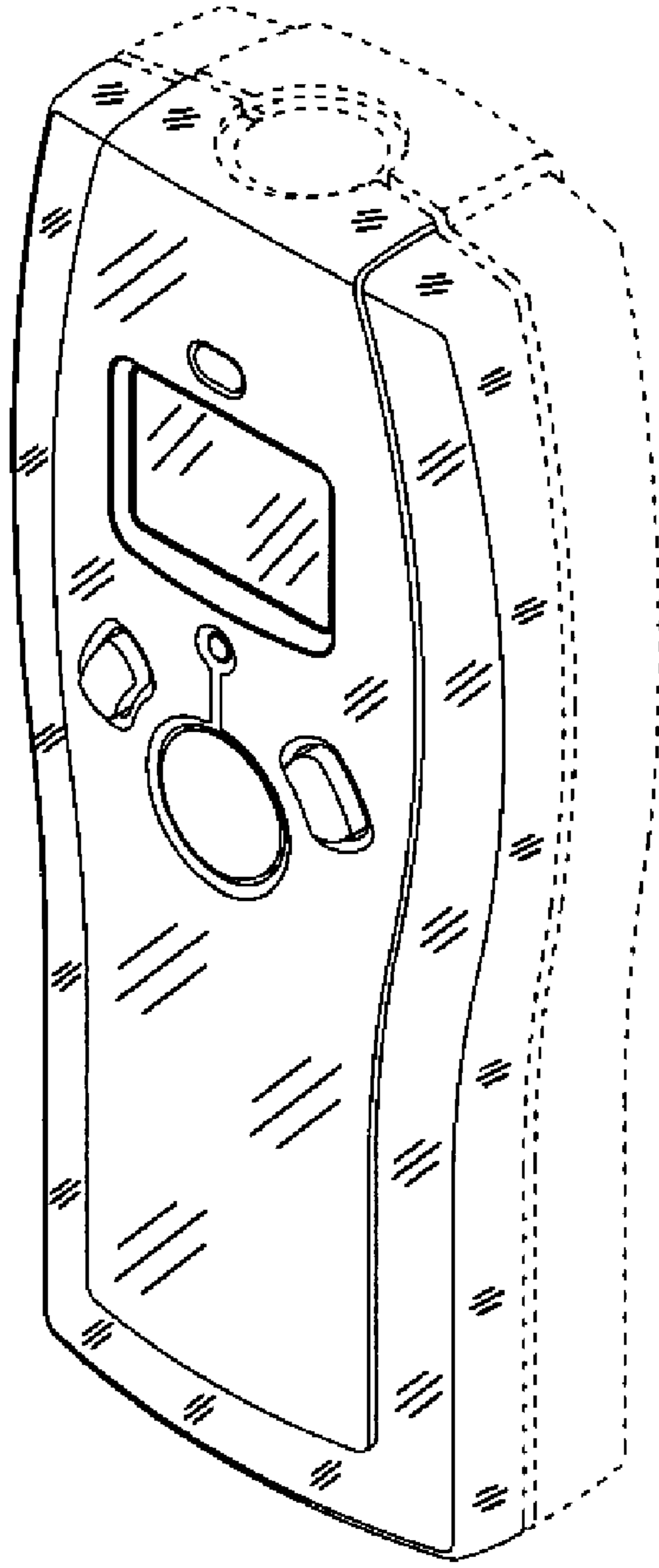
**DESCRIPTION**

FIG. 1 is a perspective view of the electronic timer housing; FIG. 2 is a front elevation thereof; FIG. 3 is a left side elevation thereof; FIG. 4 is a right side elevation thereof; FIG. 5 is a top plan view thereof; and, FIG. 6 is a bottom plan view thereof.

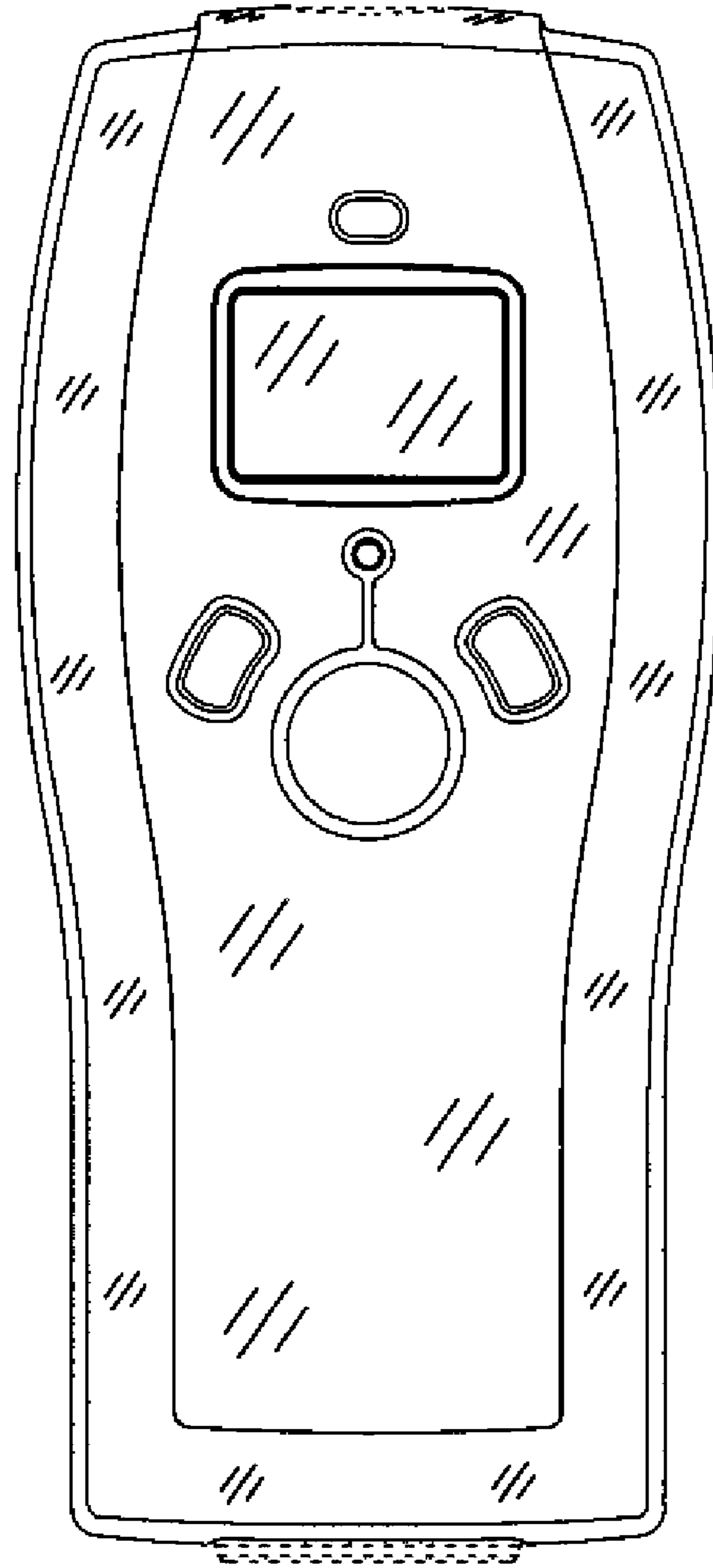
The portions illustrated in broken lines on the figures are for illustrative purposes only and form no part of the claimed design.

**1 Claim, 6 Drawing Sheets**

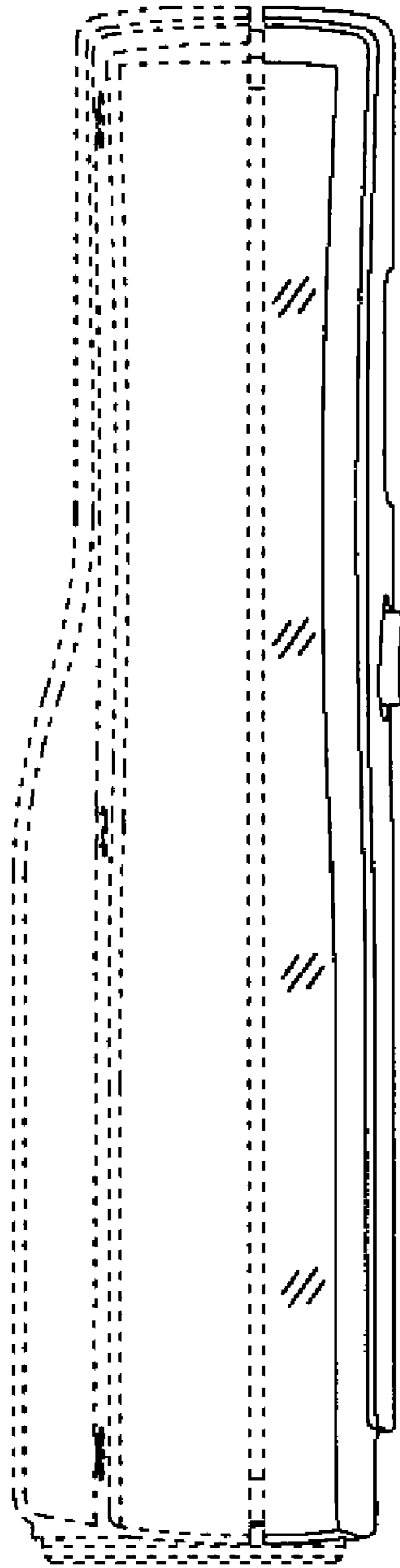




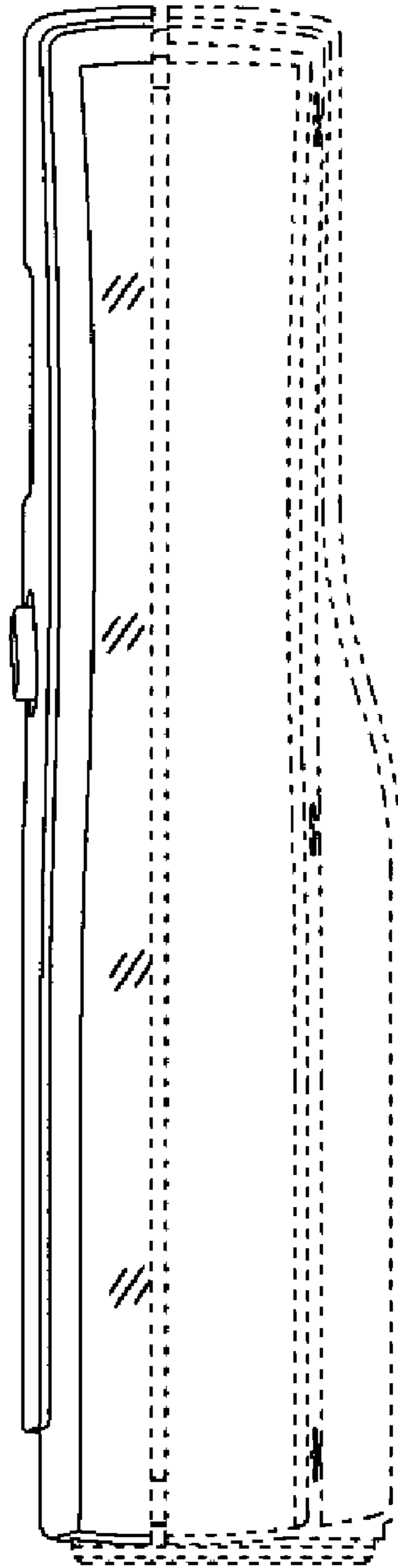
*Figure 1*



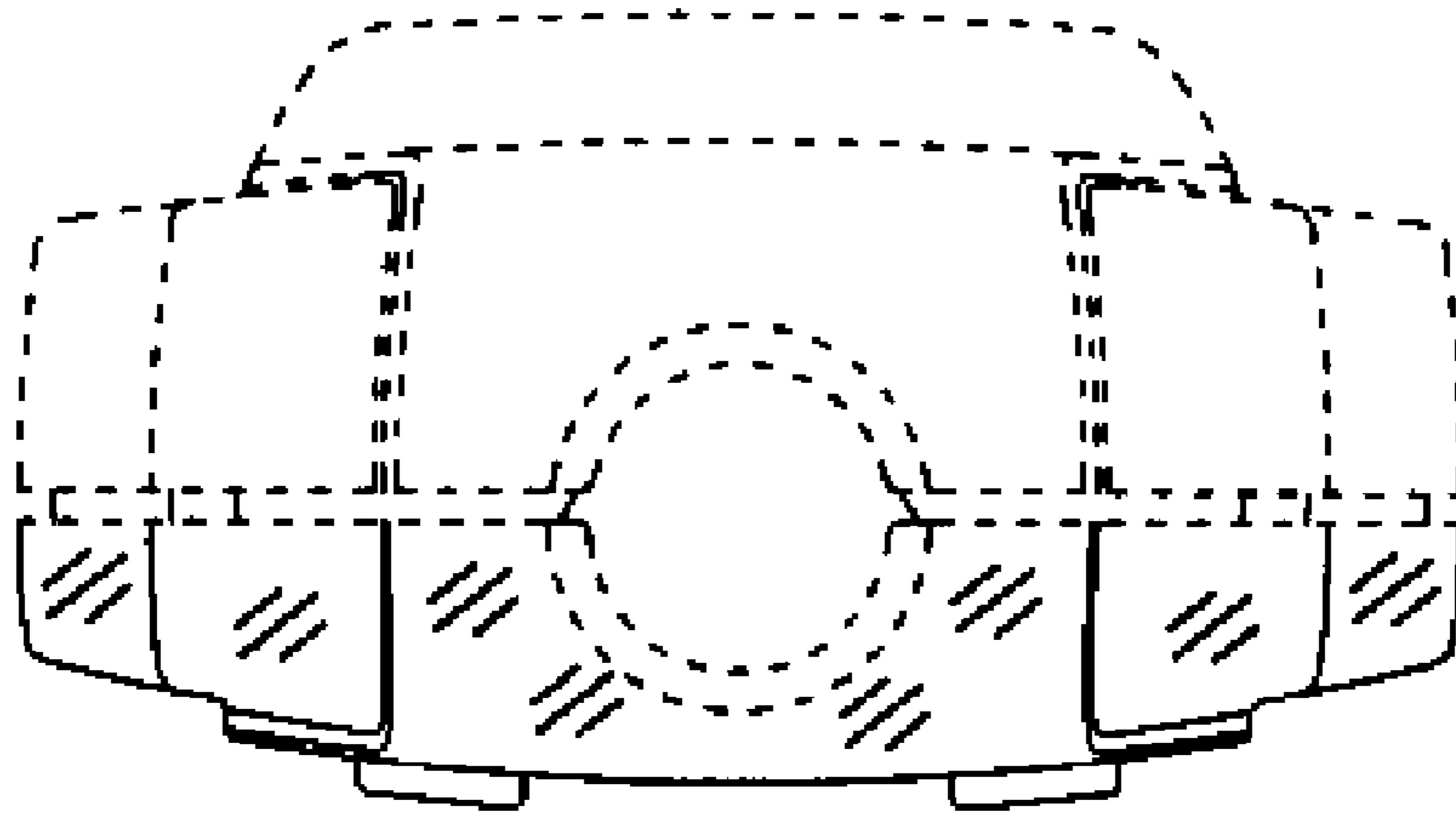
*Figure 2*



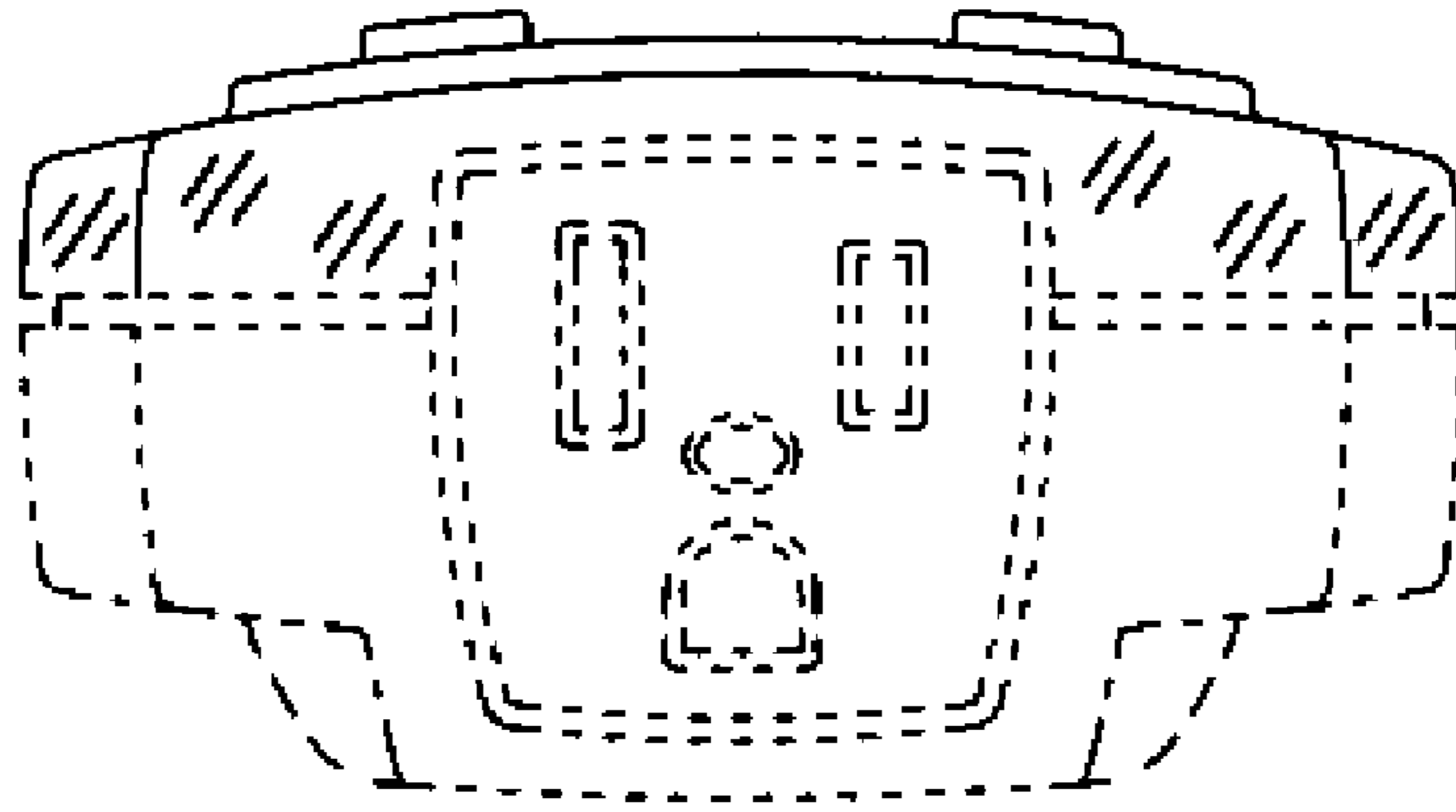
*Figure 3*



*Figure 4*



*Figure 5*



*Figure 6*