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(12) **United States Design Patent**
Wayman

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(45) **Date of Patent:** **** Aug. 17, 2010**

(54) **CHASSIS FOR ELECTRONIC CIRCUITRY**

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(Continued)

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Prairie, MN (US)

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(**) Term: **14 Years**

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(21) Appl. No.: **29/278,367**

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retrieved from internet Nov. 5, 2009.*

(22) Filed: **Mar. 27, 2007**

(51) **LOC (9) Cl.** **14-03**

(52) **U.S. Cl.** **D14/140.5**

(58) **Field of Classification Search** D14/140-140.6,
D14/142, 155, 240-243, 308-313, 358; D13/133,
D13/152, 184; 174/50, 52.1, 58; 220/3.8,
220/4.02; 312/223.1, 223.2; 340/870.02;
361/300, 600-601, 688, 704, 724, 796, 679.01,
361/722; D3/269

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See application file for complete search history.

(57) **CLAIM**

I claim the ornamental design for a chassis for electronic
circuitry, as shown and described.

(56) **References Cited**

DESCRIPTION

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FIG. 1 is a front perspective view of a chassis for electronic
circuitry incorporating an ornamental design according to the
present invention.

FIG. 2 is a back view of the chassis for electronic circuitry of
FIG. 1.

FIG. 3 is a right side view of the chassis for electronic cir-
cuitry of FIG. 1.

FIG. 4 is a front view of the chassis for electronic circuitry of
FIG. 1.

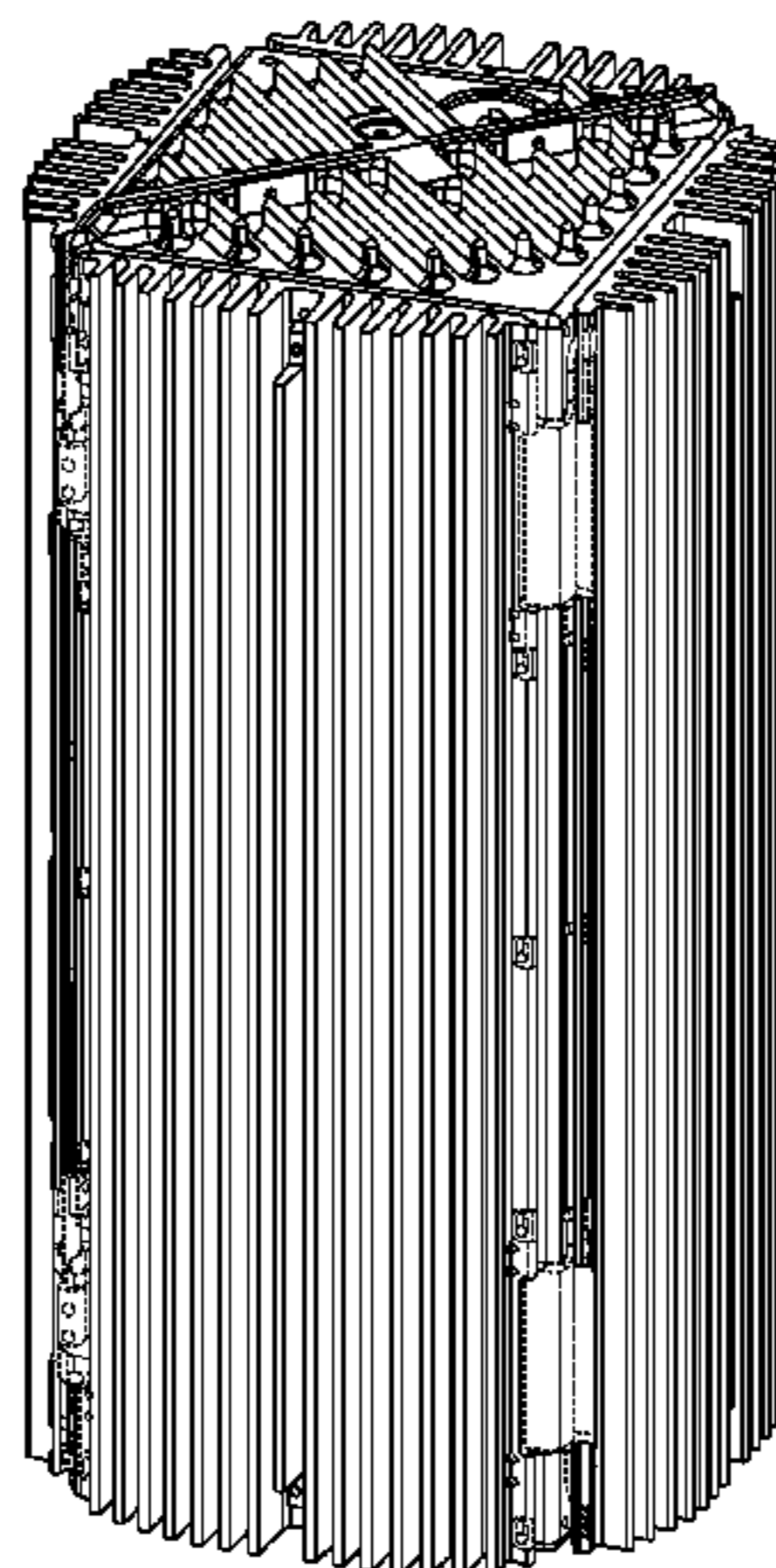
FIG. 5 is a top view of the chassis for electronic circuitry of
FIG. 1.

FIG. 6 is a bottom view of the chassis for electronic circuitry
of FIG. 1; and,

FIG. 7 is a left side view of the chassis for electronic circuitry
of FIG. 1.

In the drawing, the broken lines depict unclaimed environ-
ment.

1 Claim, 7 Drawing Sheets



US D621,812 S

Page 2

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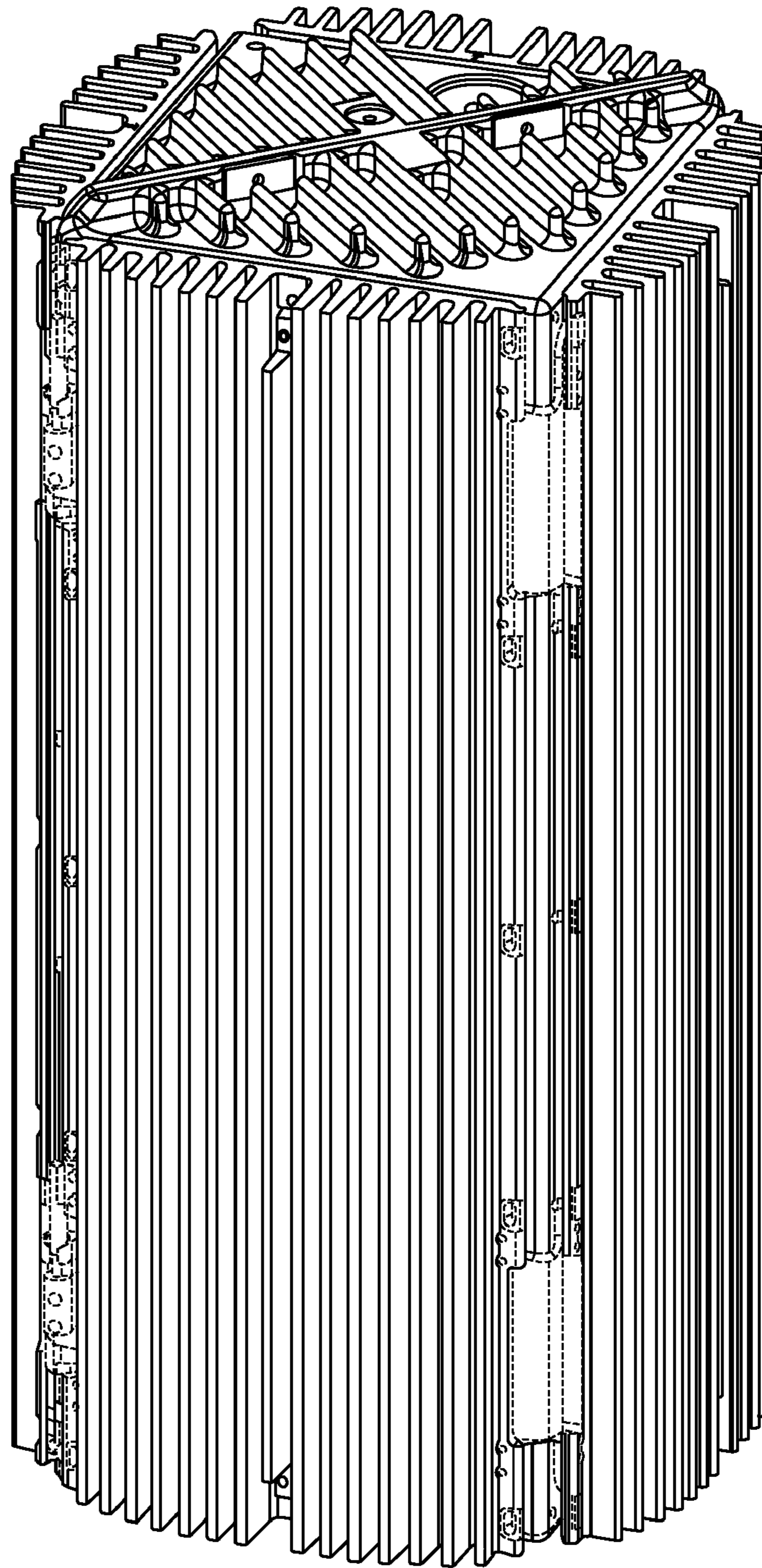


FIG. 1

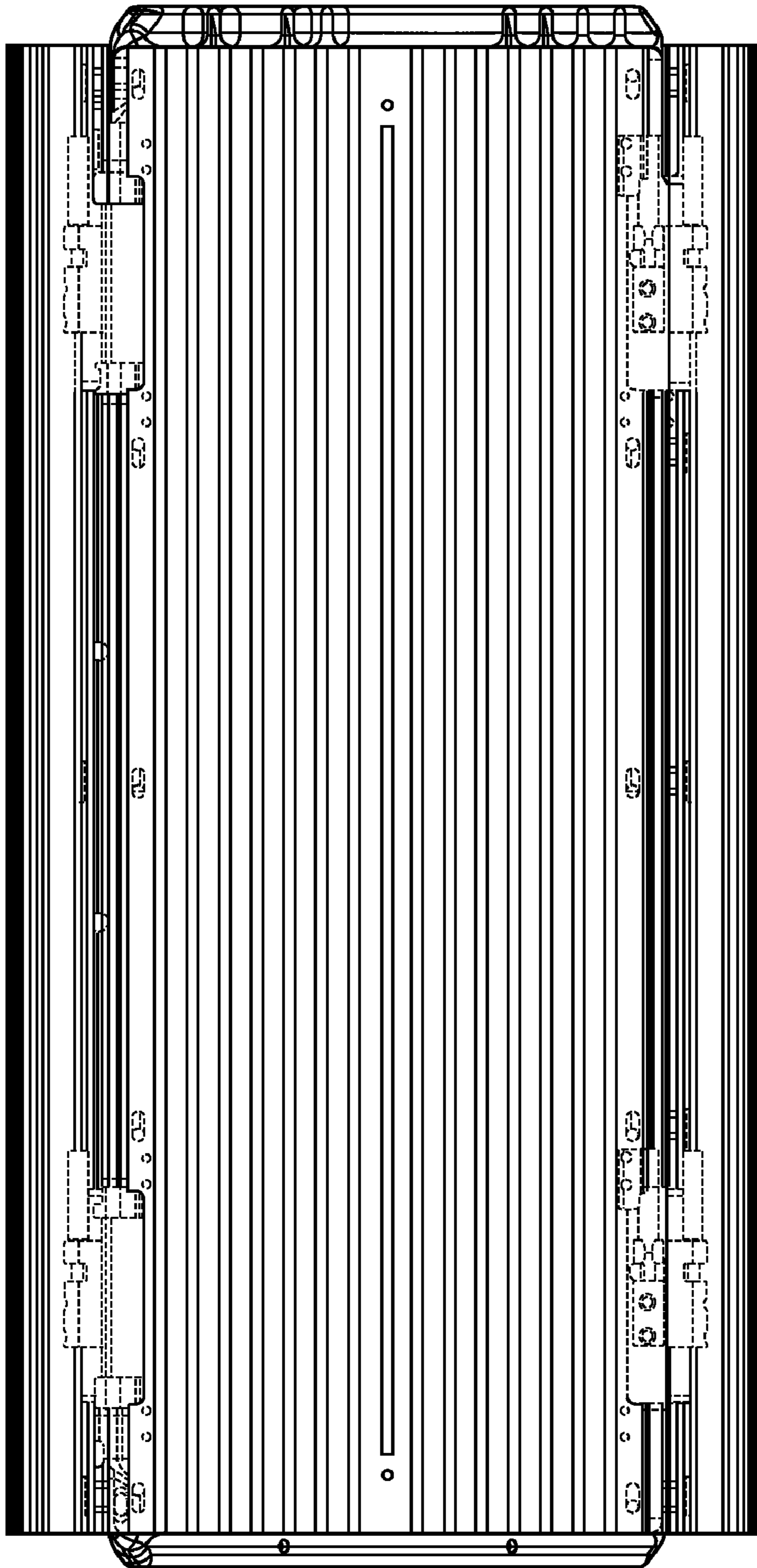


FIG. 2

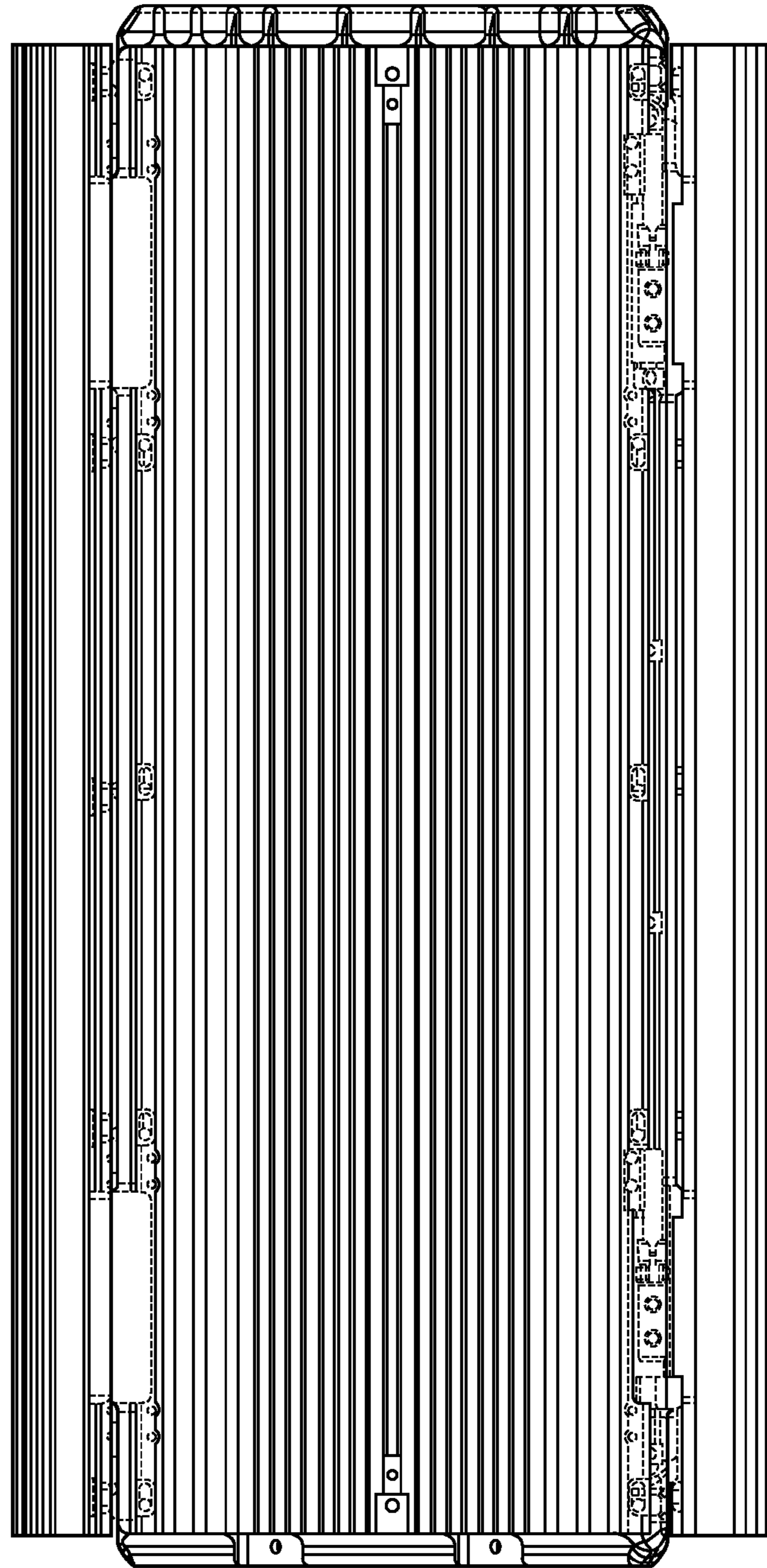


FIG. 3

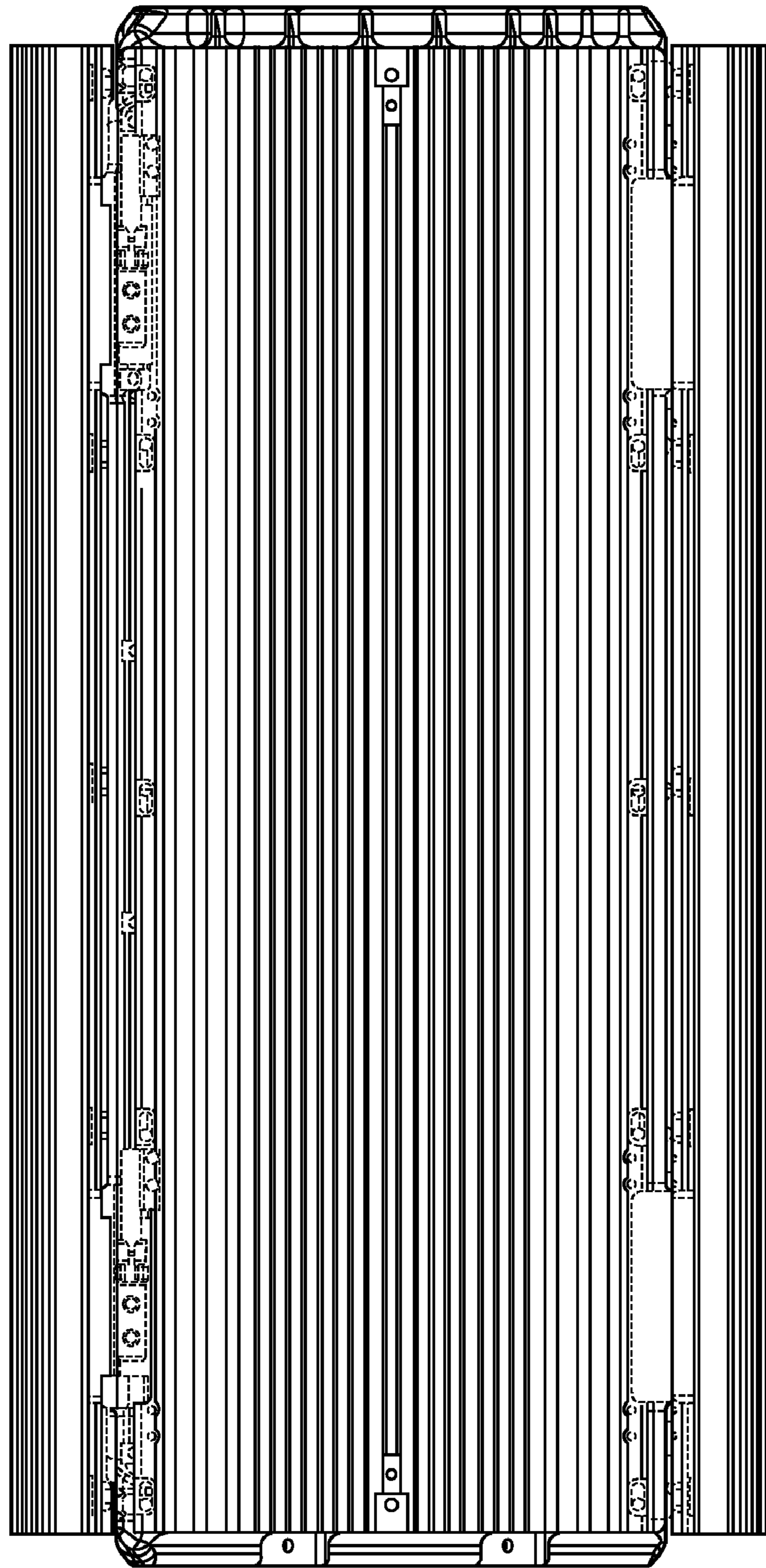


FIG. 4

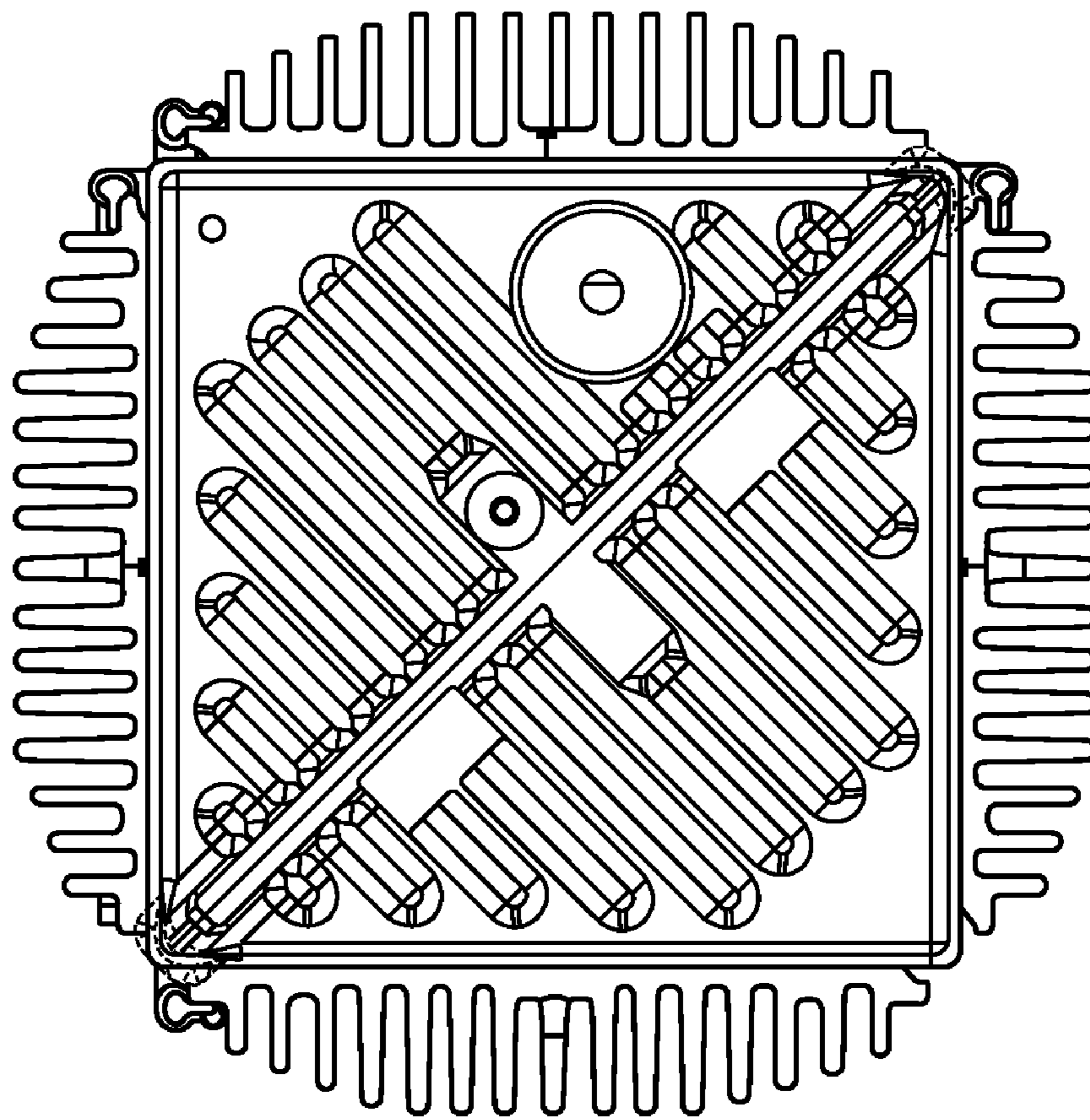


FIG. 5

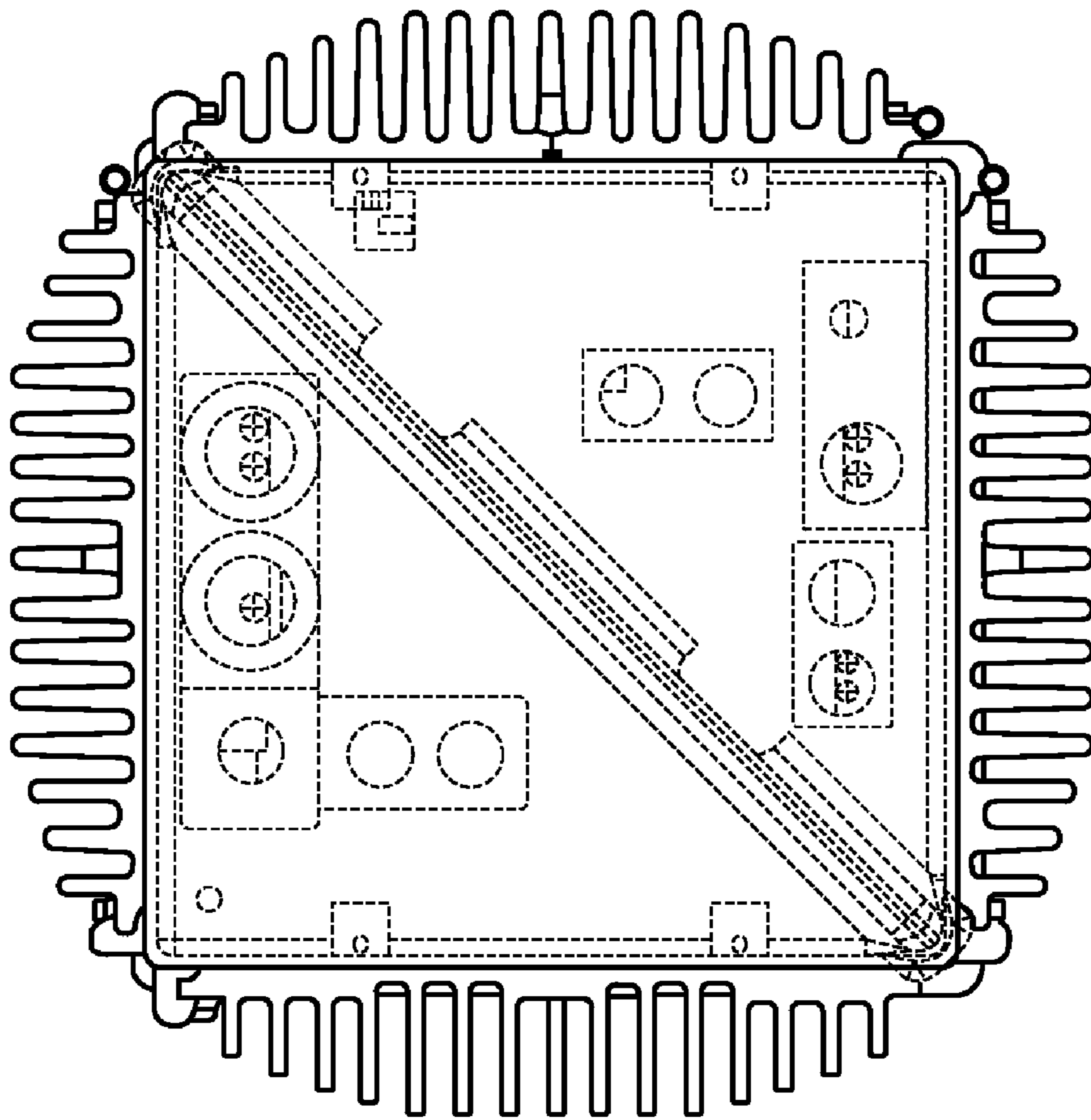


FIG. 6

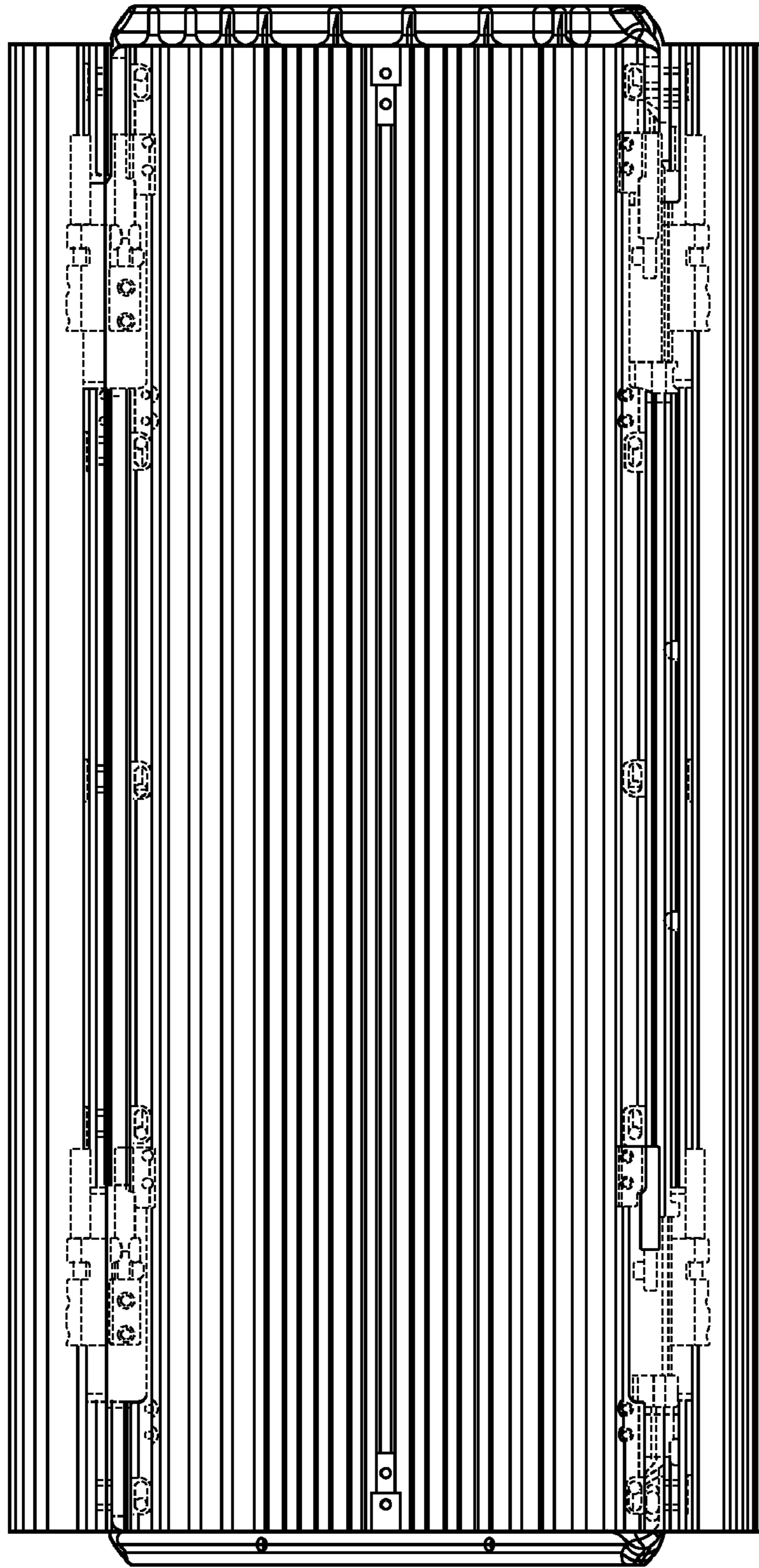


FIG. 7