



US00D620152S

(12) **United States Design Patent**
Lai et al.

(10) **Patent No.:** **US D620,152 S**
(45) **Date of Patent:** **** Jul. 20, 2010**

(54) **LIGHT EMITTING DIODE LAMP**
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(TW)

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* cited by examiner

(**) Term: **14 Years**

Primary Examiner—Marcus A Jackson
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(21) Appl. No.: **29/352,934**

(57) **CLAIM**

(22) Filed: **Dec. 30, 2009**

The ornamental design for a light emitting diode lamp, as shown and described.

(30) **Foreign Application Priority Data**

DESCRIPTION

Nov. 26, 2009 (CN) 200930326711

(51) **LOC (9) Cl.** **26-04**

FIG. 1 is a perspective view of a light emitting diode lamp showing our new design.

(52) **U.S. Cl.** **D26/2**

FIG. 2 is a front elevational view thereof.

(58) **Field of Classification Search** D26/1-4;
313/313, 315, 317, 318, 493; 315/52, 53,
315/56, 57, 58

FIG. 3 is a rear elevational view thereof.

See application file for complete search history.

FIG. 4 is a left-side, elevational view thereof.

FIG. 5 is a right-side, elevational view thereof.

(56) **References Cited**

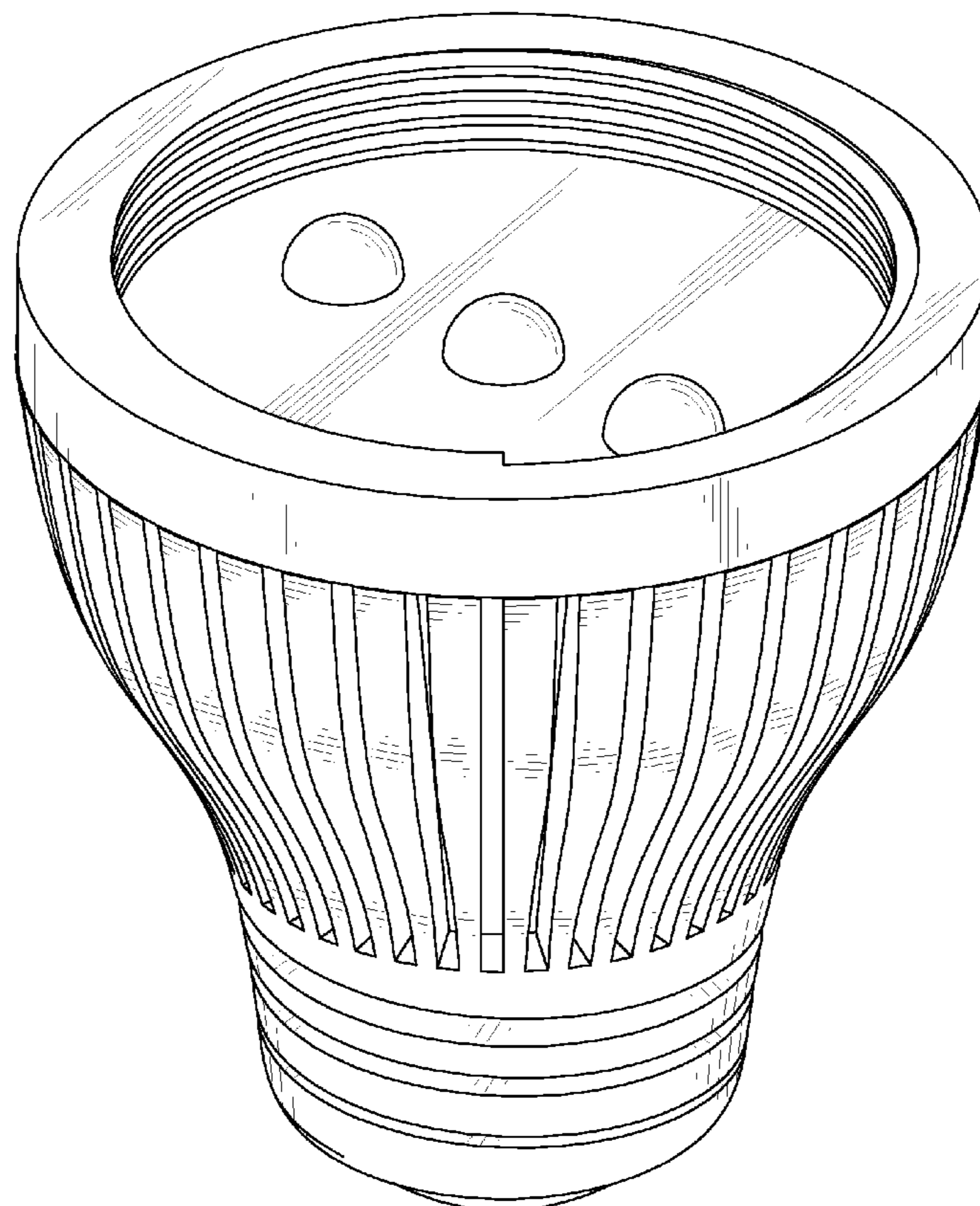
FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom plan view thereof.

U.S. PATENT DOCUMENTS

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1 Claim, 7 Drawing Sheets



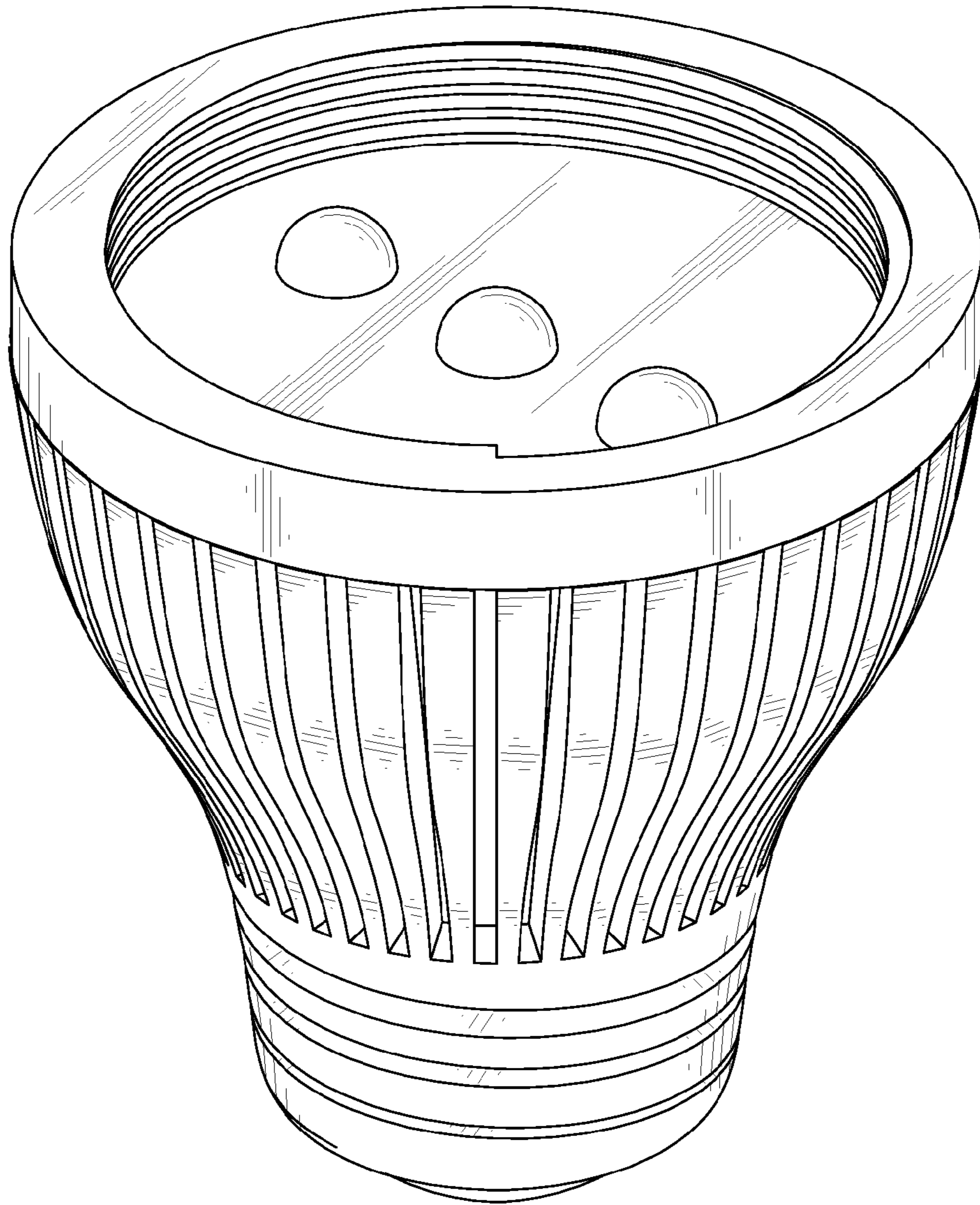


FIG. 1

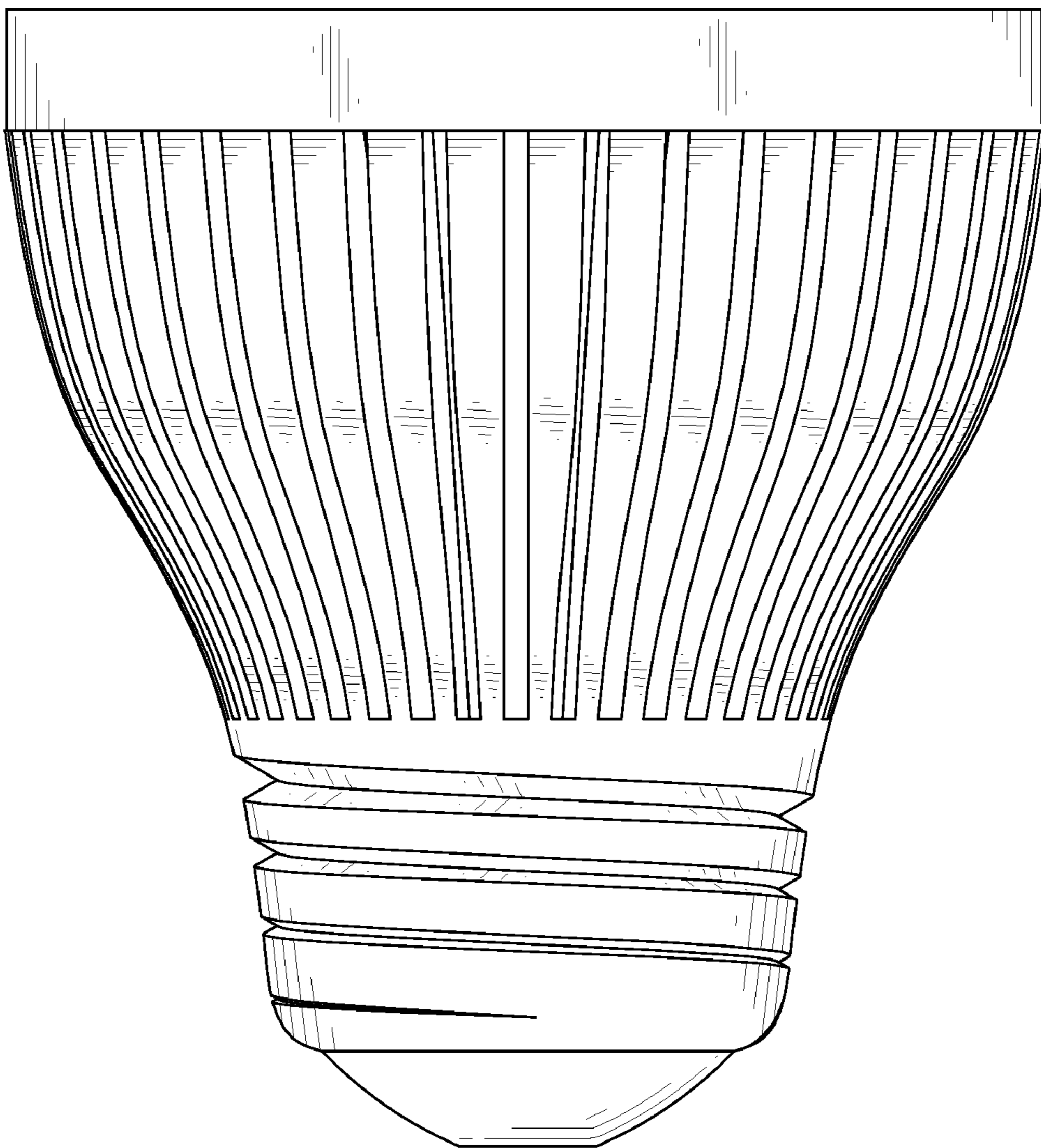


FIG. 2

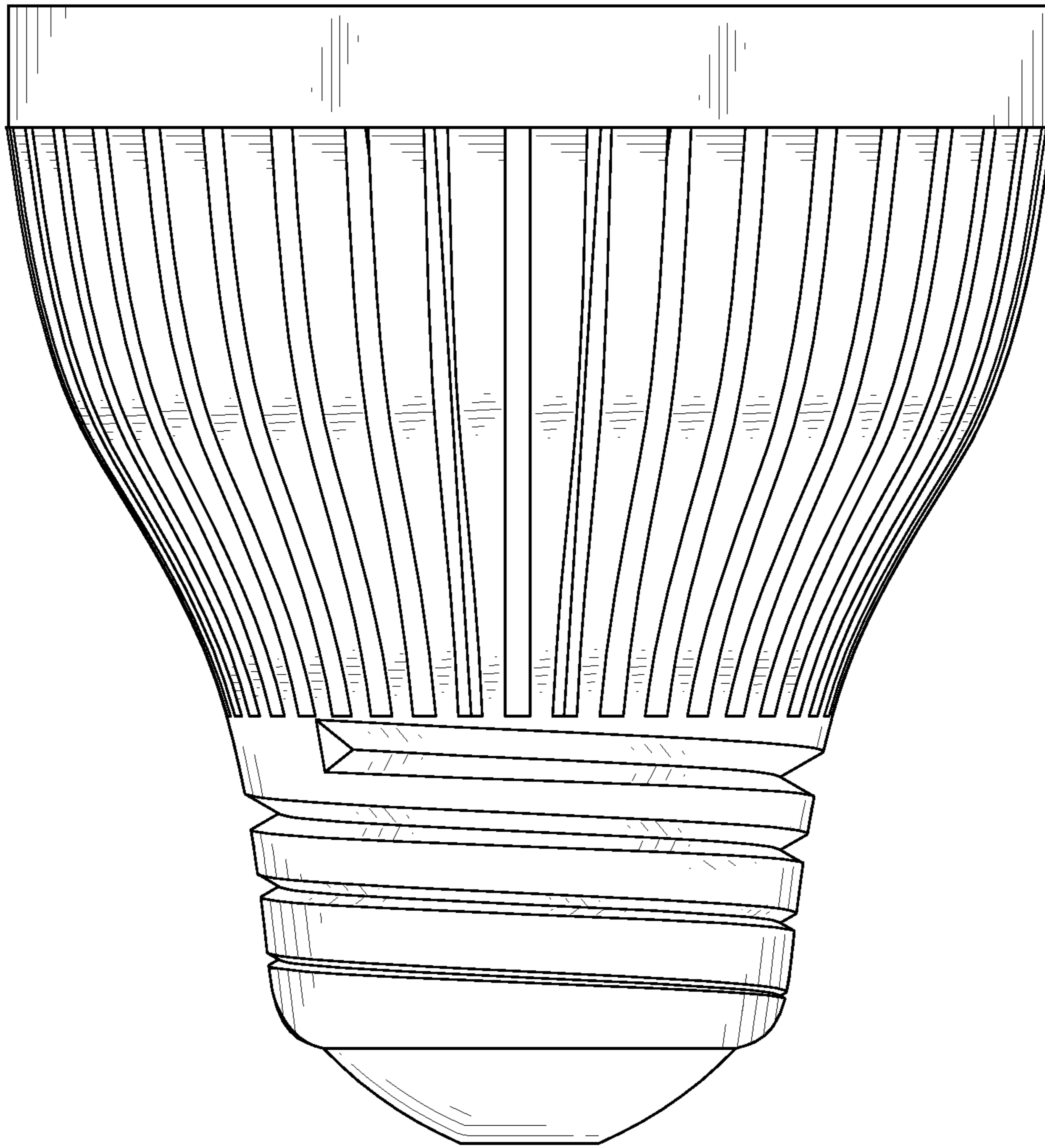


FIG. 3

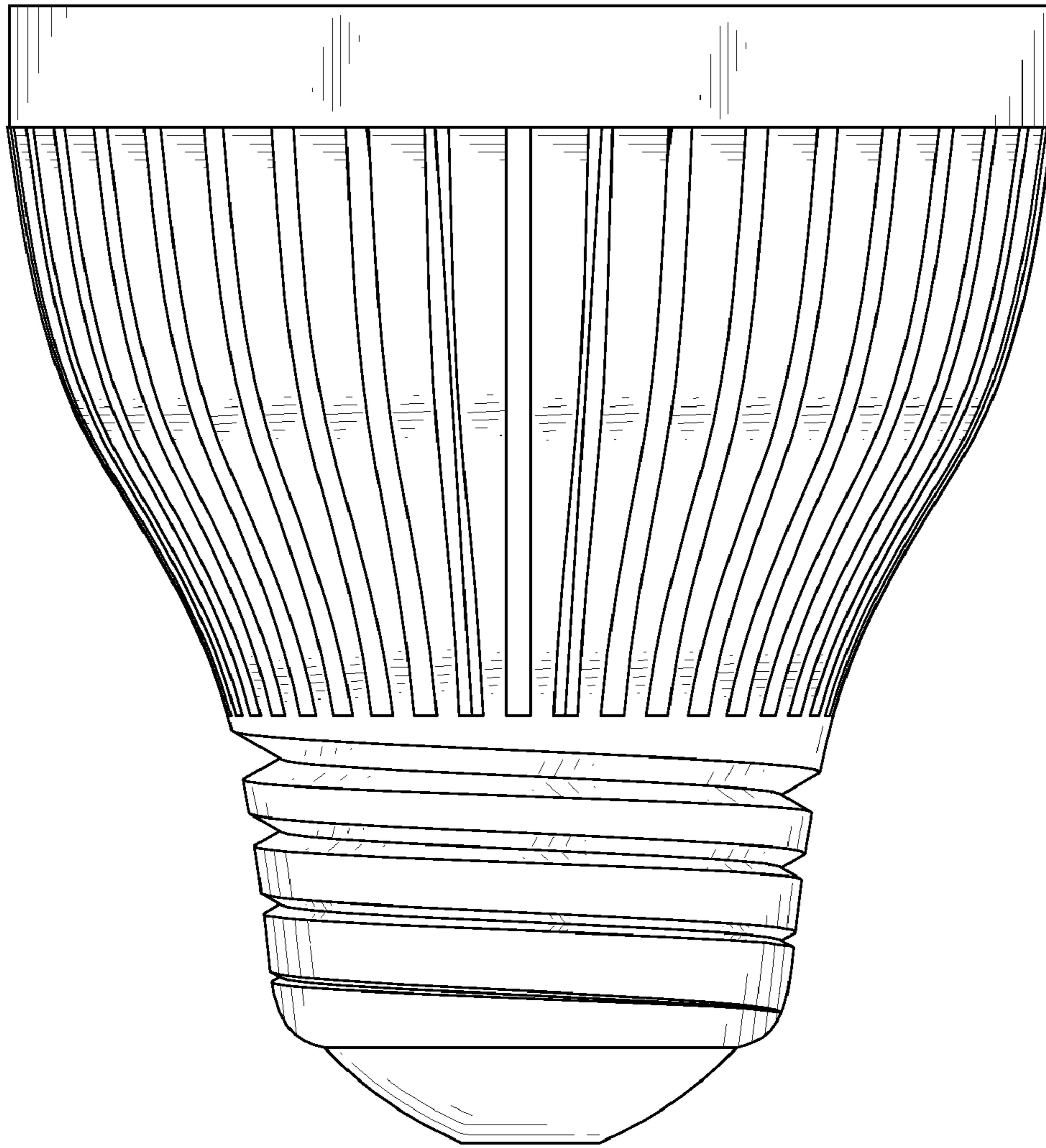


FIG. 4

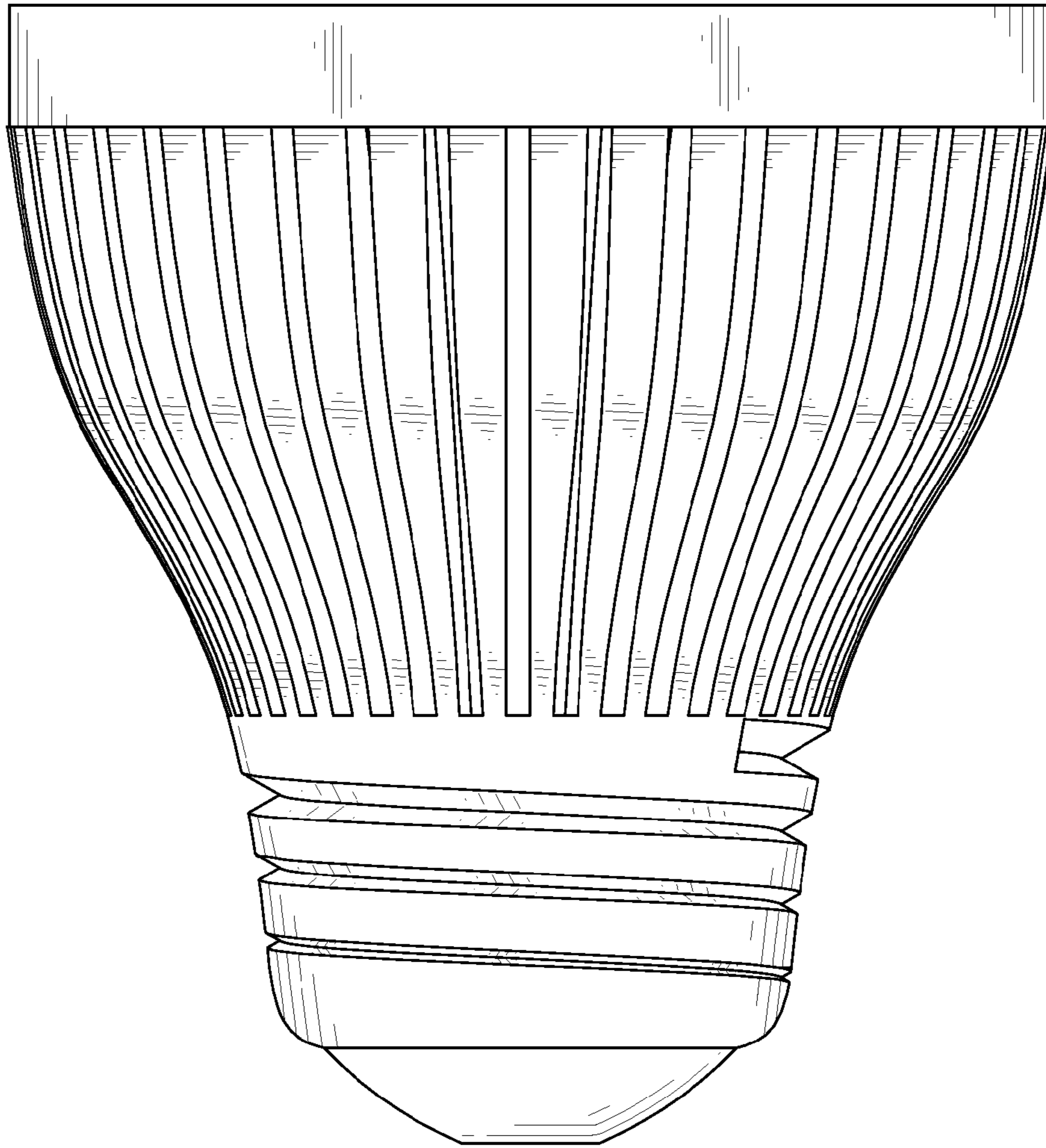


FIG. 5

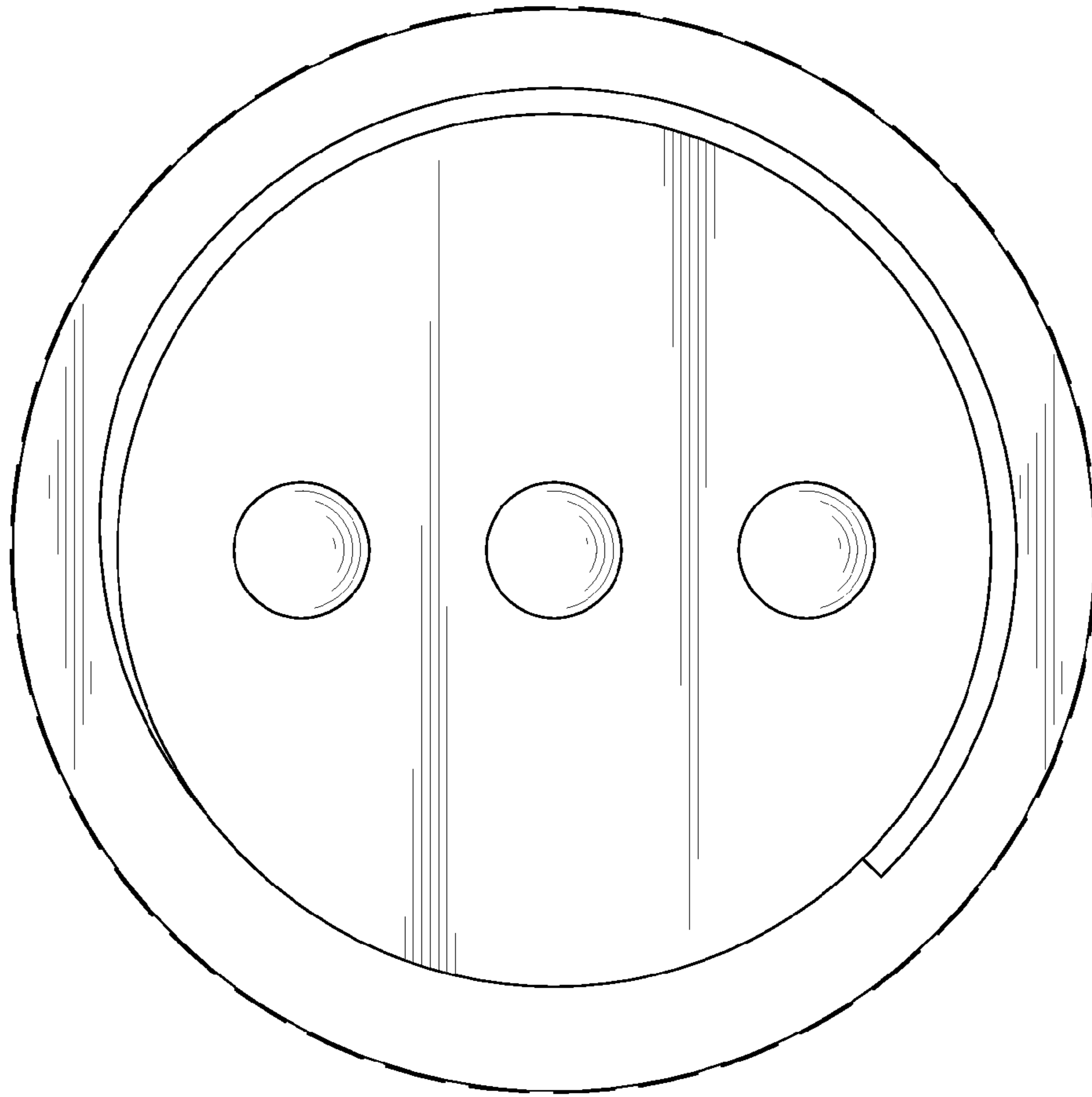


FIG. 6

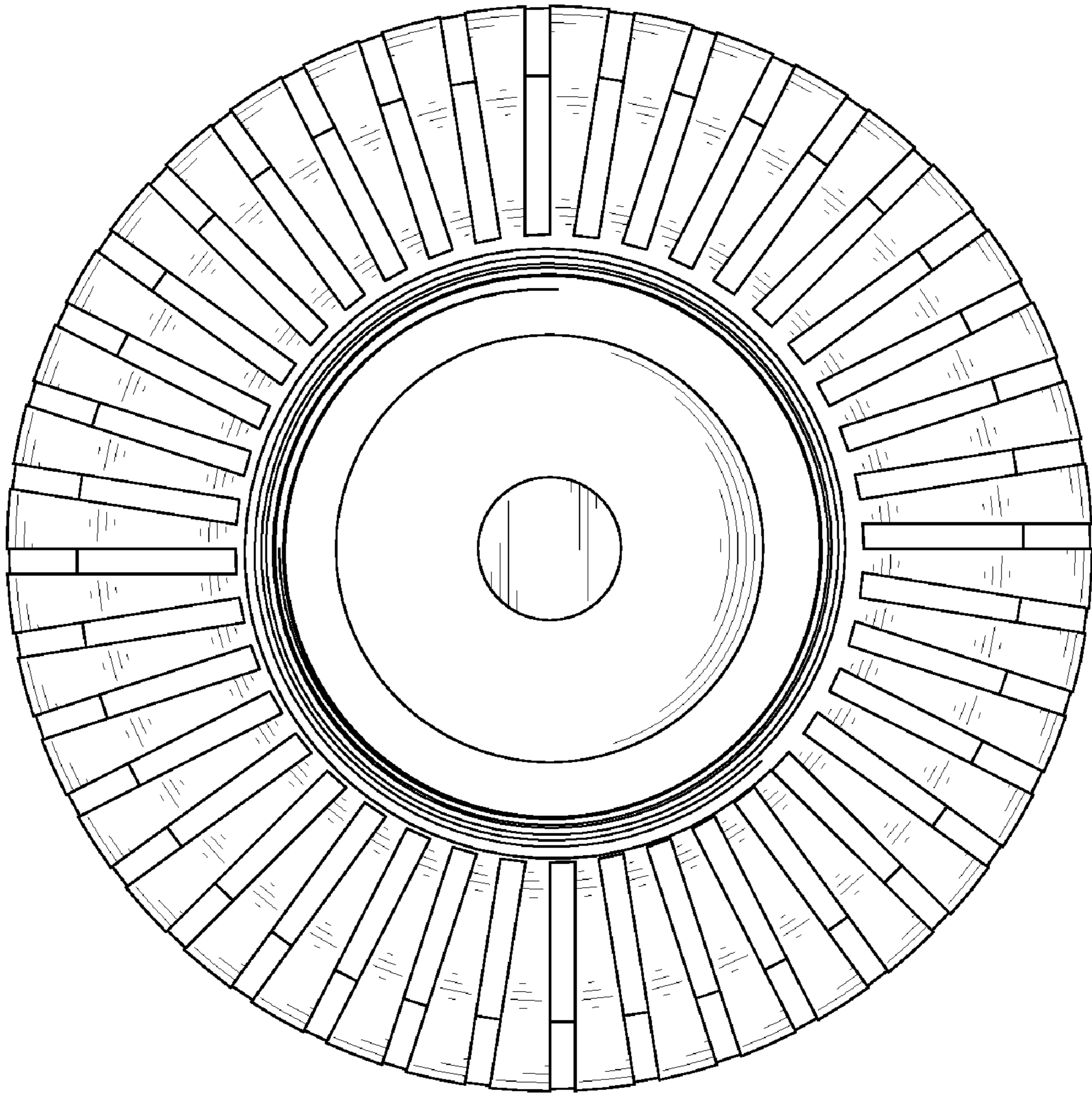


FIG. 7