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(12) **United States Design Patent**
Okada

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(54) **GAS SUPPLY PIPE FOR MANUFACTURING SEMICONDUCTOR WAFERS**

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(73) Assignee: **Tokyo Electron Limited**, Tokyo (JP)

(**) Term: **14 Years**

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(30) **Foreign Application Priority Data**

May 8, 2007 (JP) 2007-011995

(51) **LOC (9) Cl.** **23-01**

(52) **U.S. Cl.** **D23/266**

(58) **Field of Classification Search** D23/259–269;
285/231–232, 95, 205, 197, 370; 277/910,
277/602, 630

See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a gas supply pipe for manufacturing semiconductor wafers, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of the design for a gas supply pipe for manufacturing semiconductor wafers in accordance with the invention;

FIG. 2 is a front view thereof;

FIG. 3 is a right side view thereof;

FIG. 4 is a left side view thereof;

FIG. 5 is a top view thereof;

FIG. 6 is a bottom view thereof;

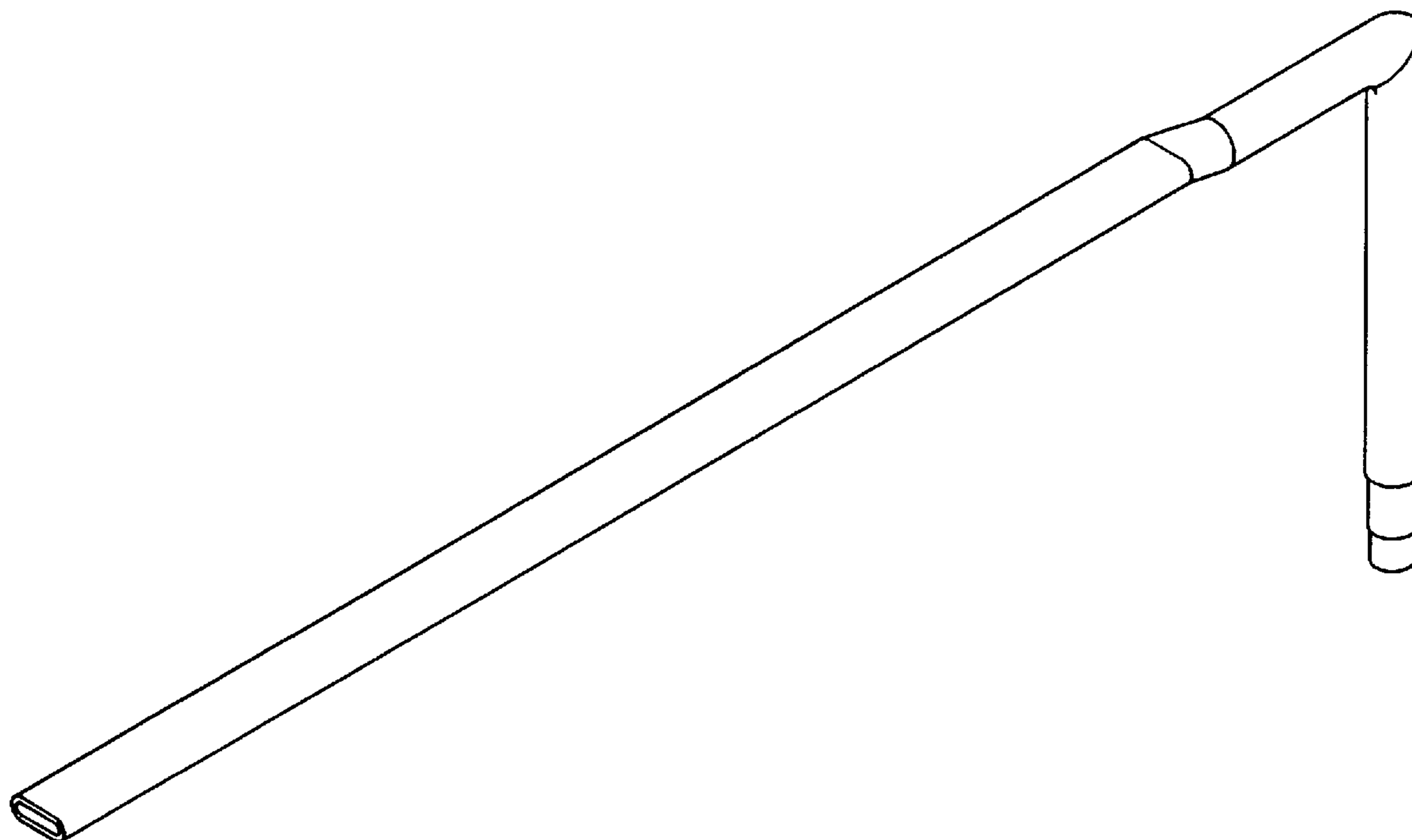
FIG. 7 is a sectional view through line 7—7 of FIG. 2;

FIG. 8 is a sectional view through line 8—8 of FIG. 2; and,

FIG. 9 is a sectional view through line 9—9 of FIG. 2.

The rear view of the gas supply pipe for manufacturing semiconductor wafers appears the same as its front view shown in FIG. 1. It supplies reactive gas in a process tube for semiconductor manufacture.

1 Claim, 9 Drawing Sheets



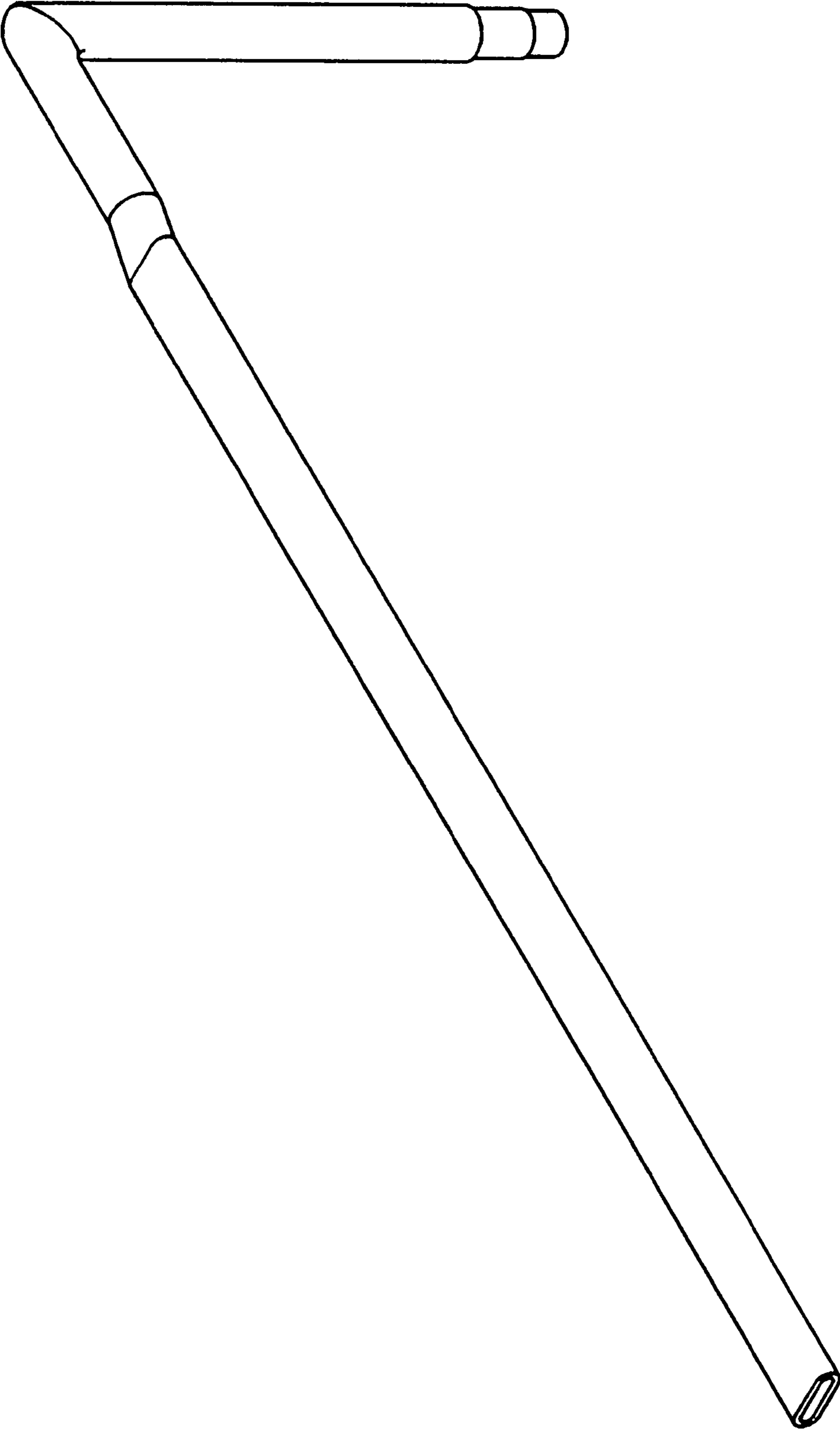


FIG. 1

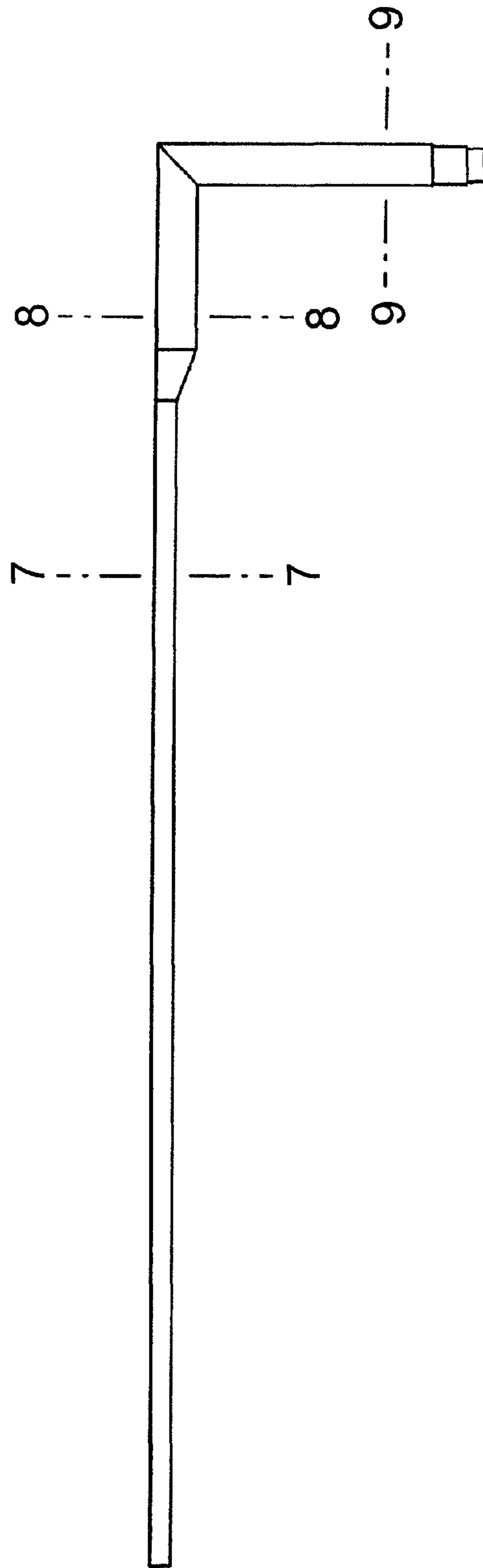


FIG. 2

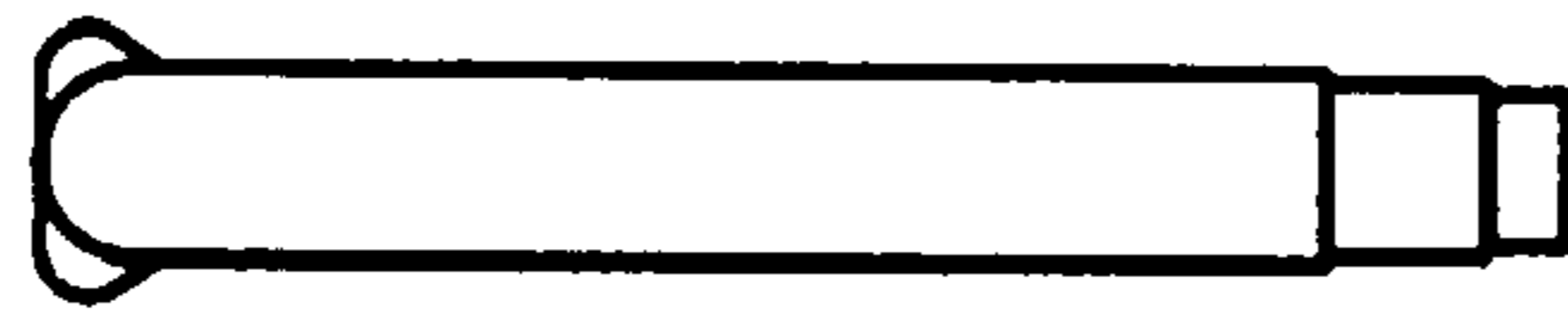


FIG. 3



FIG. 4



FIG. 5



FIG. 6

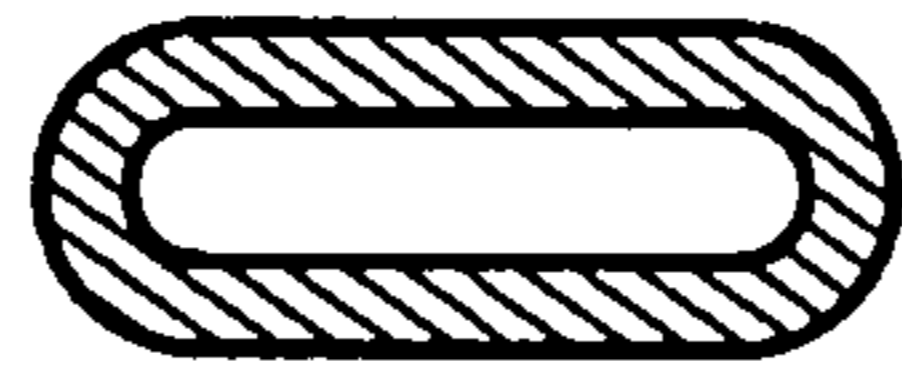


FIG. 7

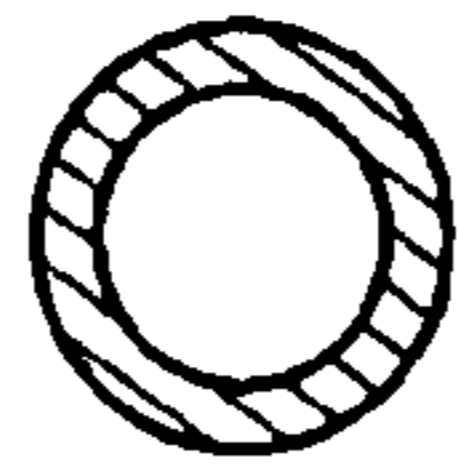


FIG. 8

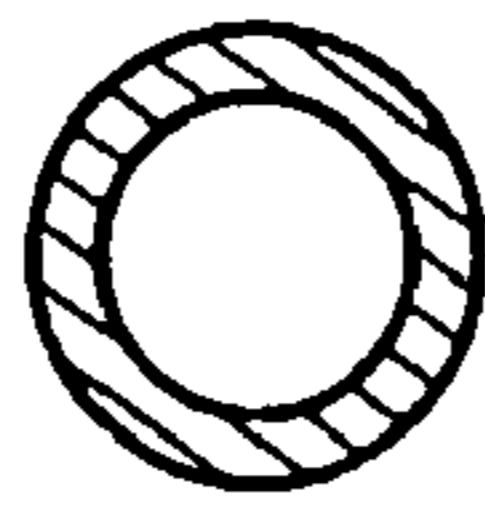


FIG. 9