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(12) **United States Design Patent**  
**Yamada**

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(54) **LIGHT EMITTING DIODE MODULE**

D607,420 S \* 1/2010 Imai et al. .... D13/180

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(73) Assignee: **Nichia Corporation**, Anan-shi (JP)

(\*\*) Term: **14 Years**

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Apr. 8, 2009	(JP)	.....	2009-008099
Apr. 8, 2009	(JP)	.....	2009-008100
Apr. 8, 2009	(JP)	.....	2009-008102

(51) **LOC (9) Cl.** ..... **13-03**

(52) **U.S. Cl.** ..... **D13/180**

(58) **Field of Classification Search** ..... D13/180;  
D26/2; 257/79, 80, 81, 88, 89, 95, 98, 99,  
257/100, E33.058; 313/483, 498, 500; 362/555,  
362/800

See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a light emitting diode module, as shown and described.

**DESCRIPTION**

FIG. 1 is a front elevational view of a light emitting diode module in accordance with my new design;

FIG. 2 is a top plan view of the light emitting diode module in accordance with my new design;

FIG. 3 is a bottom plan view of the light emitting diode module in accordance with my new design;

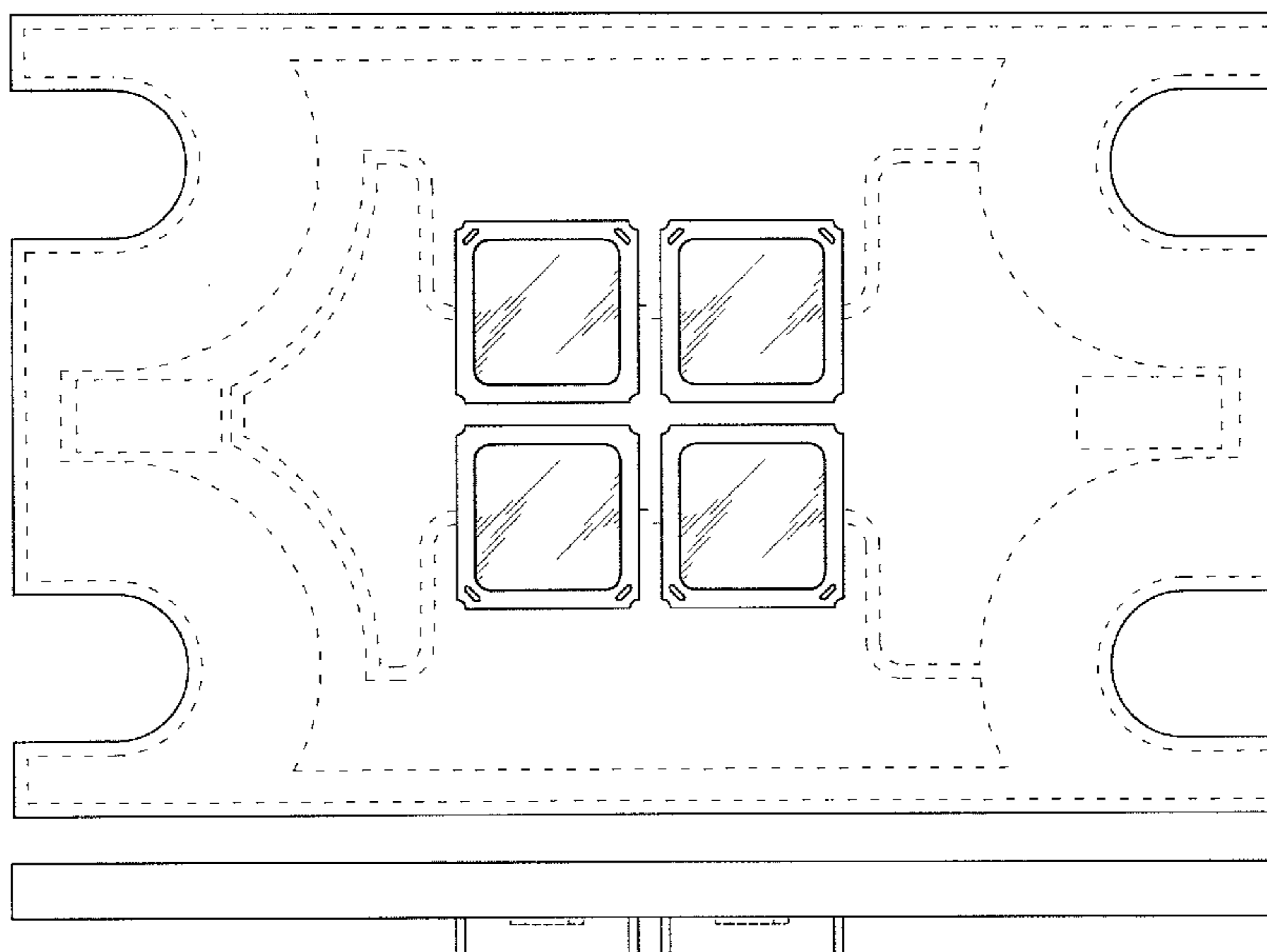
FIG. 4 is a left side elevational view of the light emitting diode module in accordance with my new design; and,

FIG. 5 is a right side elevational view of the light emitting diode module in accordance with my new design.

The opaque line shading illustrates a transparent portion of the light emitting diode module.

The bottom plan view of the light emitting diode module is omitted because the bottom surface of the light emitting diode module is flat and devoid of surface ornamentation.

**1 Claim, 3 Drawing Sheets**



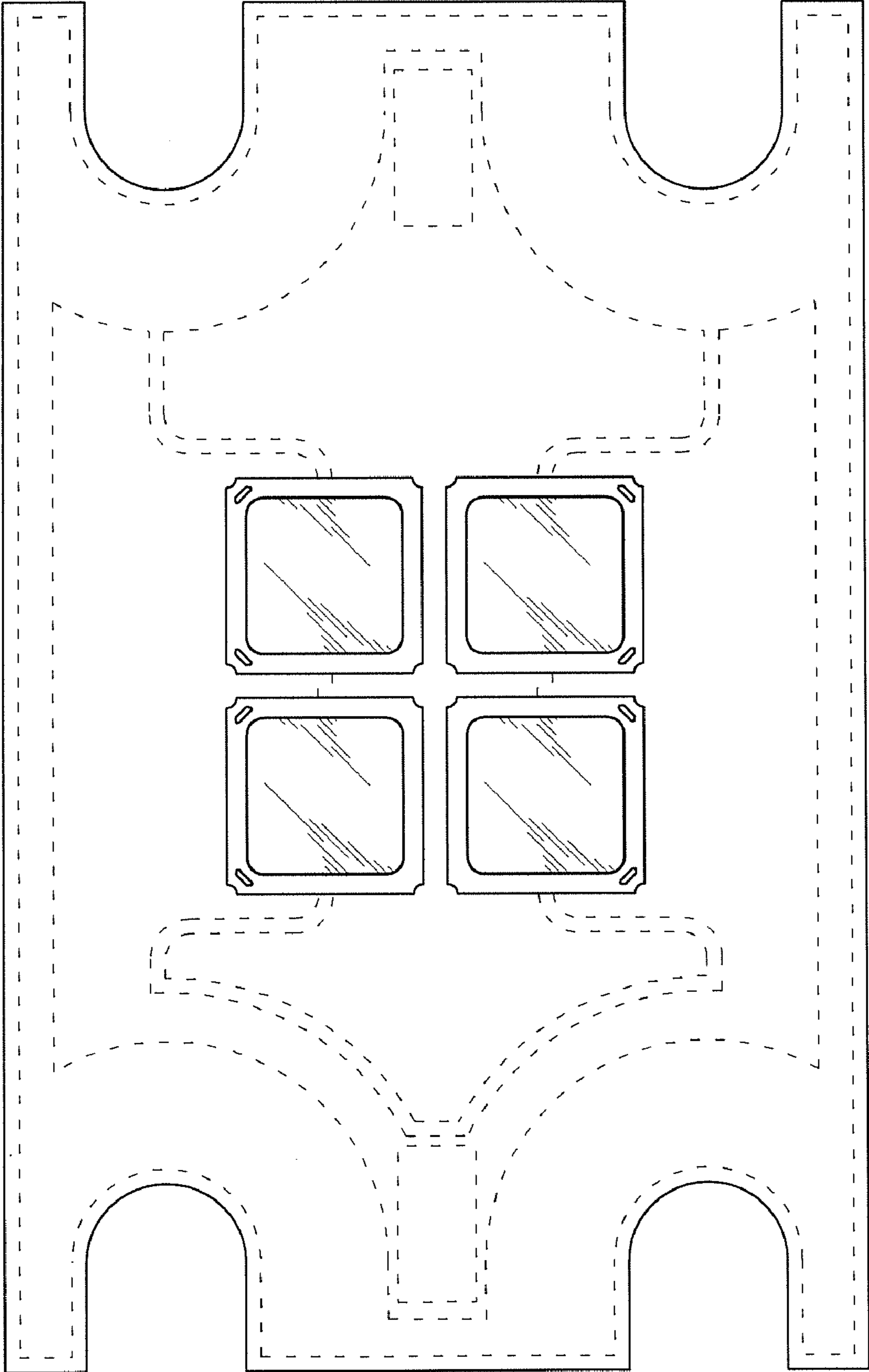
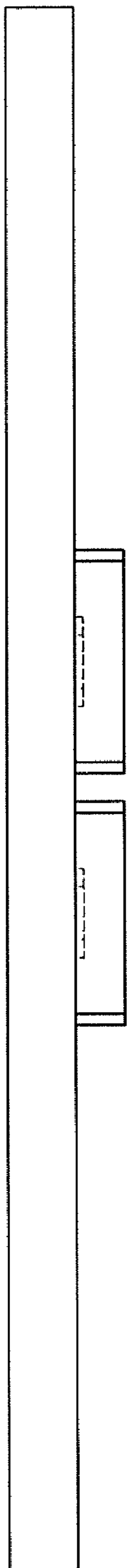
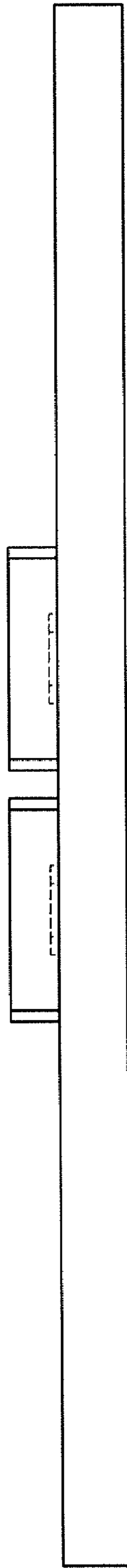


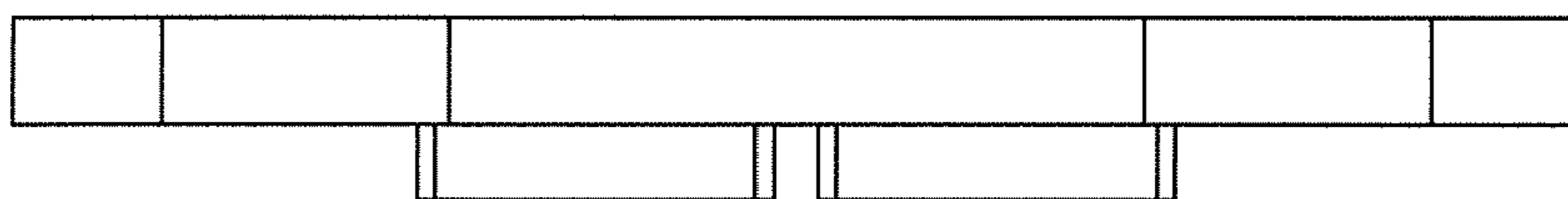
FIG. 1



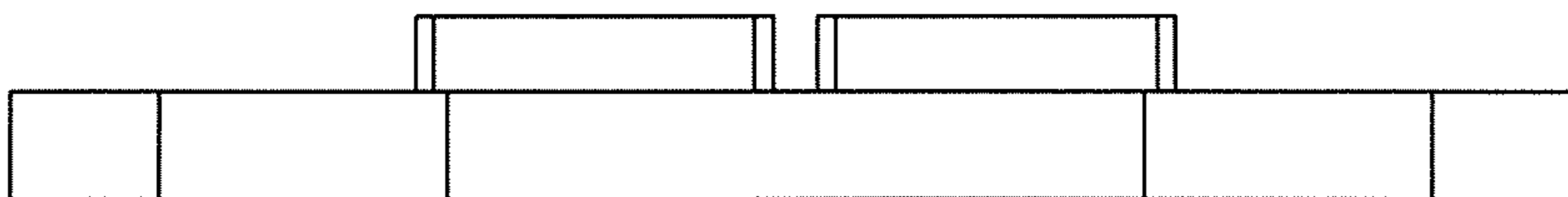
**FIG. 2**



**FIG. 3**



**FIG. 5**



**FIG. 4**