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(12) **United States Design Patent**
Kaneko

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(54) **PROCESS TUBE FOR MANUFACTURING SEMICONDUCTOR WAFERS**

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(73) Assignee: **Tokyo Electron Limited**, Tokyo (JP)

(**) Term: **14 Years**

(21) Appl. No.: **29/290,247**

(22) Filed: **Nov. 8, 2007**

(30) **Foreign Application Priority Data**

May 8, 2007 (JP) 2007-011996

(51) **LOC (9) Cl.** **15-09**
(52) **U.S. Cl.** **D15/138**
(58) **Field of Classification Search** D9/428, D9/435, 447, 452; D13/182; D15/138, 144.1, D15/199; D23/206; D32/53, 54; 117/102; 118/715, 724, 725; 219/390, 486
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a process tube for manufacturing semiconductor wafers, as shown and described.

DESCRIPTION

FIG. **1** is a perspective view of the design for a process tube for manufacturing semiconductor wafers in accordance with the invention;

FIG. **2** is a front view thereof;

FIG. **3** is a rear view thereof;

FIG. **4** is a right side view thereof;

FIG. **5** is a left side view thereof;

FIG. **6** is a top view thereof;

FIG. **7** is a bottom view thereof;

FIG. **8** is a sectional view through line **8**—**8** of FIG. **6**;

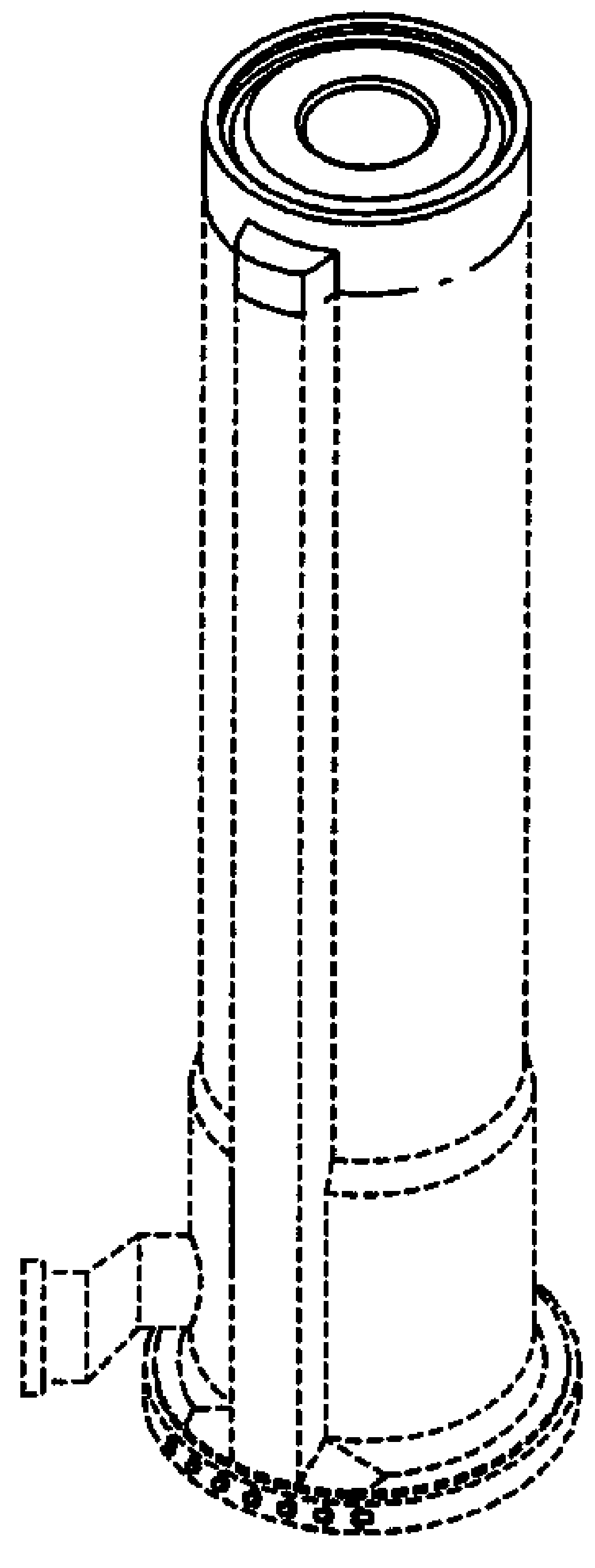
FIG. **9** is an enlarged detailed top plan view of the portion of the process tube in the rectangle shown in FIG. **8**; and,

FIG. **10** is an enlarged detailed sectional view taken along line **8**—**8** in FIG. **6** of the portion of the process tube in the rectangle shown in FIG. **8**.

Environmental structure, which forms no part of the claimed design, is illustrated in broken lines.

The design is a type of process tube for decompression (vacuum) equipment.

1 Claim, 10 Drawing Sheets



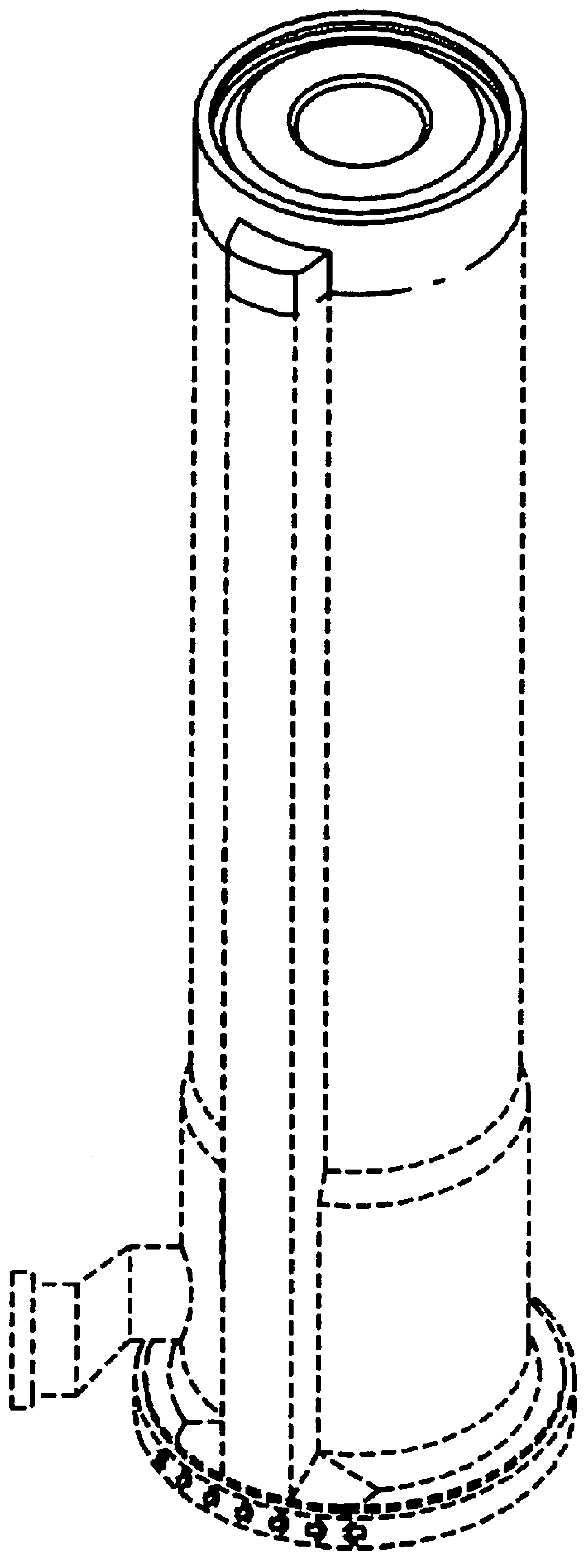


FIG. 1

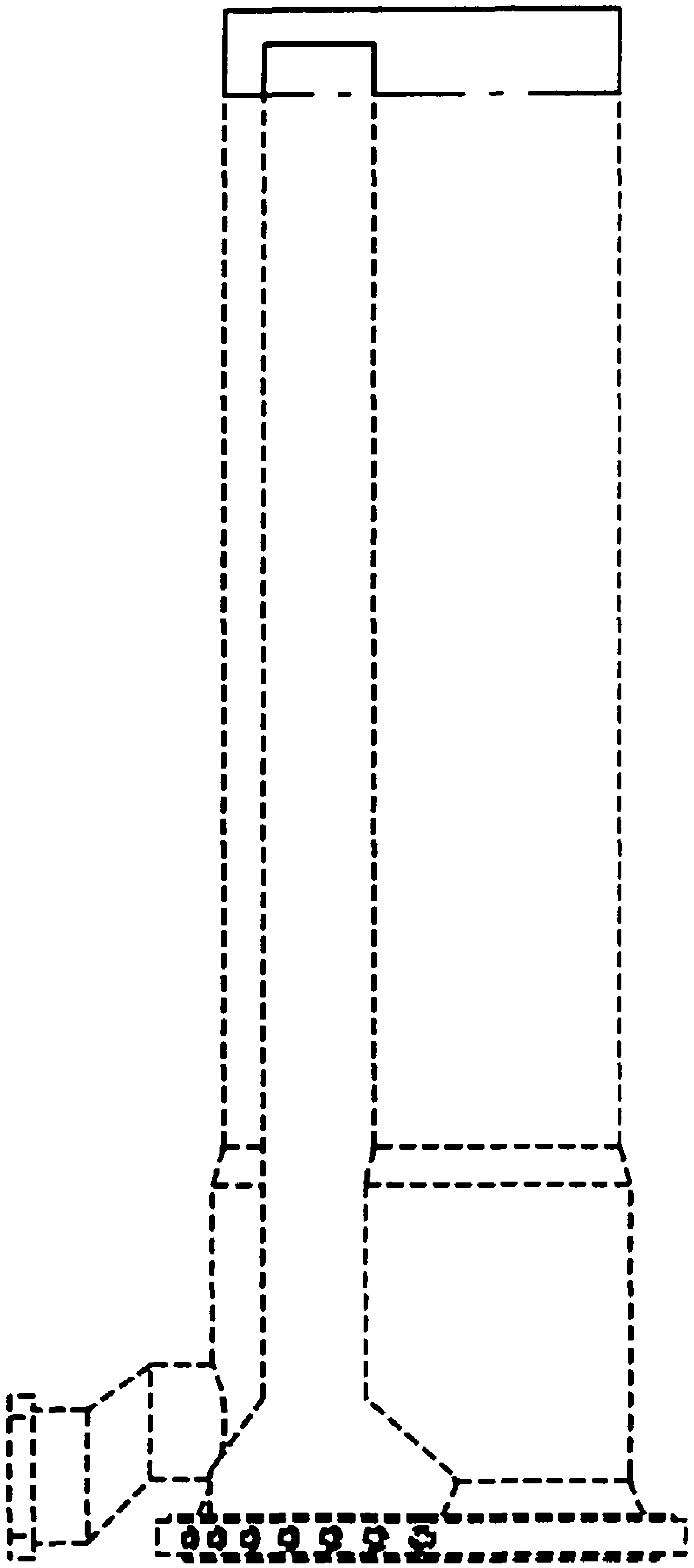


FIG. 2

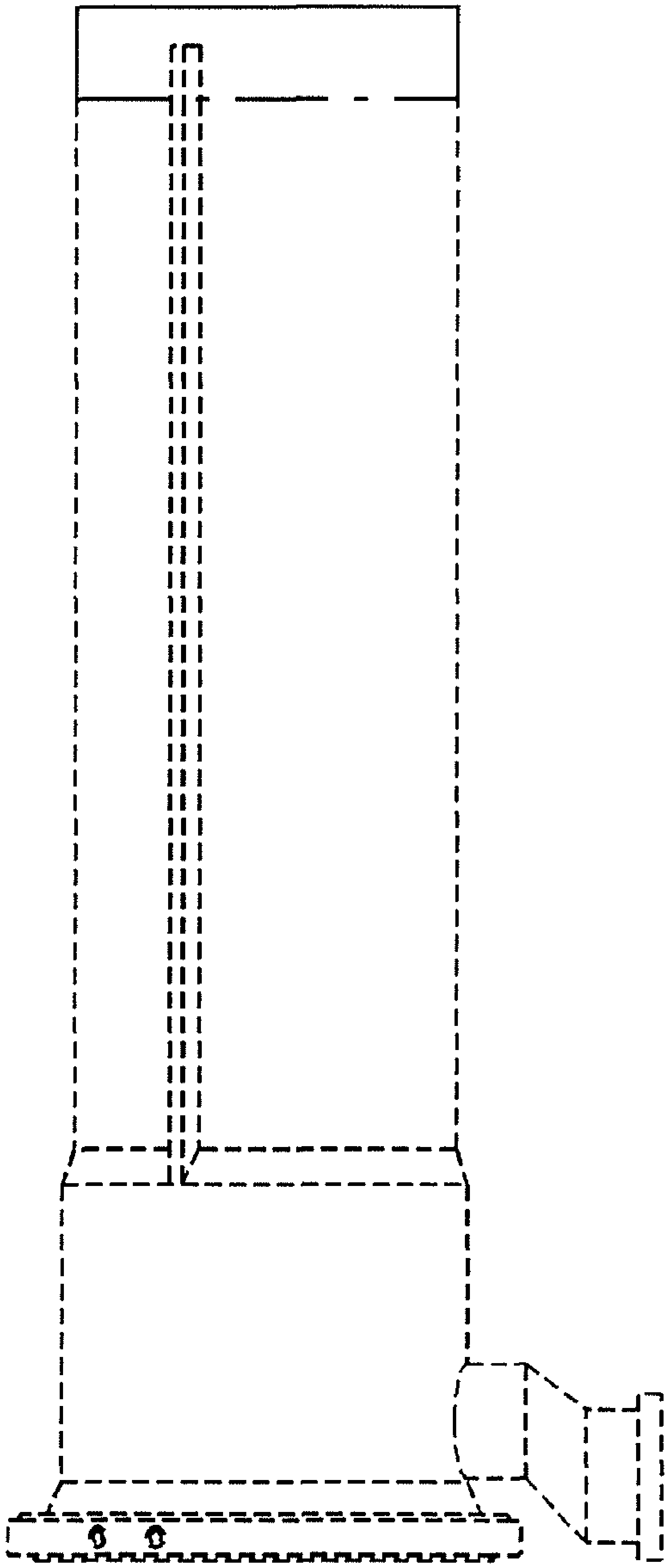


FIG. 3

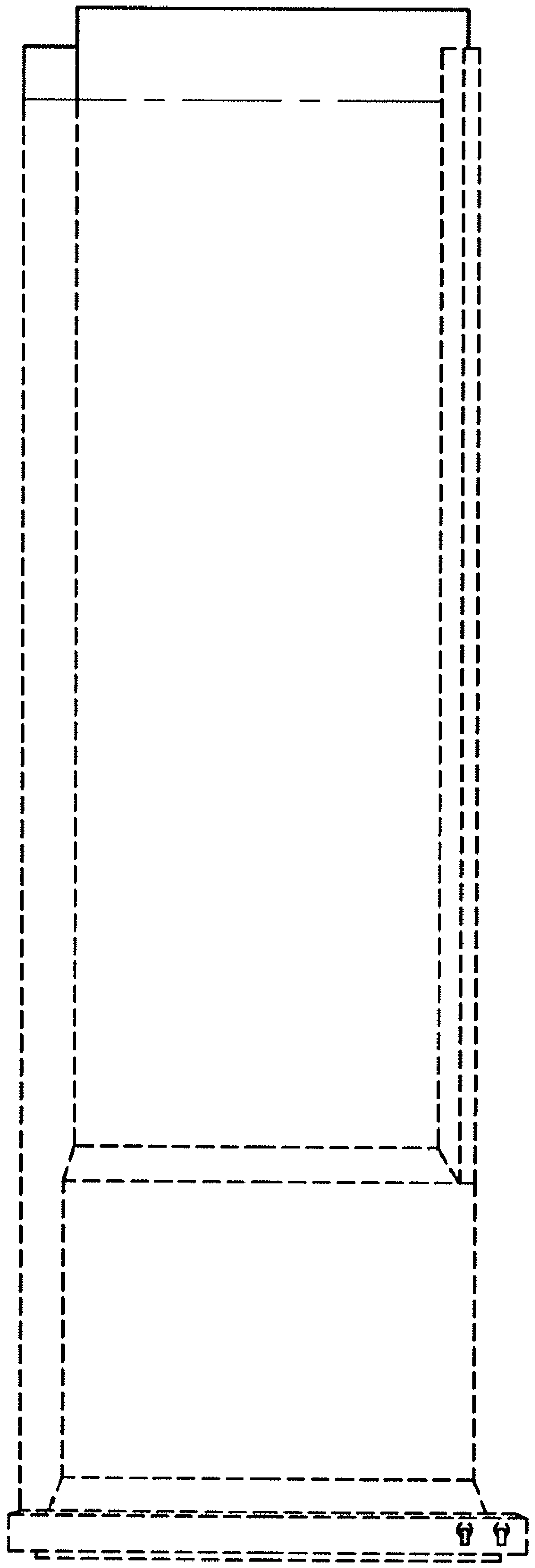


FIG. 4

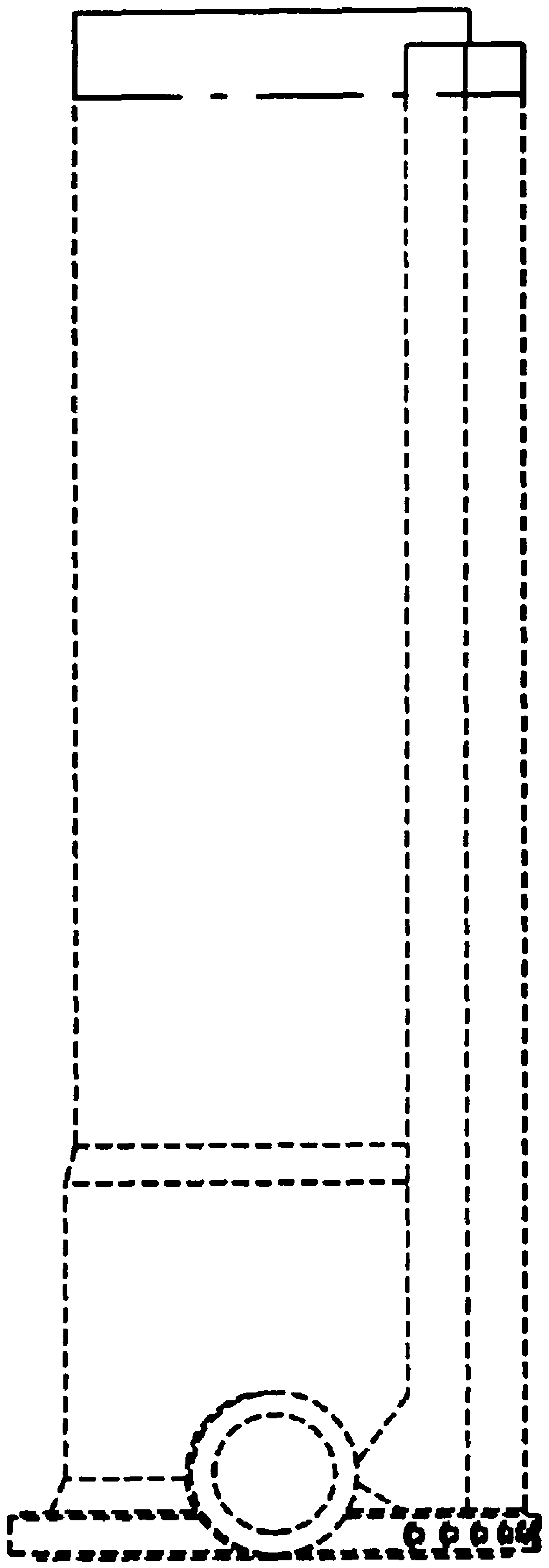


FIG. 5

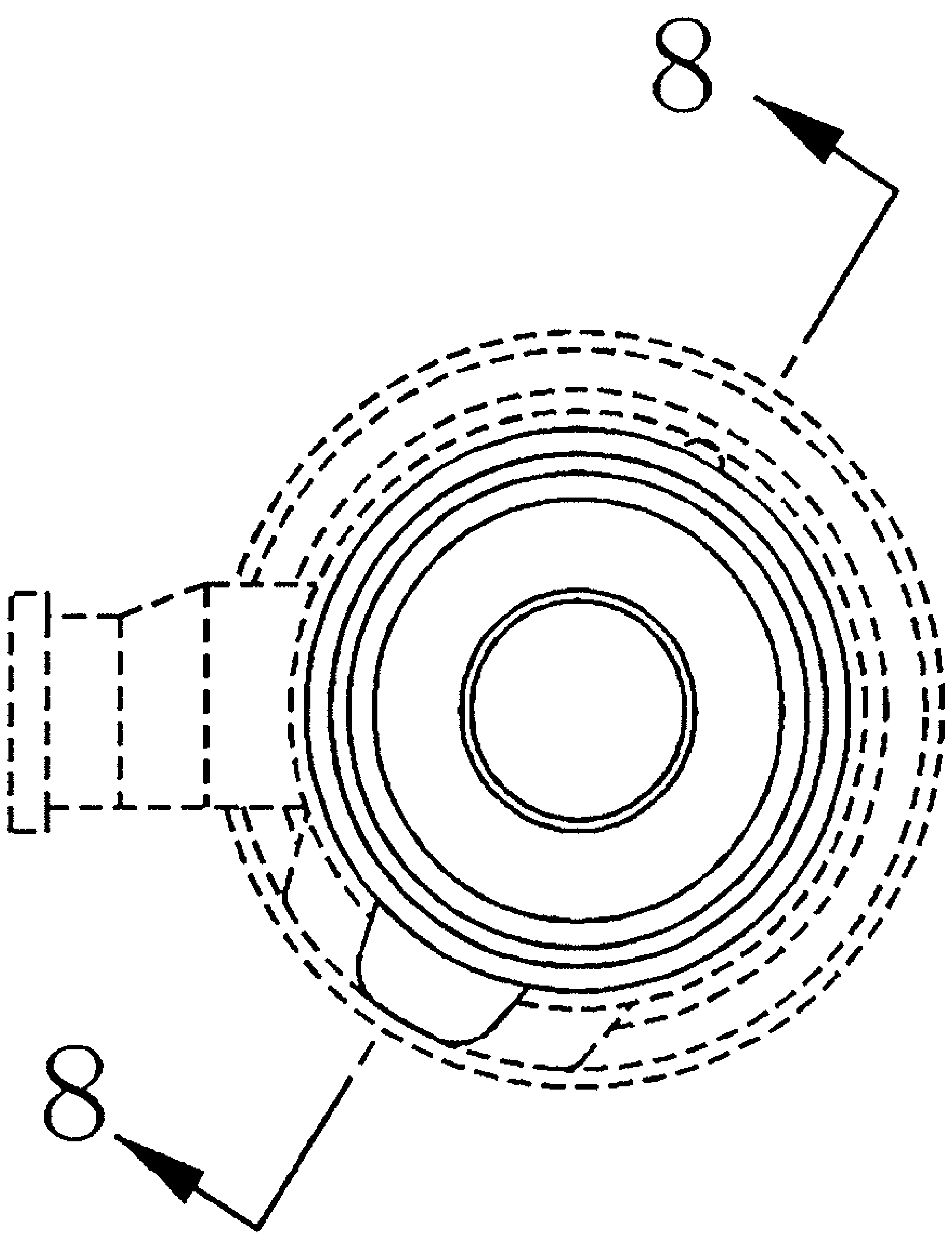


FIG. 6

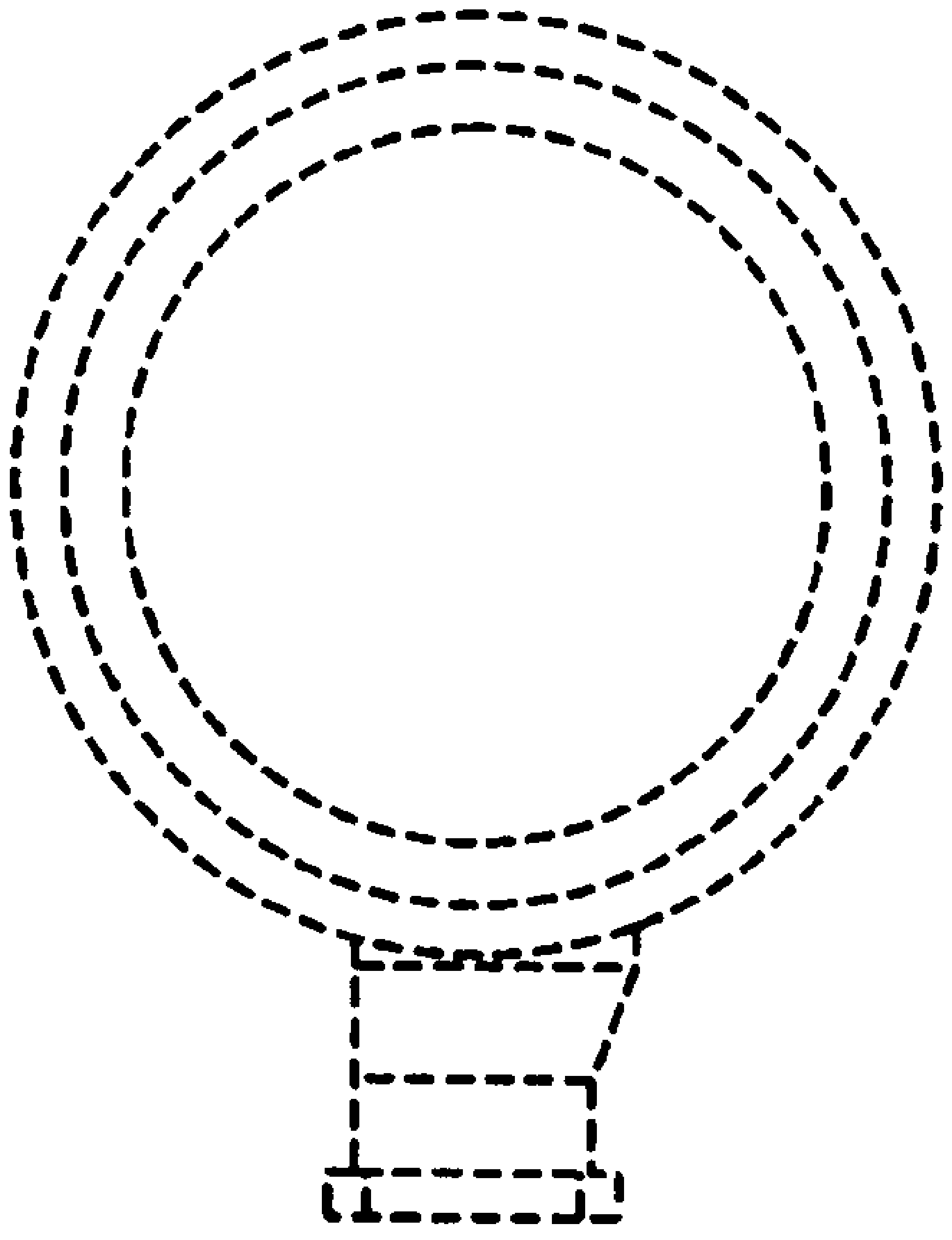


FIG. 7

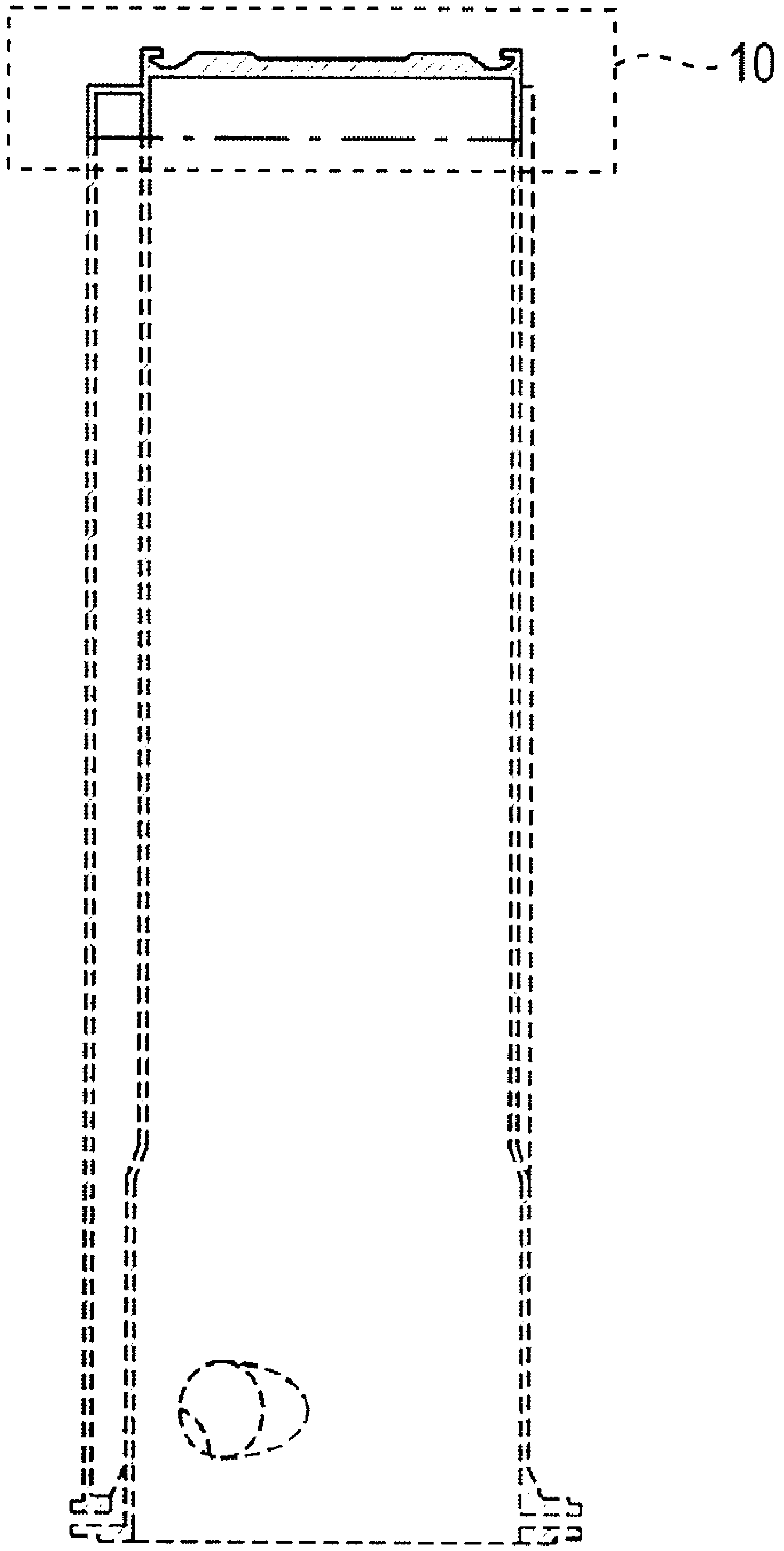


FIG. 8

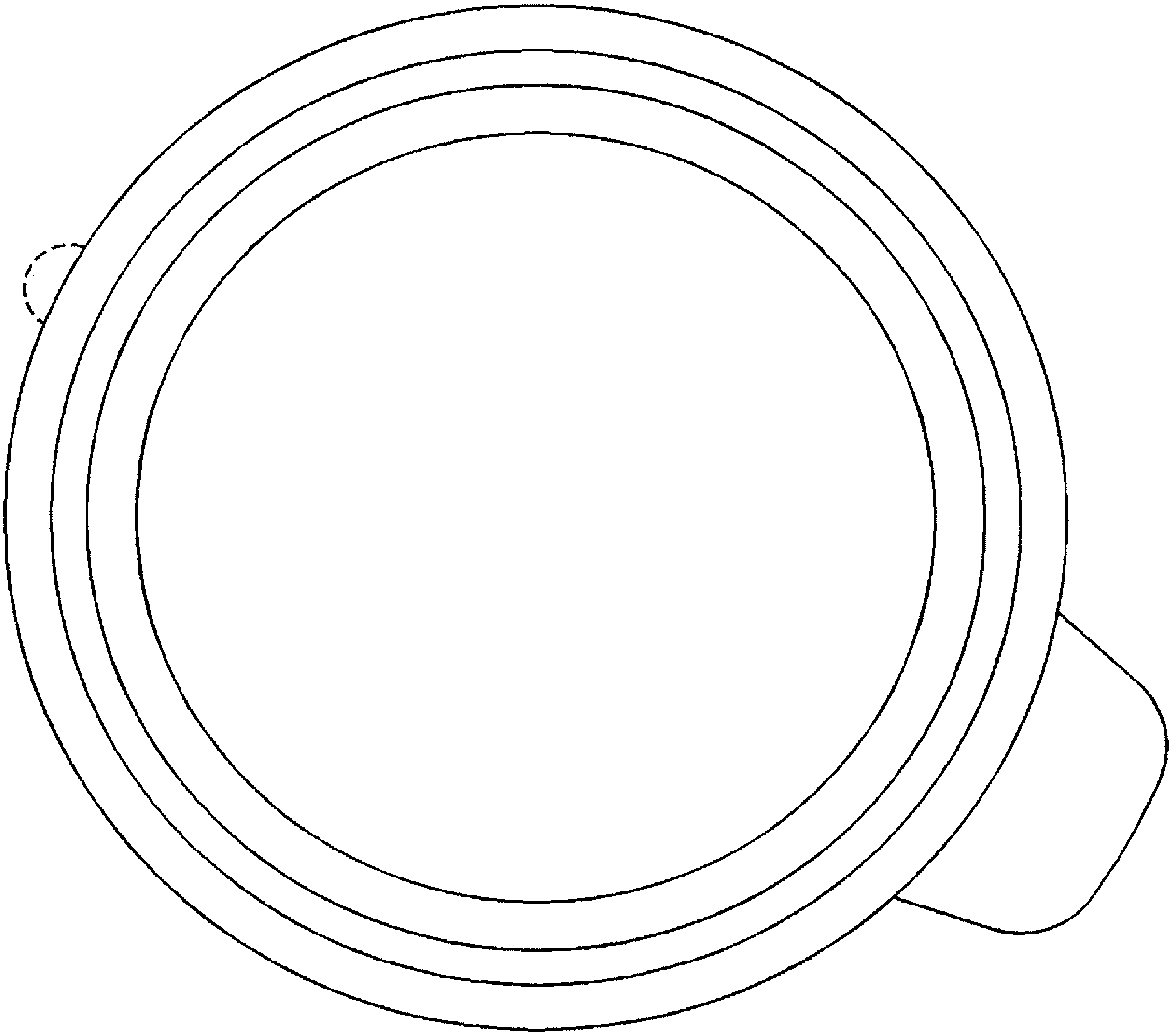


FIG. 9

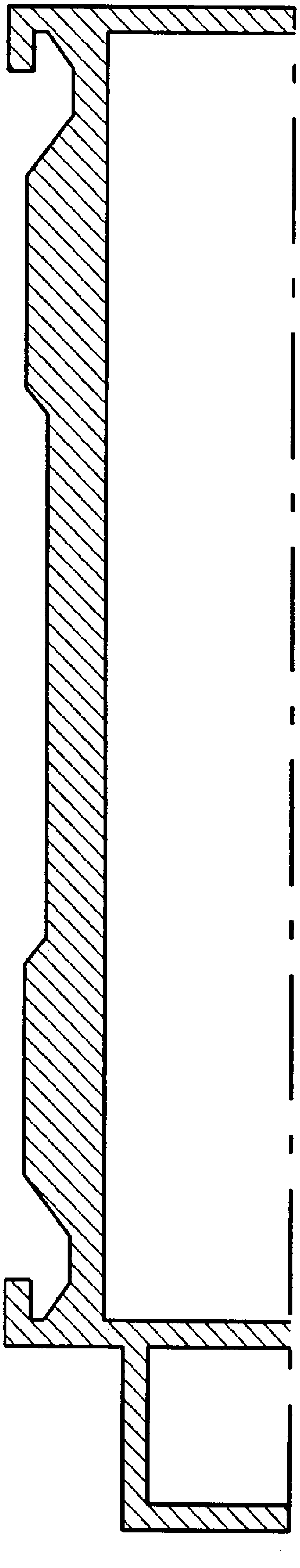


FIG. 10