



US00D616390S

(12) **United States Design Patent**  
**Sato**

(10) **Patent No.:** **US D616,390 S**

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(54) **QUARTZ COVER FOR MANUFACTURING SEMICONDUCTOR WAFERS**

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(73) Assignee: **Tokyo Electron Limited**, Minato-Ku (JP)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/342,844**

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(30) **Foreign Application Priority Data**

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(51) **LOC (9) Cl.** ..... **13-03**

(52) **U.S. Cl.** ..... **D13/182**

(58) **Field of Classification Search** ..... D13/182;  
206/454, 711; 118/728, 729; 211/41.18;  
432/241, 253, 258

See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a quartz cover for manufacturing semiconductor wafers, as shown and described.

**DESCRIPTION**

FIG. 1 is top perspective view of a quartz cover for manufacturing semiconductor wafers illustrating my new design;

FIG. 2 is a bottom perspective view thereof;

FIG. 3 is a front view thereof, the rear view being the same image and, therefore, not shown;

FIG. 4 is a right side view thereof, the left side view being the same image and, therefore, not shown;

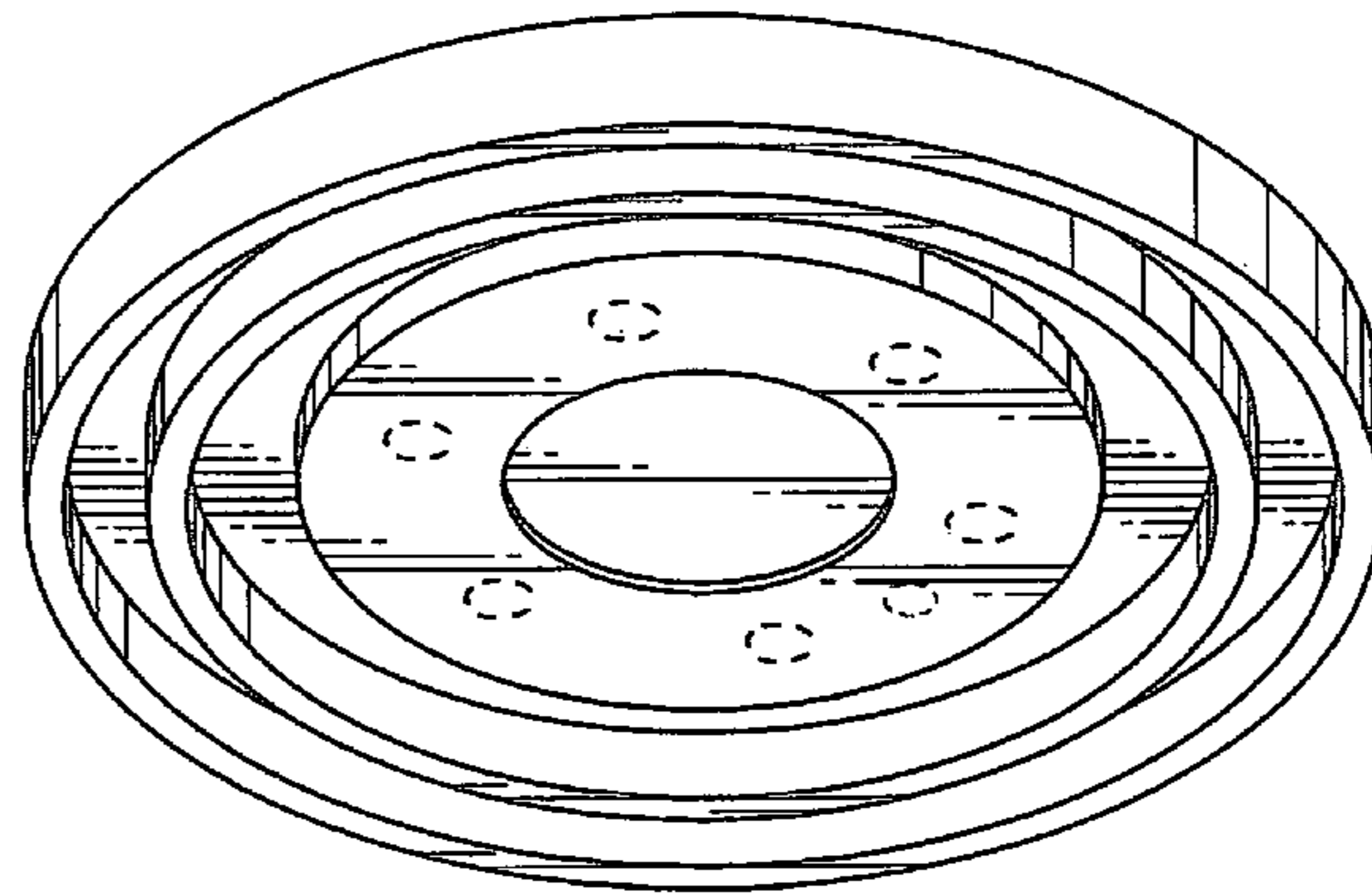
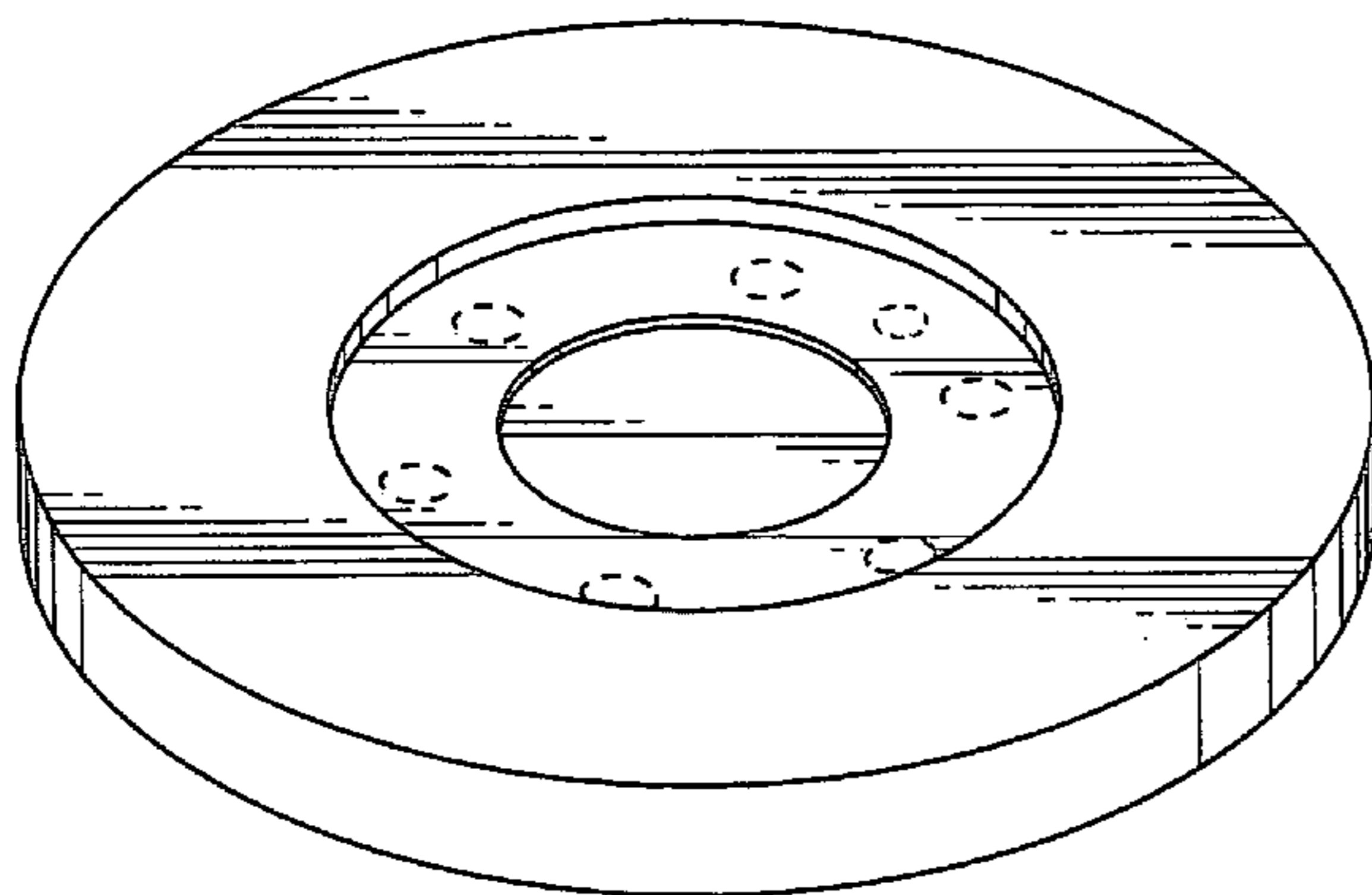
FIG. 5 is a top plan view thereof;

FIG. 6 is a bottom plan view thereof; and,

FIG. 7 is a cross-sectional view taken through line 7—7 of FIG. 5.

The broken lines are shown for illustrative purposes only and form no part of the claimed design.

**1 Claim, 3 Drawing Sheets**



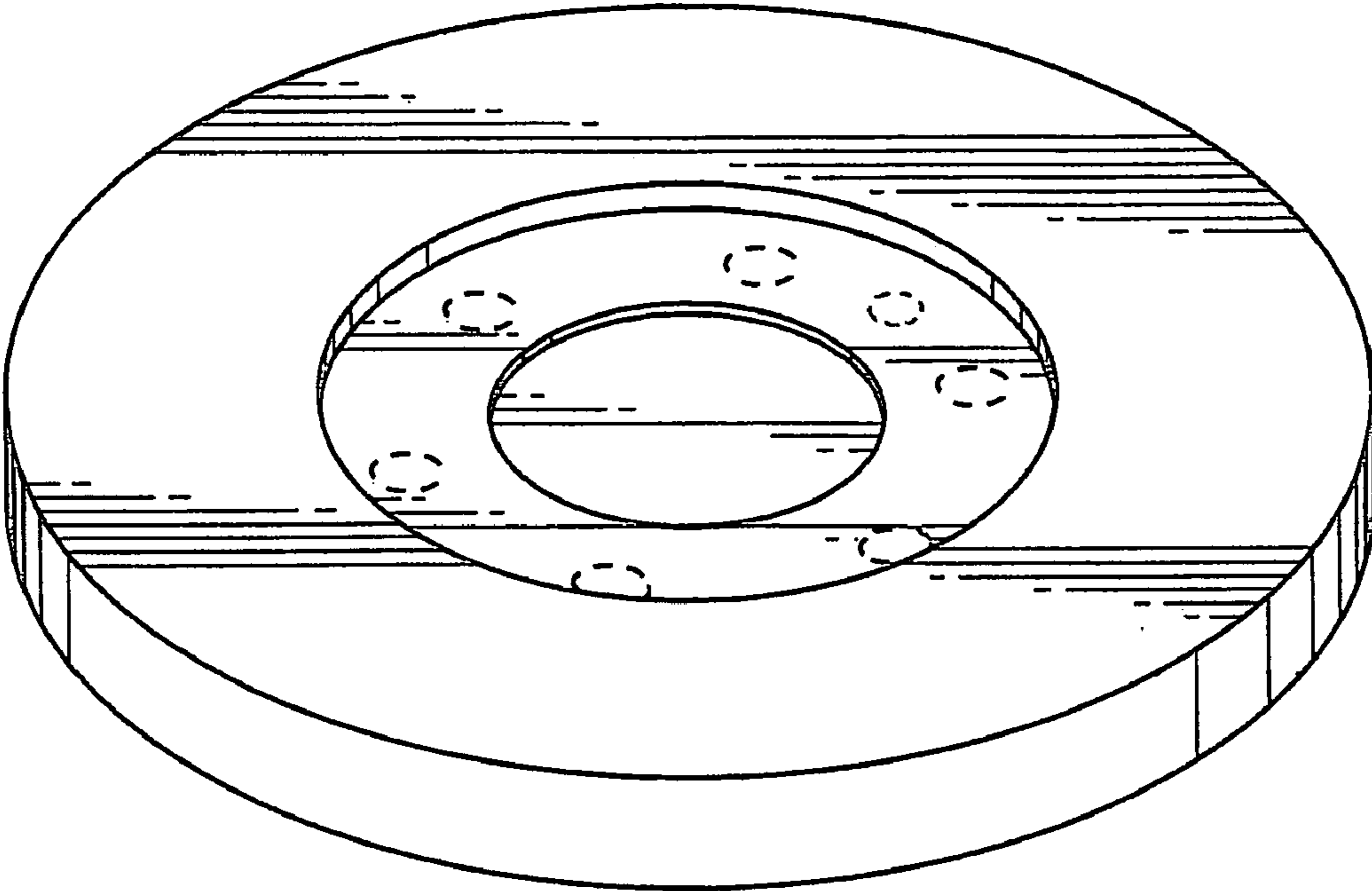


FIG. 1

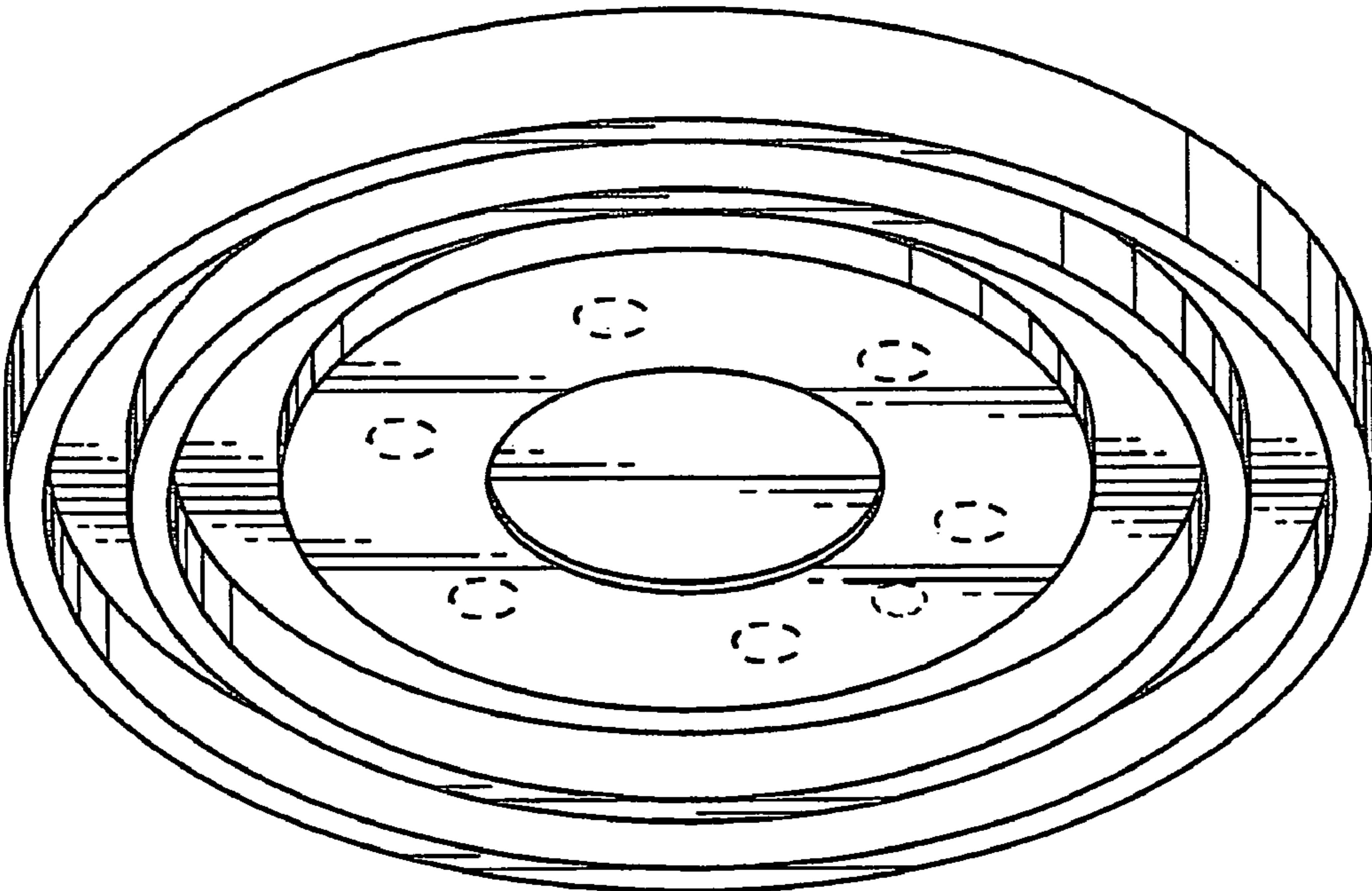


FIG. 2



FIG. 3



FIG. 4

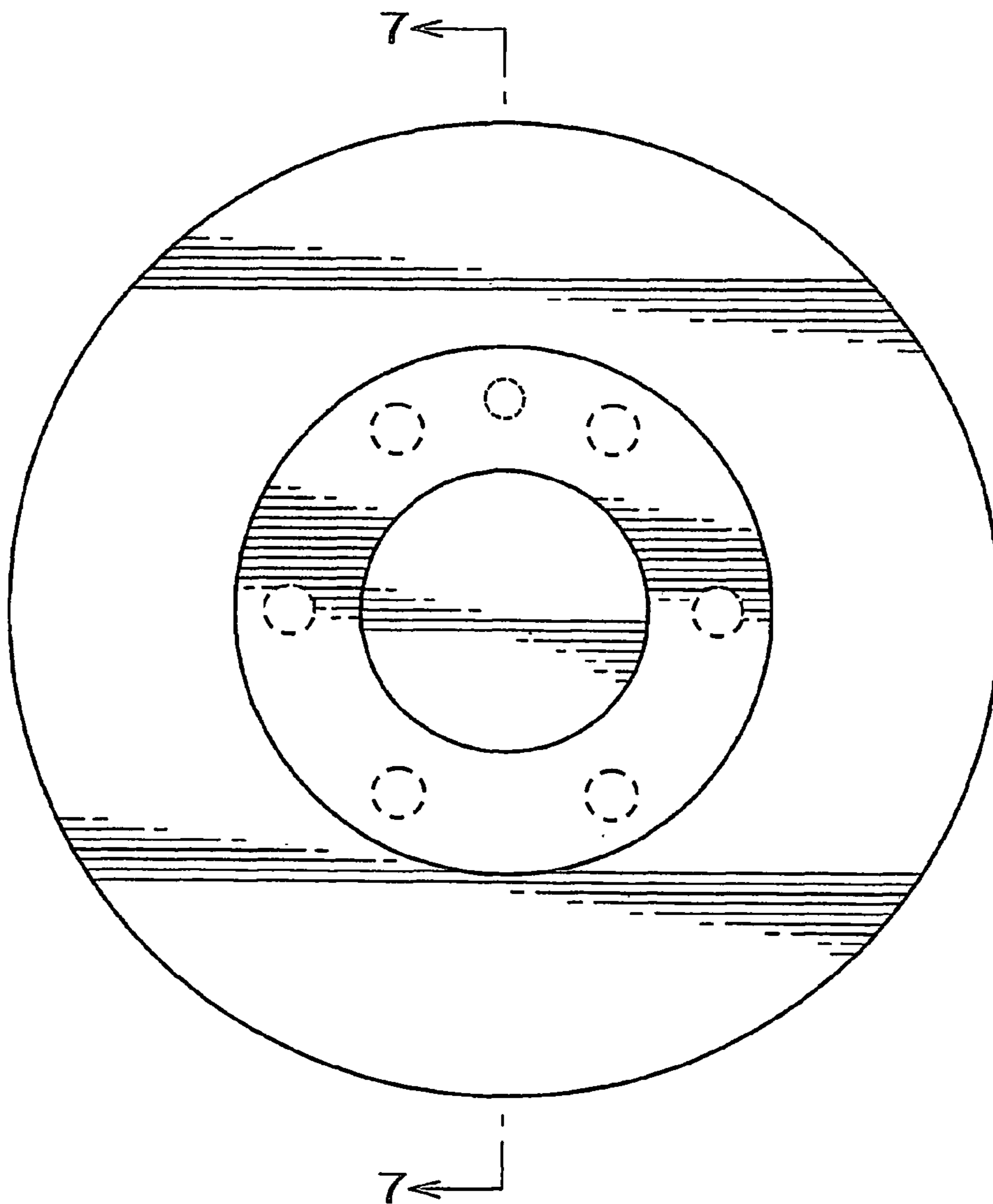


FIG. 5

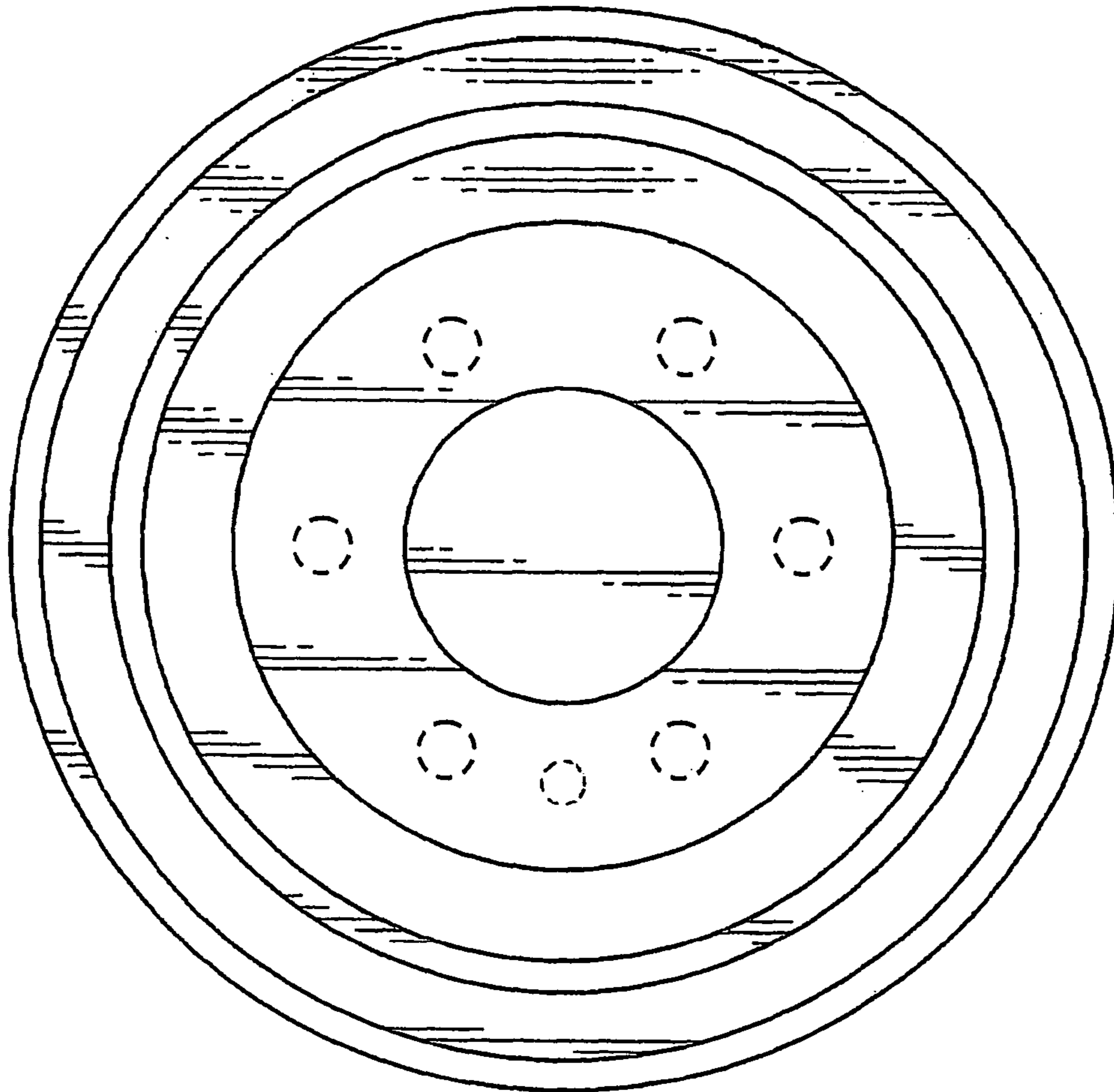


FIG. 6

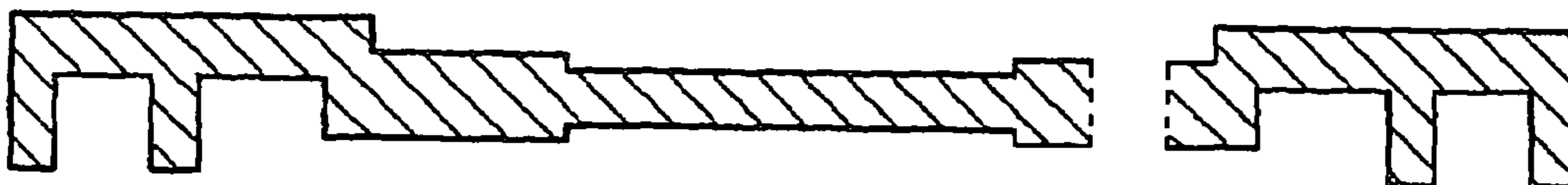


FIG. 7