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(12) **United States Design Patent**
Yokota et al.

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(54) **ELECTRIC CIRCUIT BOARD**

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(73) Assignee: **Panasonic Corporation**, Osaka (JP)

(**) Term: **14 Years**

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Jan. 14, 2005	(JP)	2005-000812
Jan. 14, 2005	(JP)	2005-000813

(51) **LOC (9) Cl.** **13-03**
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D10/62, 64; D14/436; D20/11; 220/62;
257/E23.07, E23.175, E23.179, 778, 779;
324/537, 755, 758; 361/737, 748, 760, 820;
174/250

See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

5,066,999	A *	11/1991	Casper	257/786
5,300,815	A *	4/1994	Rostoker	257/786
5,757,082	A *	5/1998	Shibata	257/786
6,269,327	B1 *	7/2001	Bednar et al.	703/8
6,348,742	B1 *	2/2002	MacPherson	257/786

6,476,505	B2 *	11/2002	Nakamura	257/786
6,483,038	B2 *	11/2002	Lee et al.	174/255
2003/0112613	A1 *	6/2003	Nishizawa et al.	361/764

FOREIGN PATENT DOCUMENTS

JP	797738	10/1990
JP	797739	10/1990
JP	1096035	1/2001
JP	1219525	10/2004

* cited by examiner

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(57) **CLAIM**

We claim the ornamental design for an electric circuit board, as shown and described.

DESCRIPTION

FIG. 1 is a top perspective view of a first embodiment of an electric circuit board of the present invention;

FIG. 2 is a top view thereof;

FIG. 3 is a top perspective view of a second embodiment of an electric circuit board of the present invention;

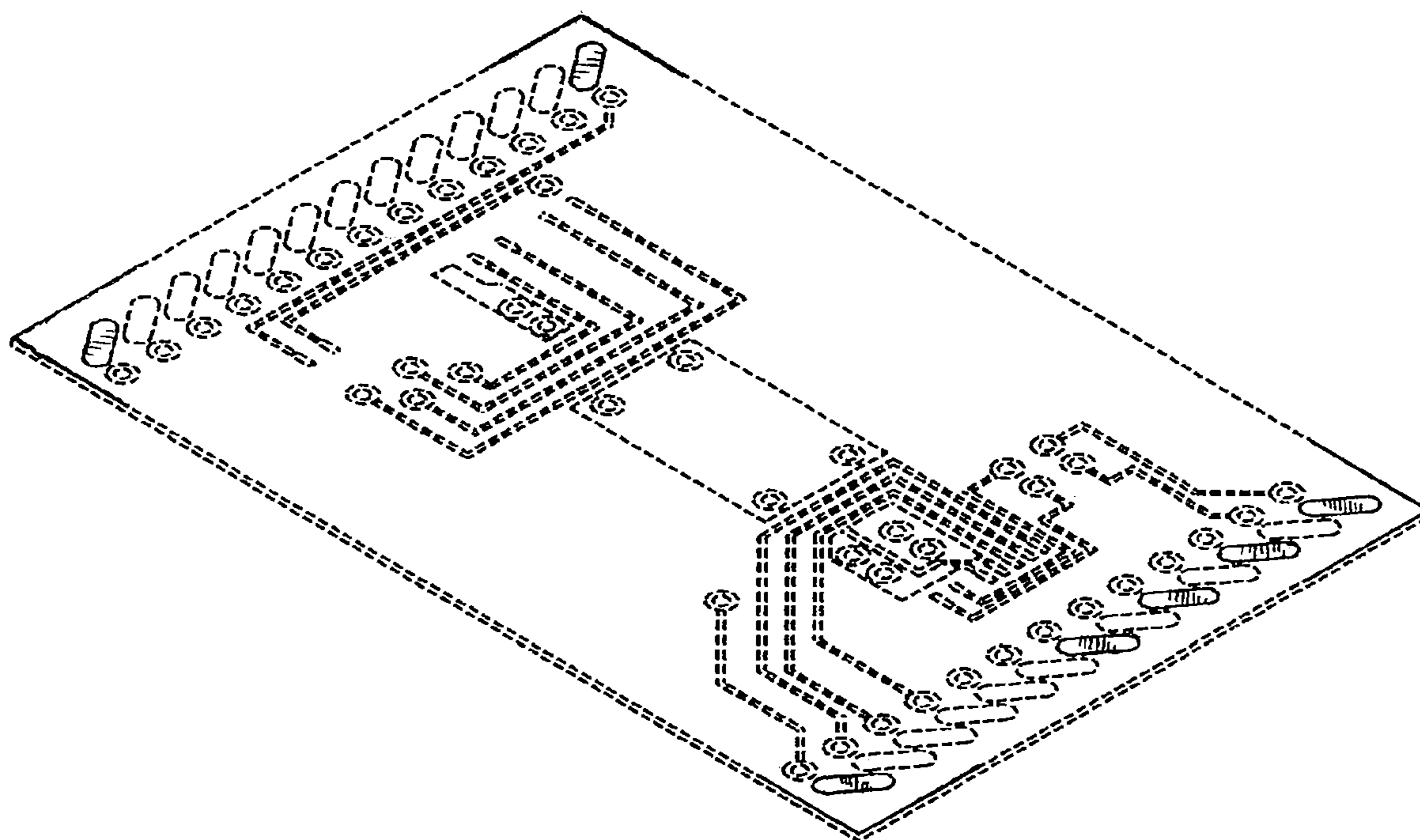
FIG. 4 is a top view thereof;

FIG. 5 is a top perspective view of a third embodiment of an electric circuit board of the present invention; and,

FIG. 6 is a top view thereof.

The ornamental design which is claimed is shown in solid lines in the drawings. Any broken lines in the drawings are for illustrative purposes only and form no part of the claimed design.

1 Claim, 3 Drawing Sheets



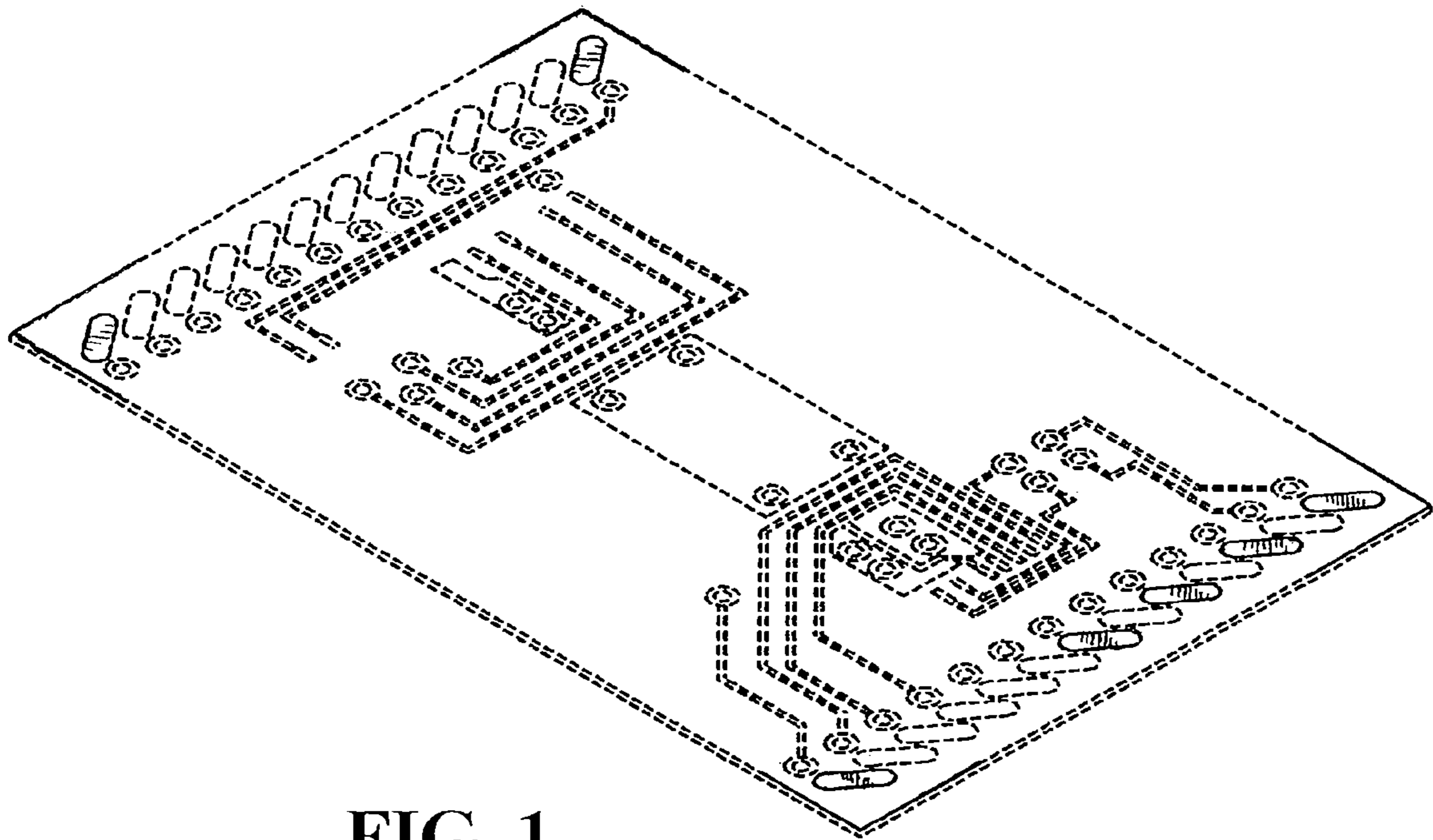


FIG. 1

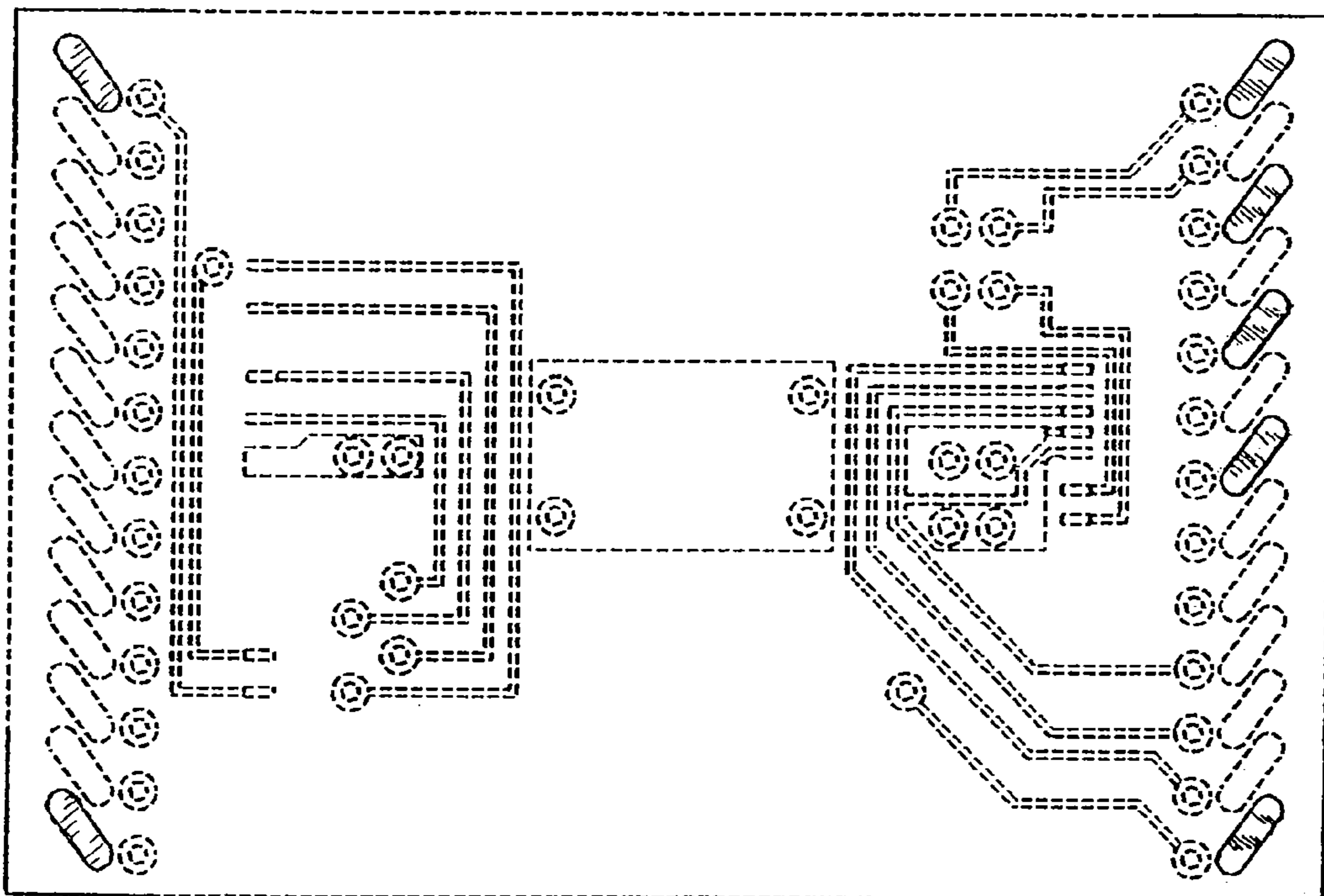


FIG. 2

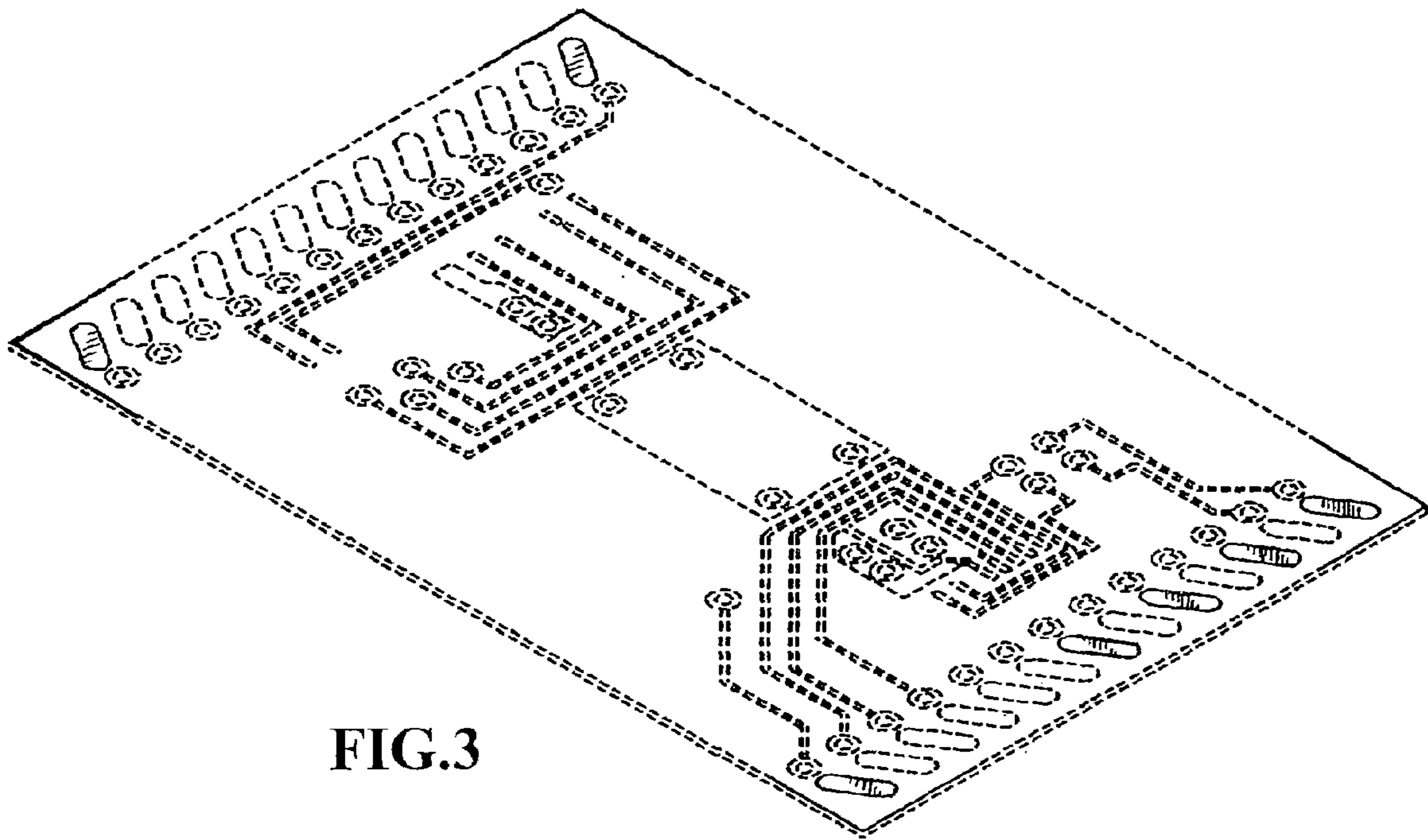


FIG.3

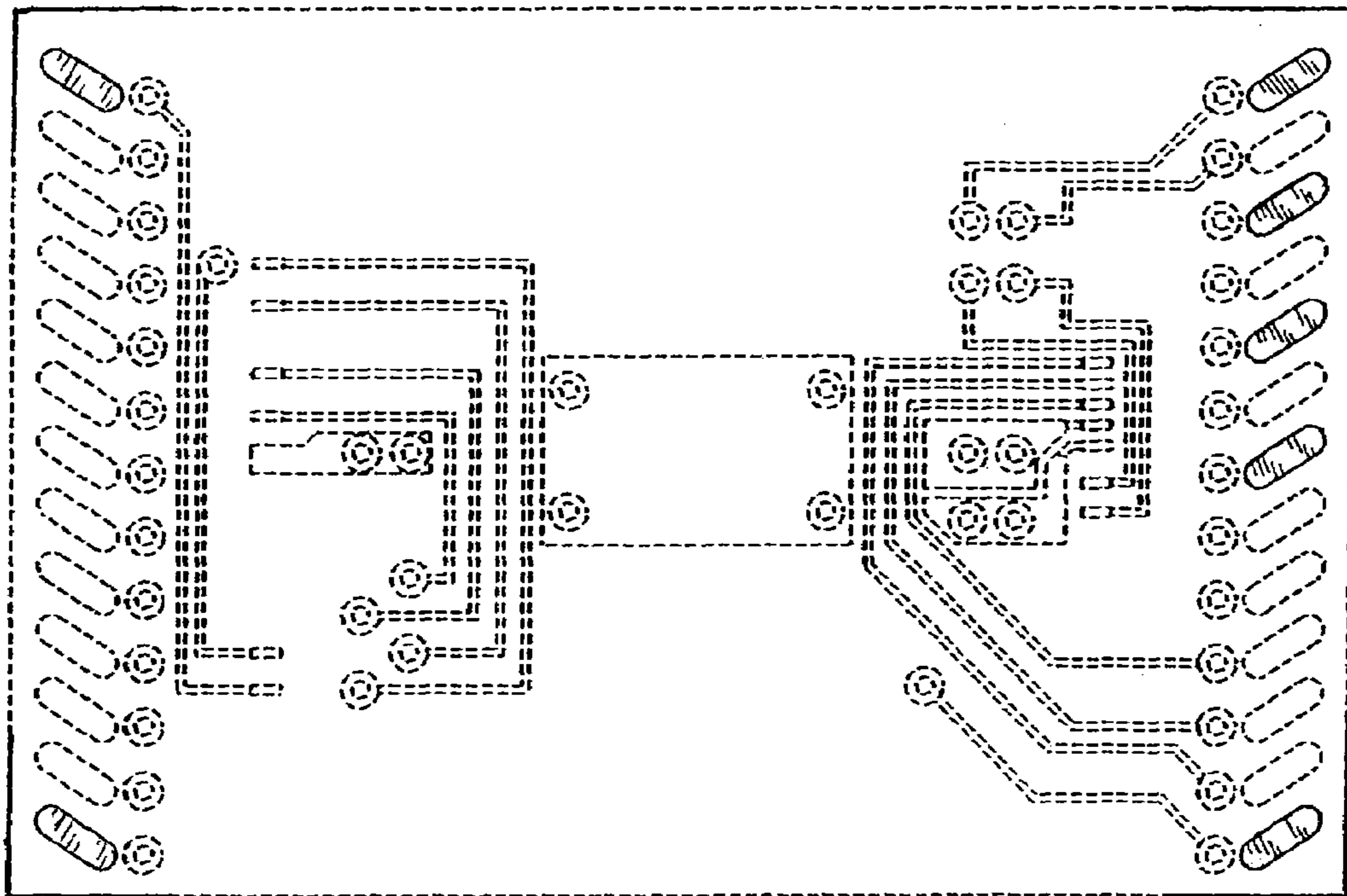


FIG.4

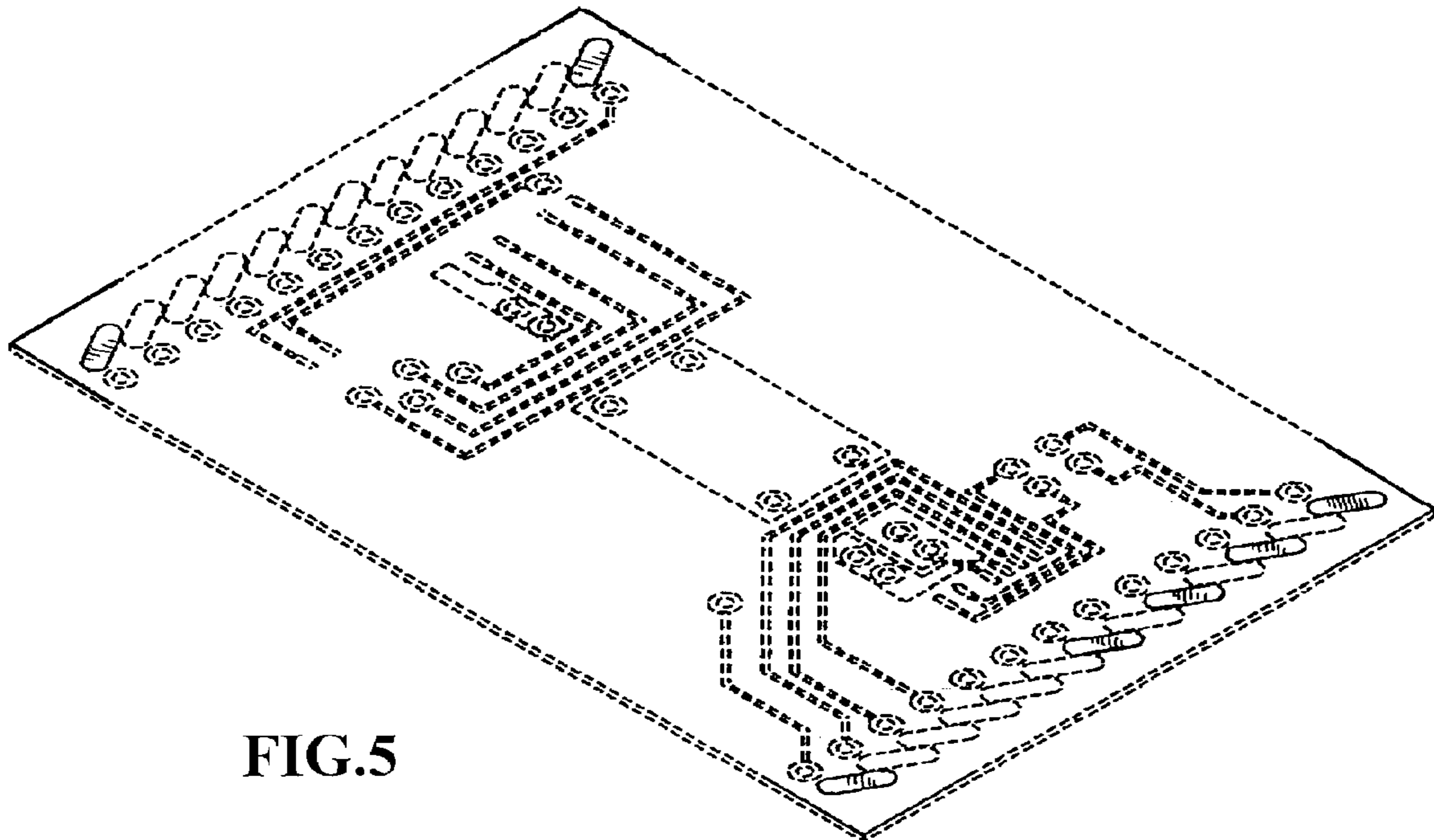


FIG.5

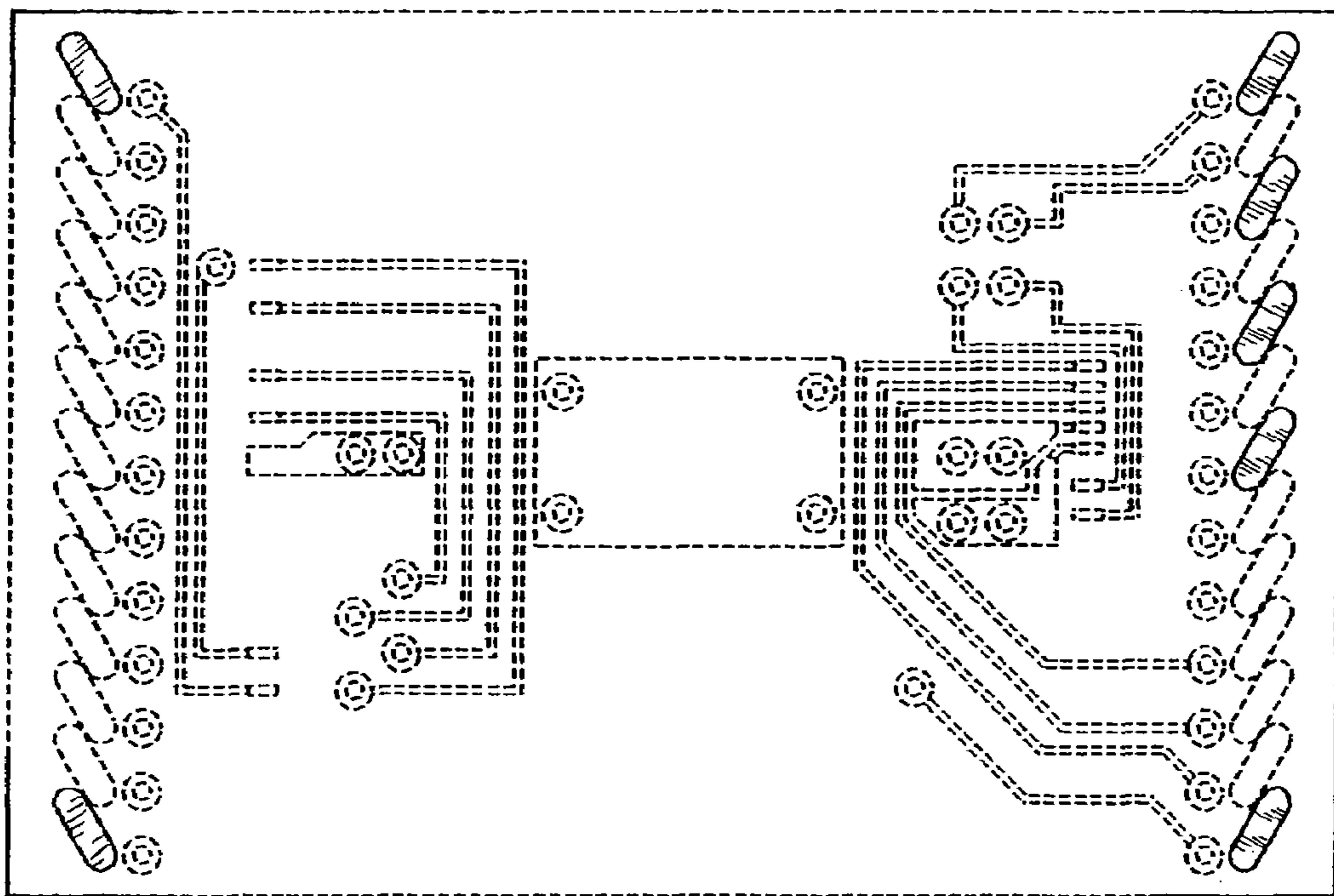


FIG.6