



US00D612347S

(12) **United States Design Patent**
Yokota et al.

(10) **Patent No.:** **US D612,347 S**
(45) **Date of Patent:** **** *Mar. 23, 2010**

(54) **ELECTRIC CIRCUIT BOARD**

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(73) Assignee: **Panasonic Corporation**, Osaka (JP)

(**) Term: **14 Years**

(21) Appl. No.: **29/232,152**

(22) Filed: **Jun. 15, 2005**

(30) **Foreign Application Priority Data**

Jan. 14, 2005 (JP) 2005-000811
Jan. 14, 2005 (JP) 2005-000812
Jan. 14, 2005 (JP) 2005-000813

(51) **LOC (9) Cl.** **13-03**
(52) **U.S. Cl.** **D13/182**
(58) **Field of Classification Search** D13/182;
D10/62, 64; D14/436; D20/11; 220/62;
257/E23.07, E23.175, E23.179, 778, 779;
324/537, 755, 758; 361/737, 748, 760, 820;
174/250

See application file for complete search history.

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(57) **CLAIM**

We claim the ornamental design for an electric circuit board, as shown and described.

DESCRIPTION

FIG. 1 is a top perspective view of a first embodiment of an electric circuit board of the present invention;

FIG. 2 is a top view thereof;

FIG. 3 is a top perspective view of a second embodiment of an electric circuit board of the present invention;

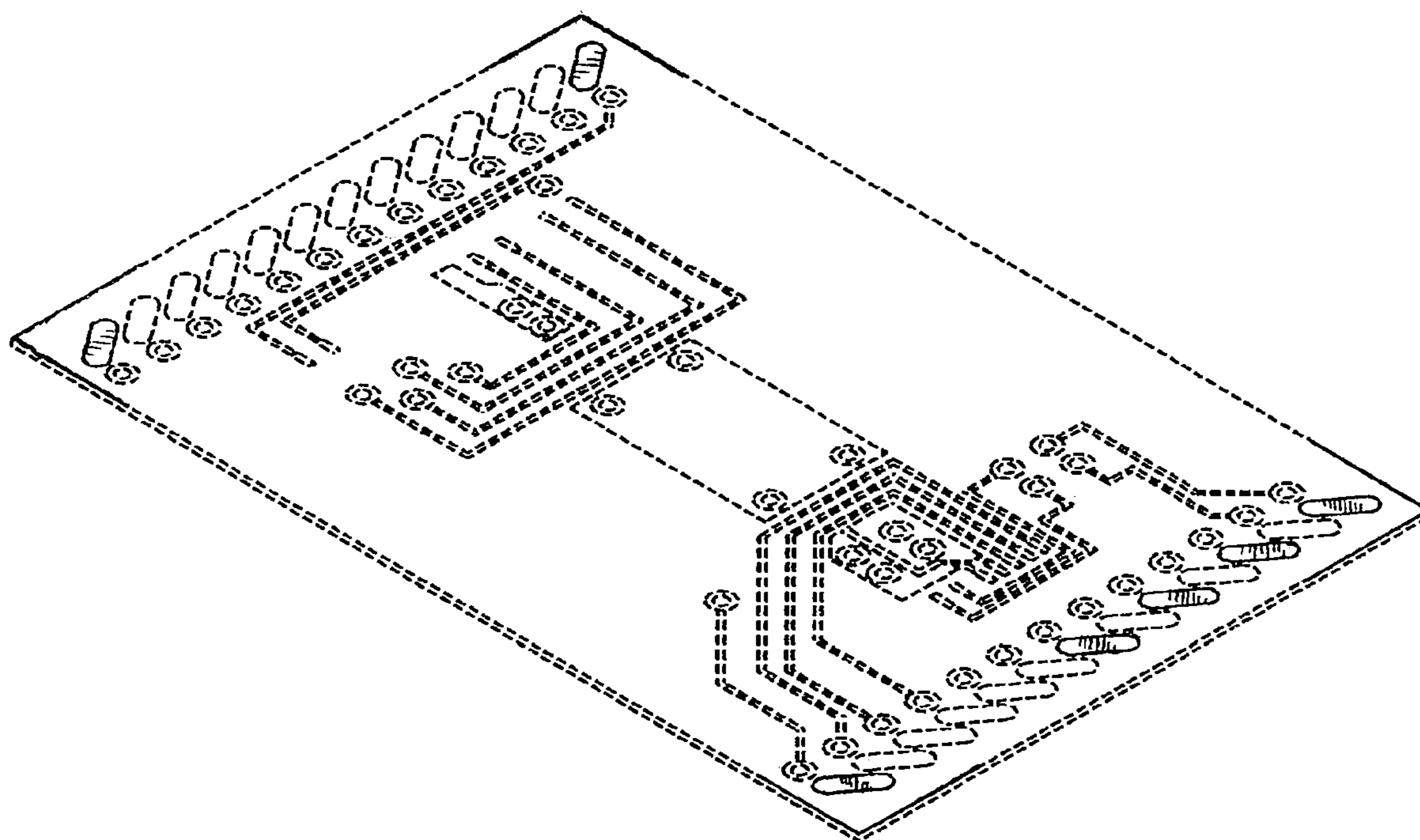
FIG. 4 is a top view thereof;

FIG. 5 is a top perspective view of a third embodiment of an electric circuit board of the present invention; and,

FIG. 6 is a top view thereof.

The ornamental design which is claimed is shown in solid lines in the drawings. Any broken lines in the drawings are for illustrative purposes only and form no part of the claimed design.

1 Claim, 3 Drawing Sheets



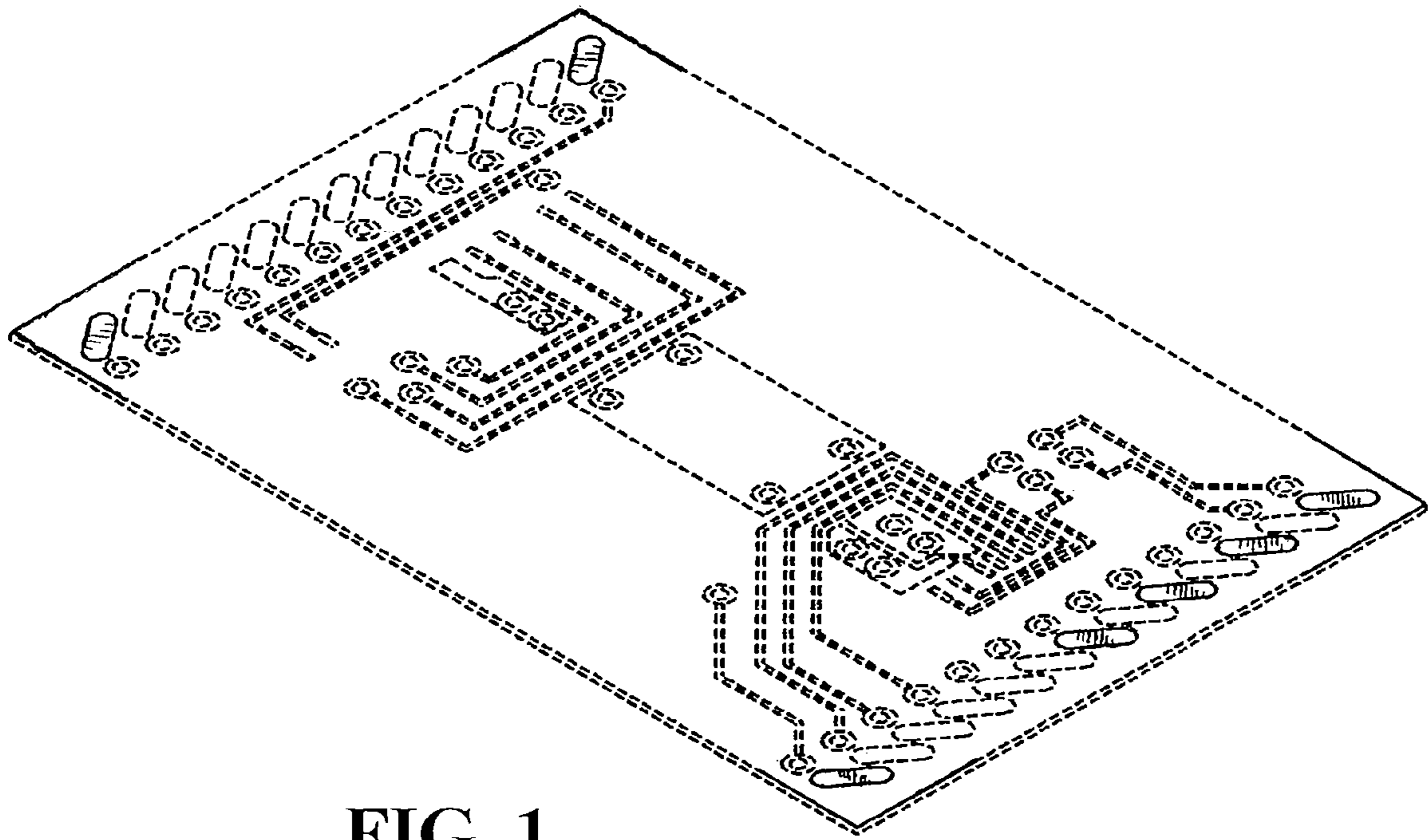


FIG. 1

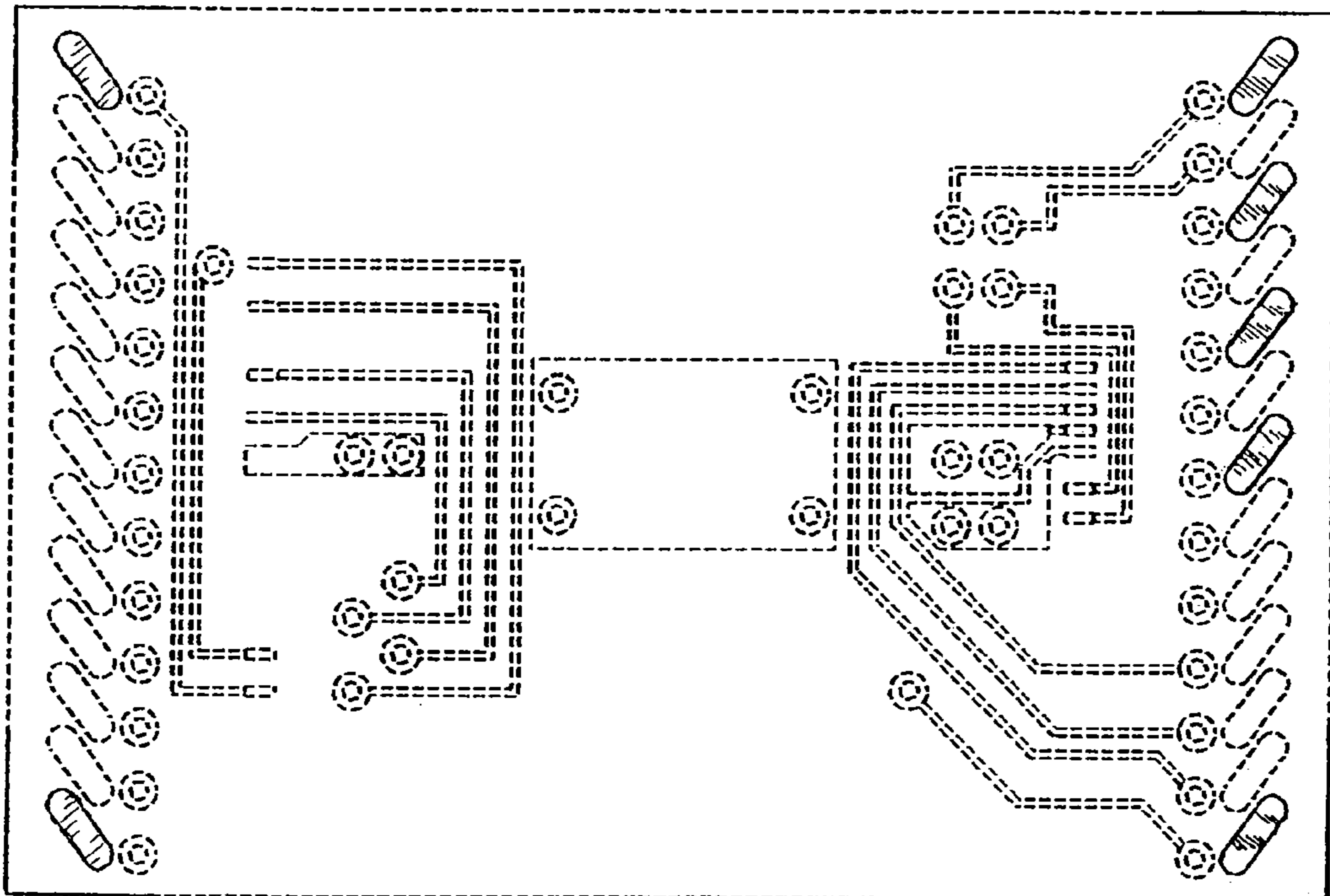


FIG. 2

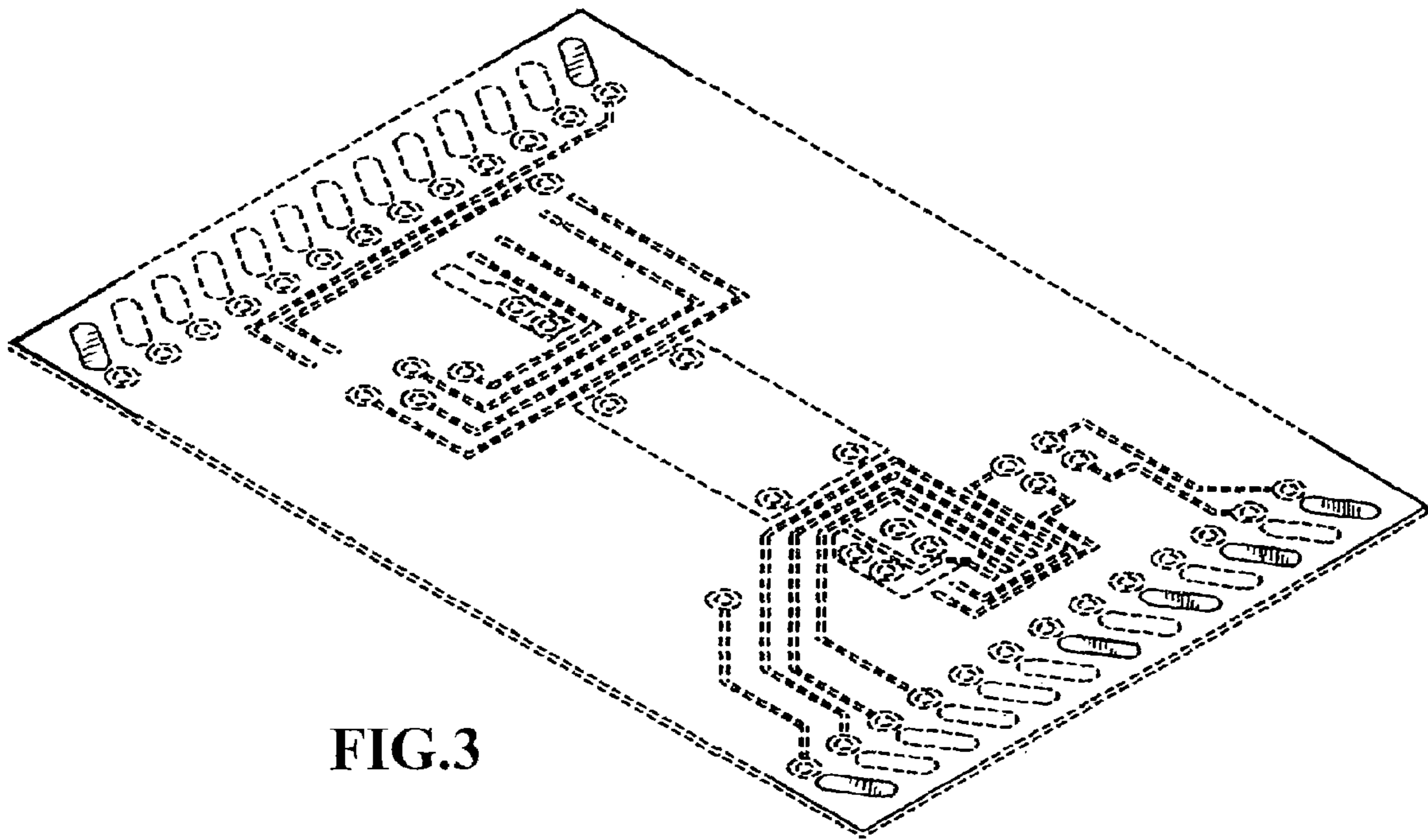


FIG.3

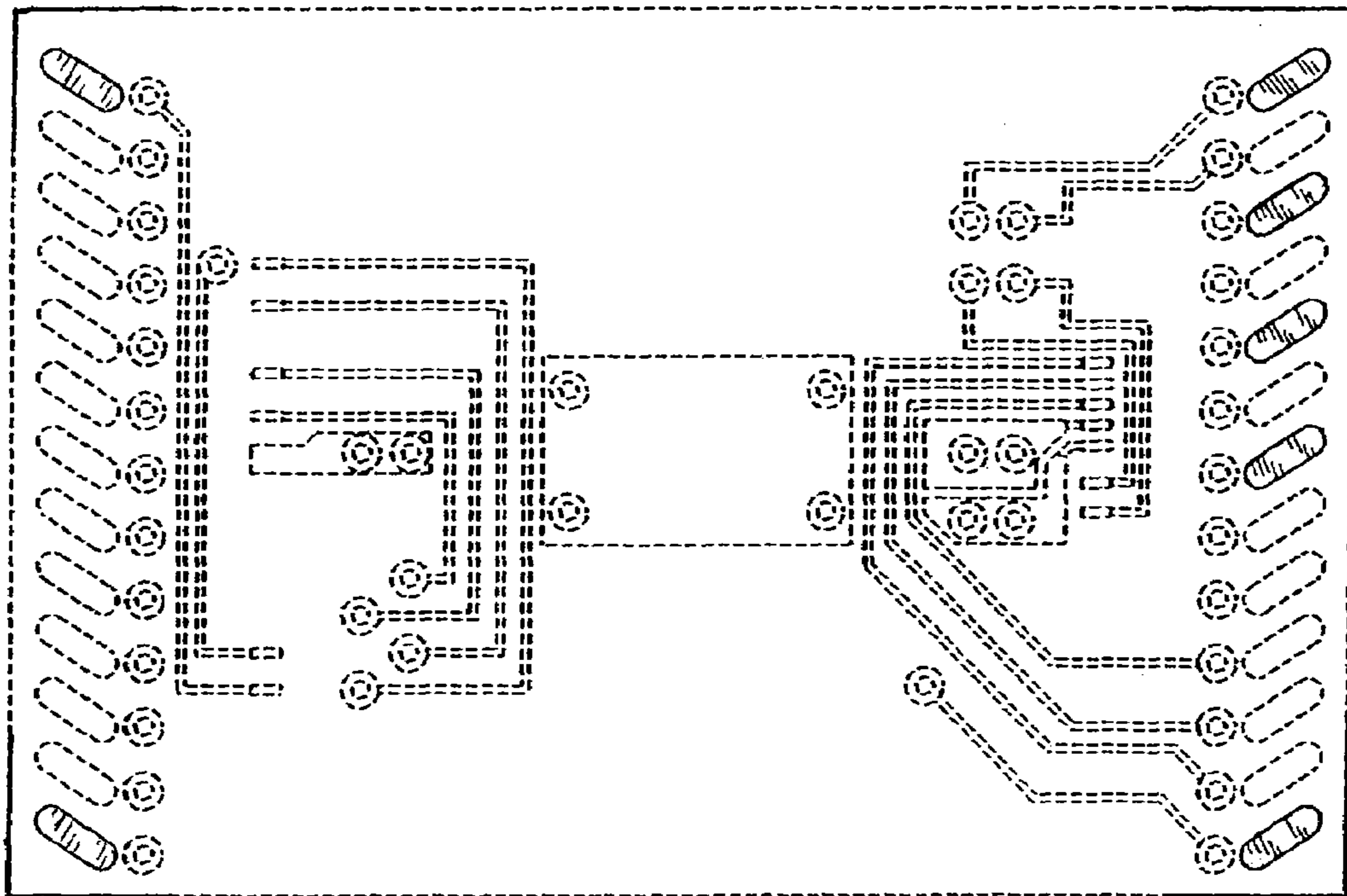


FIG.4

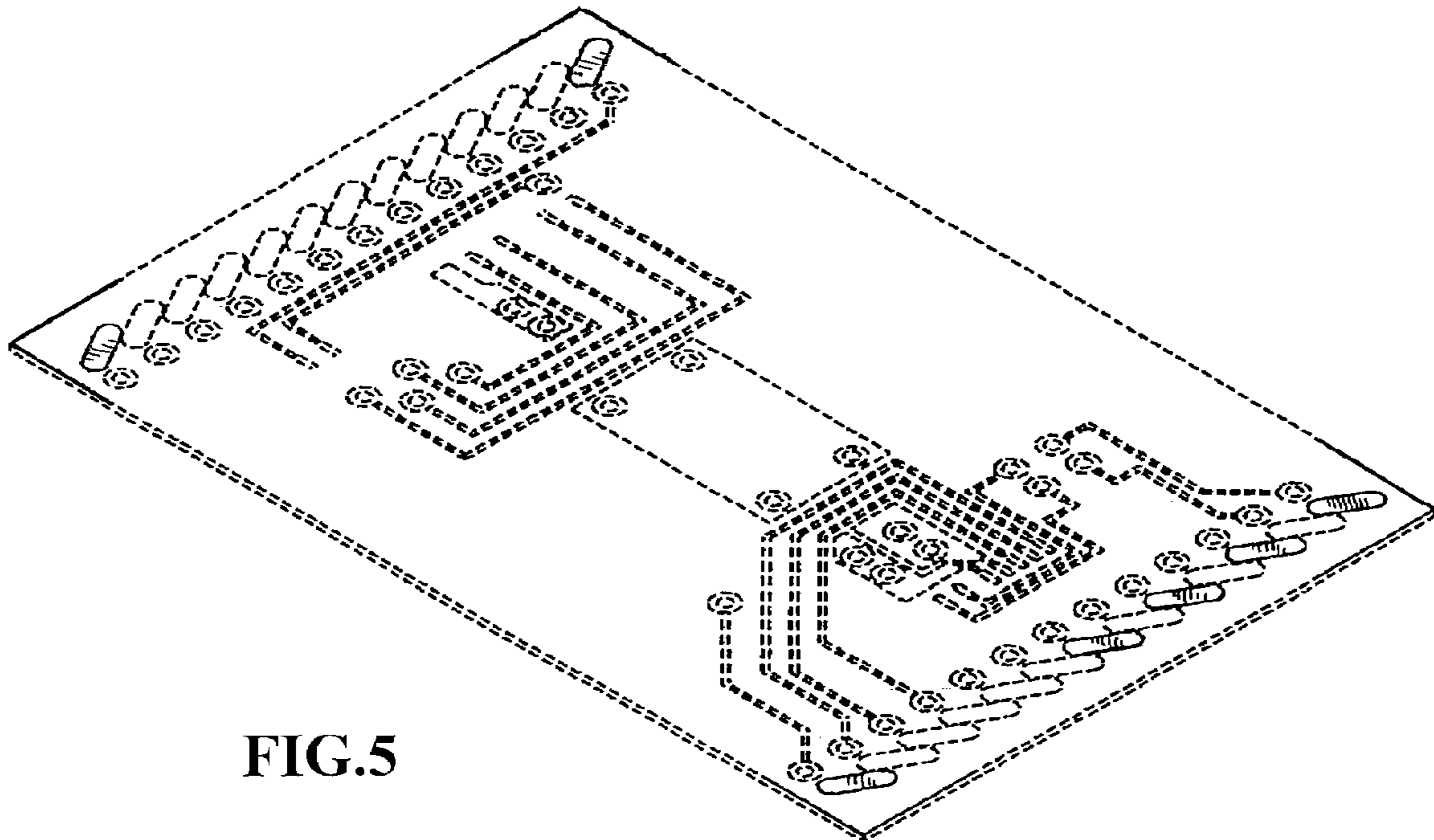


FIG.5

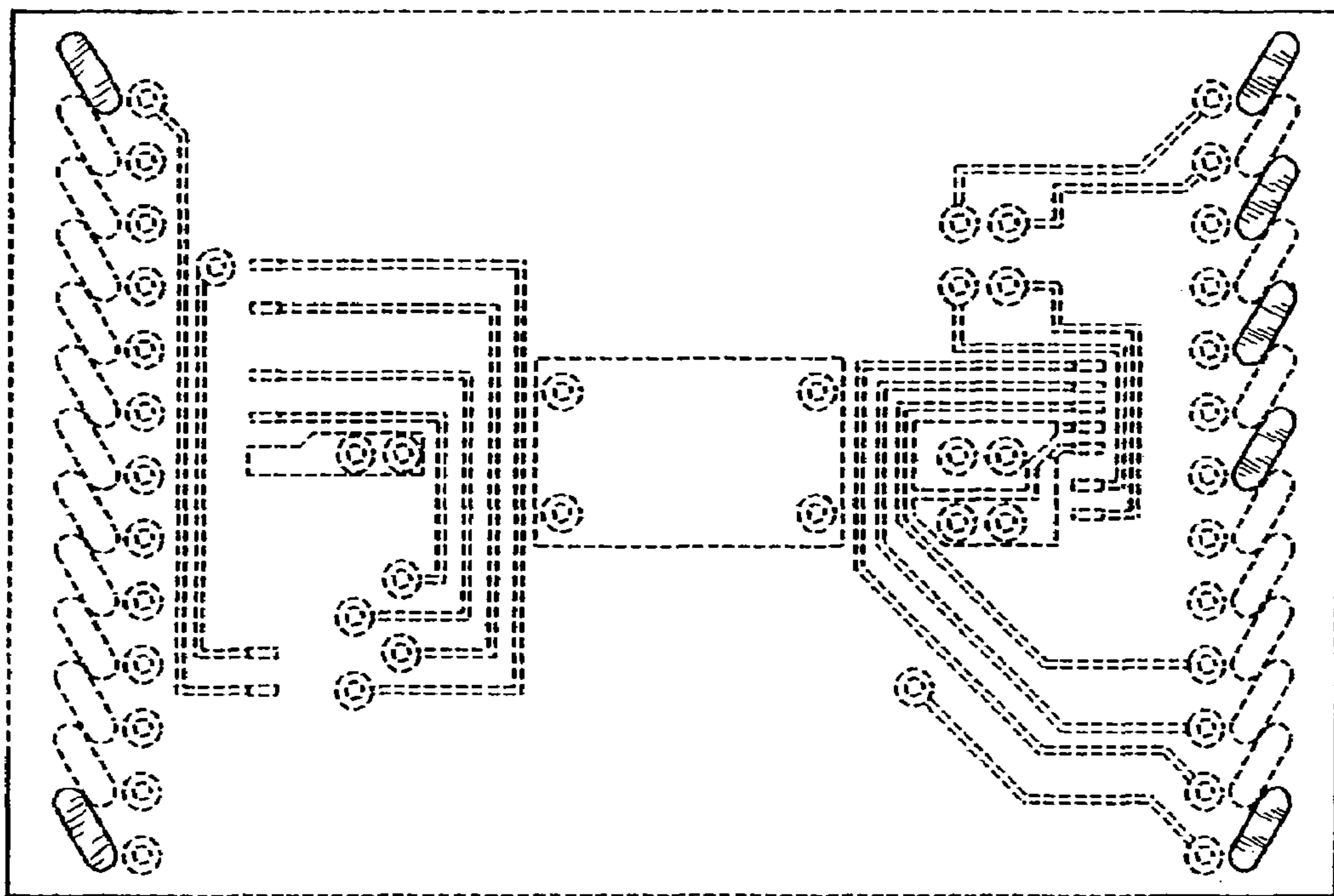


FIG.6