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(12) **United States Design Patent**
Carter et al.

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(45) **Date of Patent:** **** Dec. 8, 2009**

(54) **PRINTED CIRCUIT BOARD FOR ELECTRICAL CONNECTOR**

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2007/0230145 A1* 10/2007 Yamamoto 361/760

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FOREIGN PATENT DOCUMENTS

(73) Assignee: **ADC GmbH**, Berlin (DE)

WO WO 2004/010536 A1 1/2004
WO WO 2008/119370 A1 10/2008

(**) Term: **14 Years**

OTHER PUBLICATIONS

(21) Appl. No.: **29/327,979**

Photographs of a commercial product by ADC Krone, Highband 8 Printed Circuit Board, 2 Photographs, front and back images.
Photographs of a commercial product by ADC Krone, Highband 25 Printed Circuit Board, 2 Photographs, front and back images.

(22) Filed: **Nov. 17, 2008**

(30) **Foreign Application Priority Data**

* cited by examiner

May 15, 2008 (AU) 12334/2008

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(51) **LOC (9) Cl.** **13-03**

(52) **U.S. Cl.** **D13/182**

(58) **Field of Classification Search** D13/182;
29/829; 174/250, 253, 255, 748, 752; 361/720,
361/760; 439/68

See application file for complete search history.

(57) **CLAIM**

The ornamental design for printed circuit board for electrical connector, as shown and described.

(56) **References Cited**

DESCRIPTION

U.S. PATENT DOCUMENTS

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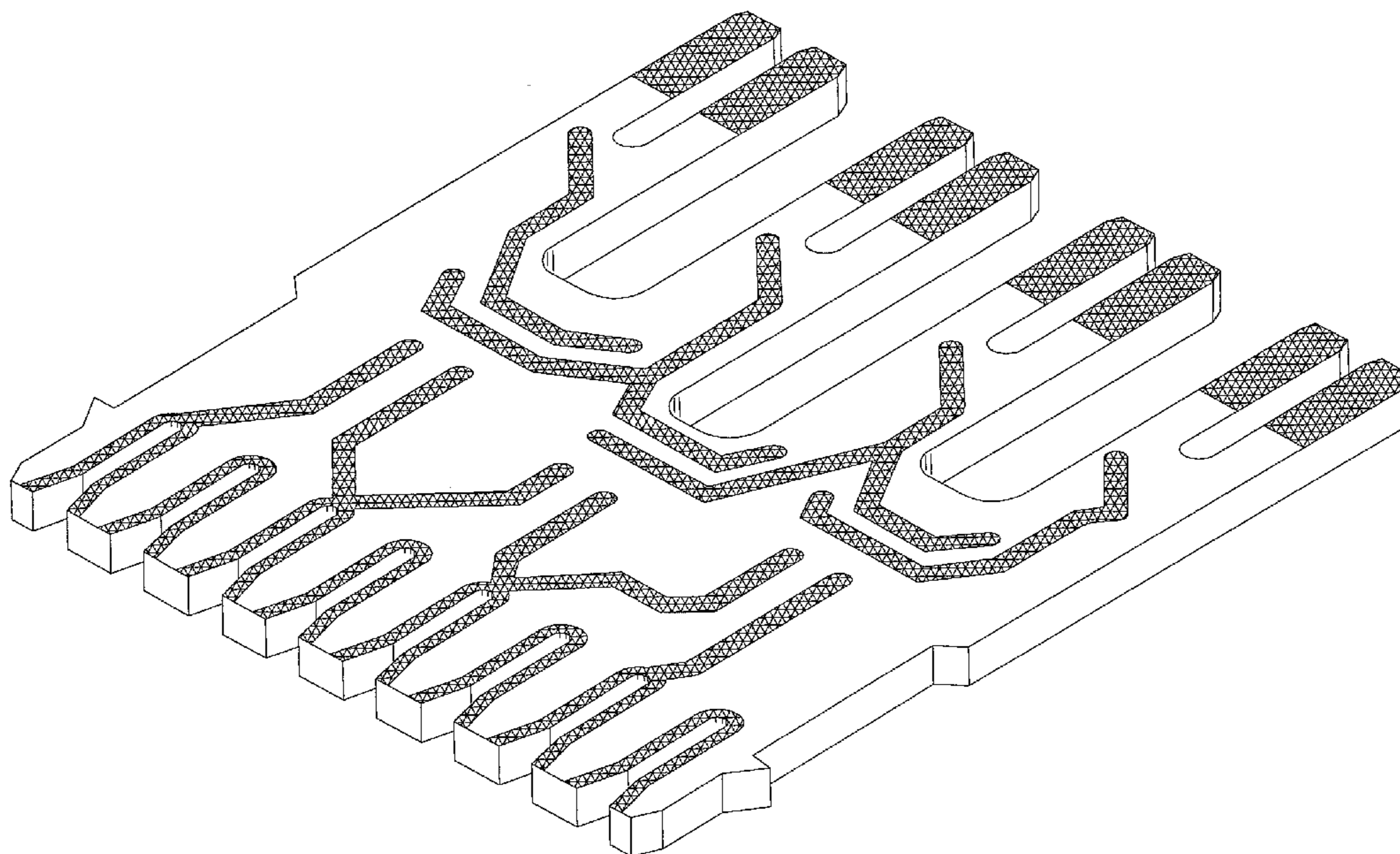
FIG. 1 is a view of one face of the printed circuit board for electrical connector;

FIG. 2 is a side view of the printed circuit board for electrical connector;

FIG. 3 is a view of an opposite face of the printed circuit board for electrical connector to the face shown in FIG. 1; and,

FIG. 4 is a perspective view of the printed circuit board for electrical connector.

1 Claim, 3 Drawing Sheets



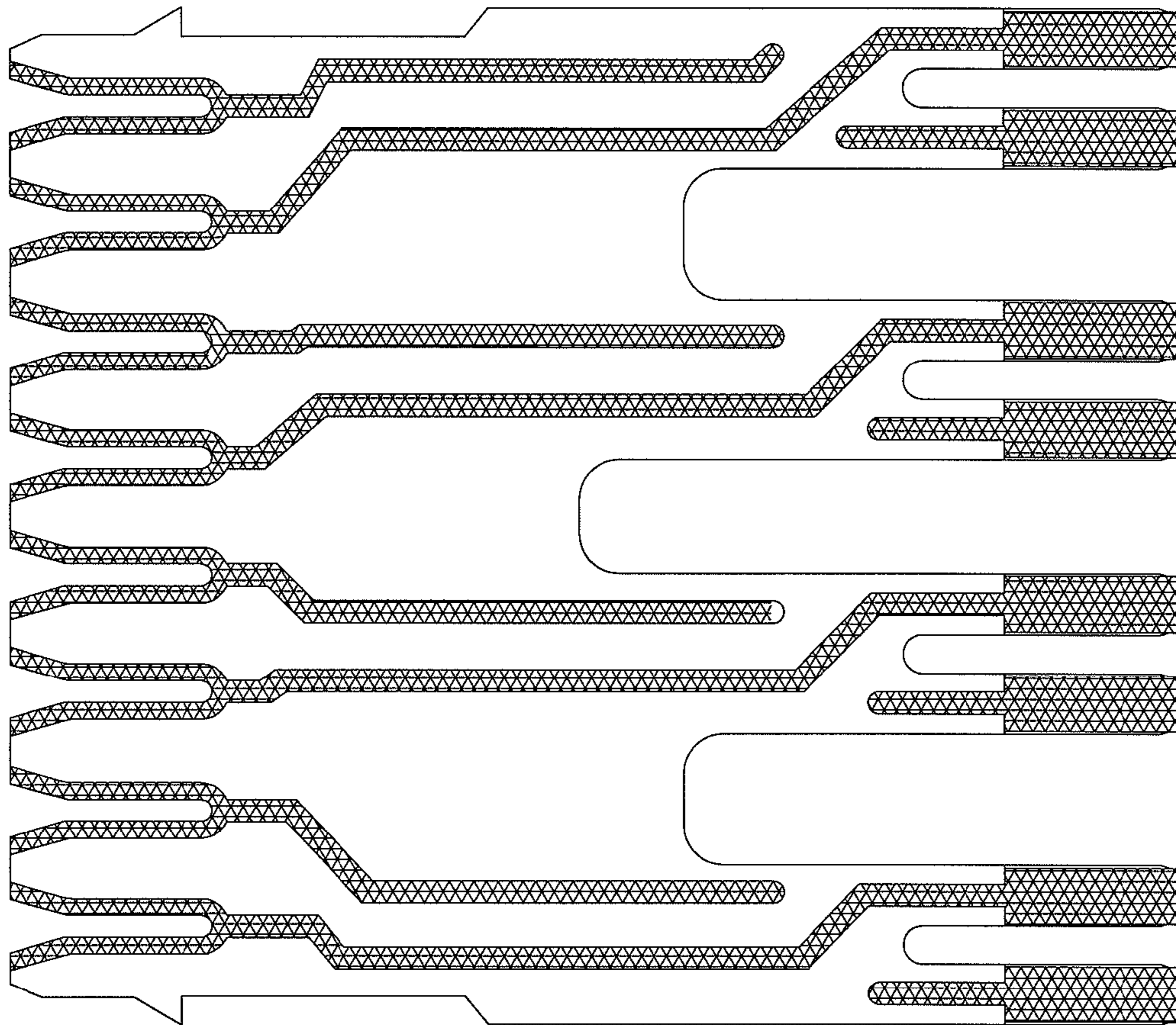


FIG. 1

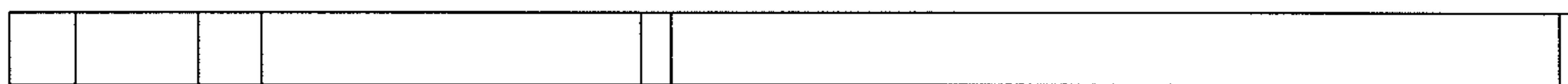


FIG. 2

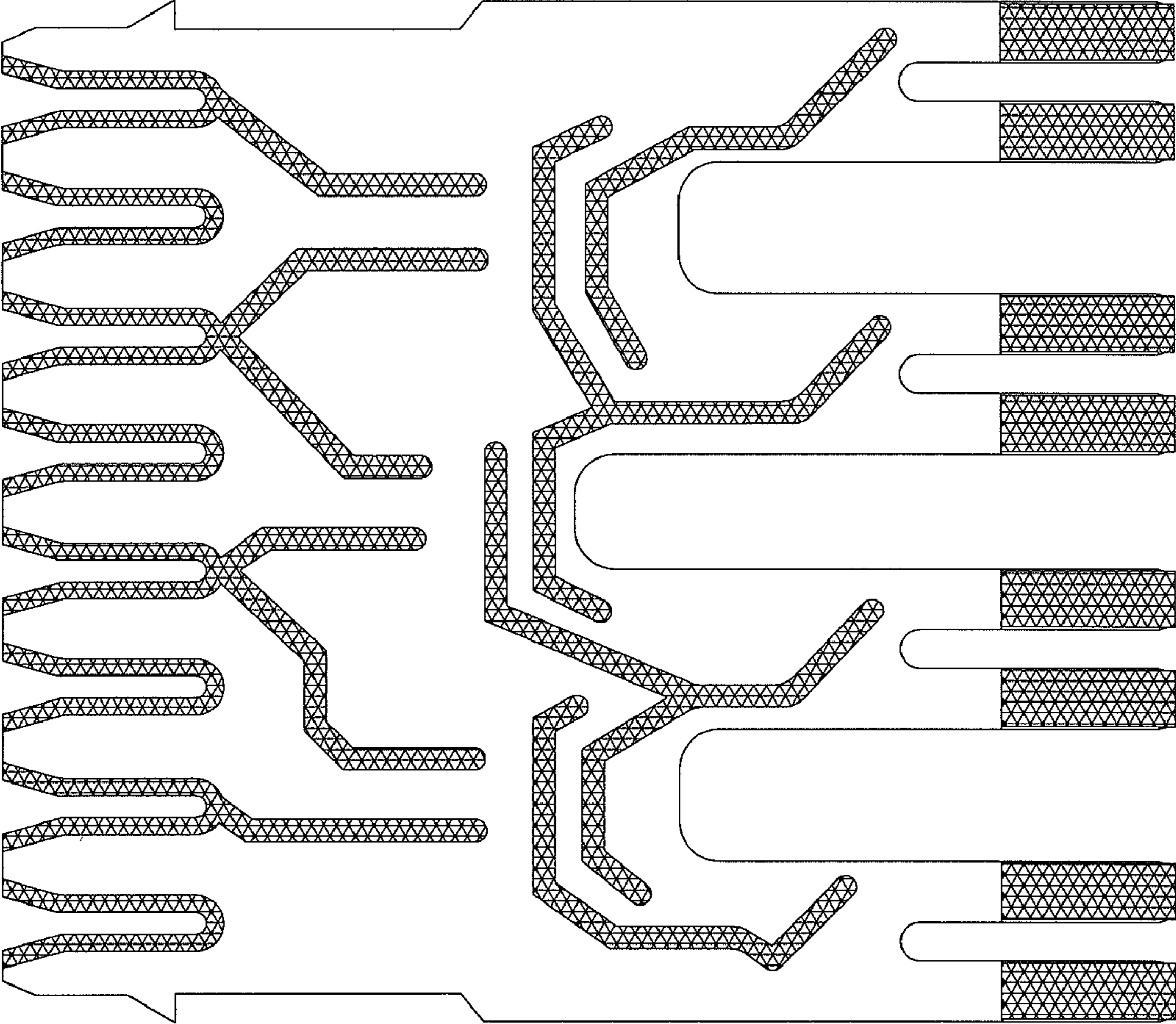


FIG. 3

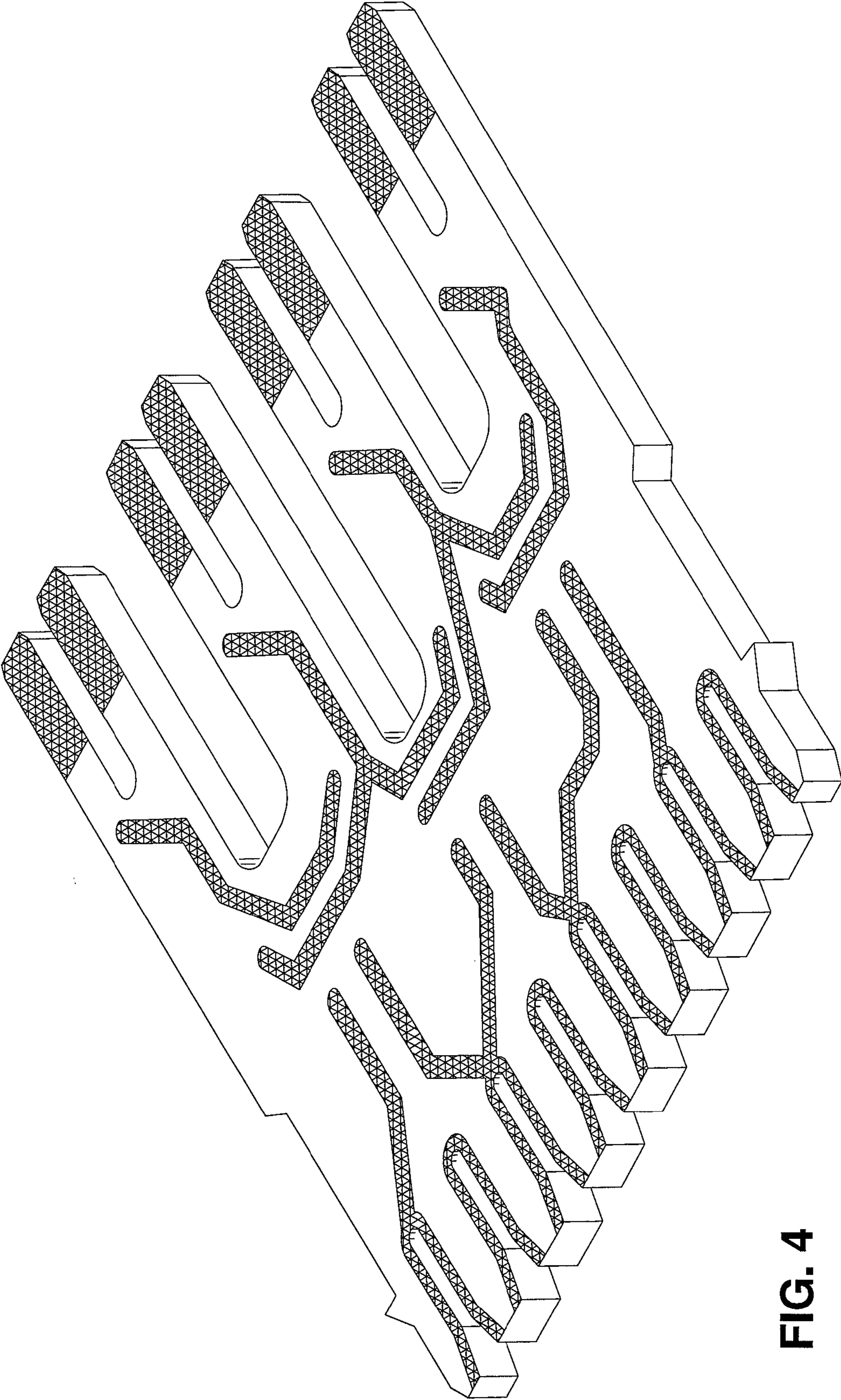


FIG. 4