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(12) **United States Design Patent**
Jones

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(45) **Date of Patent:** **** Dec. 1, 2009**

(54) **PASSIVE HEAT SINK STAND FOR ELECTRONIC DEVICES**

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(**) Term: **14 Years**

(21) Appl. No.: **29/312,378**

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(51) **LOC (9) Cl.** **13-03**

(52) **U.S. Cl.** **D13/179**

(58) **Field of Classification Search** D13/179;
165/80.3, 104.21, 104.26, 104.33, 151, 122,
165/185; 257/706, 707, 718-722; 361/687,
361/695, 697, 700, 702, 704, 709, 710, 711,
361/719; D14/439, 447

See application file for complete search history.

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(57) **CLAIM**

The ornamental design of a passive heat sink stand for electronic devices, as shown and herein described.

DESCRIPTION

FIG. 1 is a top plan view of a passive heat sink stand for electronic devices showing my new design;

FIG. 2 is a bottom elevational view thereof;

FIG. 3 is a front elevational view thereof;

FIG. 4 is a rear elevational view thereof;

FIG. 5 is a side elevational view thereof;

FIG. 6 is an opposing side elevational view thereof;

FIG. 7 is a partial perspective view from the top thereof;

FIG. 8 is a cut-away side view taken along line 8—8 of FIG. 7;

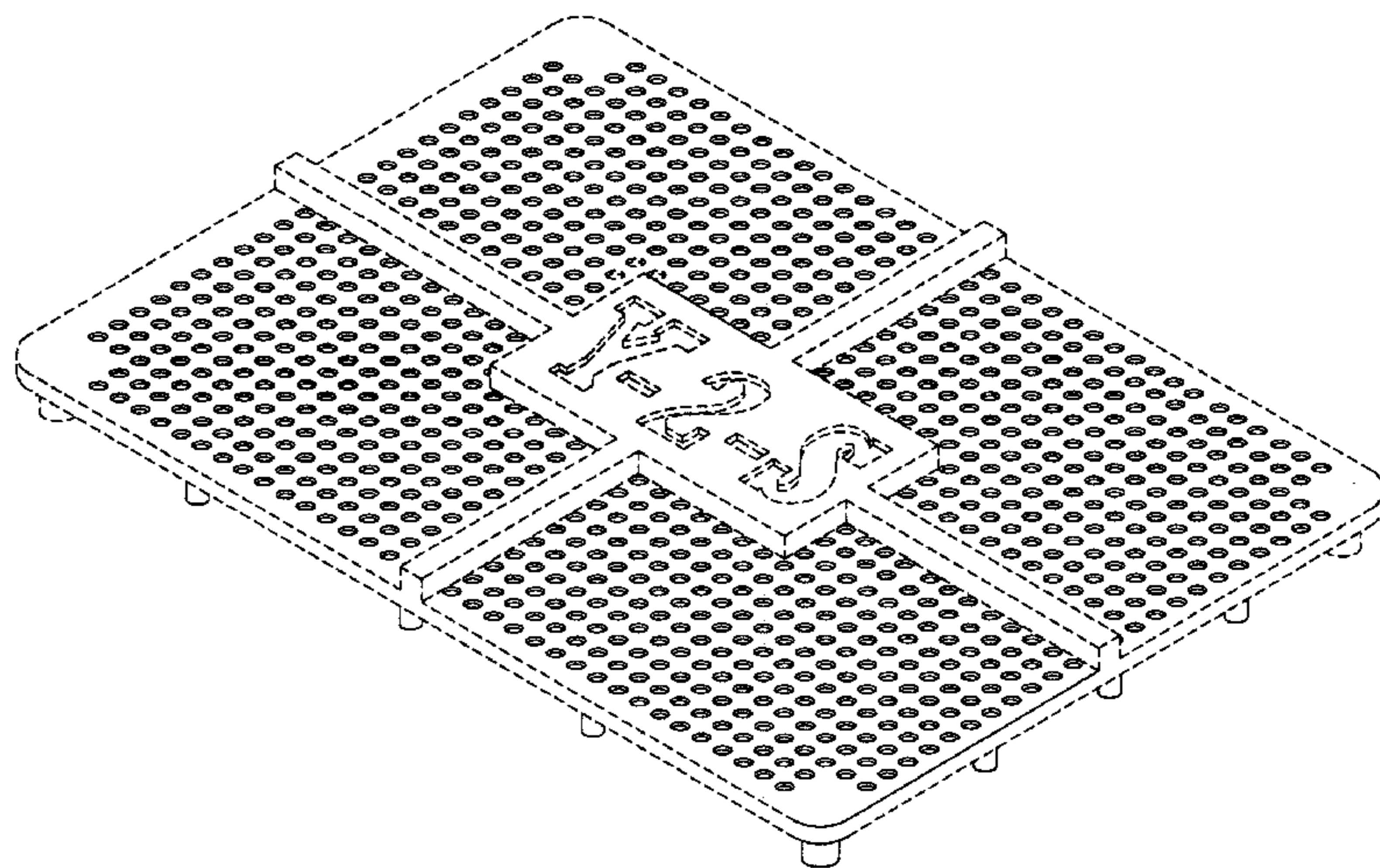
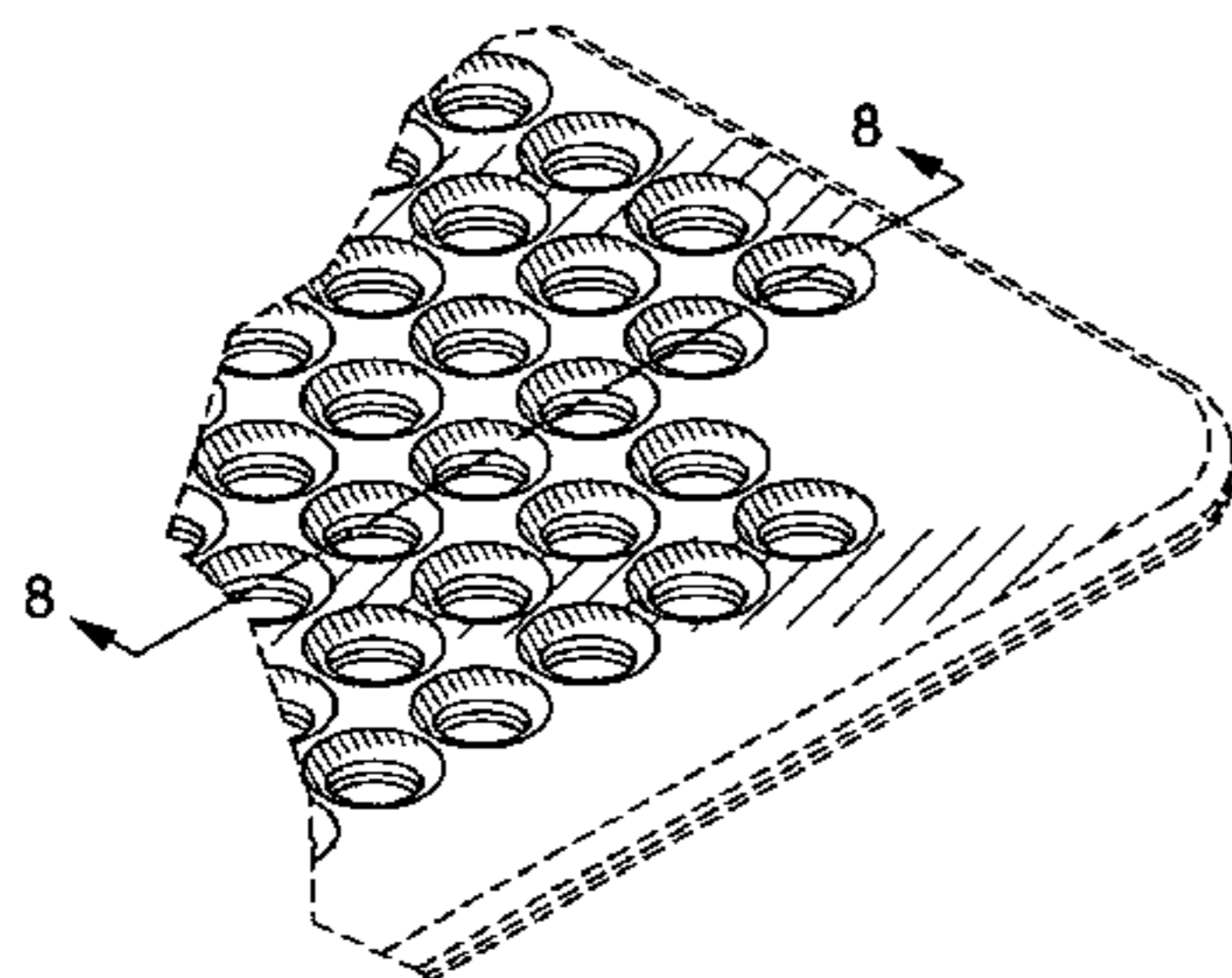
FIG. 9 is a partial perspective view from the bottom thereof;

FIG. 10 is a top perspective view thereof; and,

FIG. 11 is a top perspective view as illustrated in combination with a computer.

The broken lines shown in the drawings are for illustrative purposes only and do not form part of the claimed design.

1 Claim, 5 Drawing Sheets



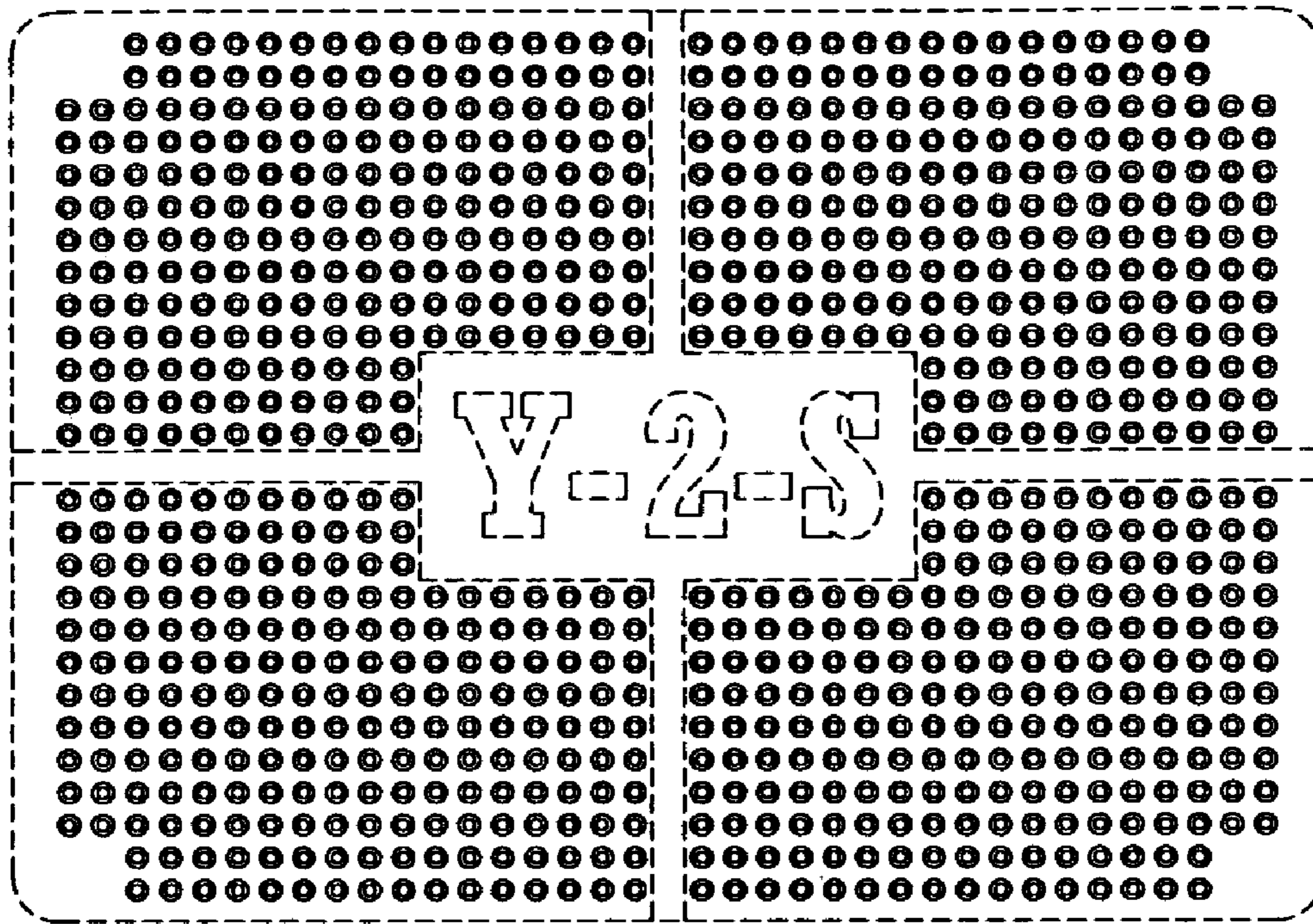


FIG. 1

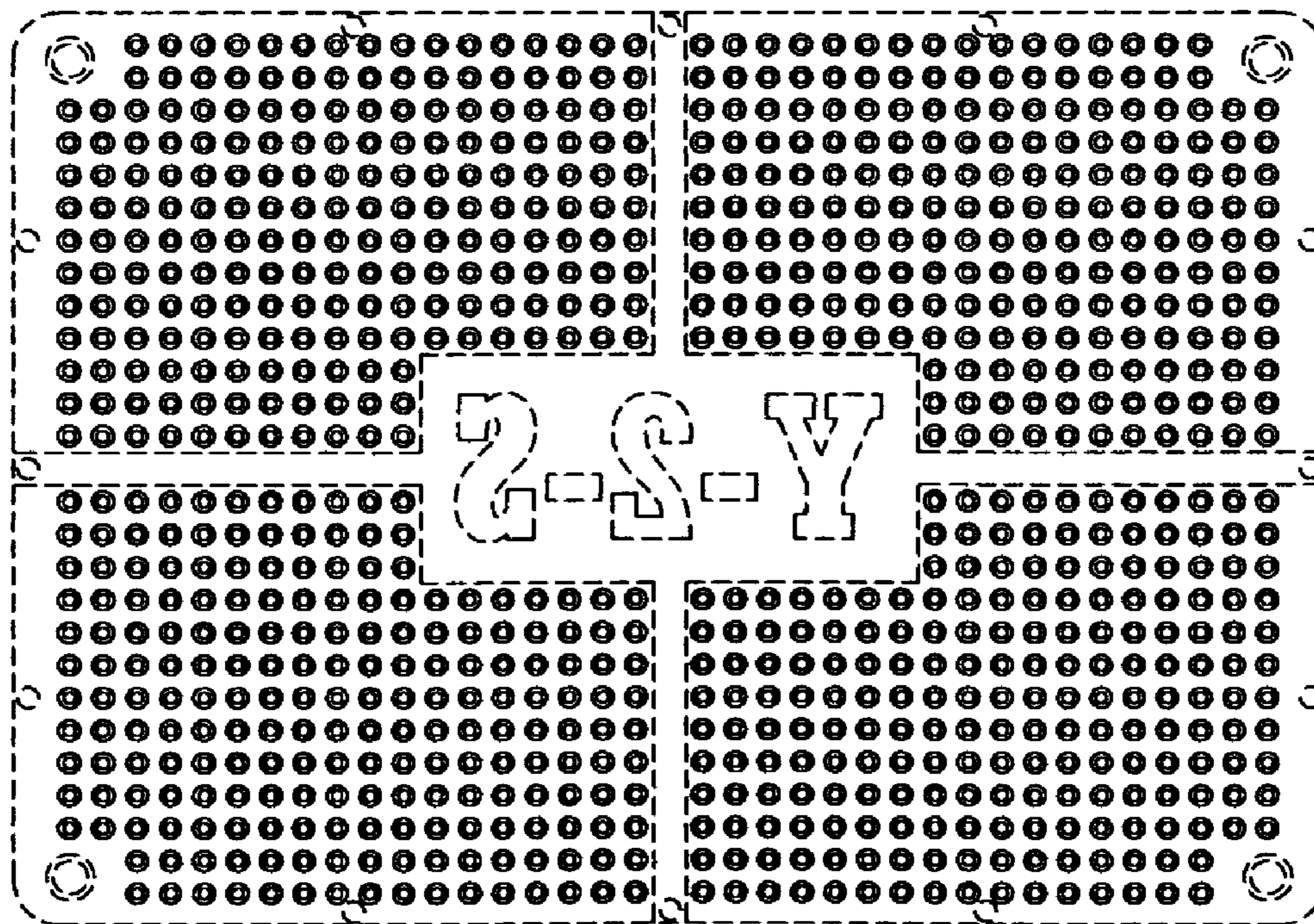


FIG. 2

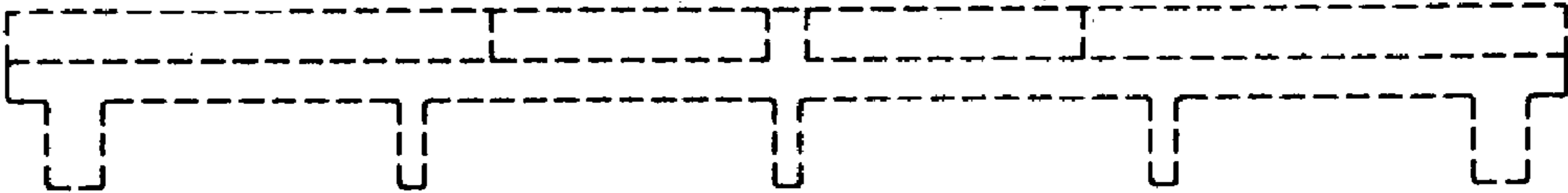


FIG. 3

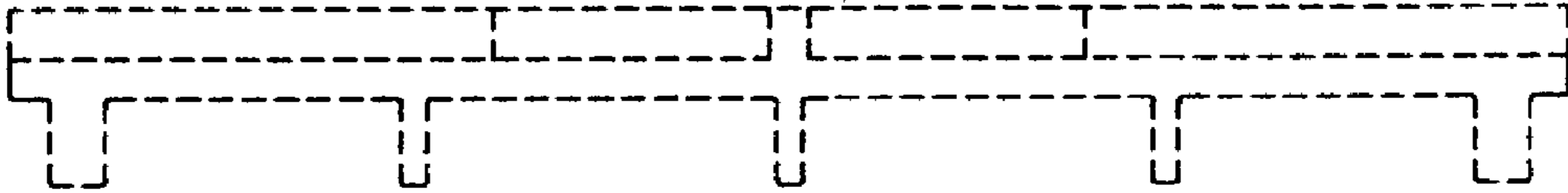


FIG. 4

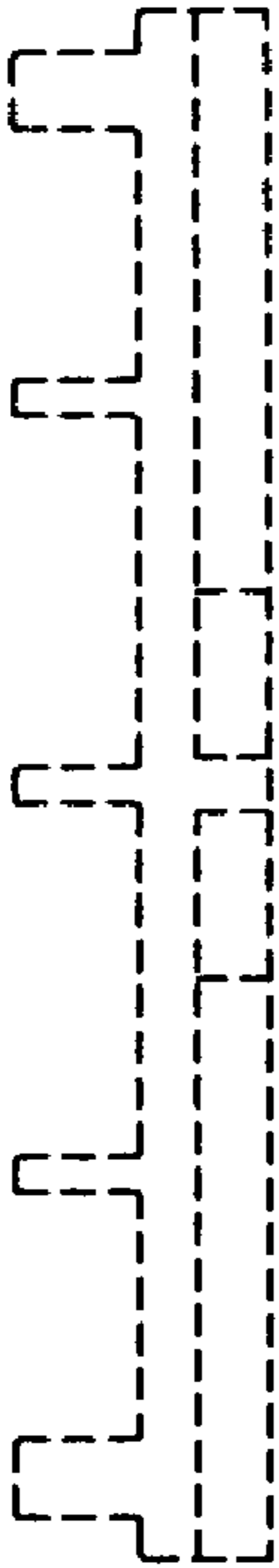


FIG. 5

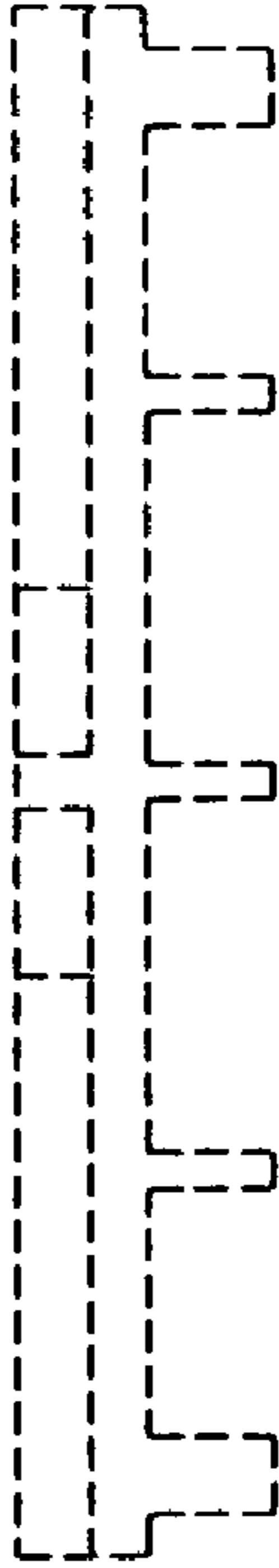


FIG. 6

FIG. 7

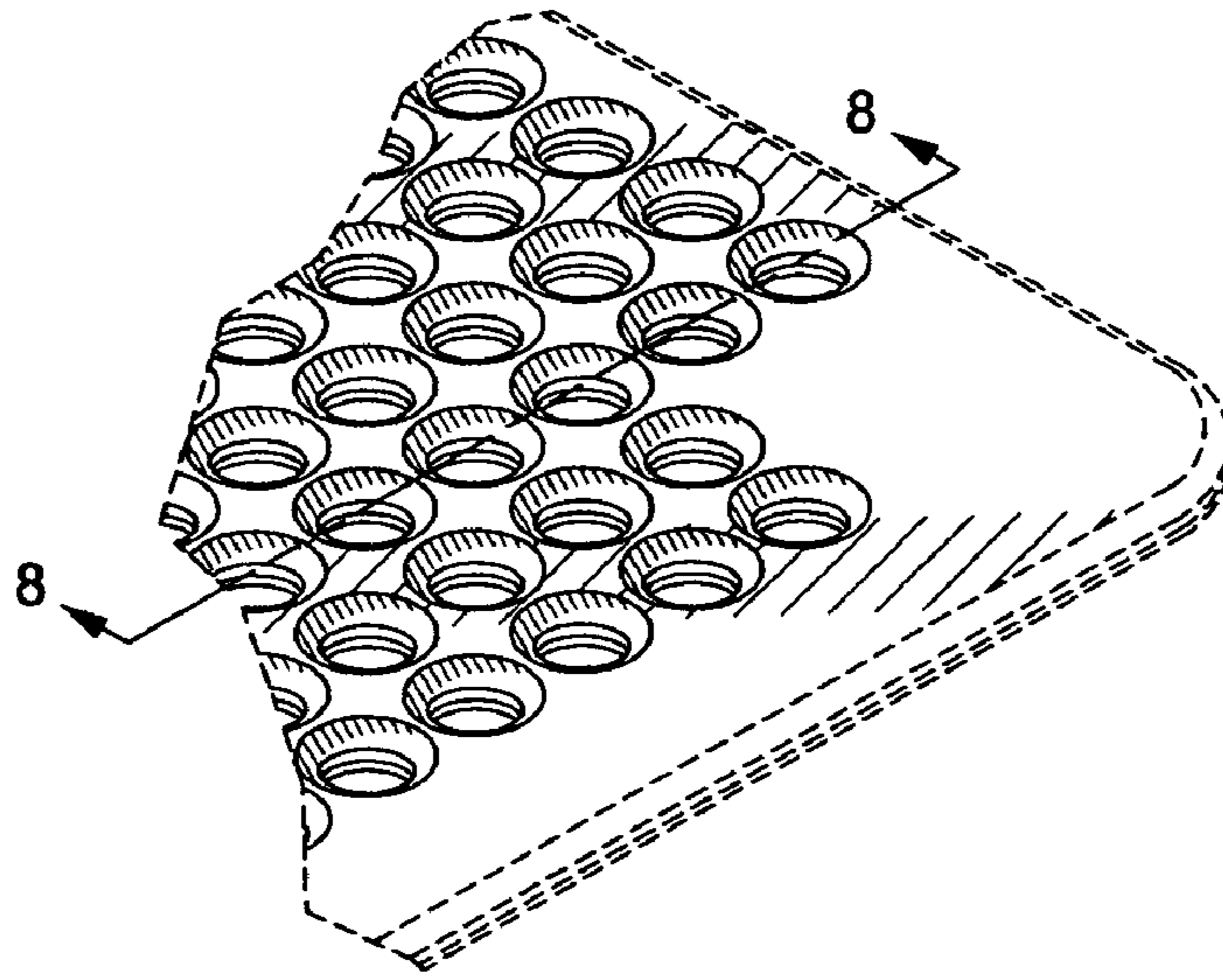
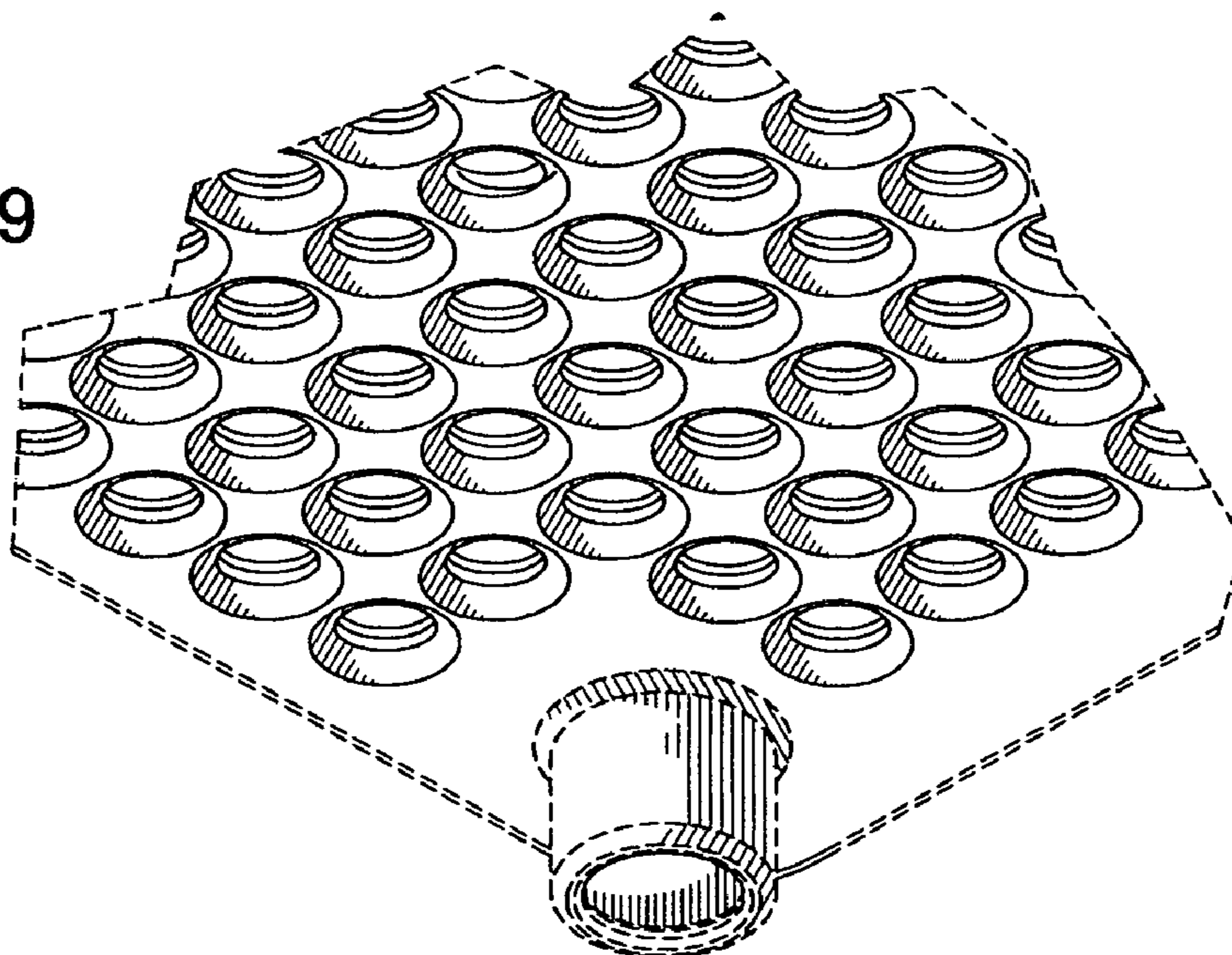


FIG. 8

FIG. 9



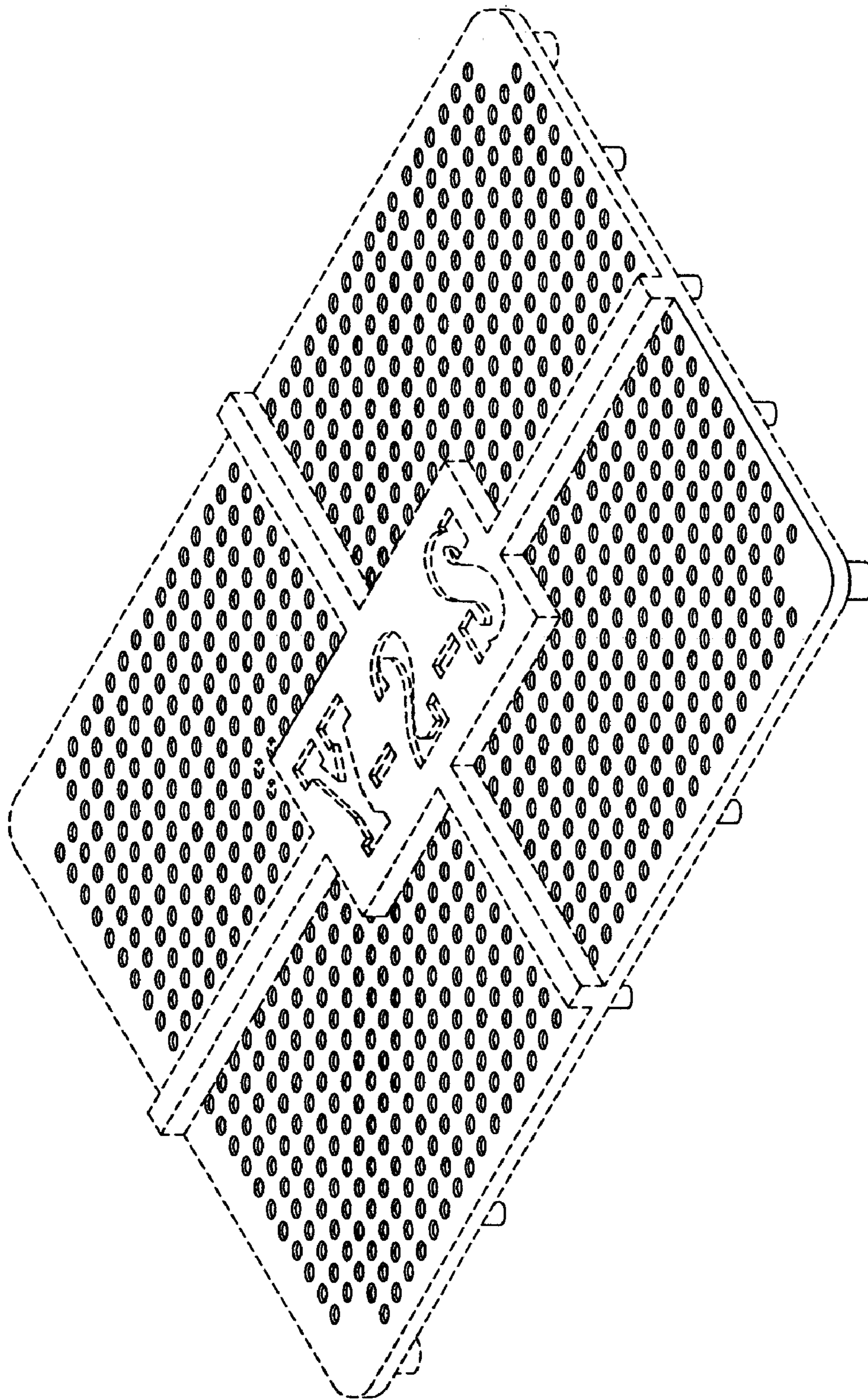


FIG. 10

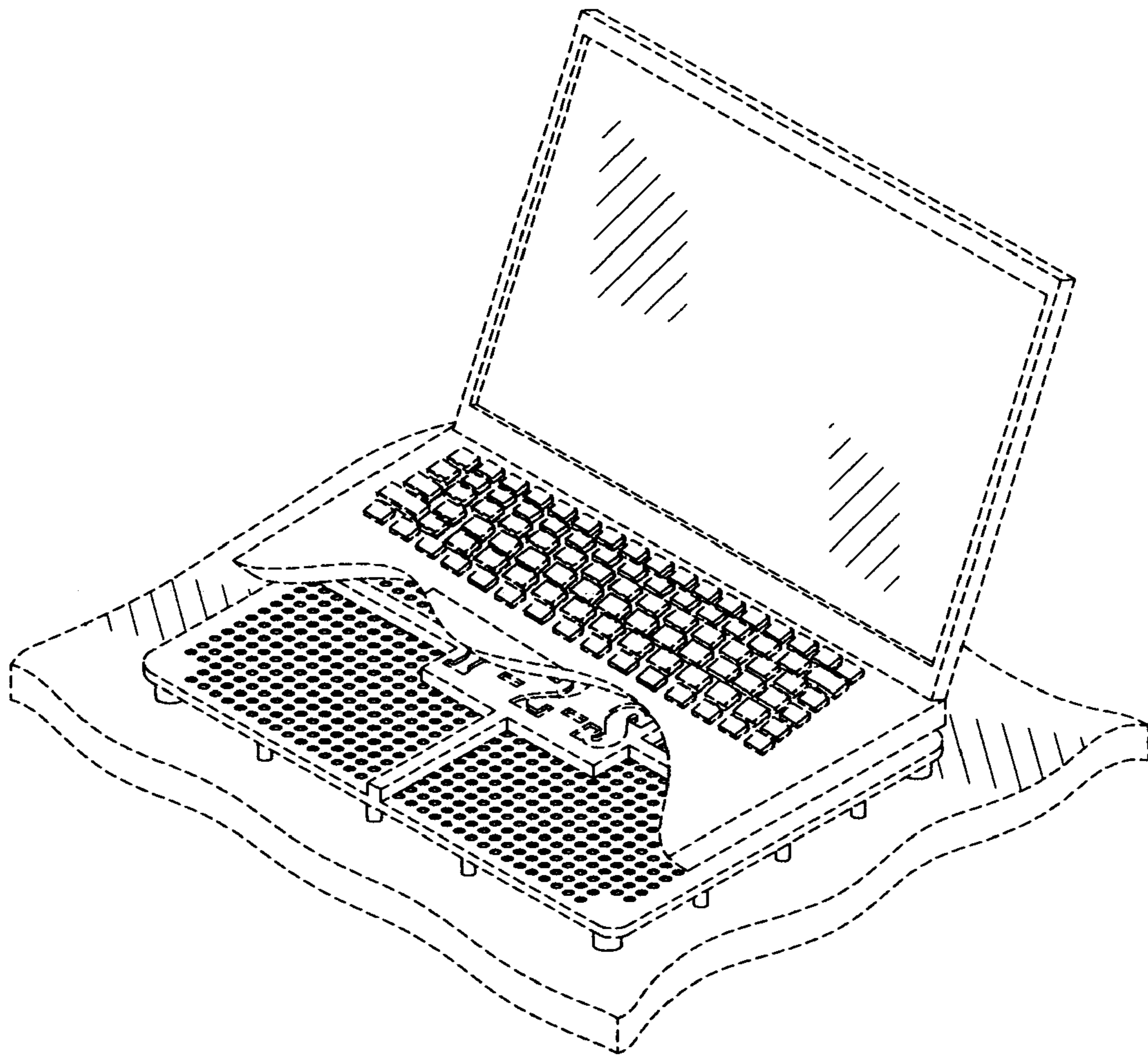


FIG. 11