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(12) **United States Design Patent**
Edmond et al.

(10) **Patent No.:** **US D602,450 S**
(45) **Date of Patent:** **** Oct. 20, 2009**

(54) **LED CHIP**

TW D109667 3/1993
TW D112798 8/1994

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(74) *Attorney, Agent, or Firm*—Myers Bigel Sibley & Sajovec

(73) Assignee: **Cree, Inc.**, Durham, NC (US)

(57) **CLAIM**

(**) Term: **14 Years**

The ornamental design for an LED chip, as shown and described.

(21) Appl. No.: **29/327,786**

(22) Filed: **Nov. 13, 2008**

DESCRIPTION

Related U.S. Application Data

(62) Division of application No. 29/284,431, filed on Sep. 7, 2007, now Pat. No. Des. 582,866.

(51) **LOC (9) Cl.** **13-03**

(52) **U.S. Cl.** **D13/180**

(58) **Field of Classification Search** D13/180;
D26/2; 257/79, 80, 81, 88, 89, 95, 98, 99,
257/100; 313/483, 498, 500; 362/555, 800
See application file for complete search history.

FIG. 1 is a top perspective view of a first embodiment of the LED chip showing our design;
FIG. 2 is a top view thereof;
FIG. 3 is a first side view thereof;
FIG. 4 is a bottom view thereof;
FIG. 5 is a different side view thereof;
FIG. 6 is an end view thereof; and
FIG. 7 is an opposing end view thereof.
FIG. 8 is a top perspective view of a second embodiment of the LED chip showing our design;
FIG. 9 is a top view thereof;
FIG. 10 is a first side view thereof;
FIG. 11 is a bottom view thereof;
FIG. 12 is a different side view thereof;
FIG. 13 is an end view thereof; and,
FIG. 14 is an opposing end view thereof.

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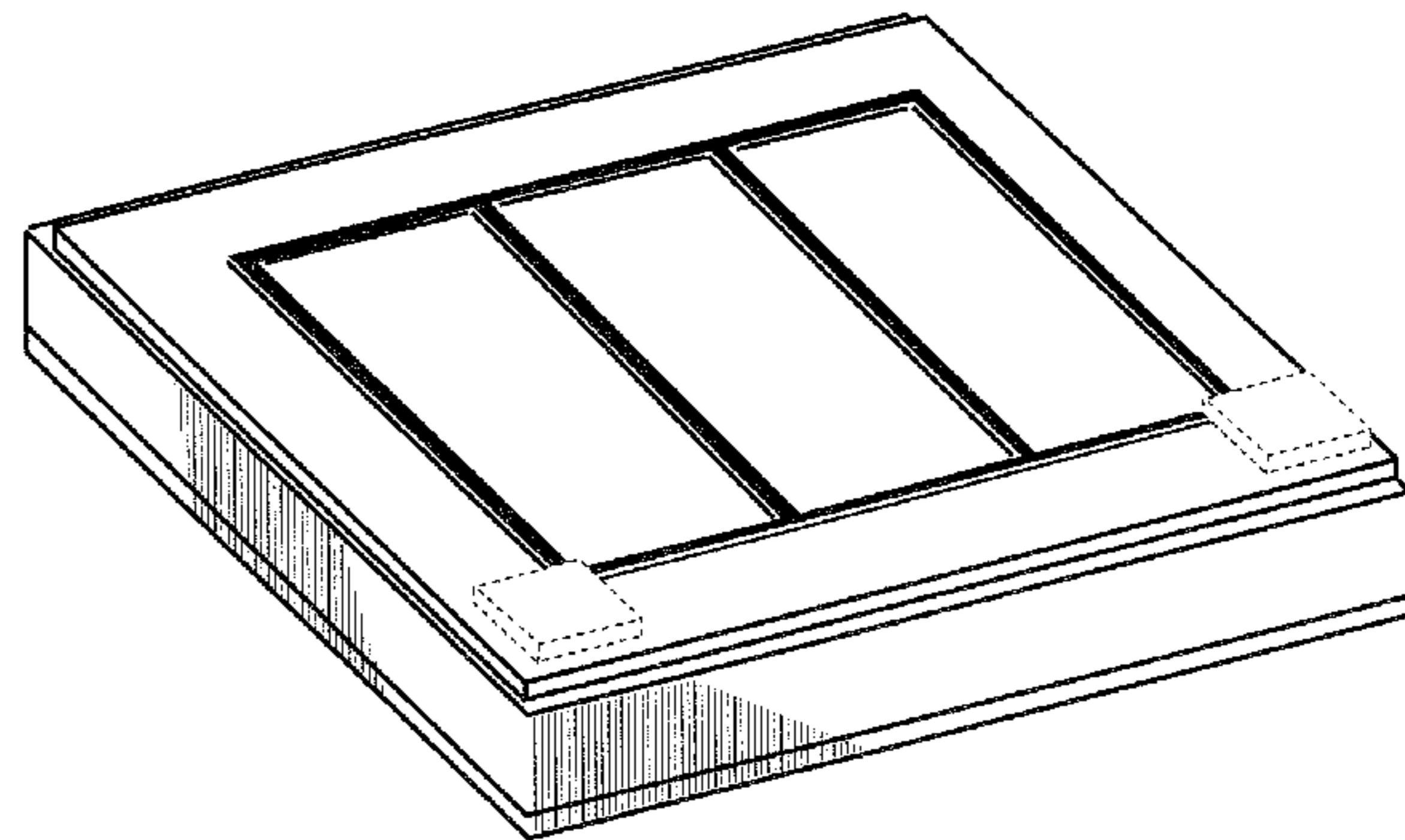
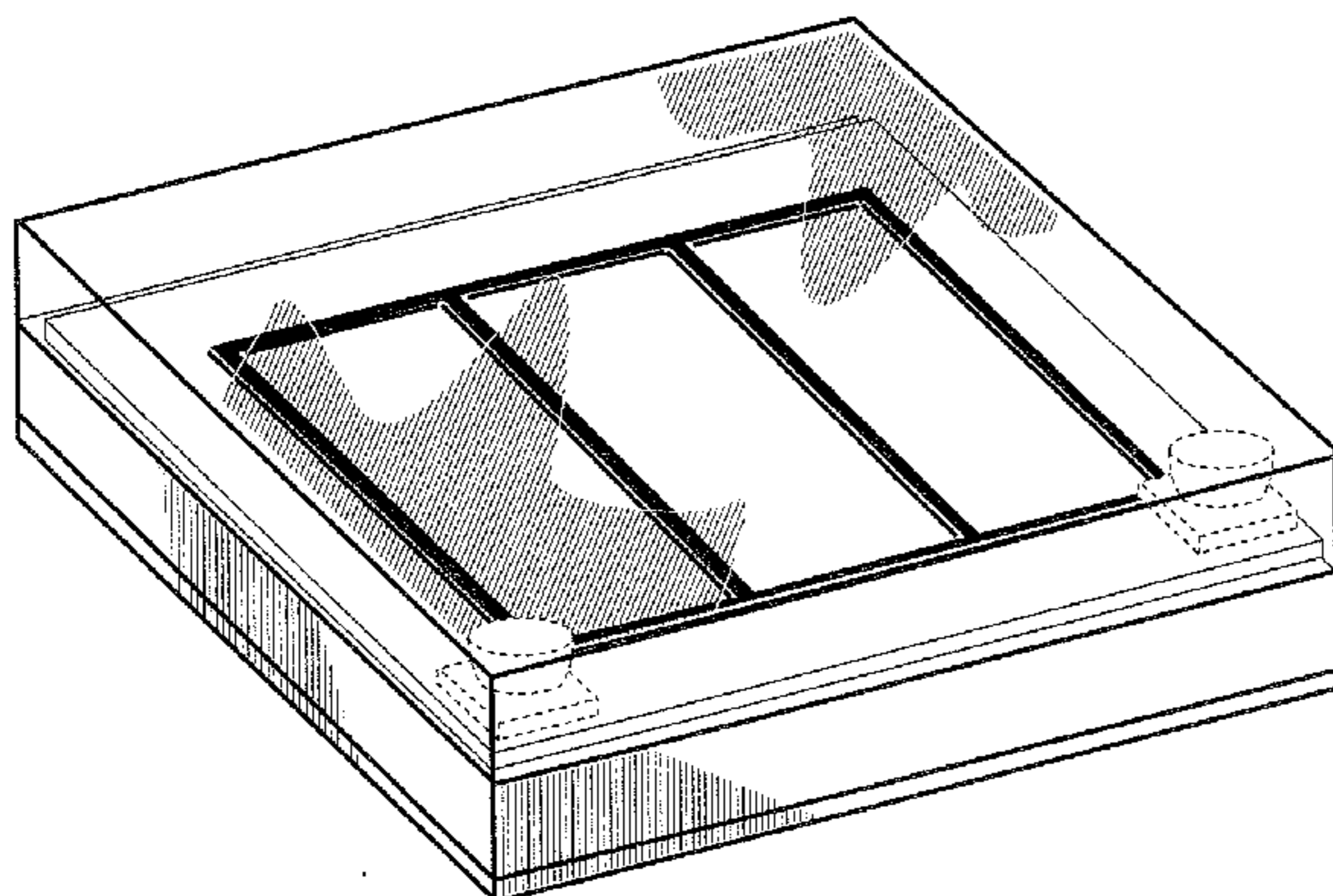
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The broken lines showing portions of the LED chip is for illustrative purposes only and form no part of the claimed design.

1 Claim, 4 Drawing Sheets



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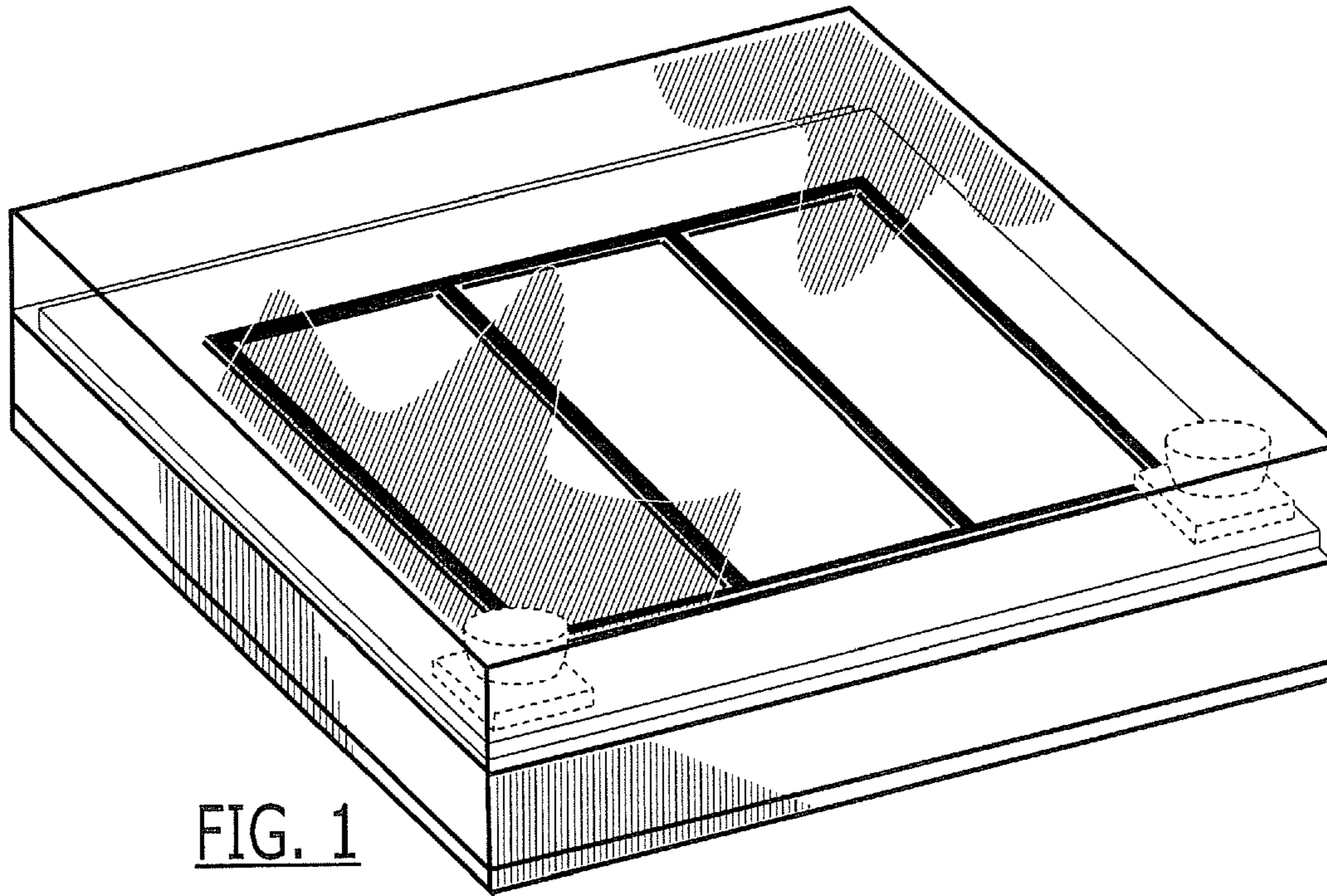


FIG. 1

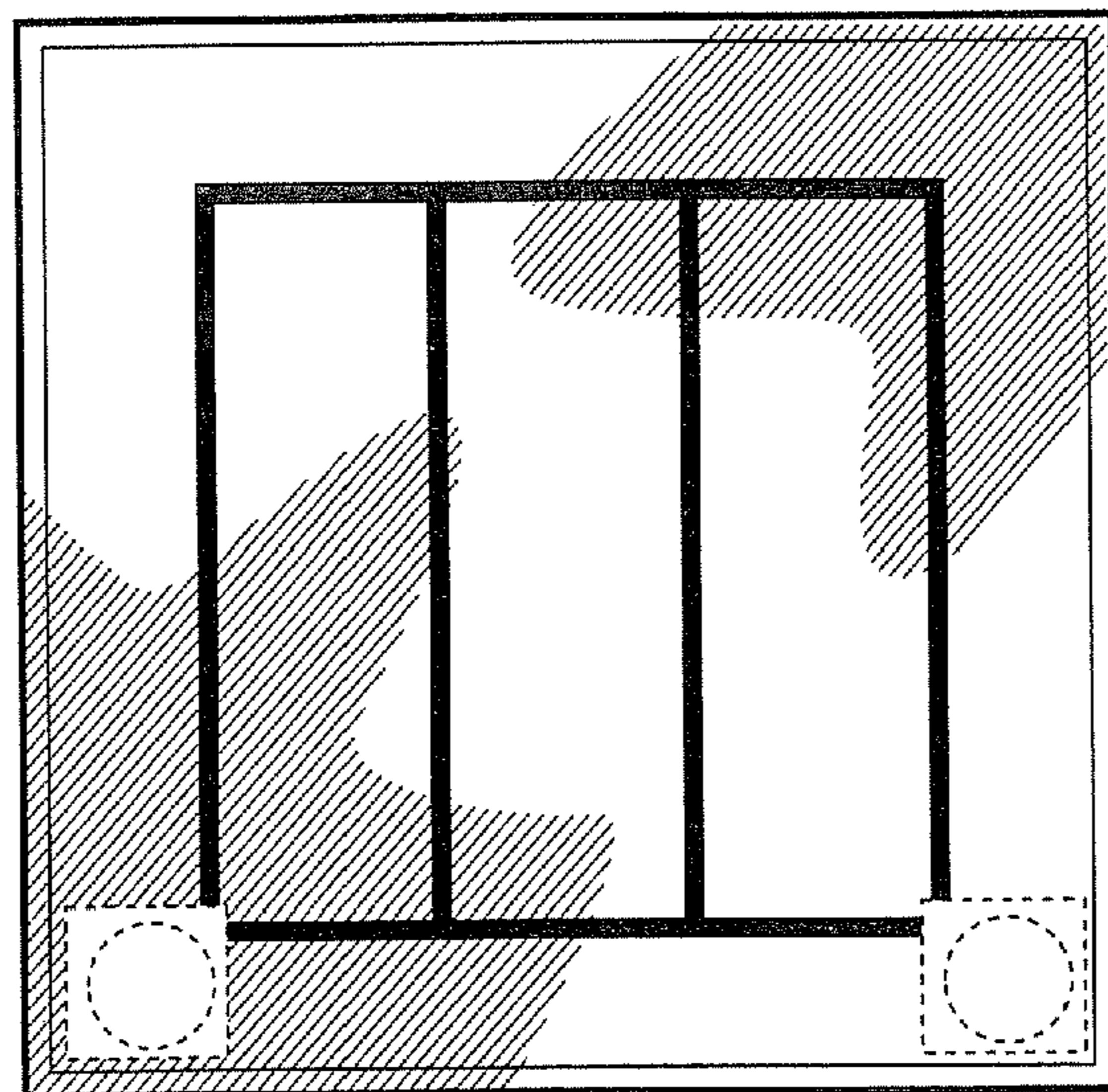


FIG. 2

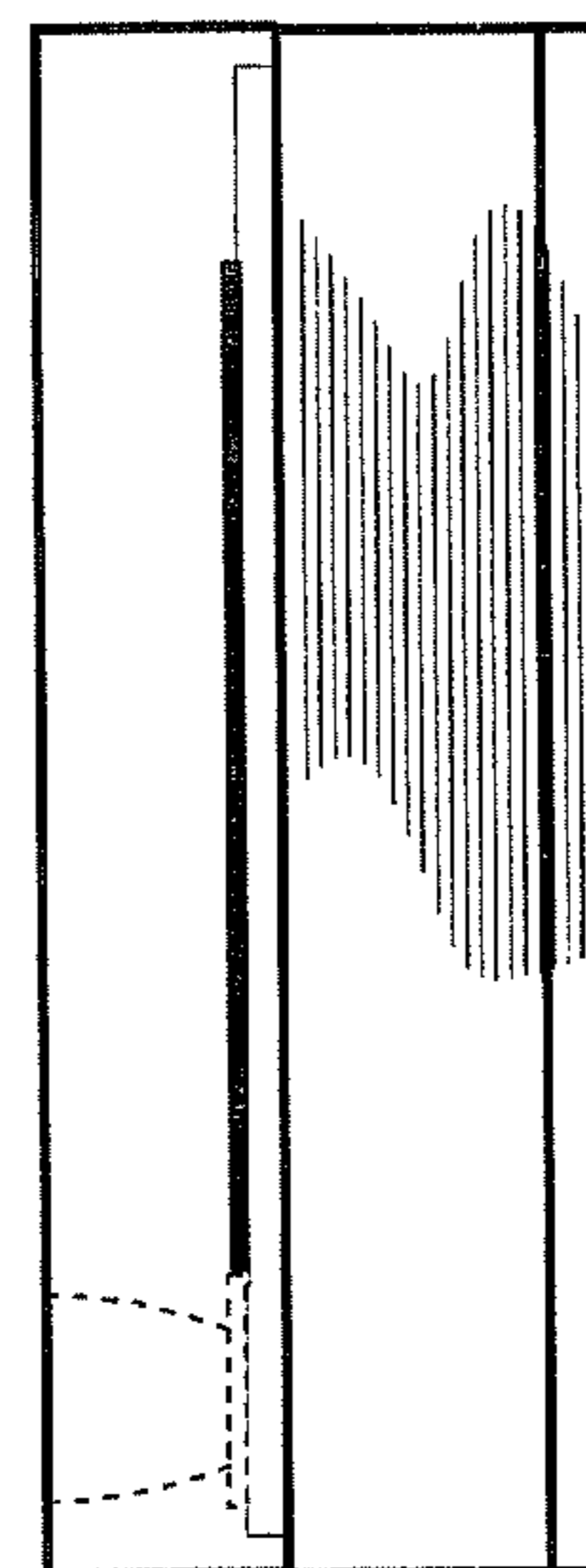


FIG. 3

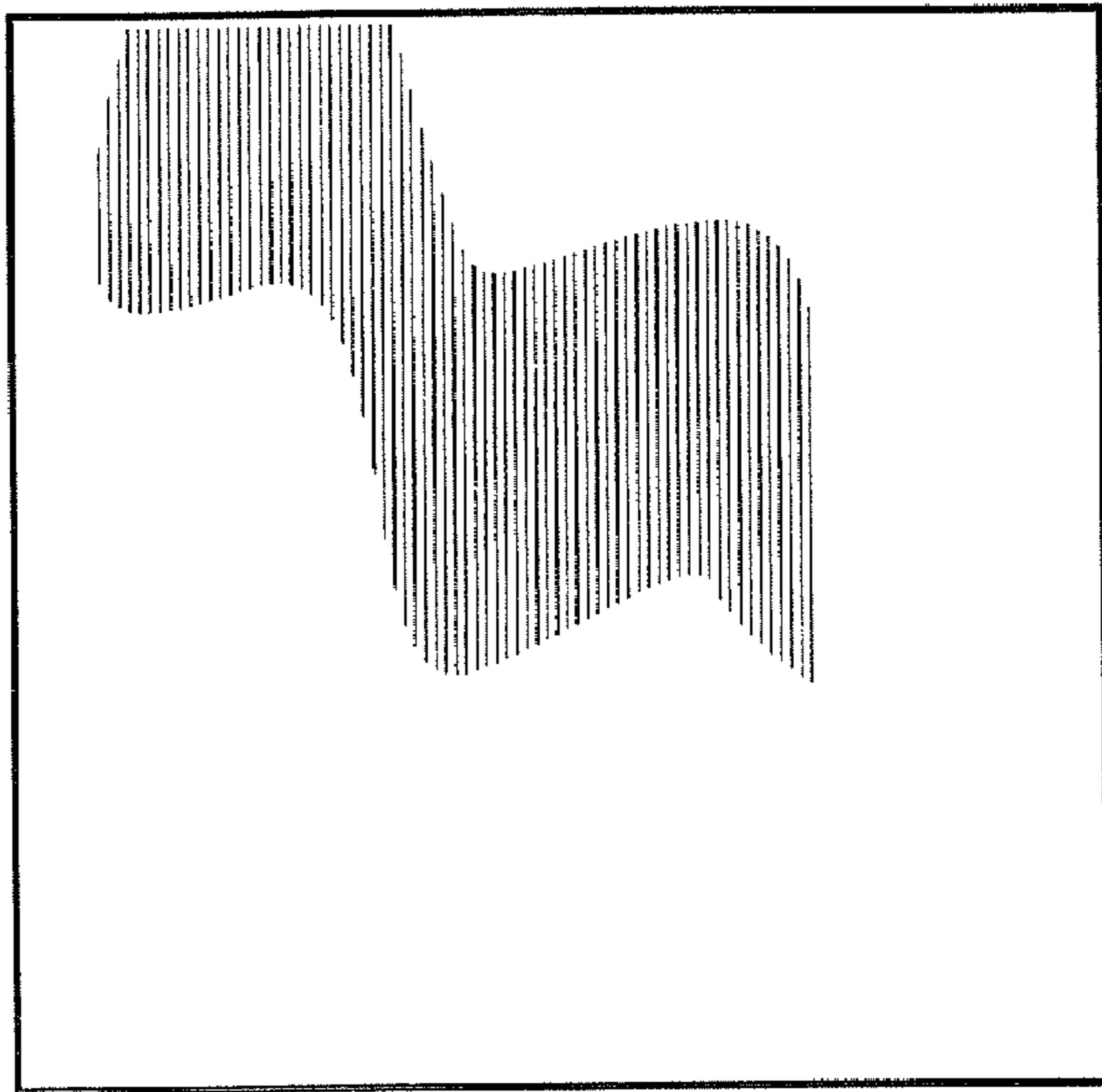


FIG. 4

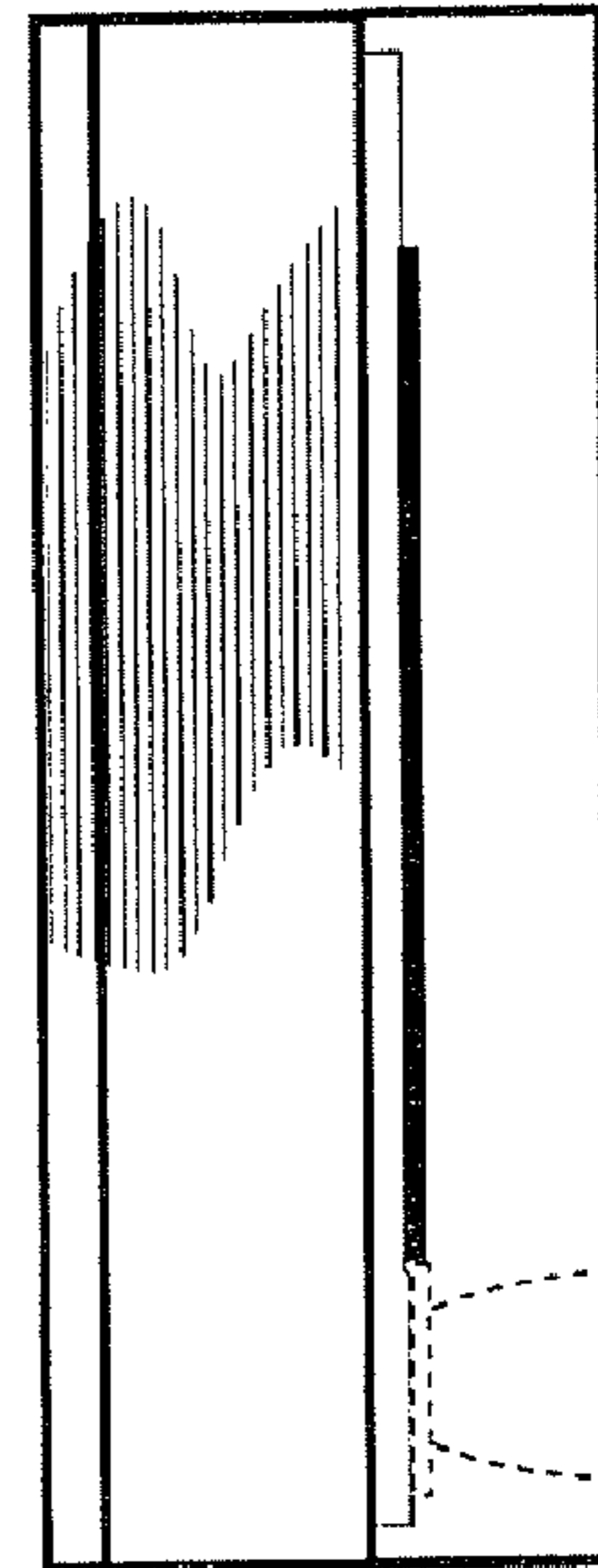


FIG. 5

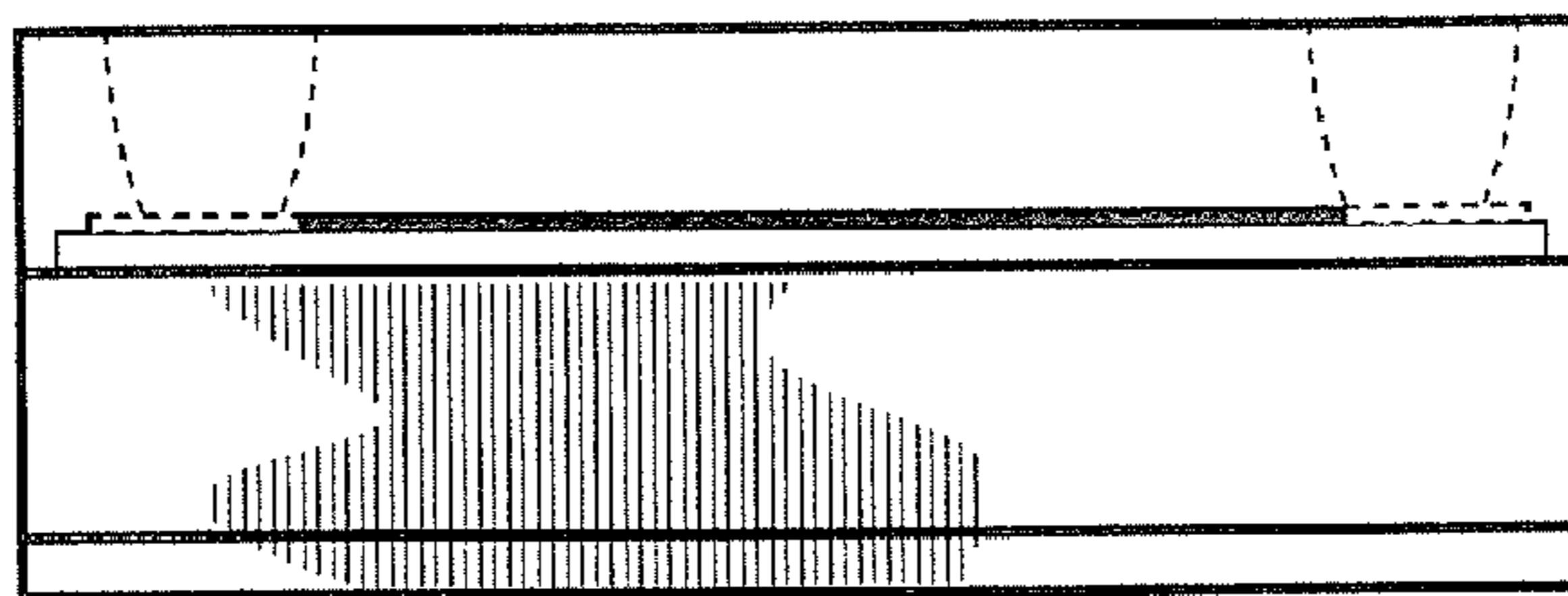


FIG. 6

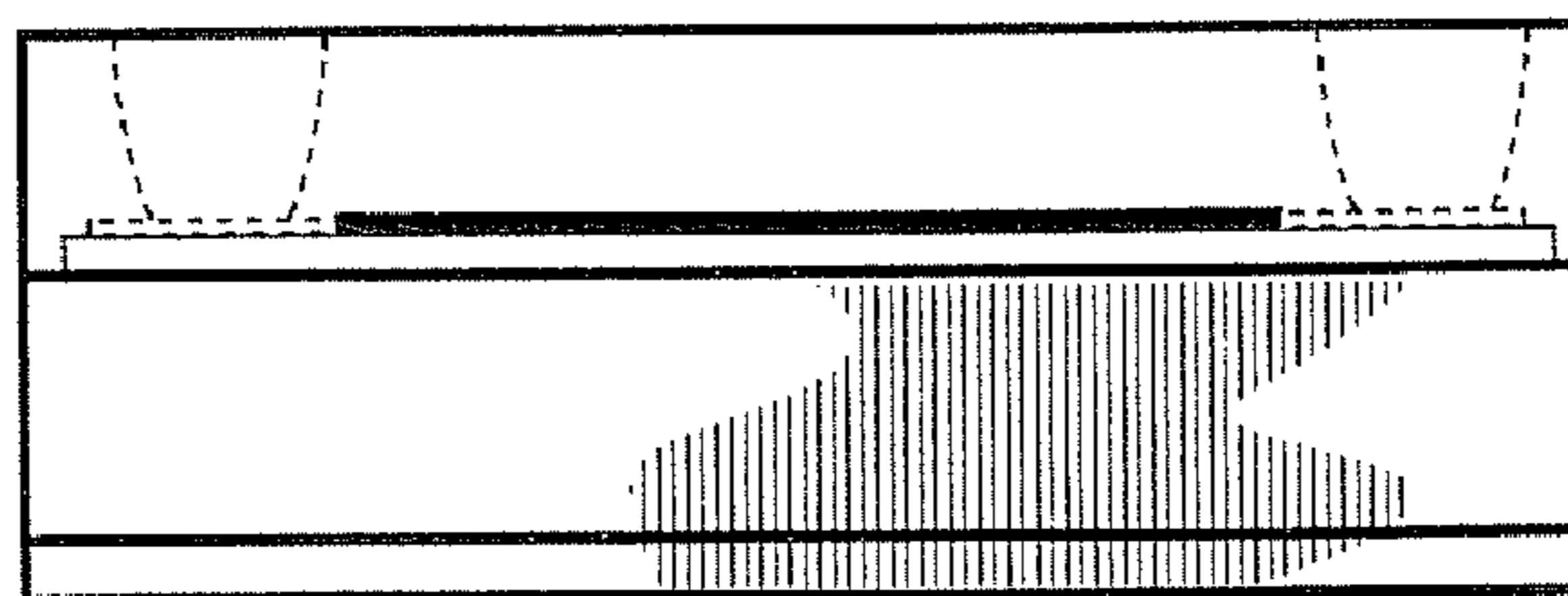


FIG. 7

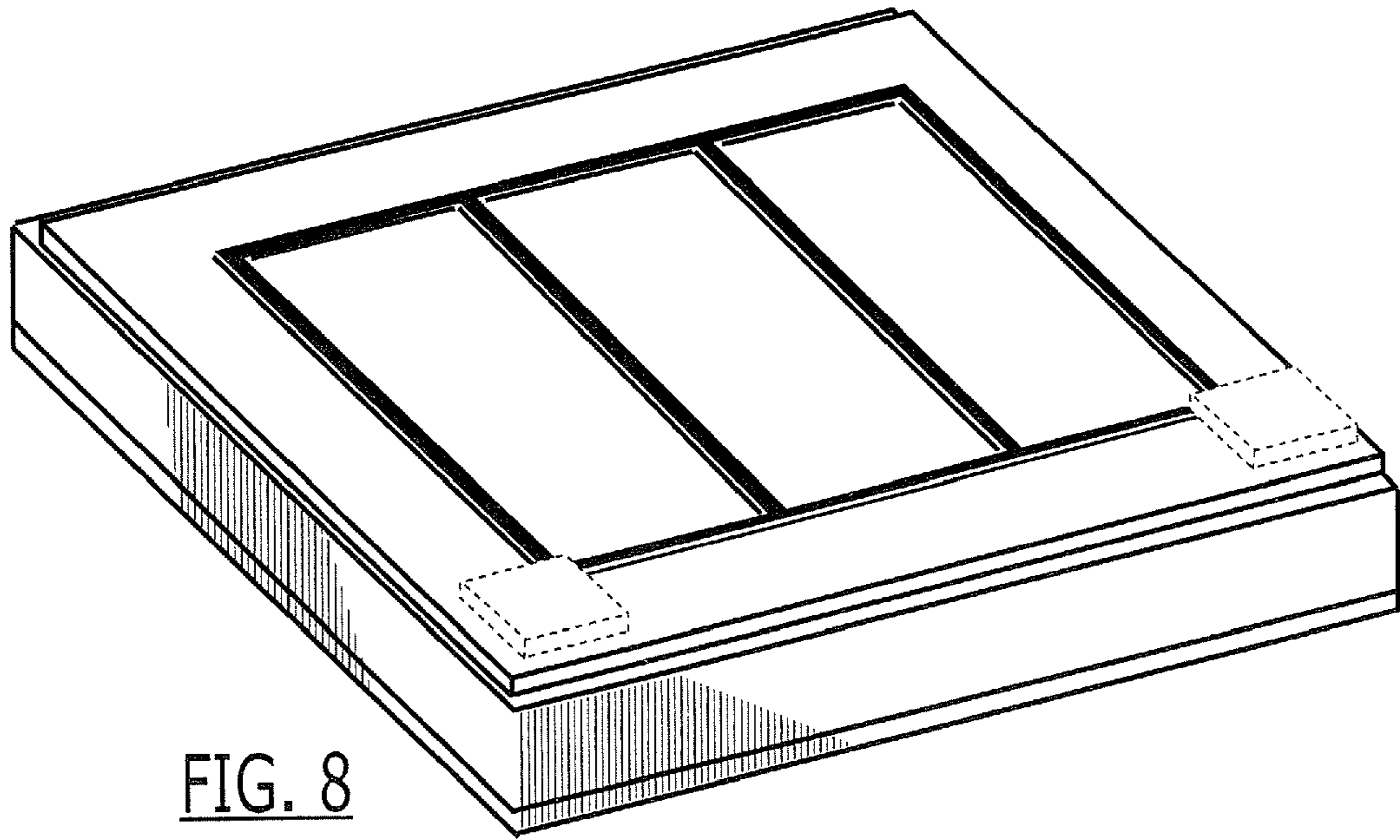


FIG. 8

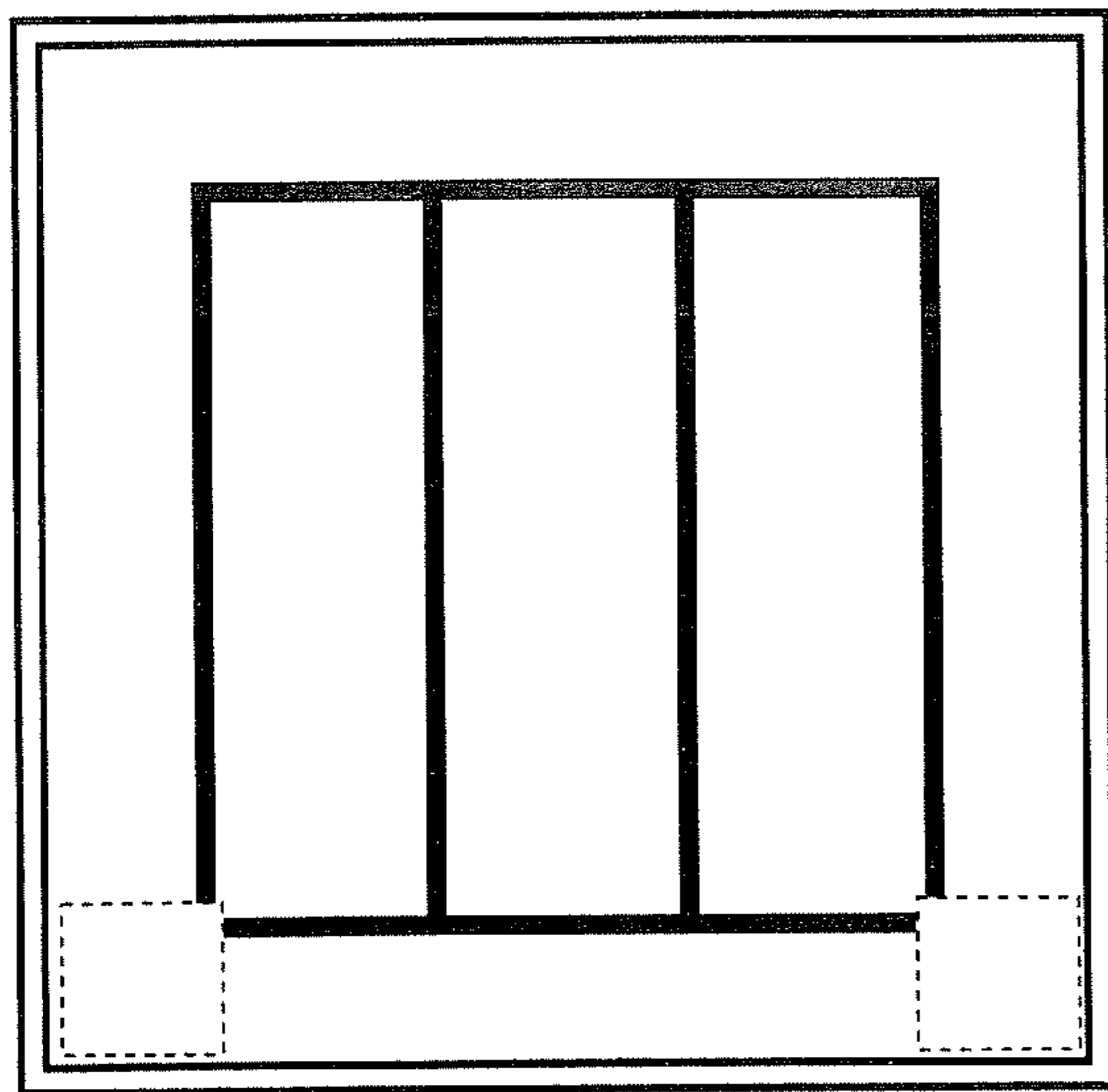


FIG. 9

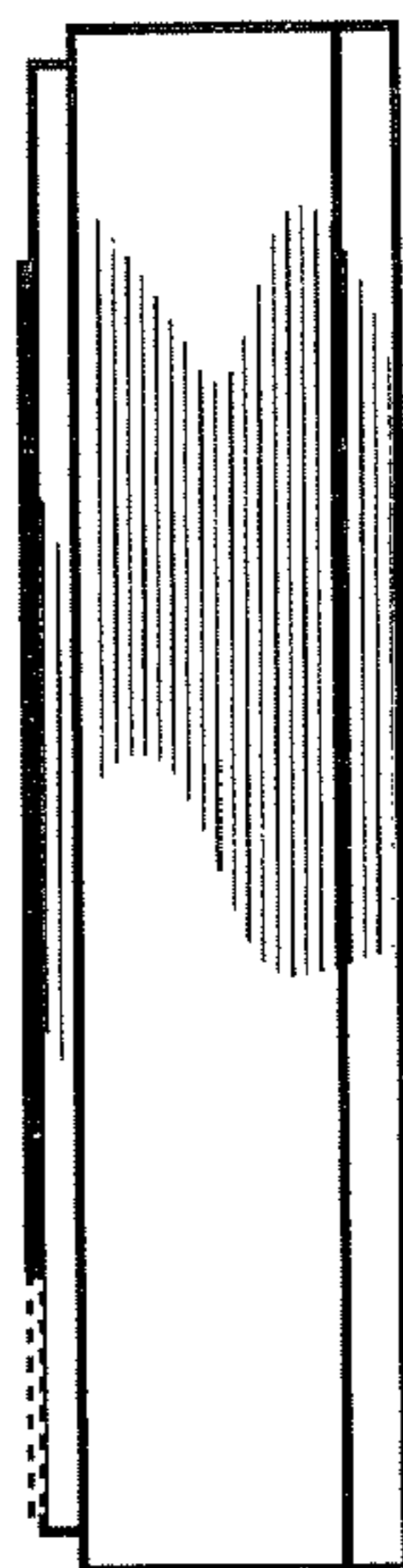


FIG. 10

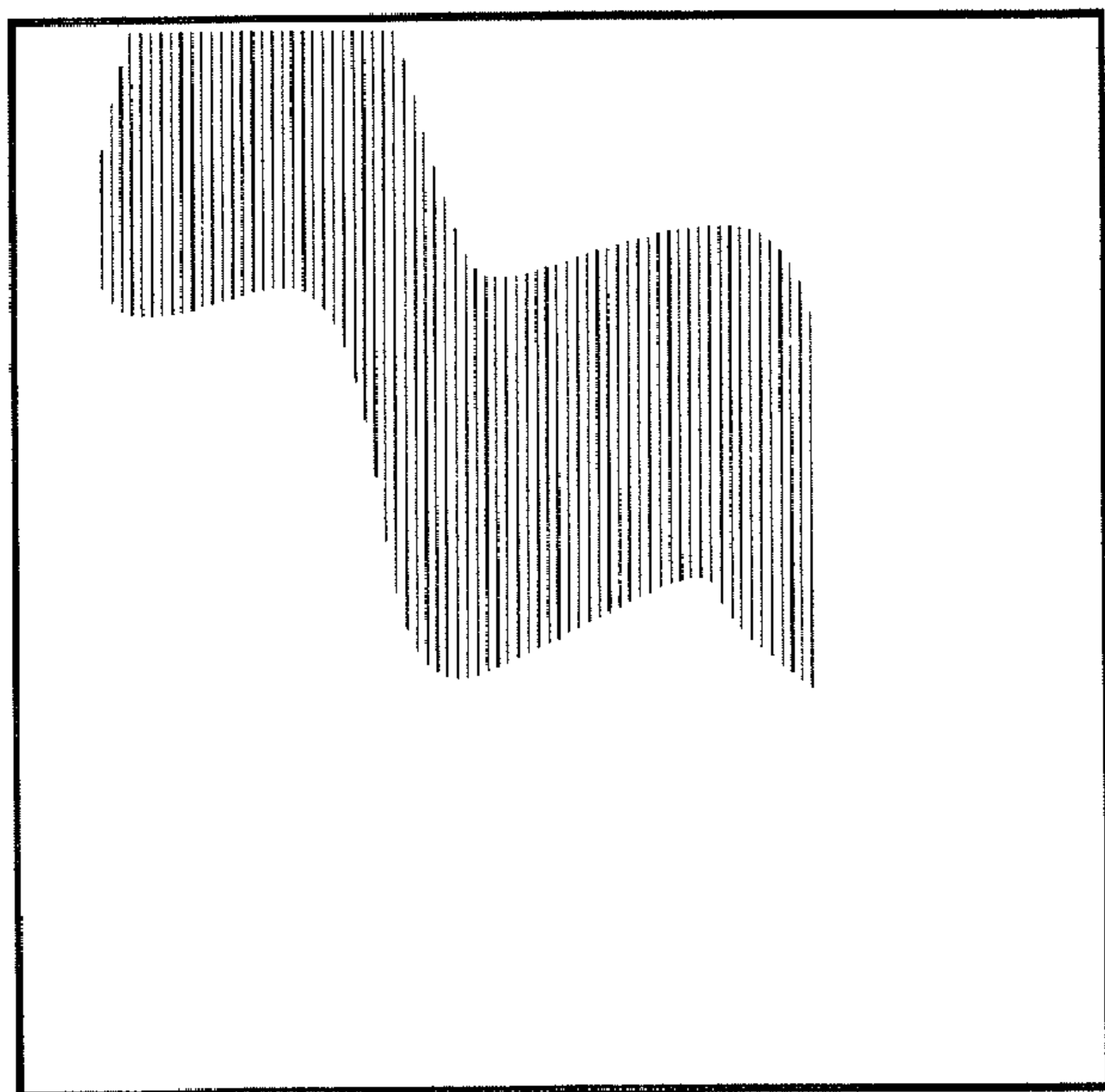


FIG. 11

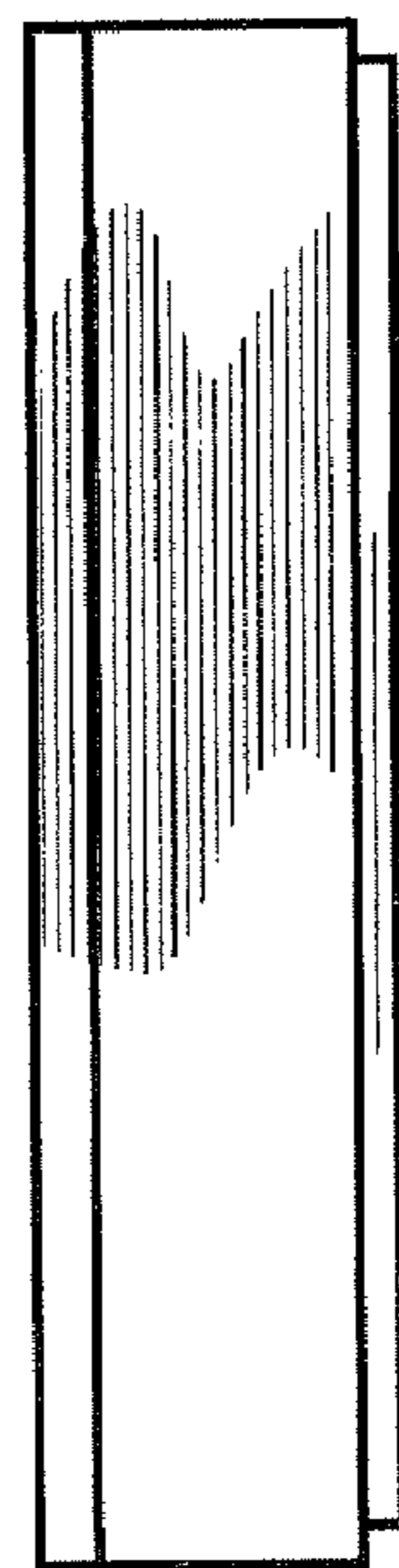


FIG. 12

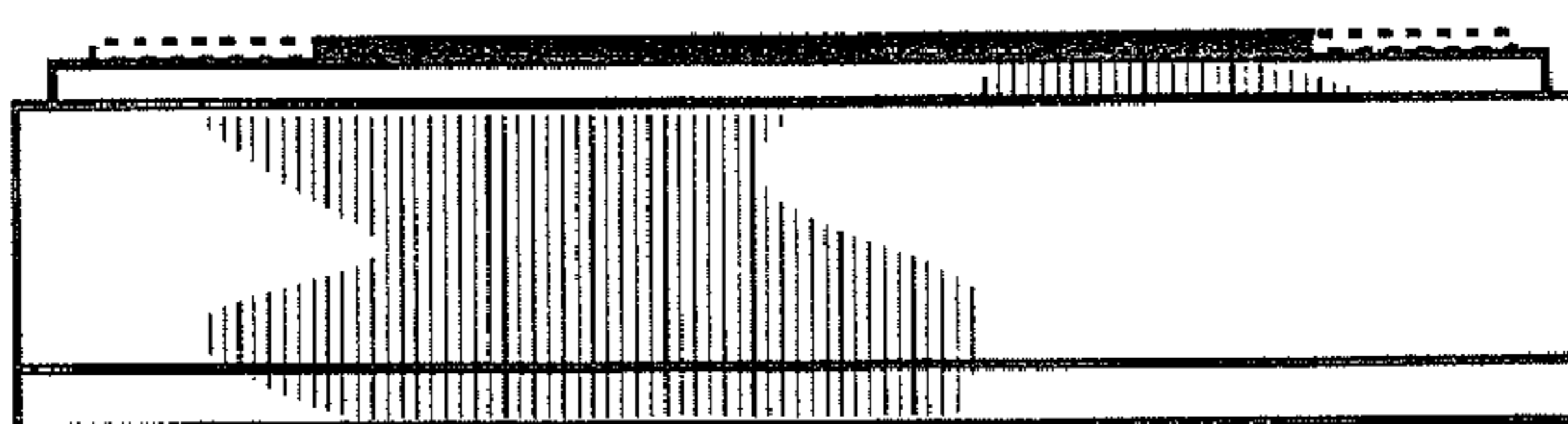


FIG. 13

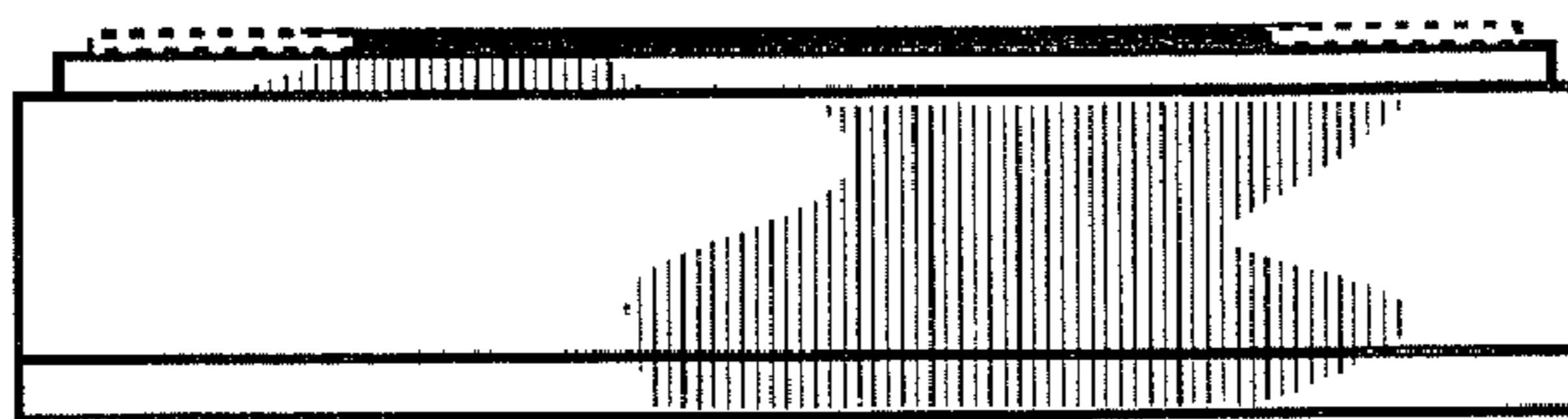


FIG. 14