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(12) **United States Design Patent**  
**Moussa**

(10) **Patent No.:** **US D602,432 S**  
(45) **Date of Patent:** **\*\* Oct. 20, 2009**

(54) **REVERSE CURRENT BLOCKING MODULE FOR USE IN A SOLAR POWER INSTALLATION**

D433,993 S \* 11/2000 Tang ..... D13/110  
\* cited by examiner

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(57) **CLAIM**

(\*\*) Term: **14 Years**

I claim the ornamental design for a reverse current blocking module for use in a solar power installation, substantially as shown and described.

(21) Appl. No.: **29/335,889**

(22) Filed: **Apr. 23, 2009**

**DESCRIPTION**

(51) **LOC (9) Cl.** ..... **13-02**

FIG. 1 is a perspective view of a reverse current blocking module for use in a solar power installation showing my new design;

(52) **U.S. Cl.** ..... **D13/110**

FIG. 2 is a top plan view thereof;

(58) **Field of Classification Search** ..... D13/110,

FIG. 3 is a bottom plan view thereof;

D13/101, 102, 123, 179, 184, 199; 136/251;

FIG. 4 is a rear elevation view thereof;

257/666, 675, 796; 361/601, 603, 676, 709

FIG. 5 is a front elevation view thereof;

See application file for complete search history.

FIG. 6 is a right side view thereof; and,

(56) **References Cited**

FIG. 7 is a left side view thereof.

**U.S. PATENT DOCUMENTS**

The features shown in broken lines in the various Figures are for illustrating environmental structure and form no part of the claimed design.

D219,597 S \* 12/1970 Smith ..... D13/110

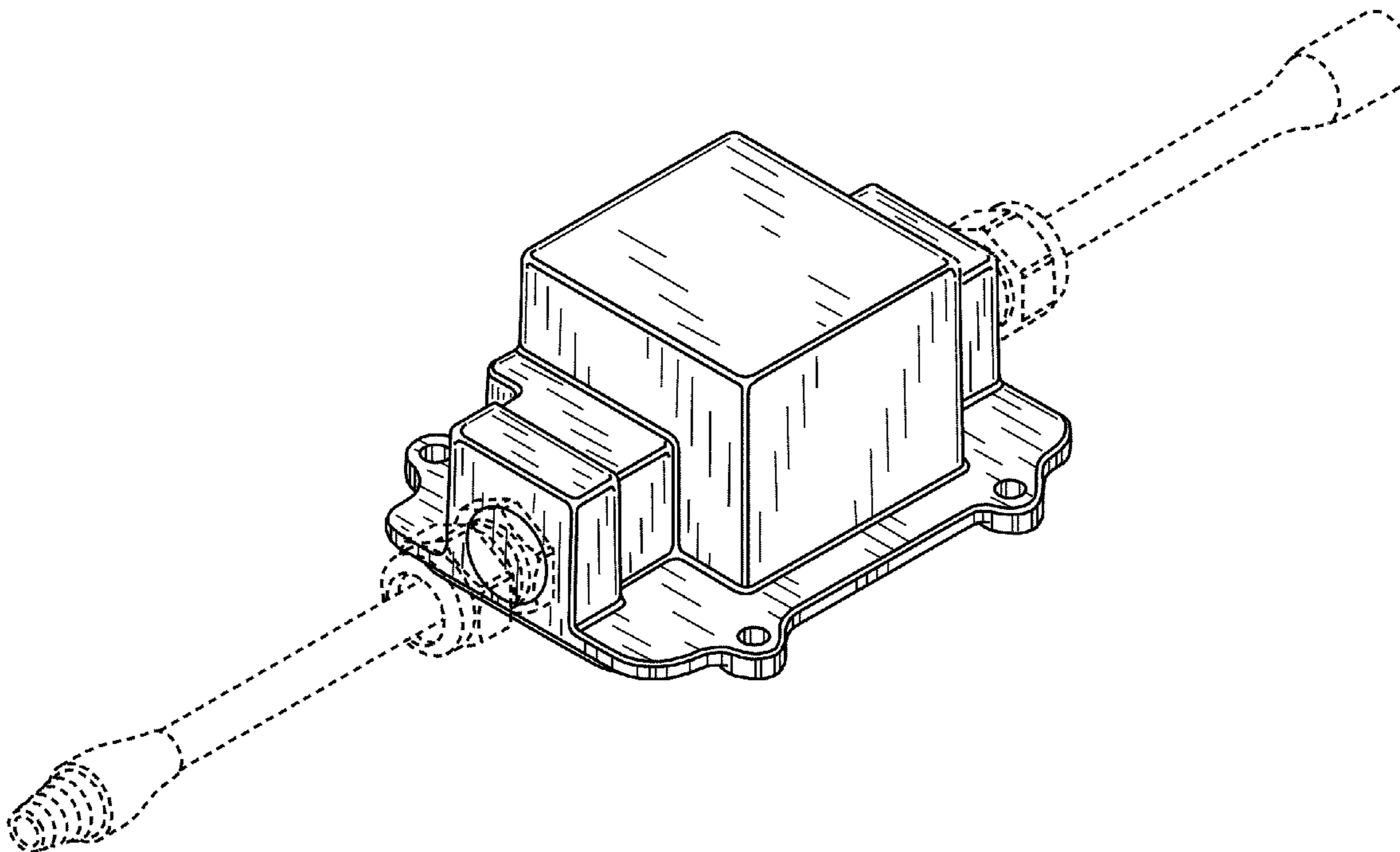
4,571,532 A \* 2/1986 Jaster ..... 320/101

D289,033 S \* 3/1987 Makinson et al. .... D13/110

4,970,453 A \* 11/1990 Oogita ..... 323/304

D374,428 S \* 10/1996 Scherer et al. .... D13/160

**1 Claim, 6 Drawing Sheets**



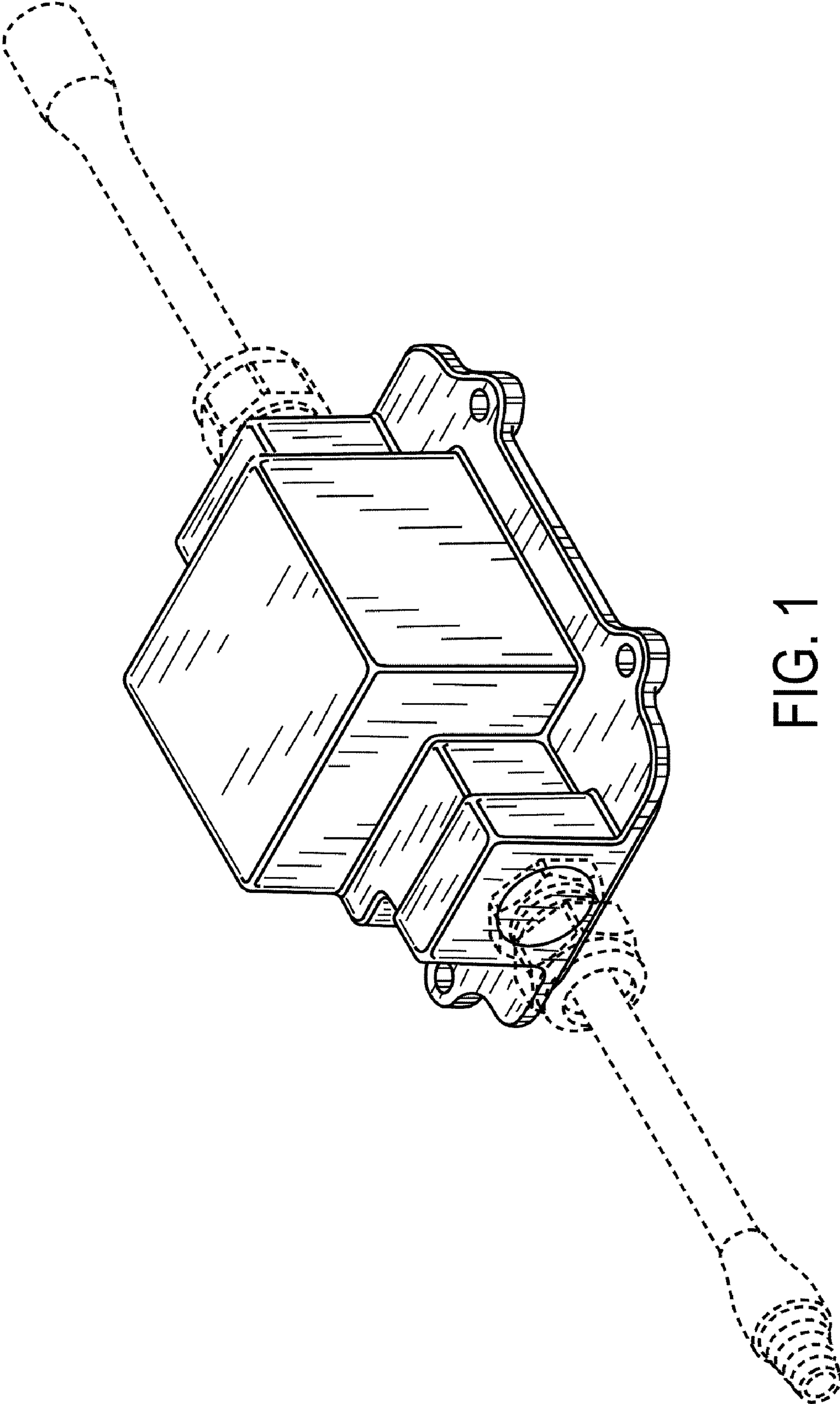


FIG. 1

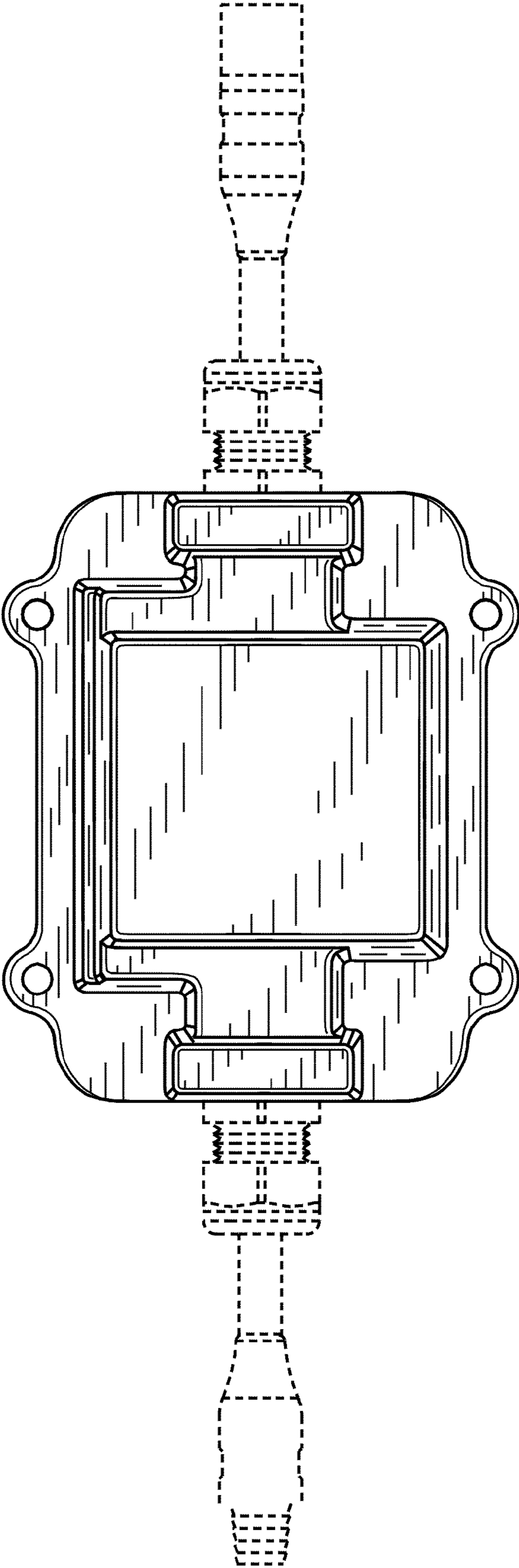


FIG. 2

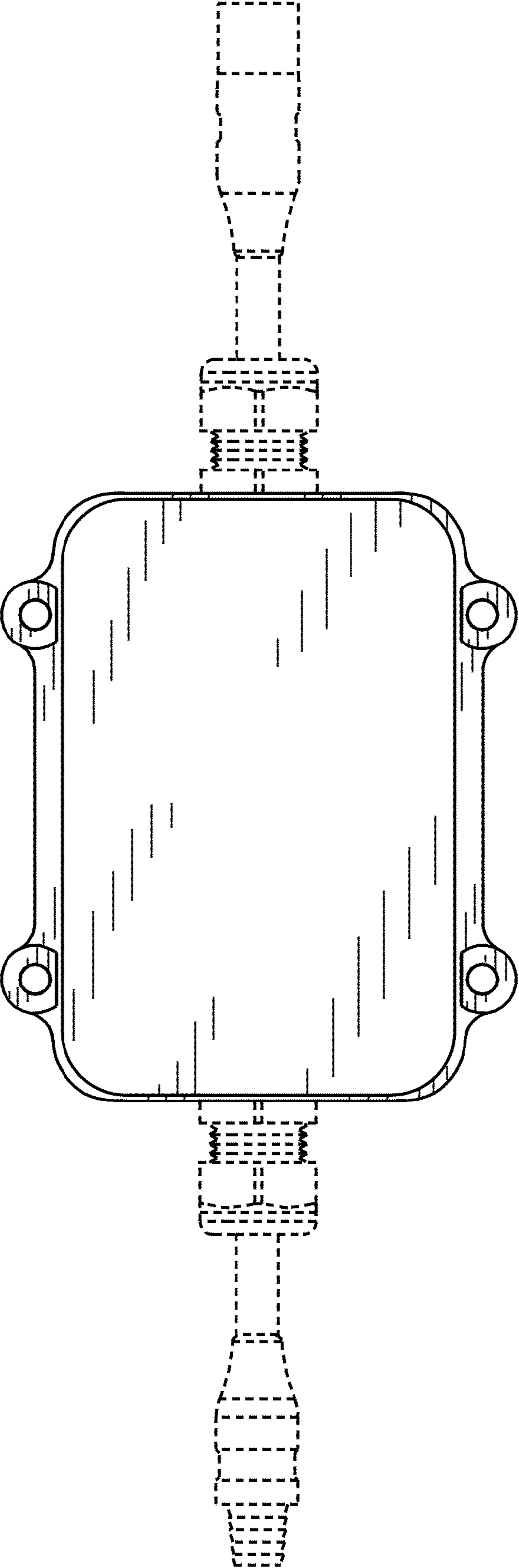


FIG. 3

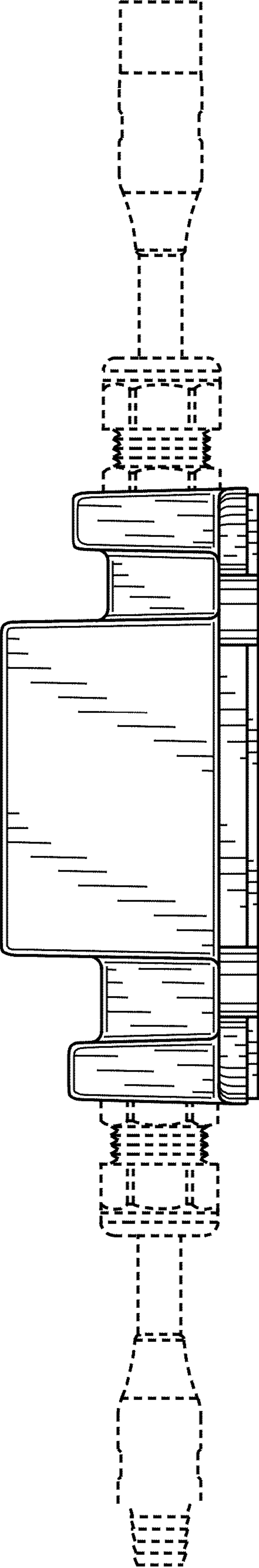


FIG. 4

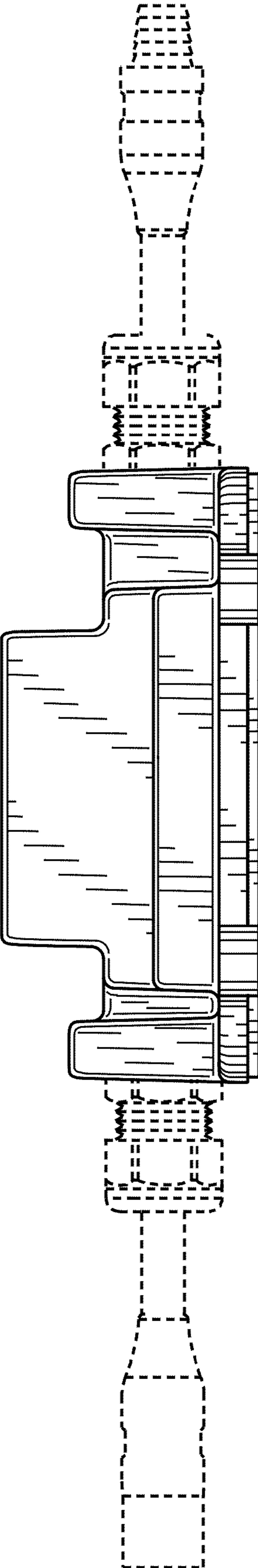


FIG. 5

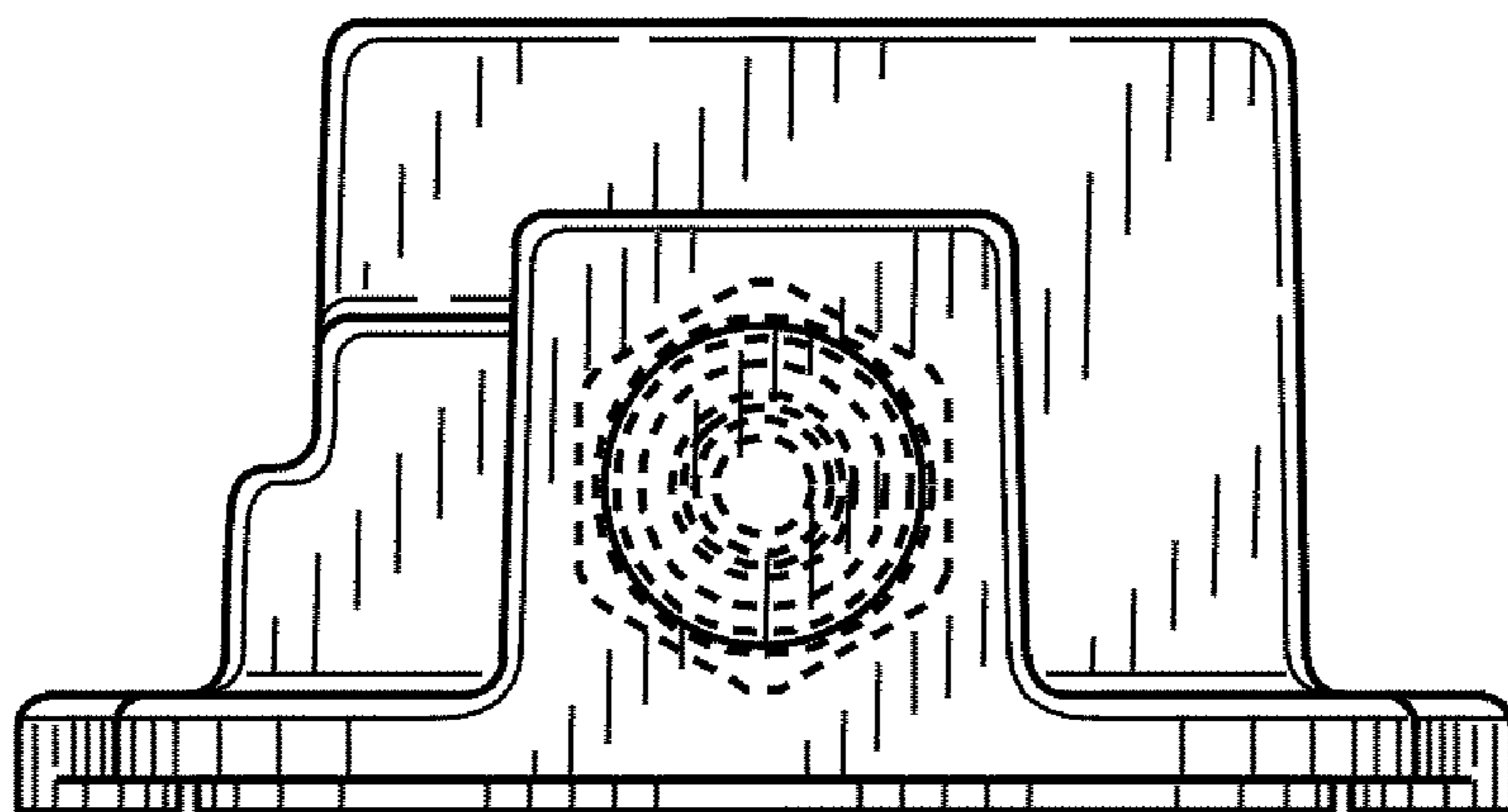


FIG. 6

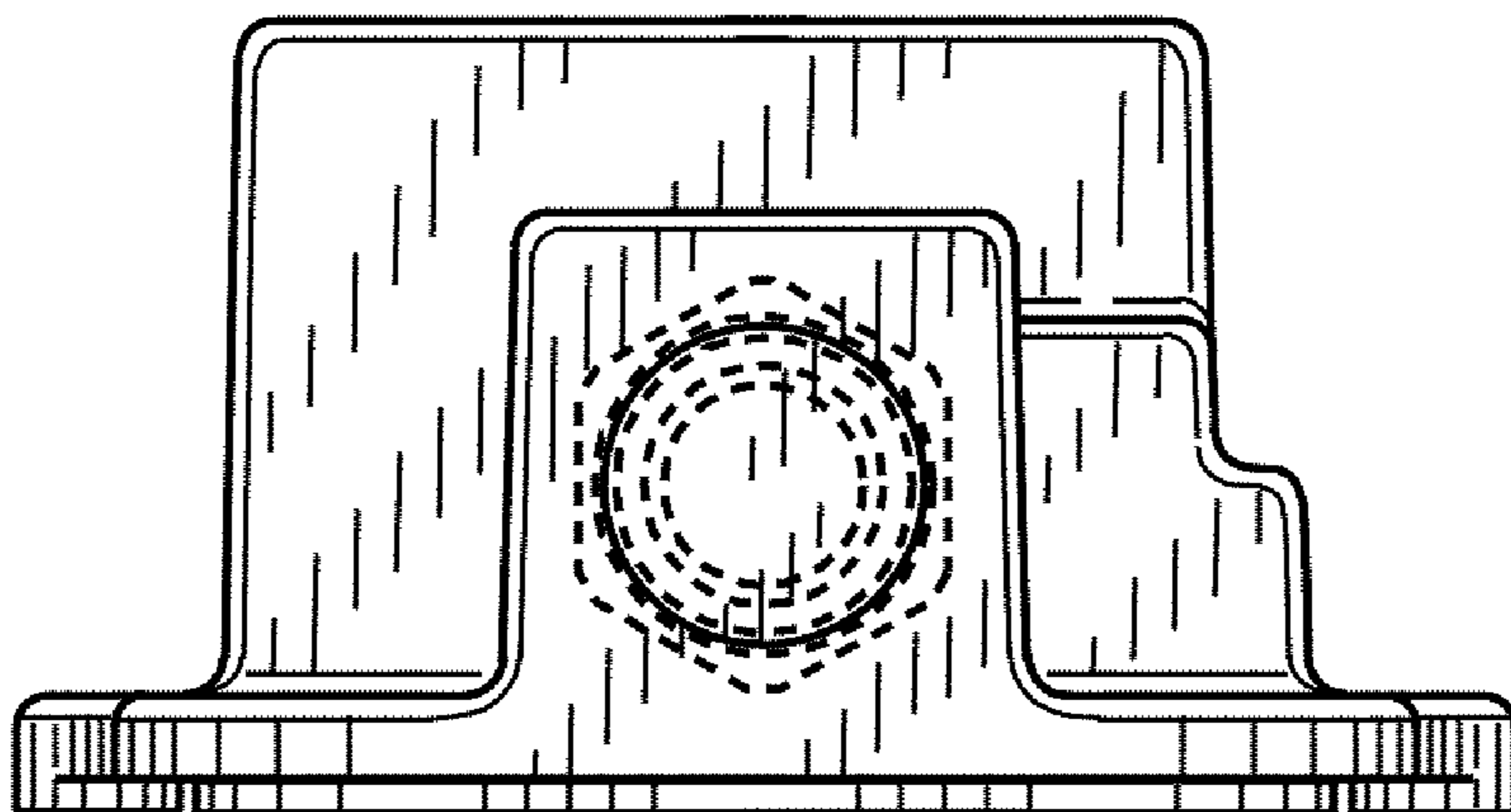


FIG. 7