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(12) **United States Design Patent**  
**Sato**

(10) **Patent No.:** **US D601,979 S**  
(45) **Date of Patent:** **\*\* Oct. 13, 2009**

(54) **PEDESTAL BASE OF A HEAT INSULATING  
CYLINDER FOR MANUFACTURING  
SEMICONDUCTOR WAFERS**

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(73) Assignee: **Tokyo Electron Limited**, Tokyo (JP)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/309,697**

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(30) **Foreign Application Priority Data**

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(51) **LOC (9) Cl.** ..... **13-03**

(52) **U.S. Cl.** ..... **D13/182**

(58) **Field of Classification Search** ..... D13/182;  
206/454, 711; 118/728, 729; 211/41.18;  
432/241, 253, 258

See application file for complete search history.

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LLP

(57) **CLAIM**

The ornamental design for a pedestal base of a heat insulating cylinder for manufacturing semiconductor wafers, as shown and described.

**DESCRIPTION**

FIG. 1 is a perspective view of the design for a pedestal base of a heat insulating cylinder for manufacturing semiconductor wafers in accordance with the invention;

FIG. 2 is a front view thereof;

FIG. 3 is a rear view thereof;

FIG. 4 is a left side view thereof;

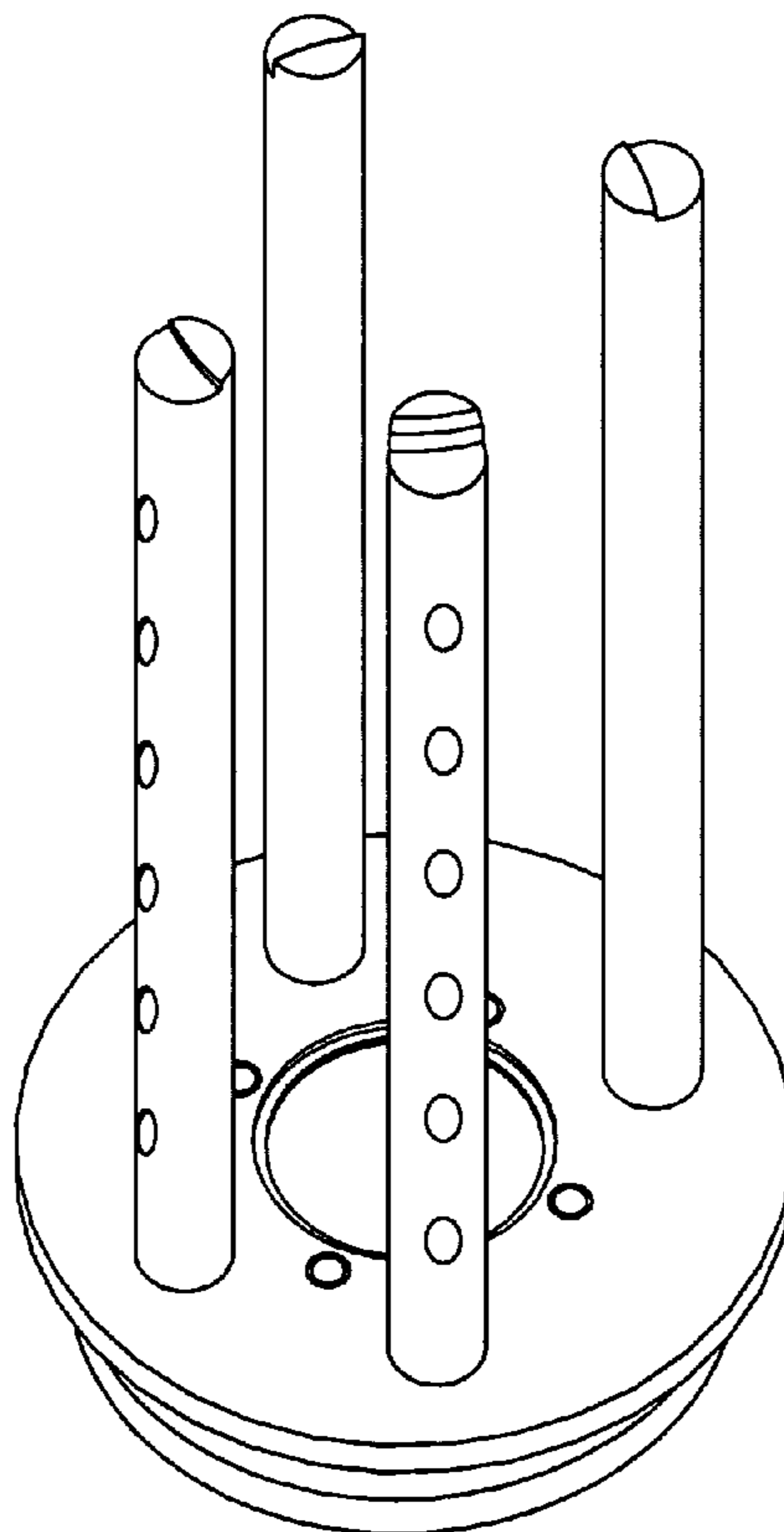
FIG. 5 is a right side view thereof;

FIG. 6 is a plan view thereof;

FIG. 7 is a bottom view thereof; and,

FIG. 8 is a sectional view along line 8—8 of FIG. 6.

**1 Claim, 8 Drawing Sheets**



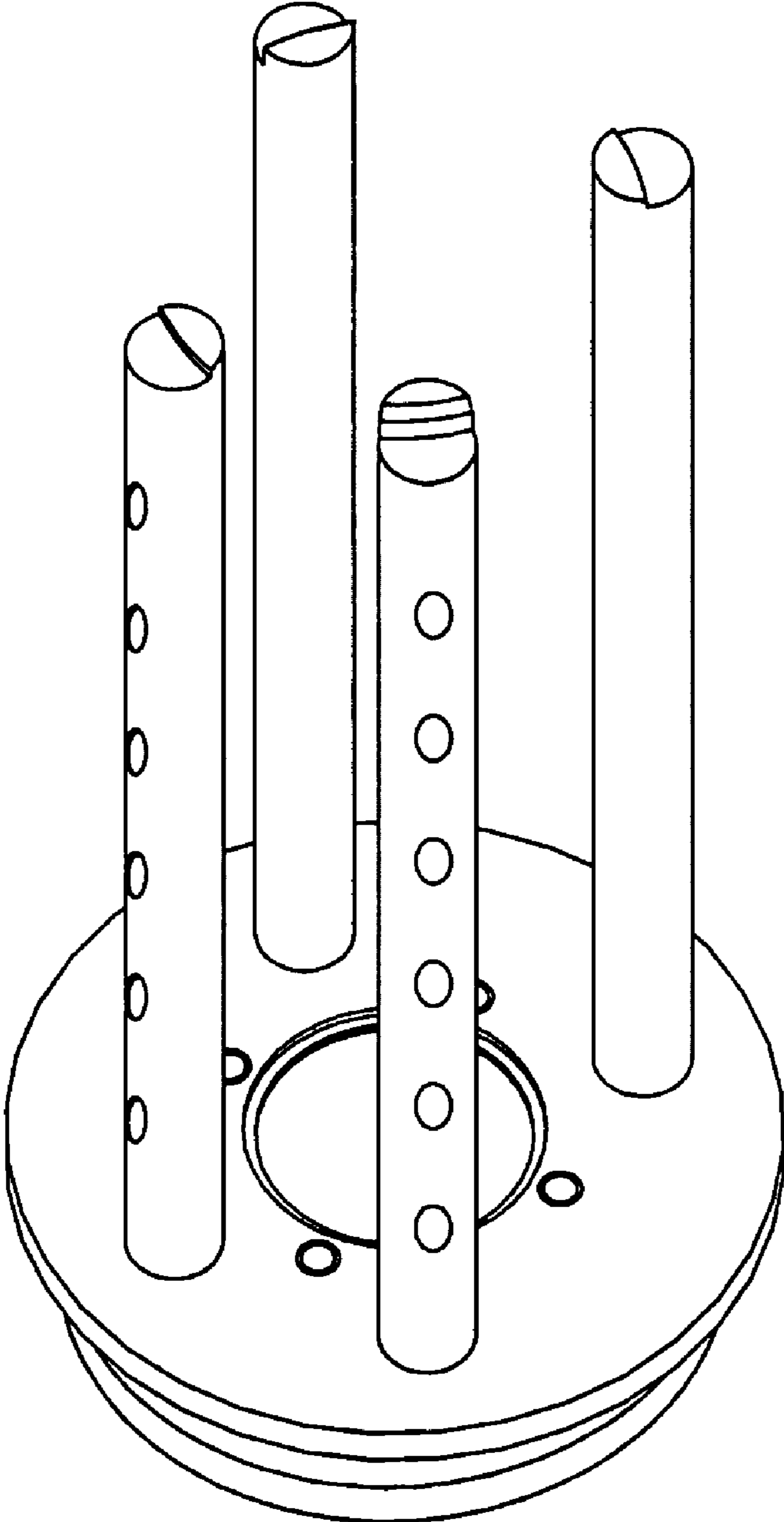


FIG. 1



FIG. 2

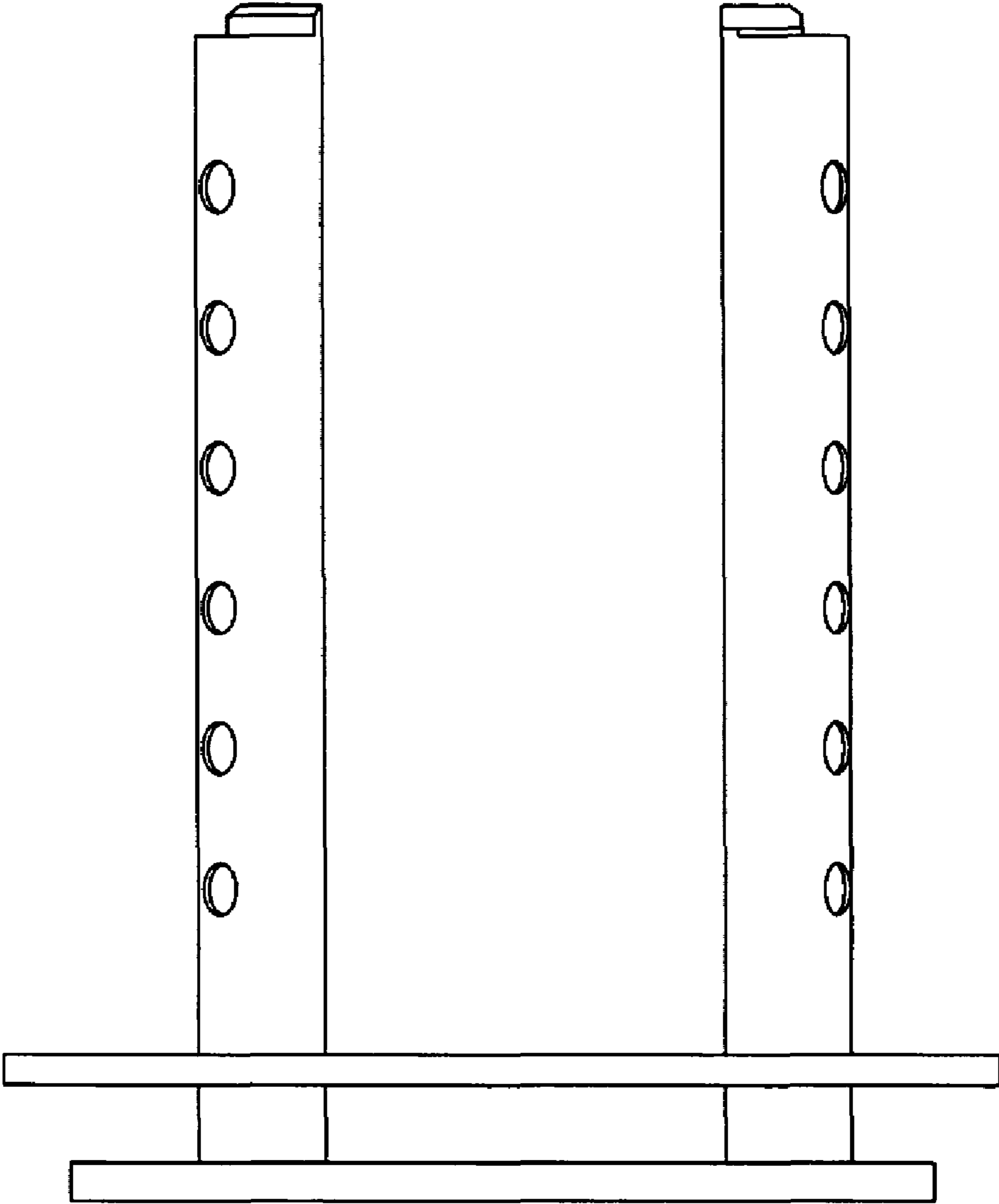


FIG. 3

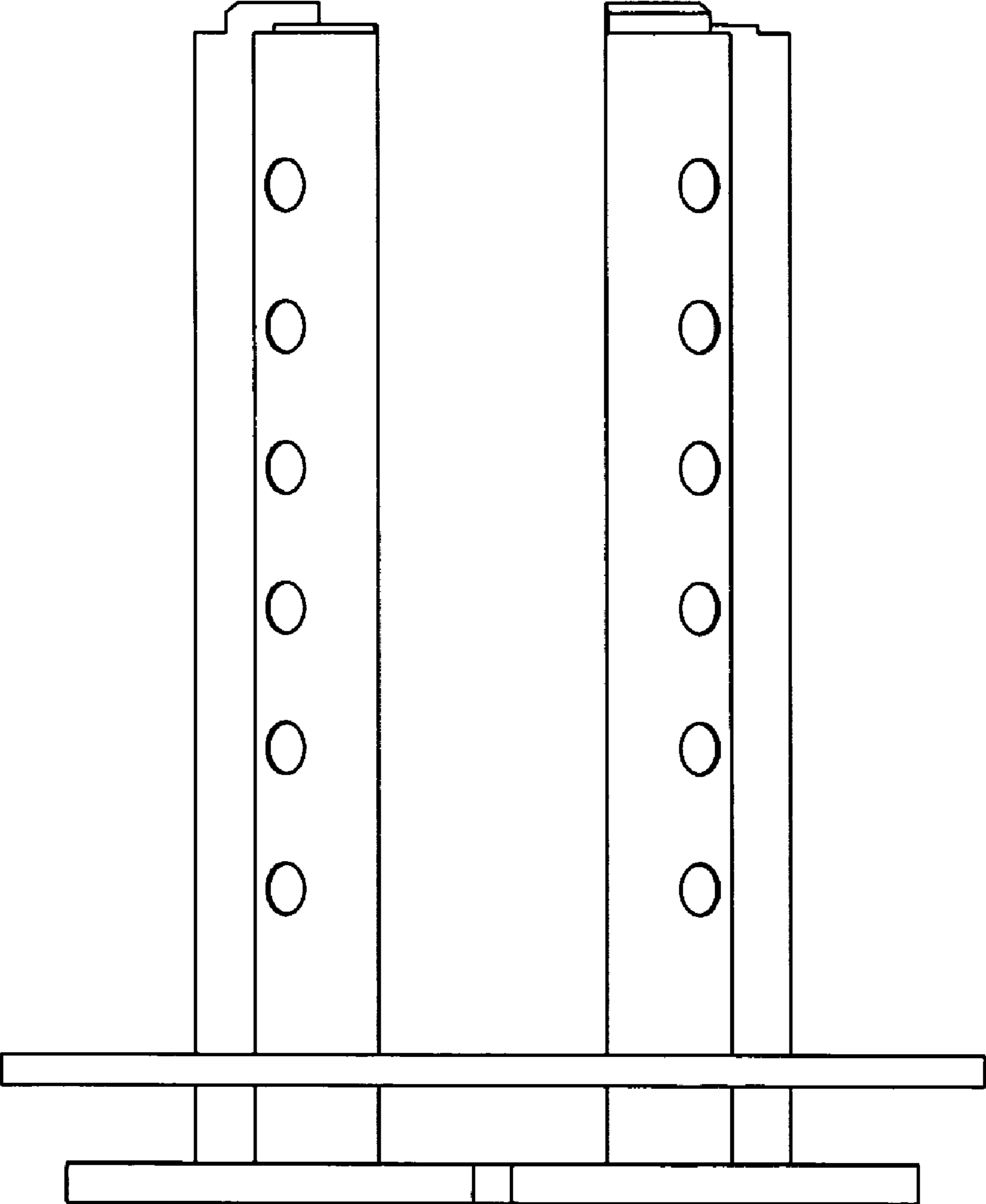


FIG. 4

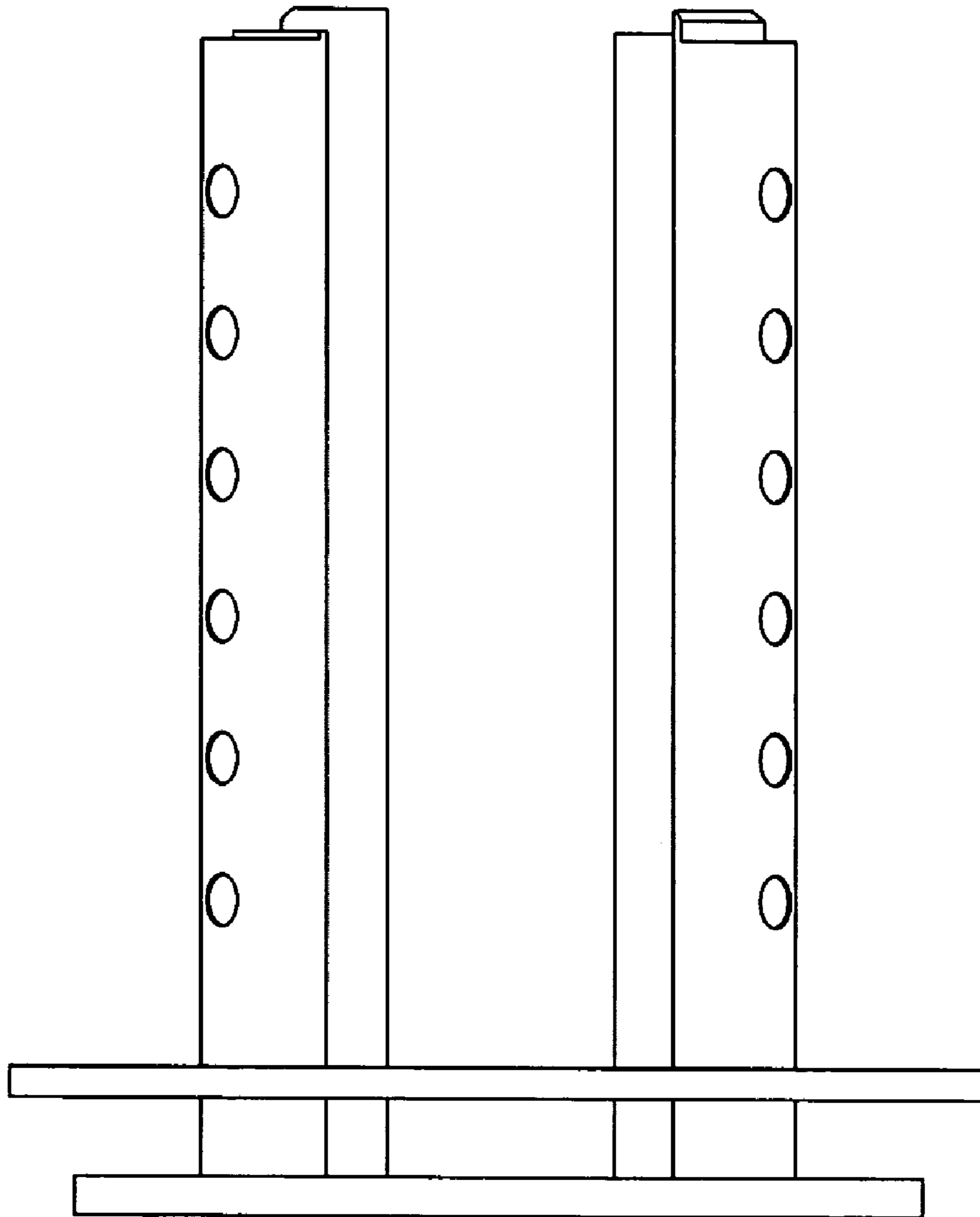


FIG. 5

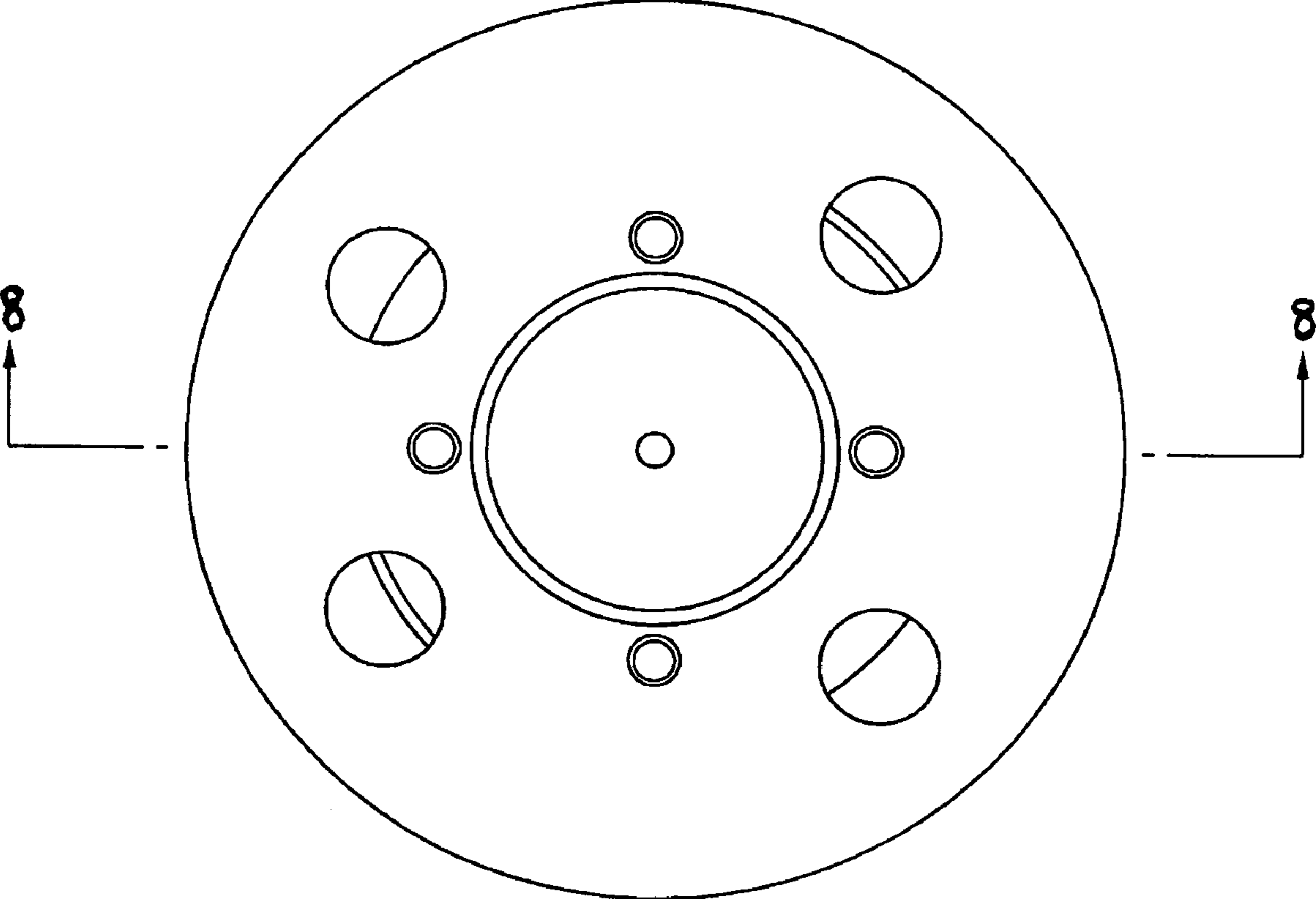


FIG. 6

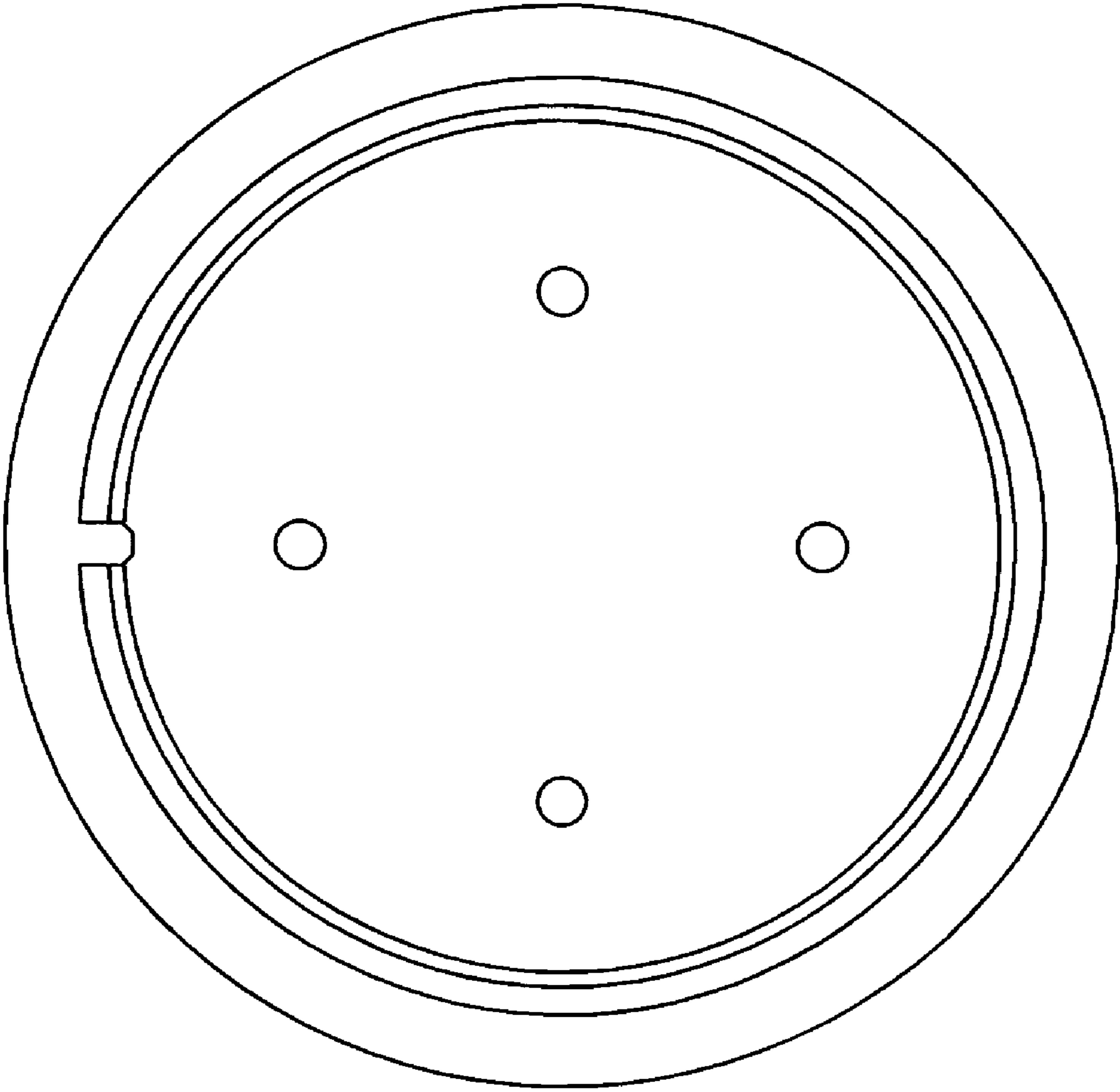


FIG. 7



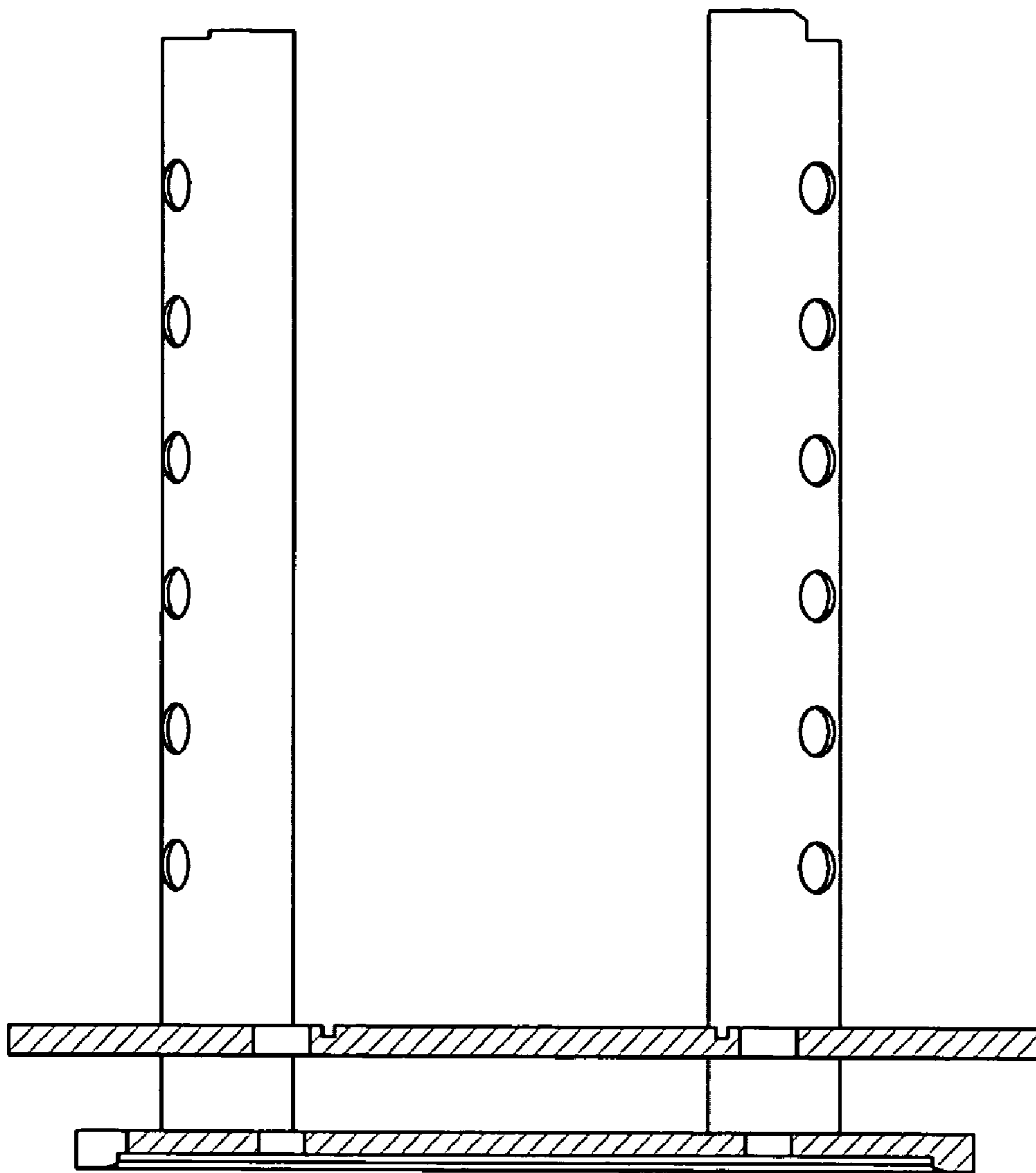


FIG. 8