



US00D600213S

(12) **United States Design Patent**
Inoue et al.

(10) **Patent No.:** **US D600,213 S**

(45) **Date of Patent:** **** Sep. 15, 2009**

(54) **ETHERNET SWITCH**

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(73) Assignee: **Omron Corporation**, Kyoto (JP)

(**) Term: **14 Years**

(21) Appl. No.: **29/332,634**

(22) Filed: **Feb. 20, 2009**

Related U.S. Application Data

(62) Division of application No. 29/318,006, filed on May 12, 2008.

(30) **Foreign Application Priority Data**

Nov. 12, 2007 (JP) D2007-031212
Nov. 12, 2007 (JP) D2007-031213

(51) **LOC (9) Cl.** **13-03**

(52) **U.S. Cl.** **D13/158; D13/133**

(58) **Field of Classification Search** D13/133,
D13/146, 147, 154, 158, 162, 164, 184, 199;
D14/140, 217, 240, 256, 299, 356, 432; 439/344,
439/535-536, 540.1, 638, 650, 660, 676;
361/686, 728, 752; 370/216, 372, 392
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a ethernet switch, as shown and described.

DESCRIPTION

FIG. 1 is a front, top and right side perspective view of an ethernet switch showing our new design;

FIG. 2 is a rear, bottom and left side perspective view thereof;

FIG. 3 is a front elevational view thereof;

FIG. 4 is a rear elevational view thereof;

FIG. 5 is a top plan view thereof;

FIG. 6 is a bottom plan view thereof;

FIG. 7 is a right side elevational view thereof; and,

FIG. 8 is a left side elevational view thereof.

The broken line portion of the figure drawings is included to show unclaimed subject matter only and forms no part of the claimed design.

1 Claim, 6 Drawing Sheets

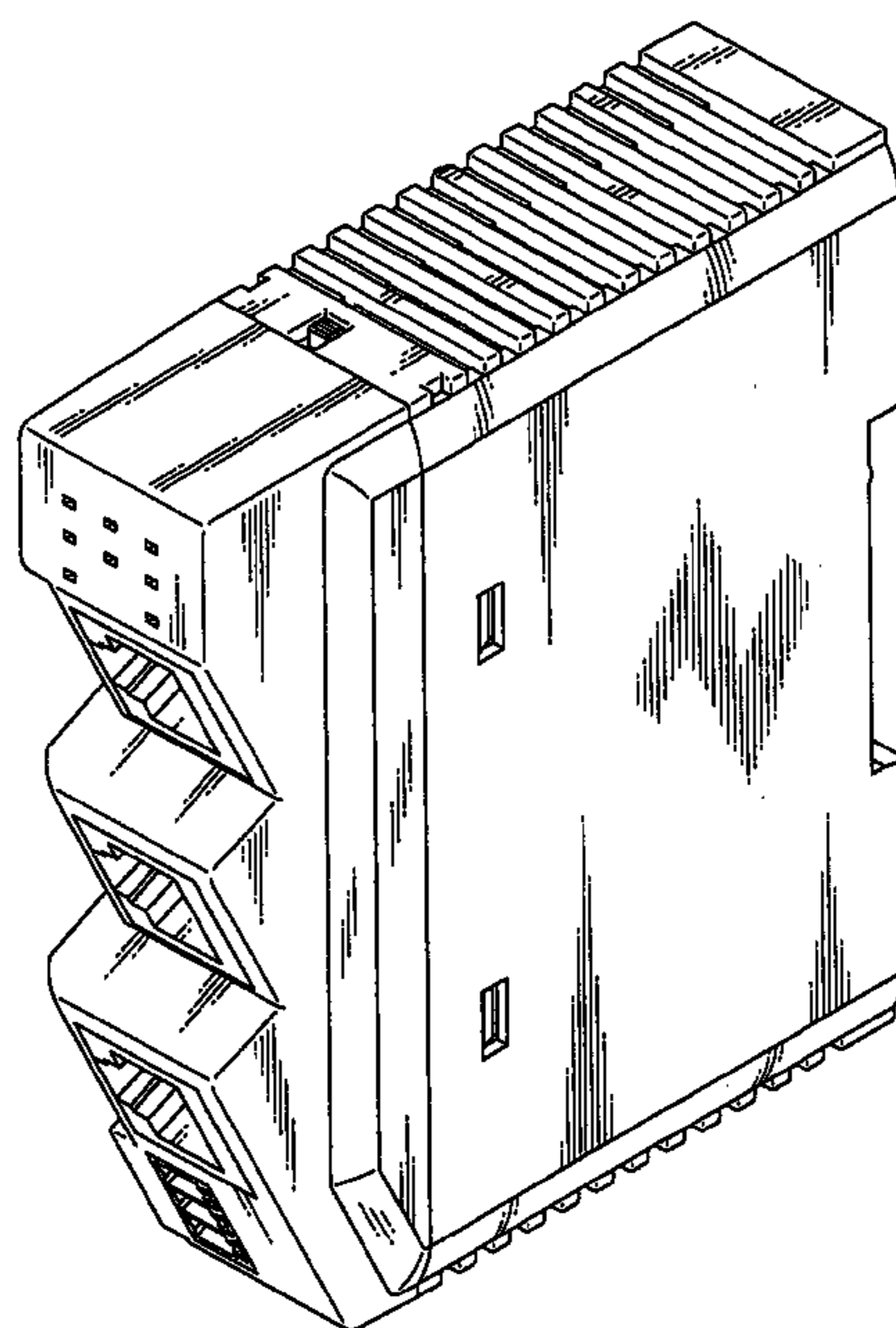


FIG. 1

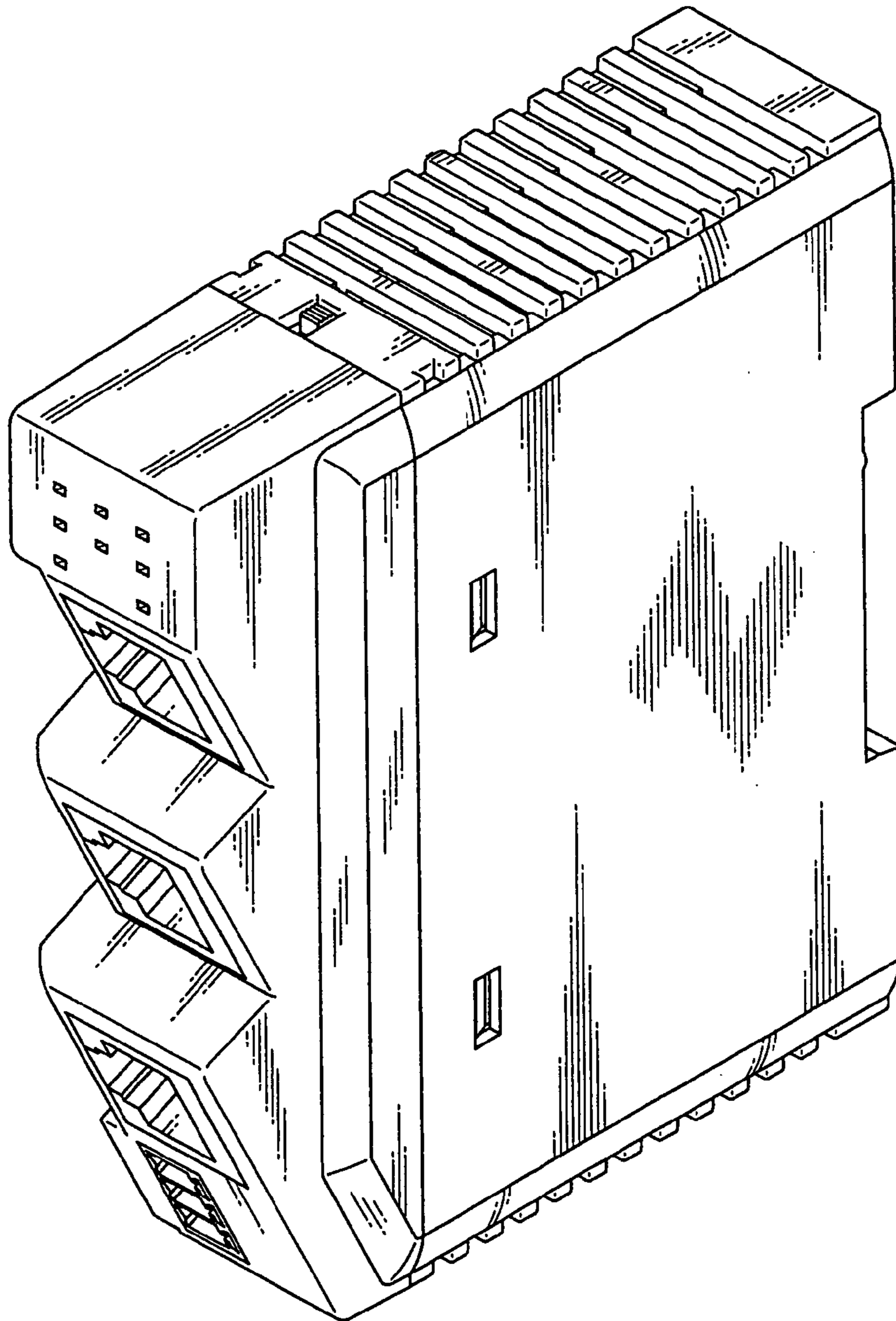


FIG.2

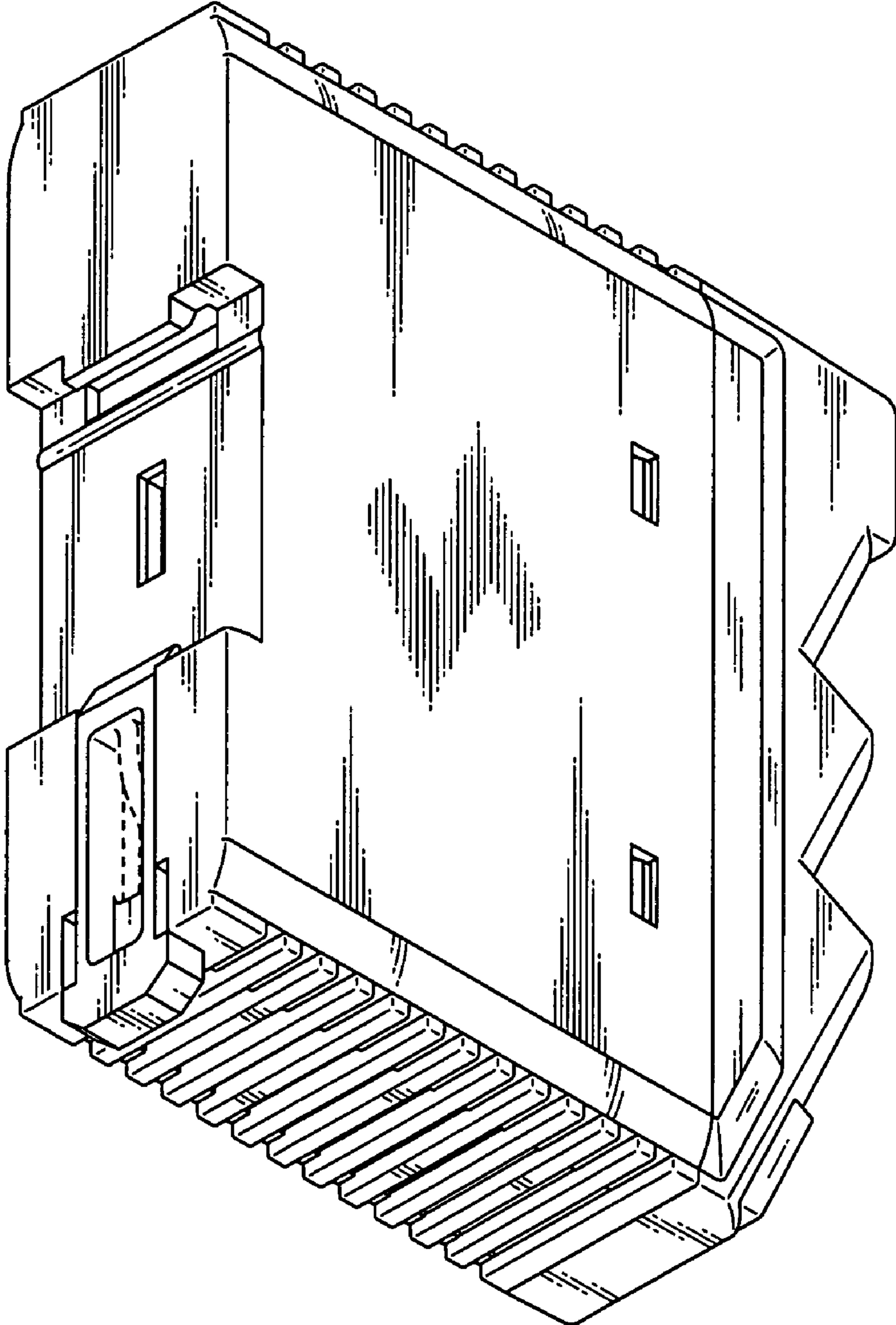


FIG.3

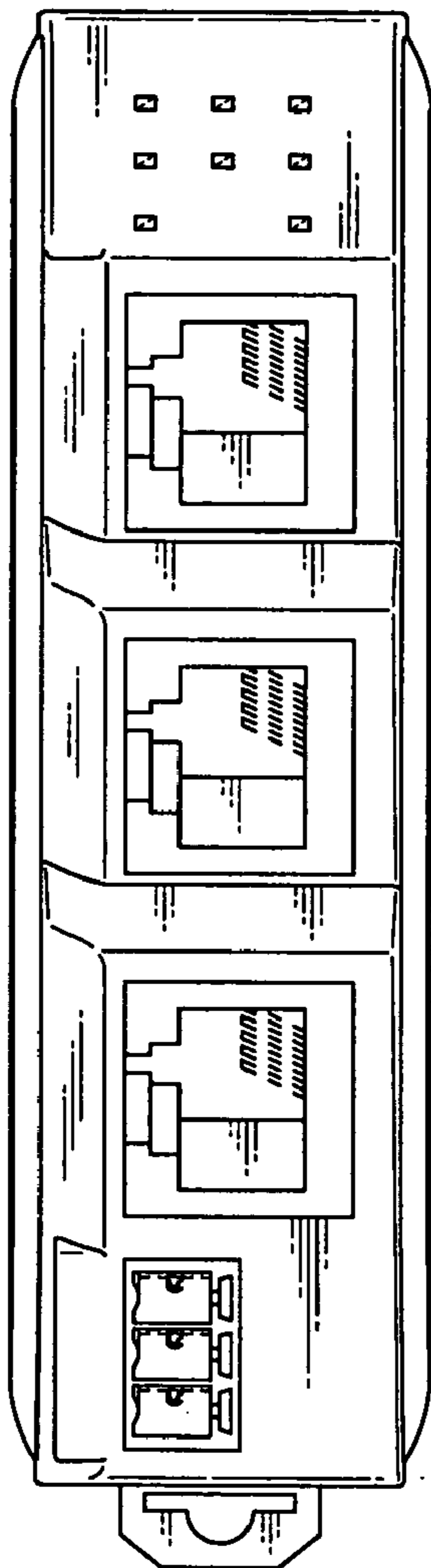


FIG.4

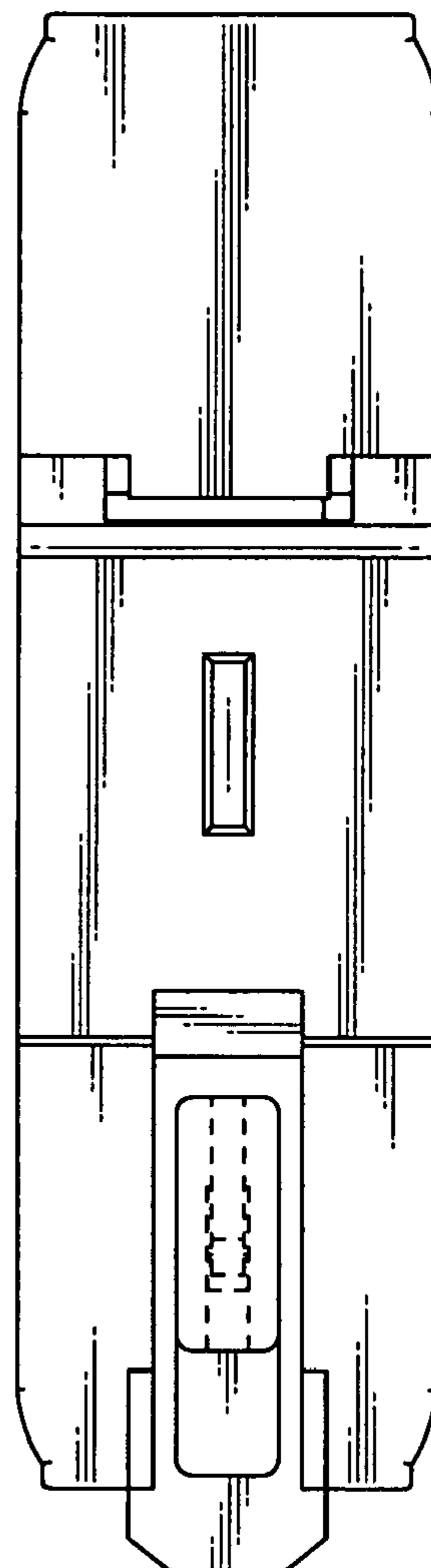


FIG.5

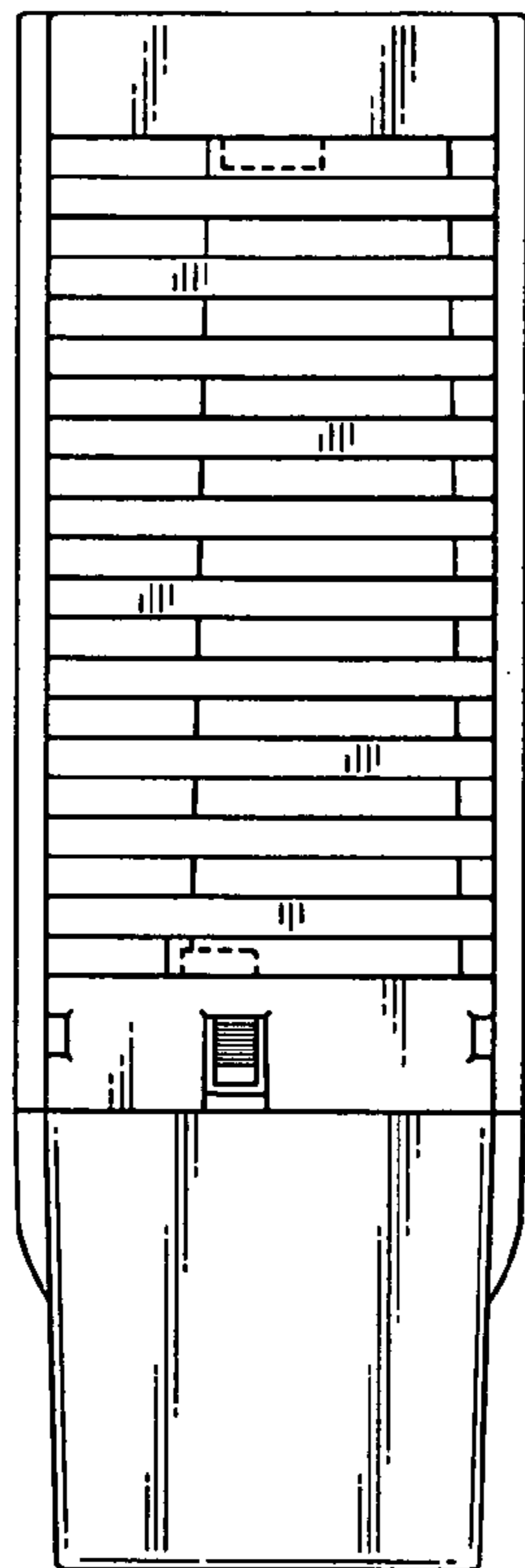


FIG.6

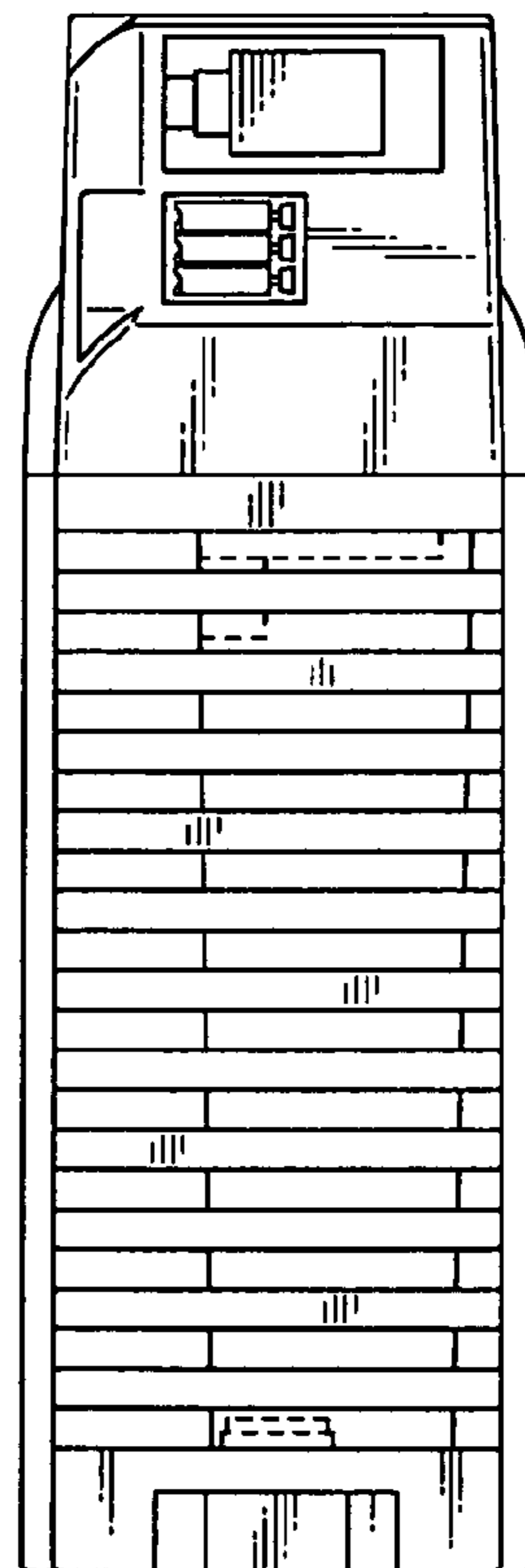


FIG. 7

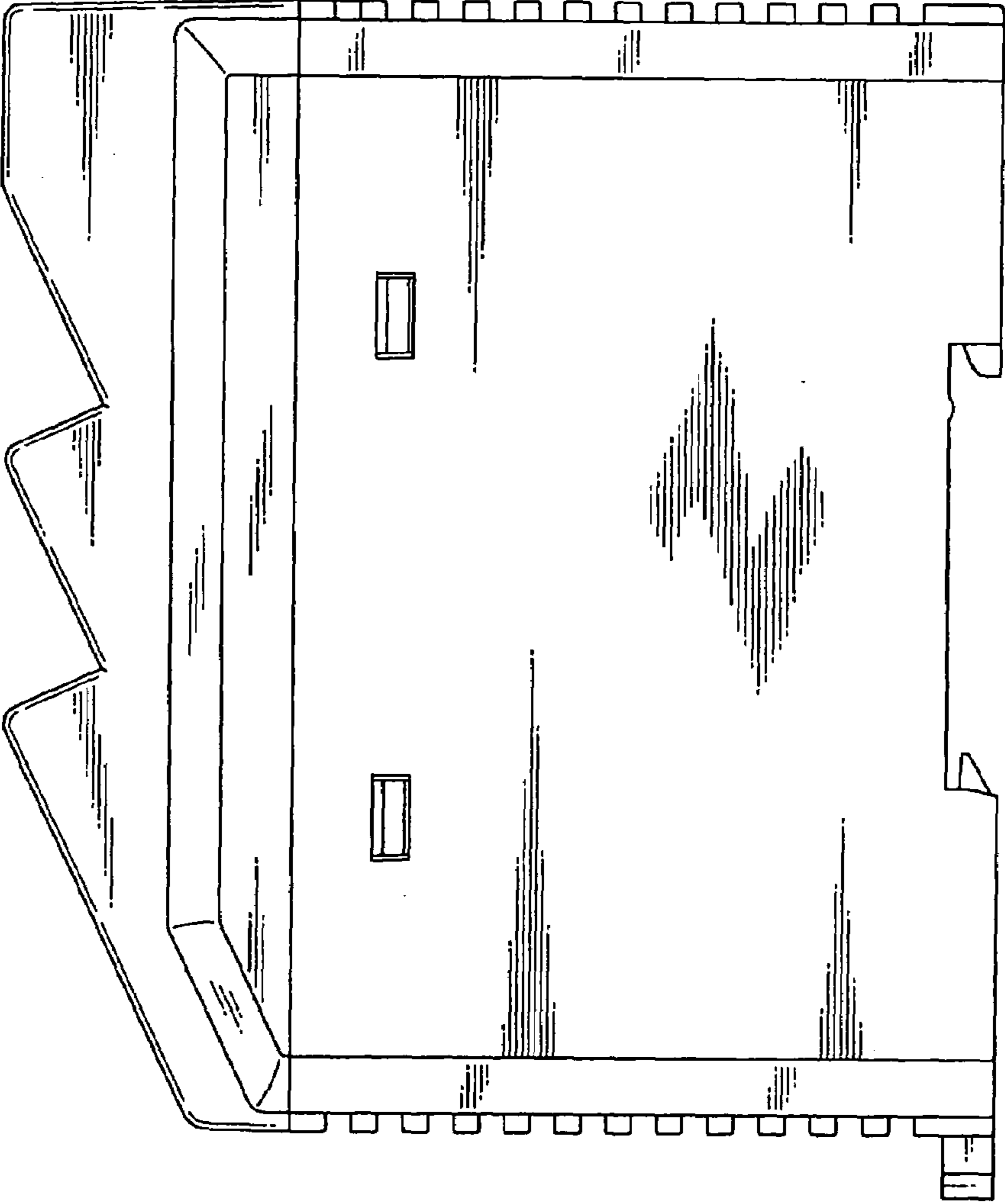


FIG.8

