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(12) **United States Design Patent**  
**Asai et al.**

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(45) **Date of Patent:** **\*\* Aug. 25, 2009**

(54) **ELECTRICAL DOUBLE LAYER CAPACITOR**

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(73) Assignee: **Meidensha Corporation**, Tokyo (JP)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/329,199**

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(30) **Foreign Application Priority Data**

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(51) **LOC (9) Cl.** ..... **13-03**

(52) **U.S. Cl.** ..... **D13/125**

(58) **Field of Classification Search** ..... D13/118,  
D13/123–125, 133, 184, 199; 361/523, 535,  
361/536, 538, 539, 540, 271, 277, 301.1,  
361/328–330, 763; 429/178; 320/2

See application file for complete search history.

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(57) **CLAIM**

The ornamental design for an electrical double layer capacitor, as shown and described.

**DESCRIPTION**

The present article is an electrical double layer capacitor used in auxiliary power supplies of electric / electronic machines.

FIG. 1 is a front view of an electrical double layer capacitor showing the new design;

FIG. 2 is a rear view of the electrical double layer capacitor showing the new design;

FIG. 3 is a left side view of the electrical double layer capacitor showing the new design;

FIG. 4 is a right side view of the electrical double layer capacitor showing the new design;

FIG. 5 is a top view of the electrical double layer capacitor showing the new design;

FIG. 6 is a bottom plan view of the electrical double layer capacitor showing the new design;

FIG. 7 is a left-side perspective view of the electrical double layer capacitor showing the new design;

FIG. 8 is a right-side perspective view of the electrical double layer capacitor showing the new design; and,

FIG. 9 is a perspective view of the electrical double layer capacitor showing the new design for a reference purpose.

**1 Claim, 8 Drawing Sheets**

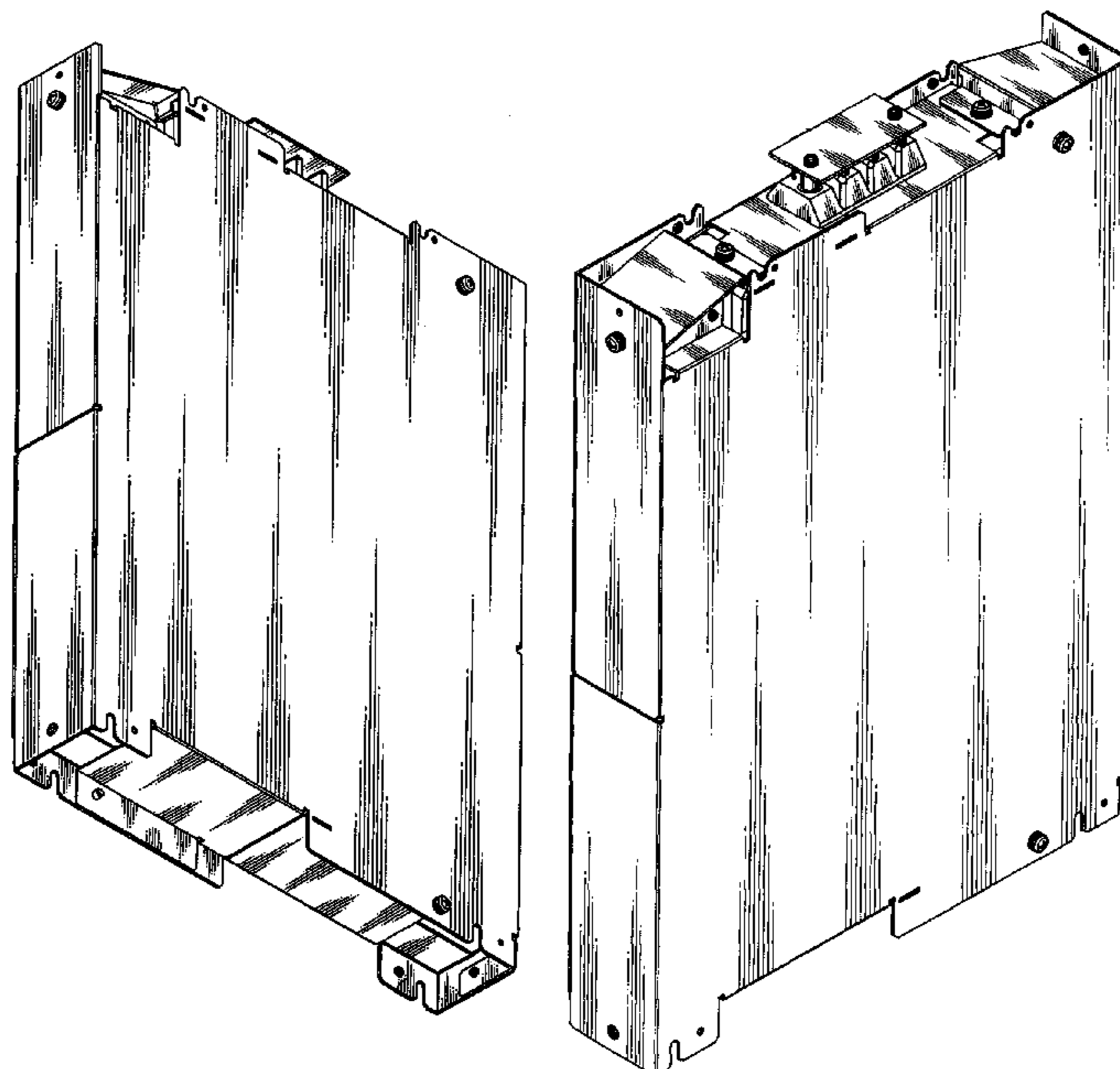


FIG. 1

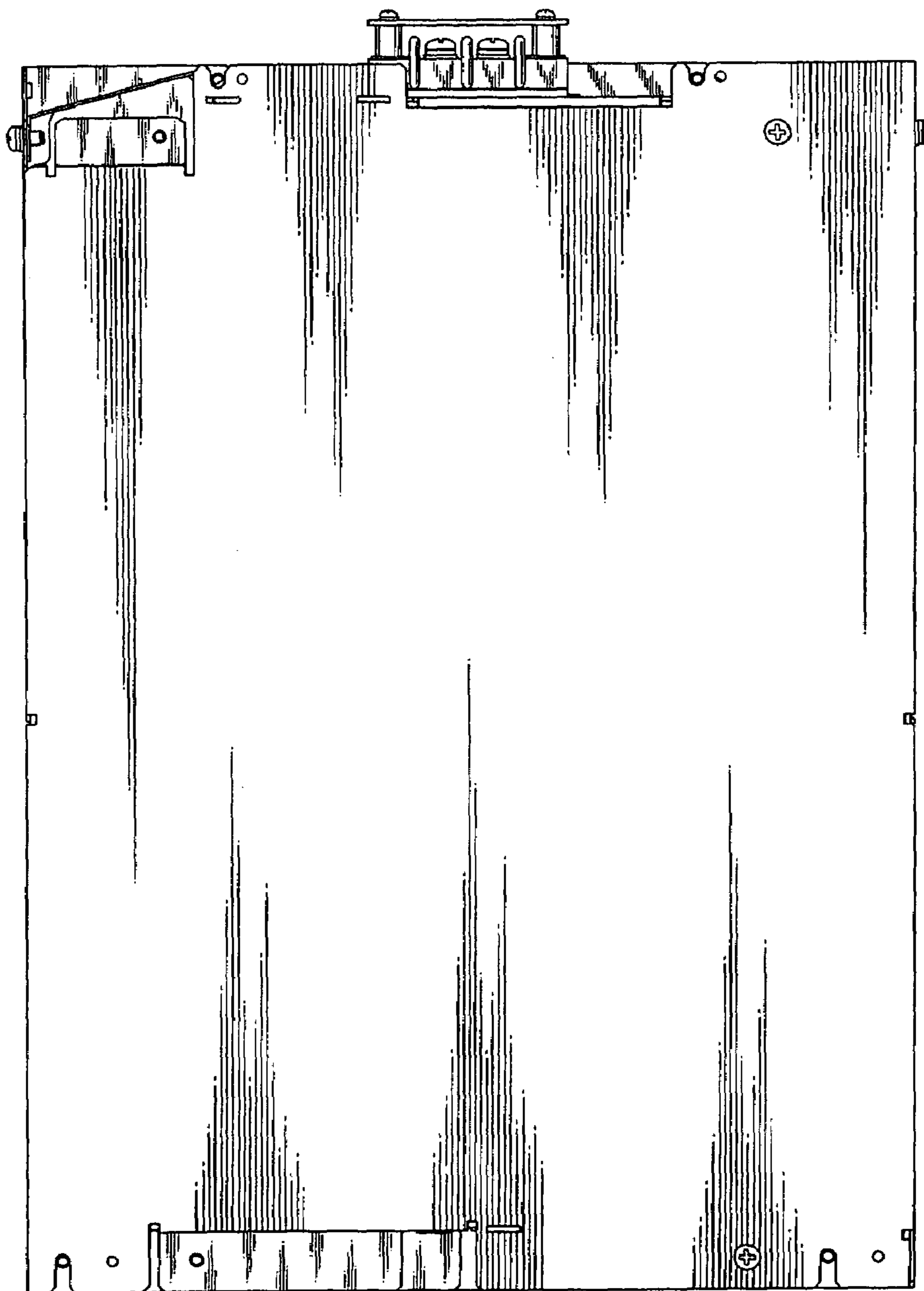
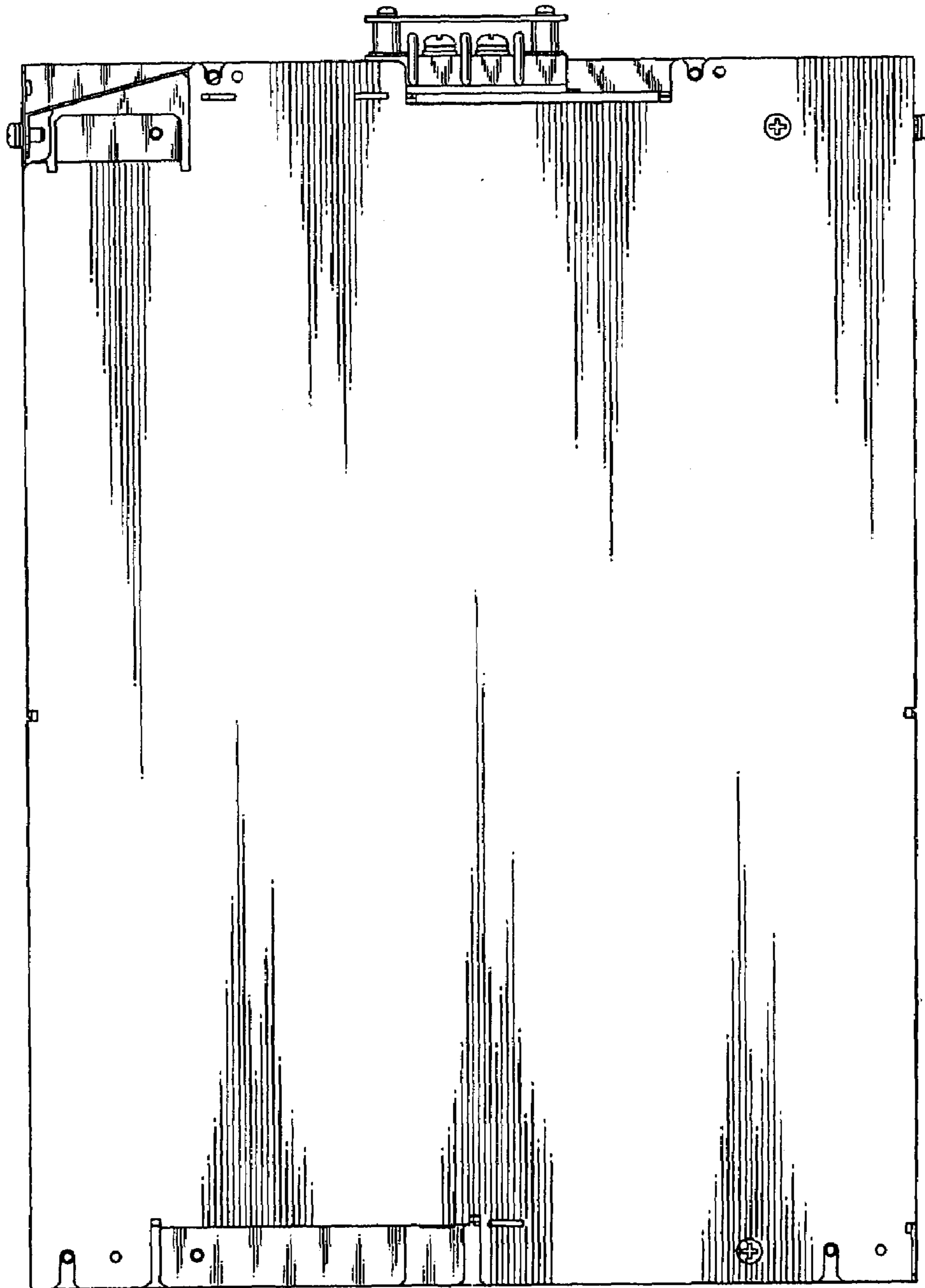
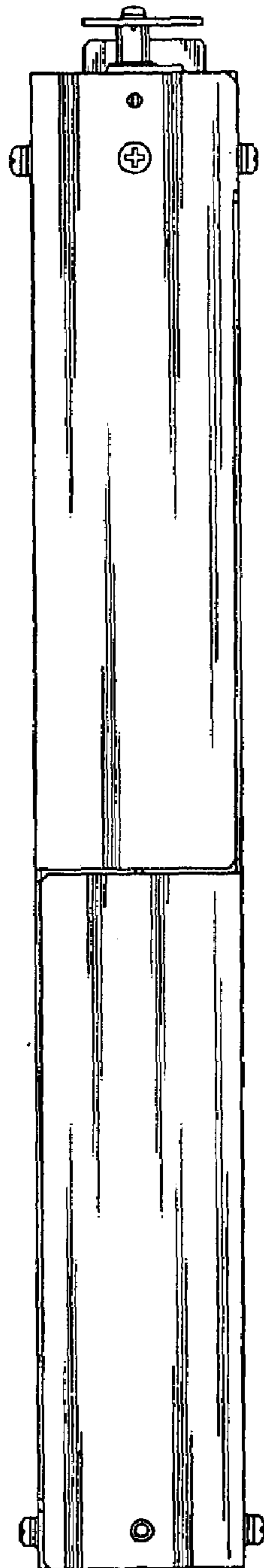


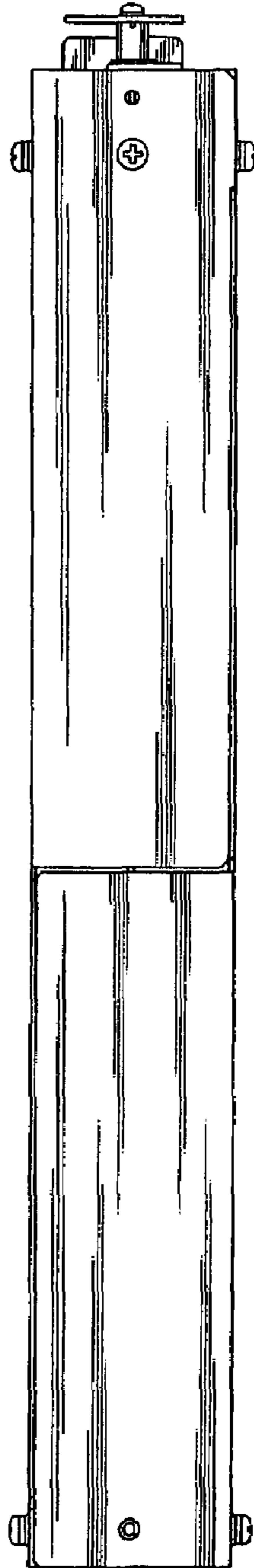
FIG.2



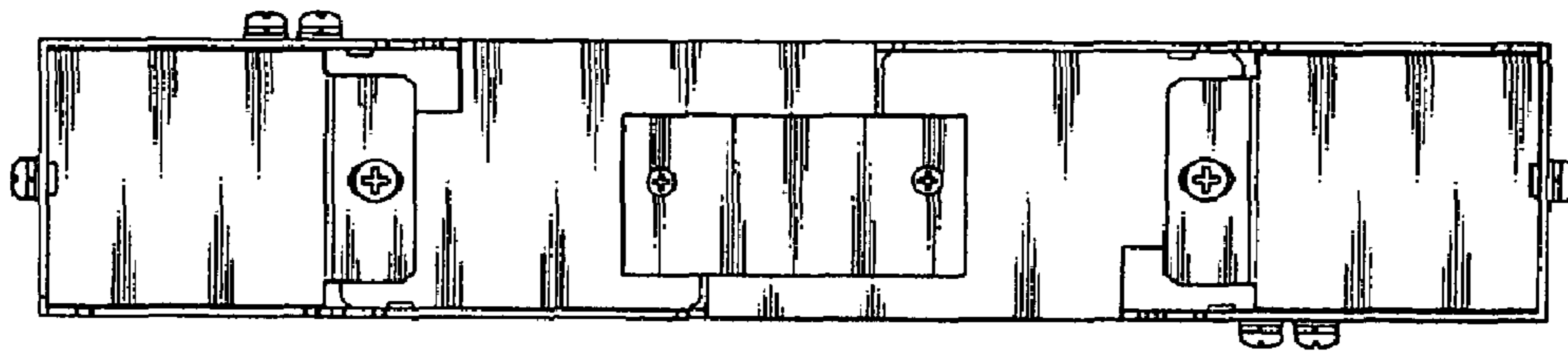
**FIG.3**



**FIG.4**



**FIG.5**



**FIG.6**

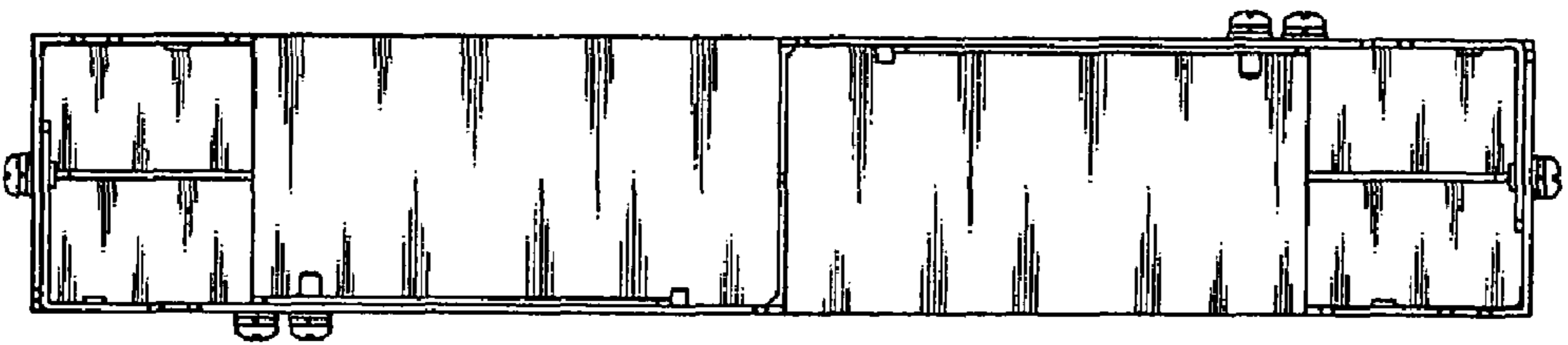
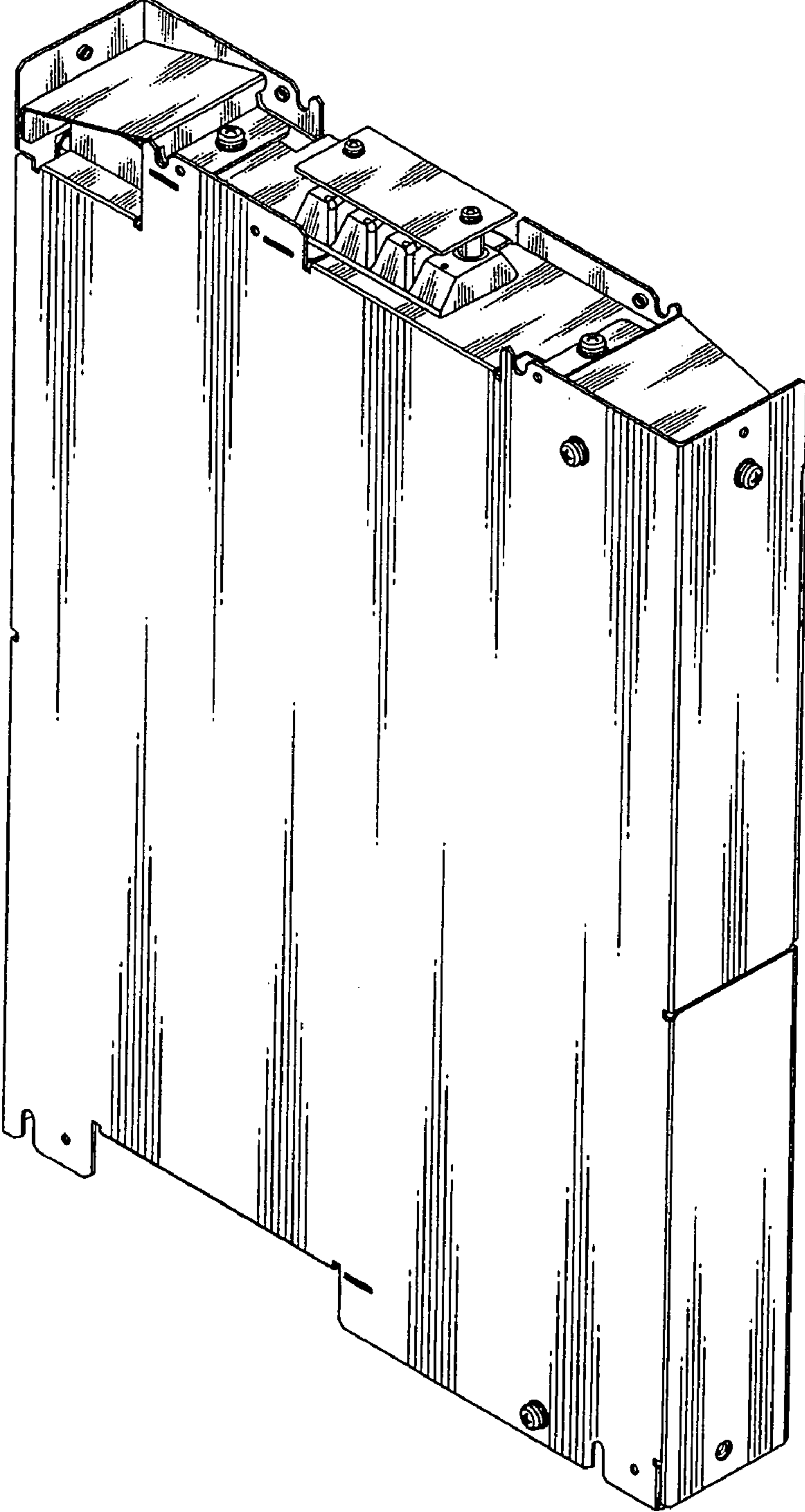
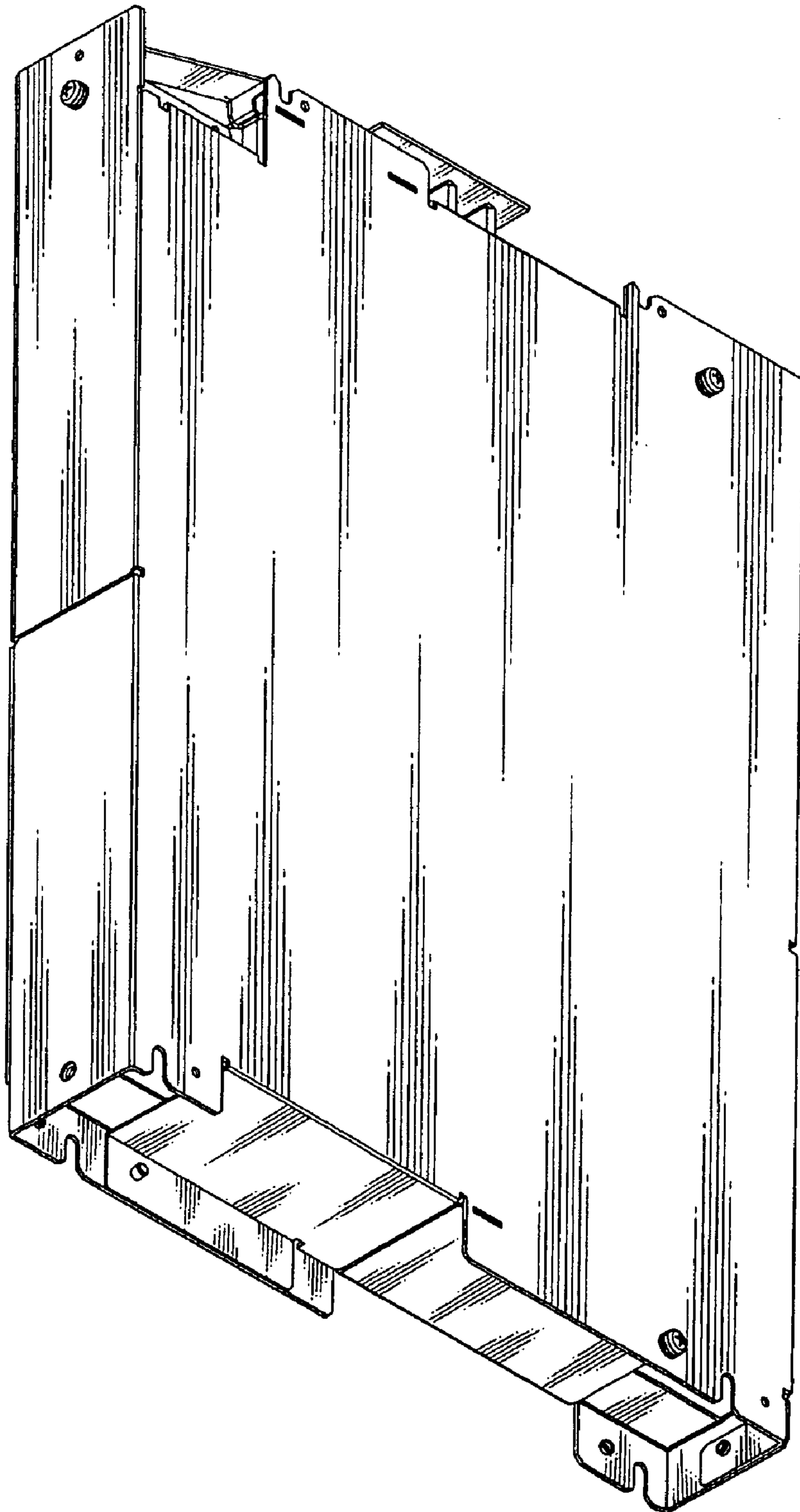




FIG.7



**FIG.8**





**FIG.9**

