



US00D593424S

(12) **United States Design Patent**
Wang

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(54) **CLOCK**

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(**) Term: **14 Years**

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(30) **Foreign Application Priority Data**

Apr. 18, 2008 (CN) 2008 3 0106422

(51) **LOC (9) Cl.** **10-01**

(52) **U.S. Cl.** **D10/15; D10/2**

(58) **Field of Classification Search** D10/1-29;
D26/38; 368/10, 276, 285, 284
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

2,237,963	A *	4/1941	Hawks	33/348
D154,687	S *	8/1949	Lambert	D10/3
2,762,190	A *	9/1956	Holzner	368/78
D228,938	S *	10/1973	Watanabe	D10/15
D235,517	S *	6/1975	Watanabe	D10/15

D235,518	S *	6/1975	Watanabe	D10/15
D241,522	S *	9/1976	Tateishi	D10/15
4,081,952	A *	4/1978	Busch et al.	368/82
D498,317	S *	11/2004	Yuen	D26/38
D517,428	S *	3/2006	Chang	D10/3
D525,536	S *	7/2006	Chan	D10/2
D577,139	S *	9/2008	Brown et al.	D26/38

* cited by examiner

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(57) **CLAIM**

The ornamental design for a clock, as shown.

DESCRIPTION

FIG. 1 is a front elevational view of a clock showing my new design;

FIG. 2 is a rear elevational view thereof;

FIG. 3 is a top plan view thereof;

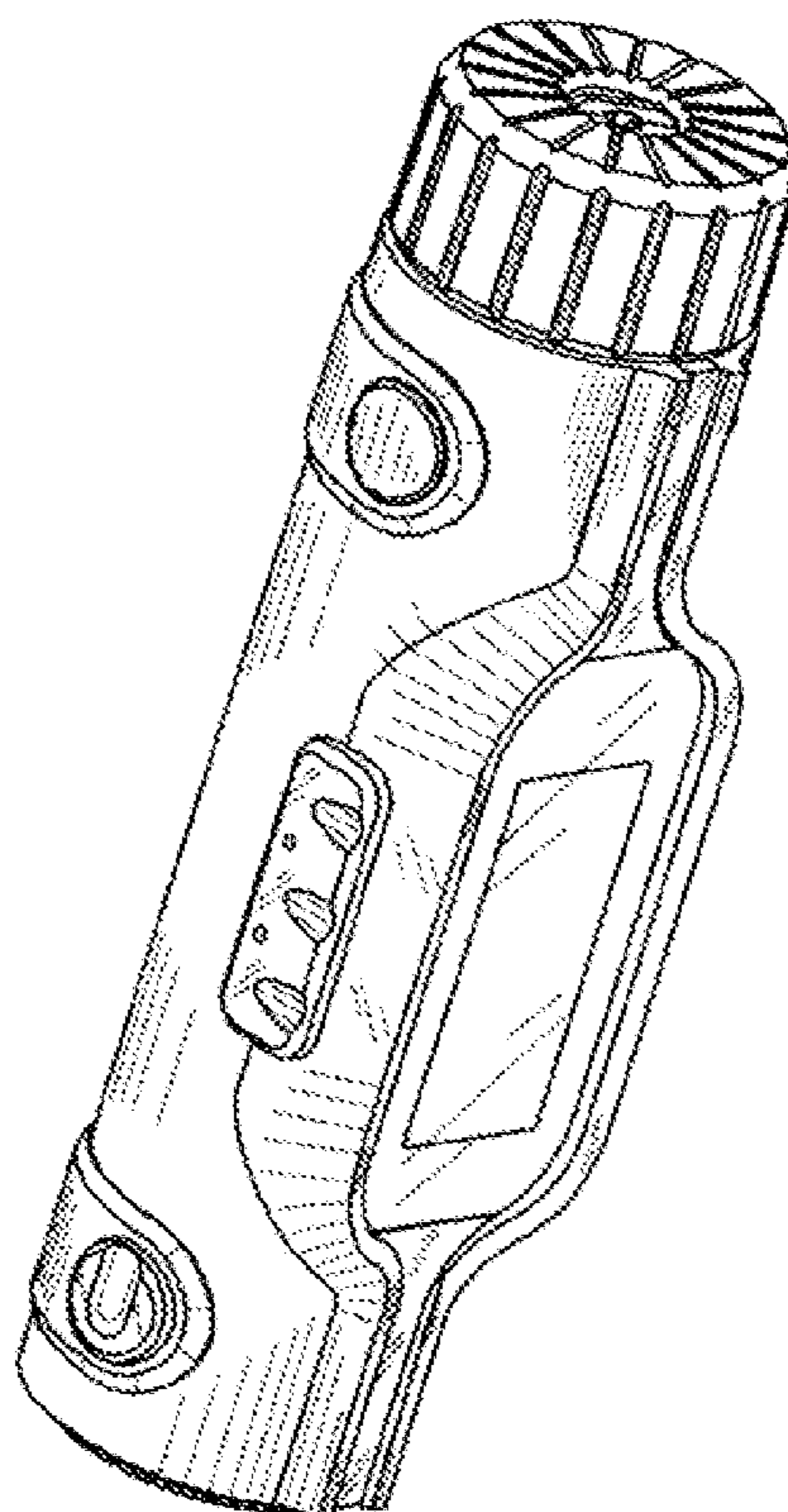
FIG. 4 is a bottom plan view thereof;

FIG. 5 is a left side elevation thereof;

FIG. 6 is a right side elevation thereof; and,

FIG. 7 is a perspective view thereof.

1 Claim, 2 Drawing Sheets



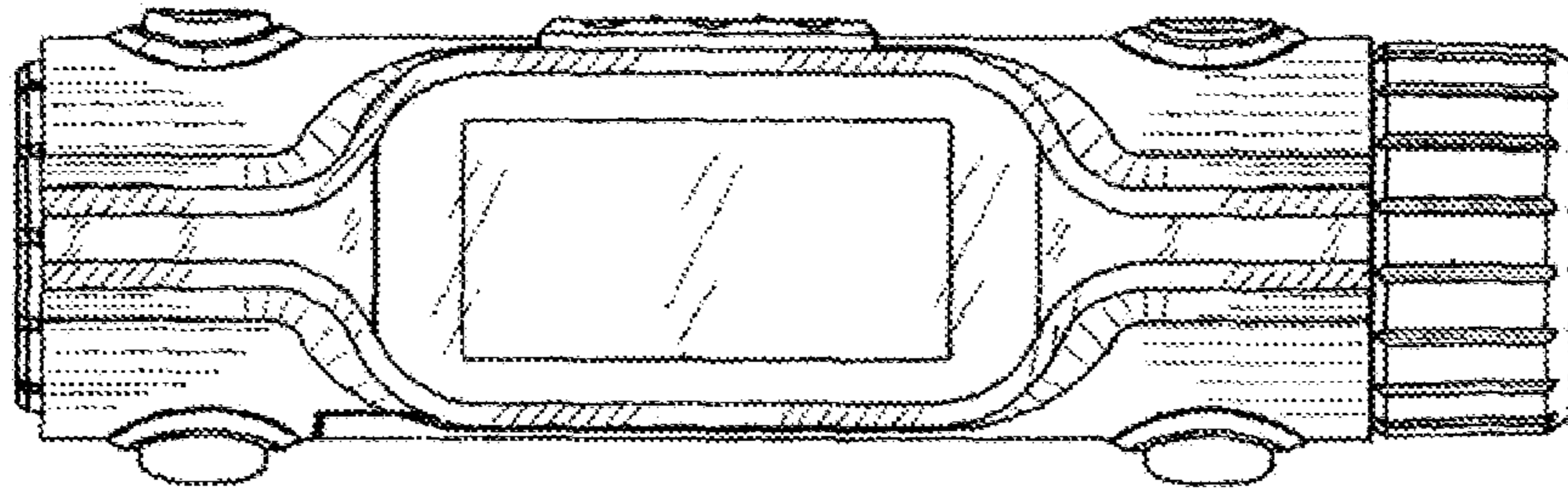


Figure 1

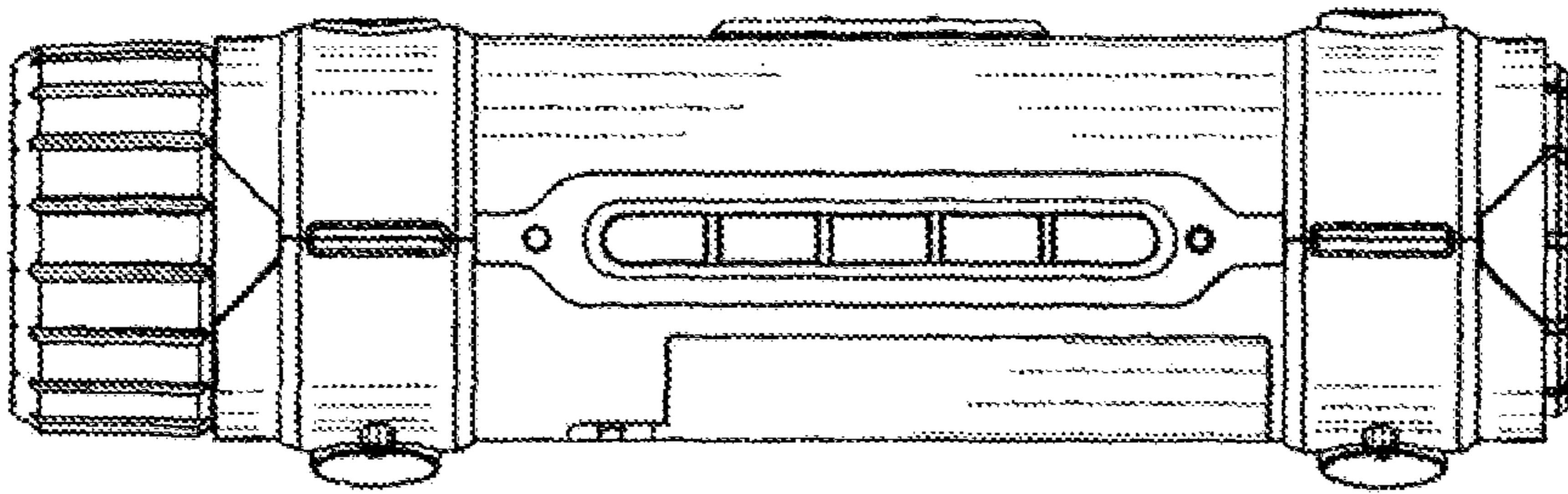


Figure 2

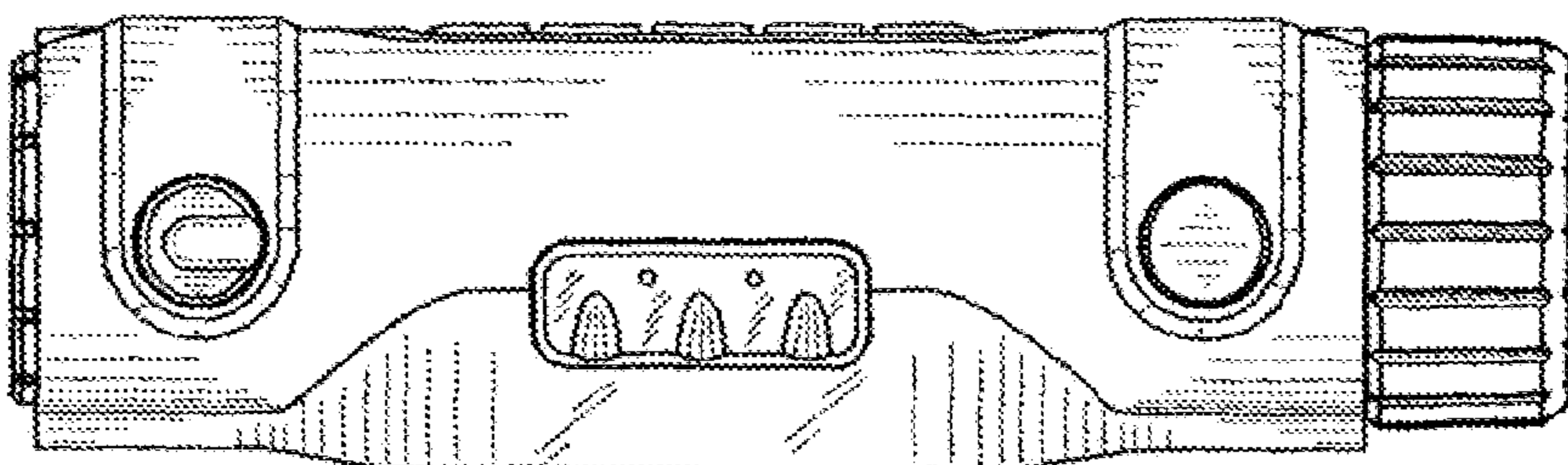


Figure 3

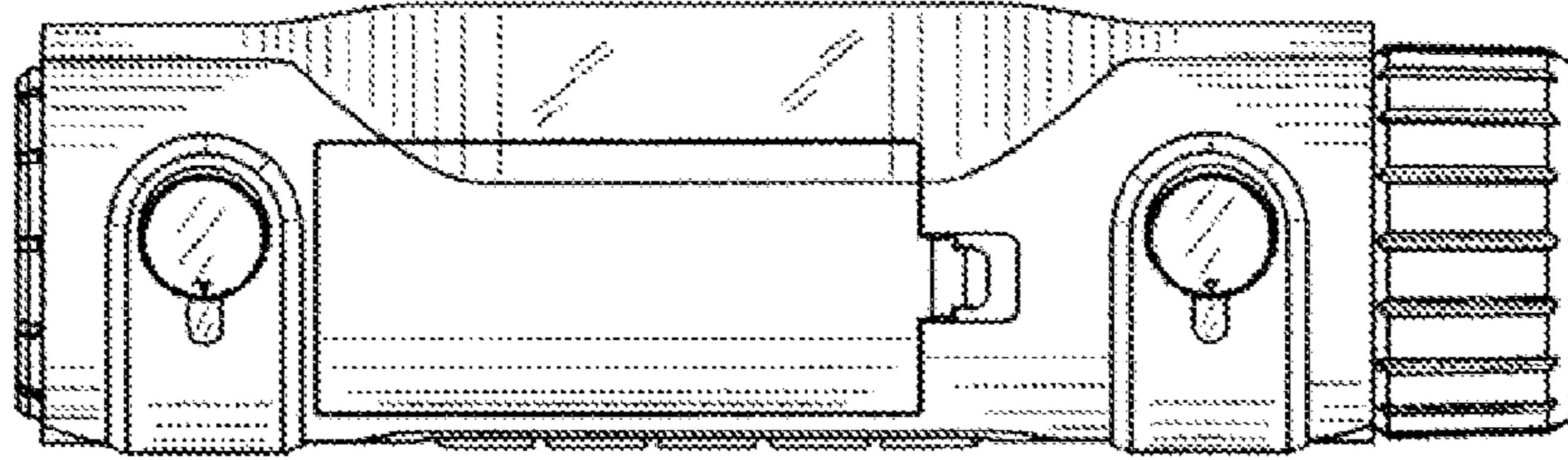


Figure 4

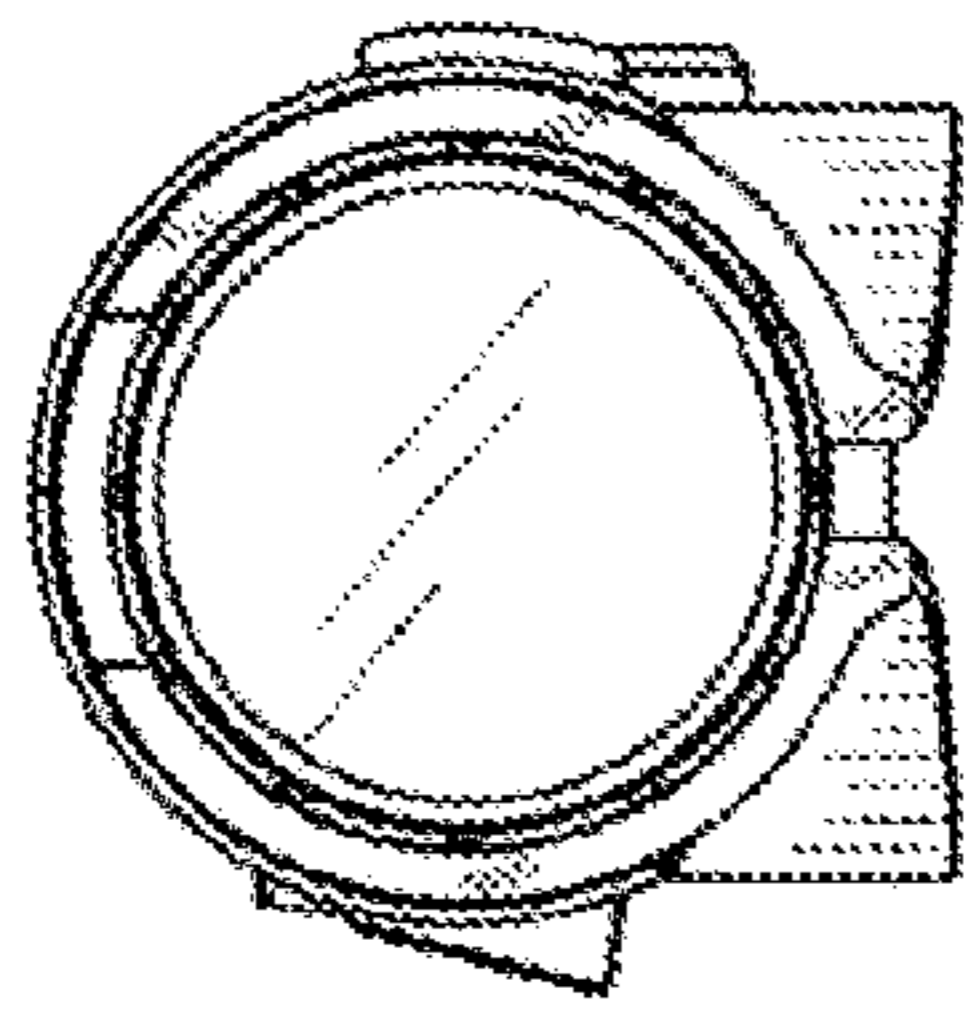


Figure 5

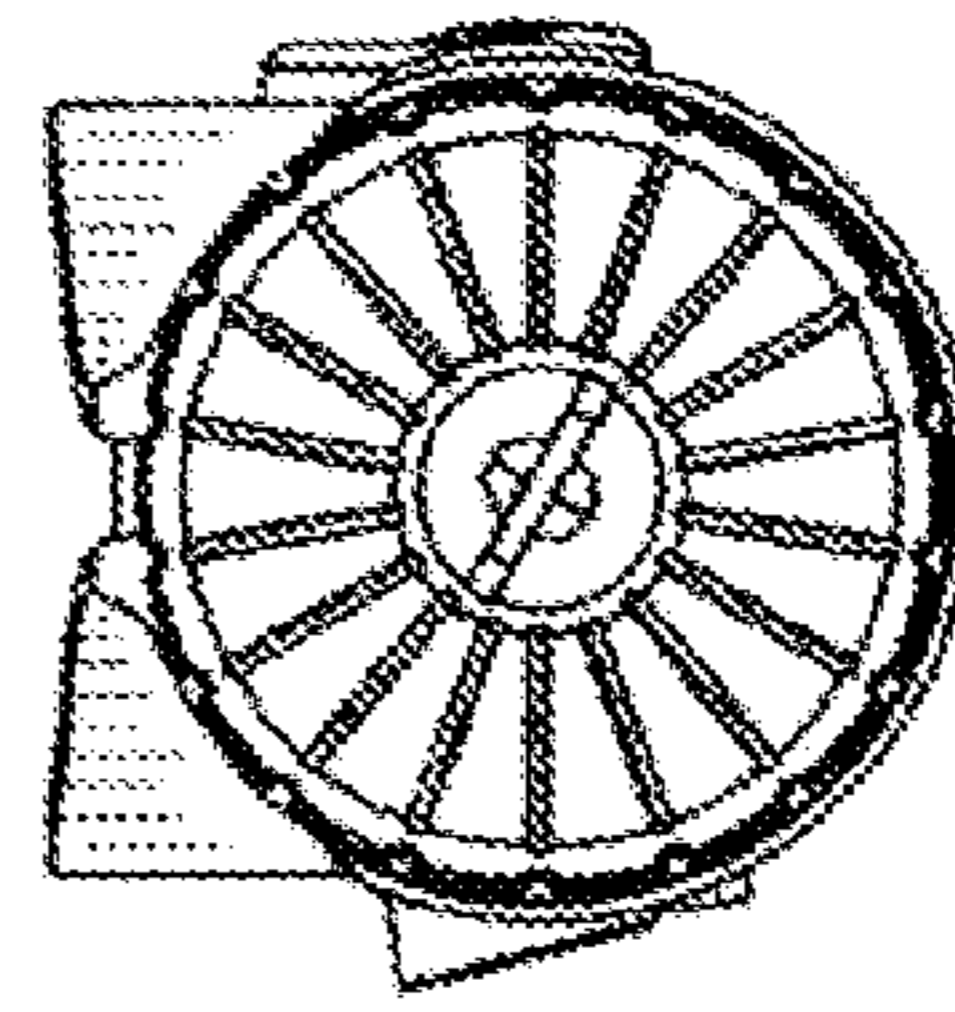


Figure 6

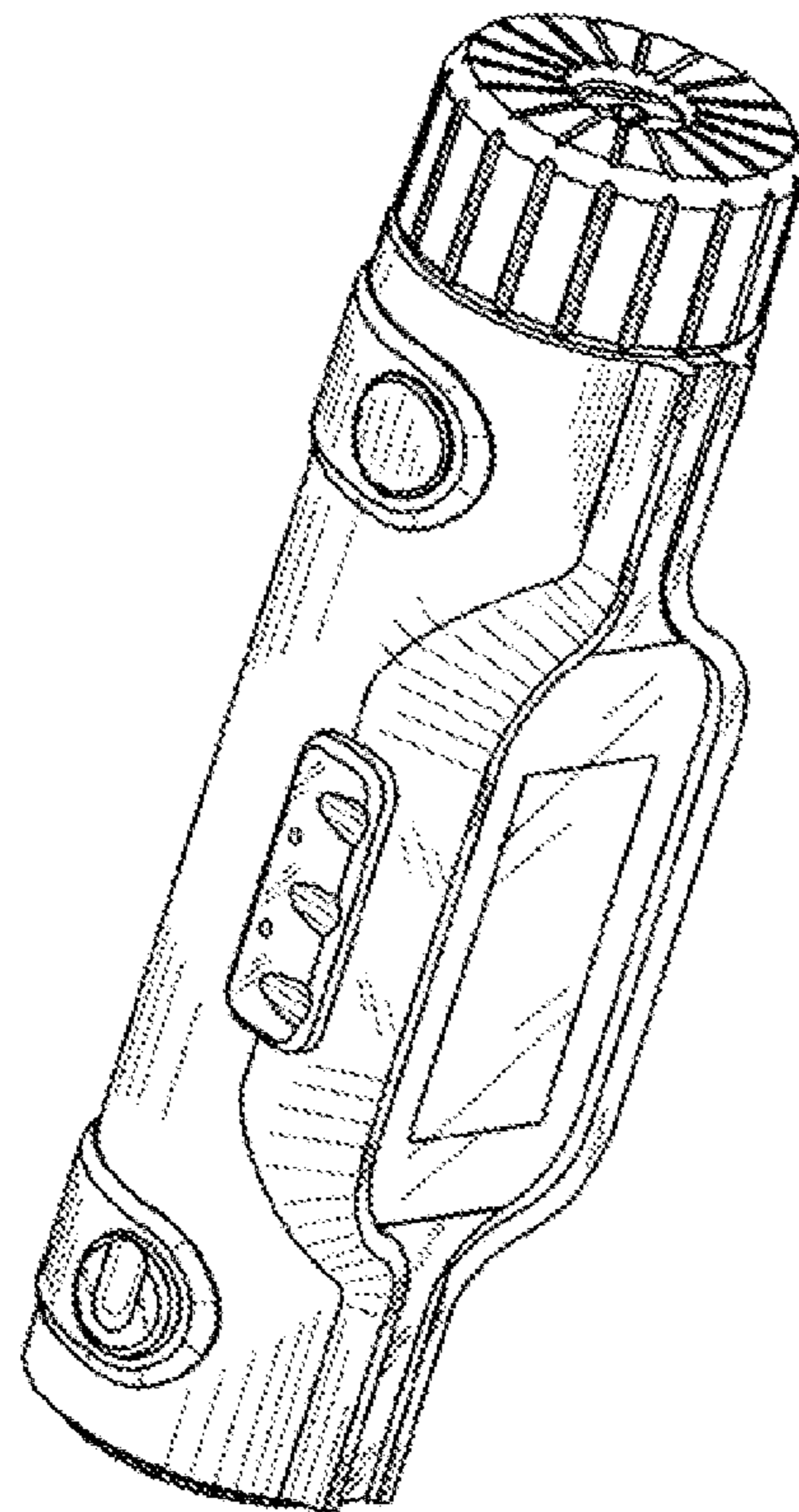


Figure 7