



US00D592146S

(12) **United States Design Patent**
Chiang

(10) **Patent No.:** **US D592,146 S**

(45) **Date of Patent:** **** May 12, 2009**

(54) **I/O PLUG CONNECTOR**

D556,690 S * 12/2007 Ikegami D13/147

(75) Inventor: **Shu Man Chiang**, Tu-Cheng (TW)

* cited by examiner

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(**) Term: **14 Years**

(57) **CLAIM**

(21) Appl. No.: **29/300,258**

The ornamental design for an I/O plug connector, as shown and described.

(22) Filed: **Mar. 6, 2008**

DESCRIPTION

(51) **LOC (9) Cl.** **13-03**

(52) **U.S. Cl.** **D13/147**

(58) **Field of Classification Search** D13/146–147,
D13/154, 184; 439/79, 108, 607–610, 660,
439/668, 680

See application file for complete search history.

FIG. 1 is a perspective view of an I/O plug connector showing my new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a rear elevational view thereof;

FIG. 4 is a left side elevational view thereof;

FIG. 5 is a right side elevational view thereof;

FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom plan view thereof.

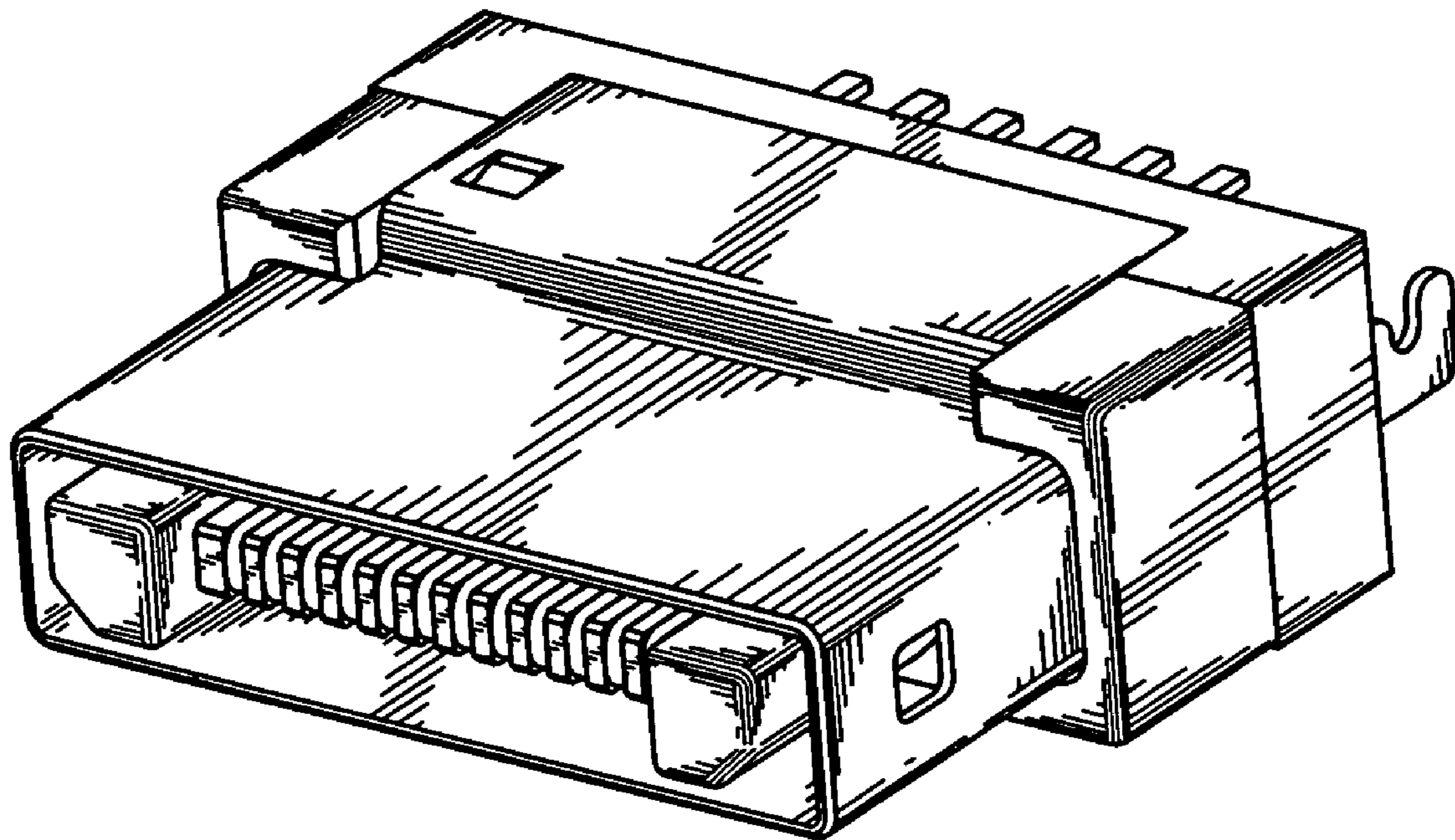
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The broken line portion of the figure drawings is included to show unclaimed subject matter only and forms no part of the claimed design.

1 Claim, 7 Drawing Sheets



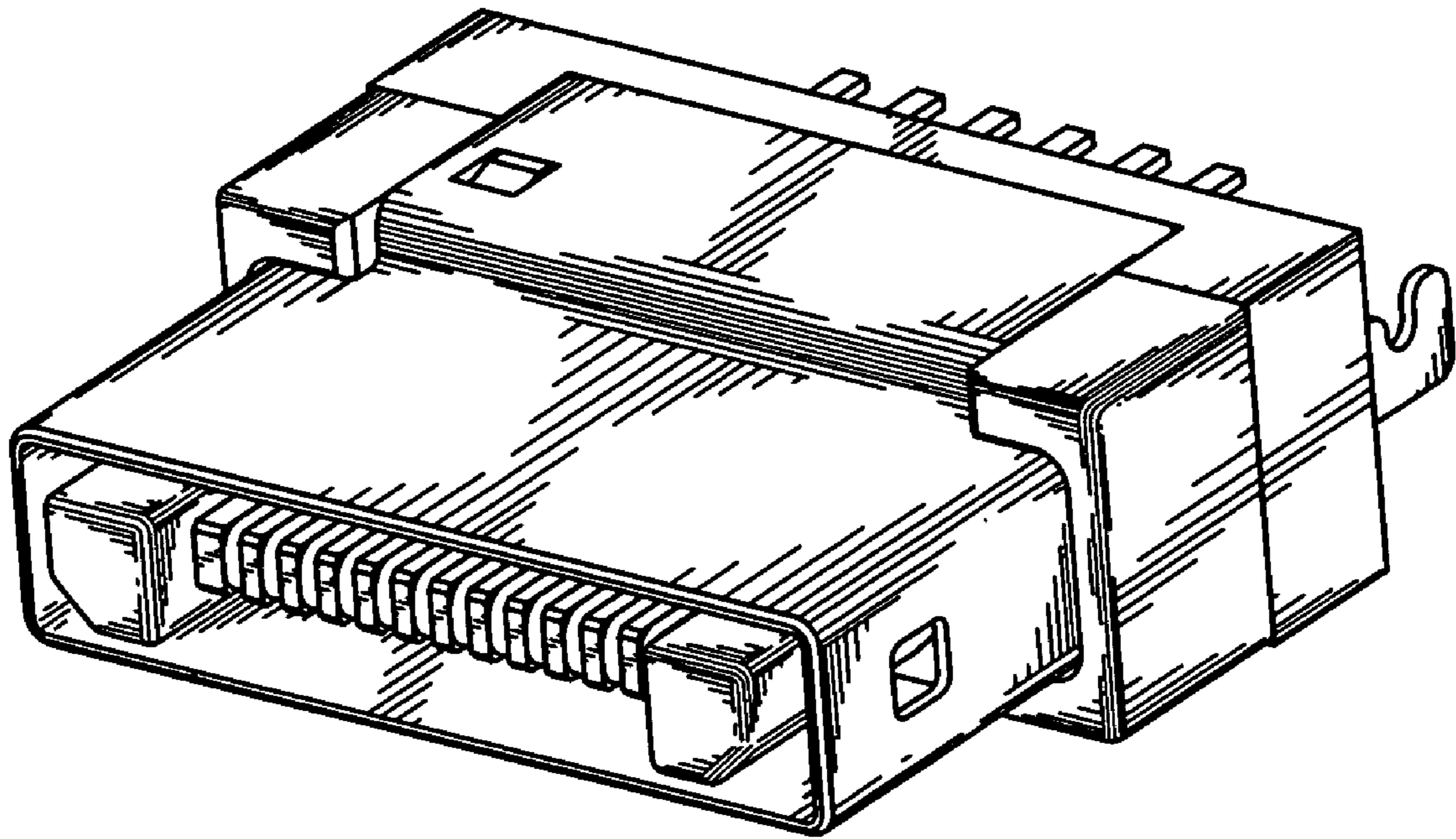


FIG. 1

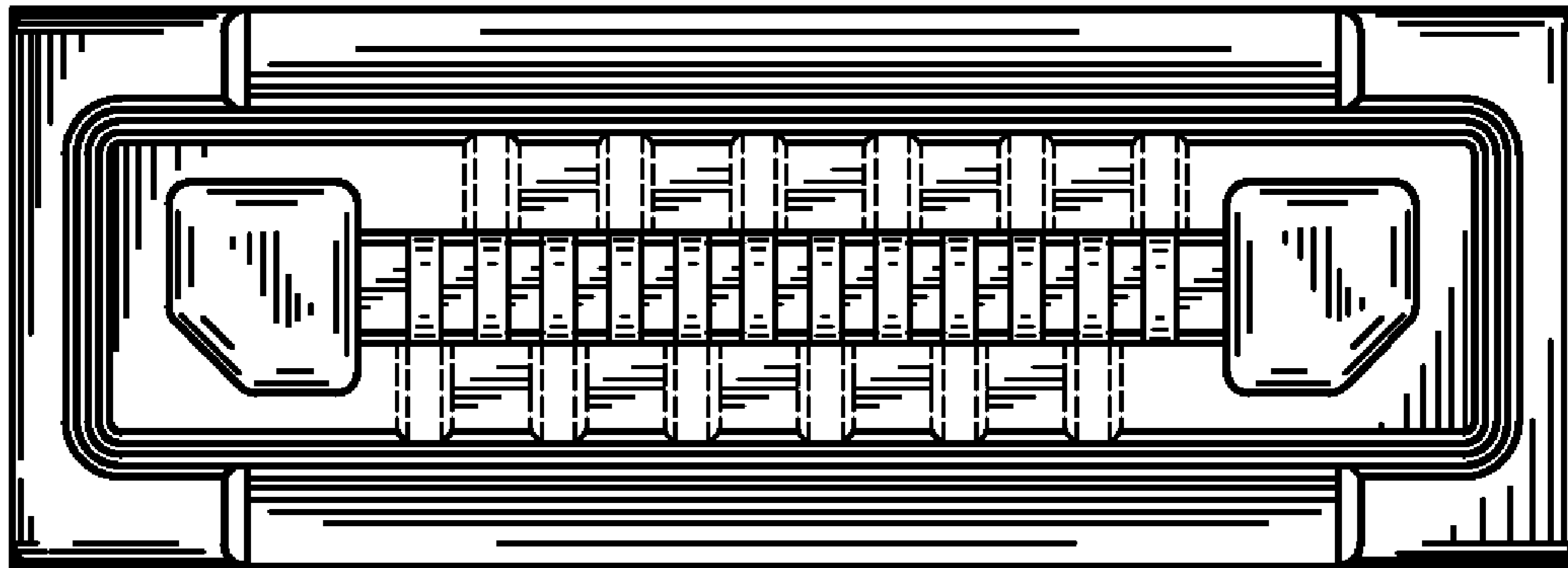


FIG. 2

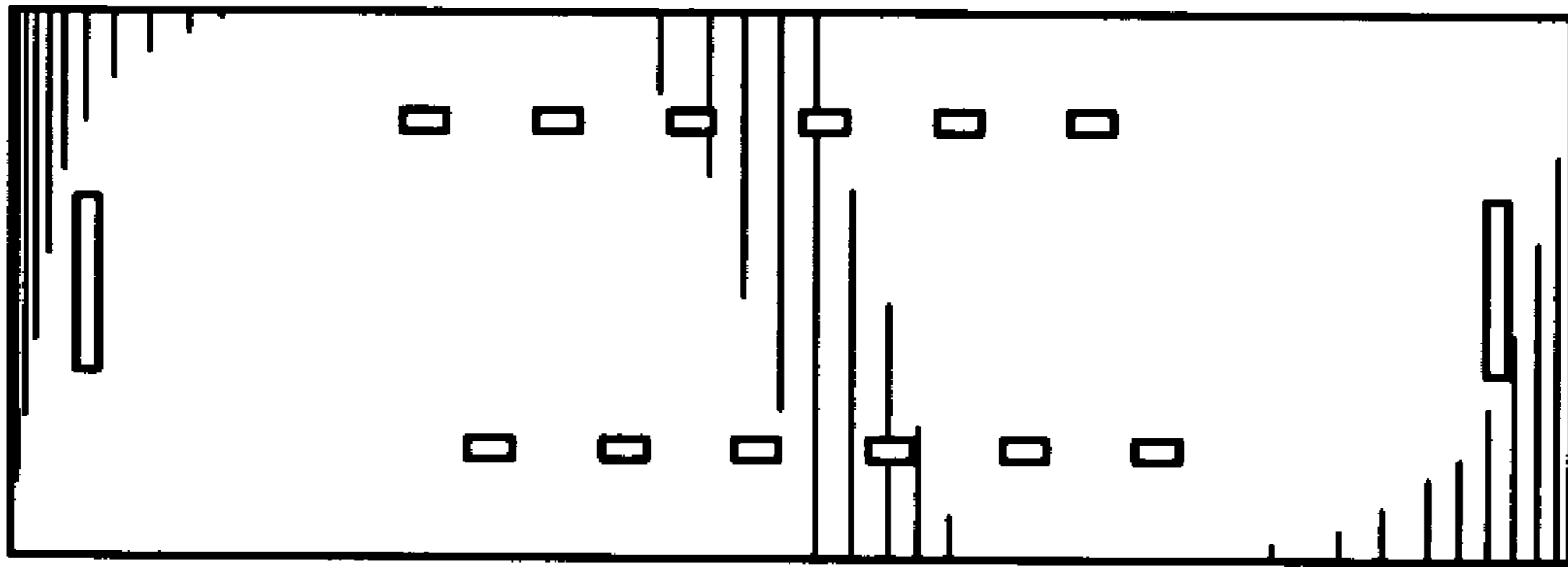


FIG. 3

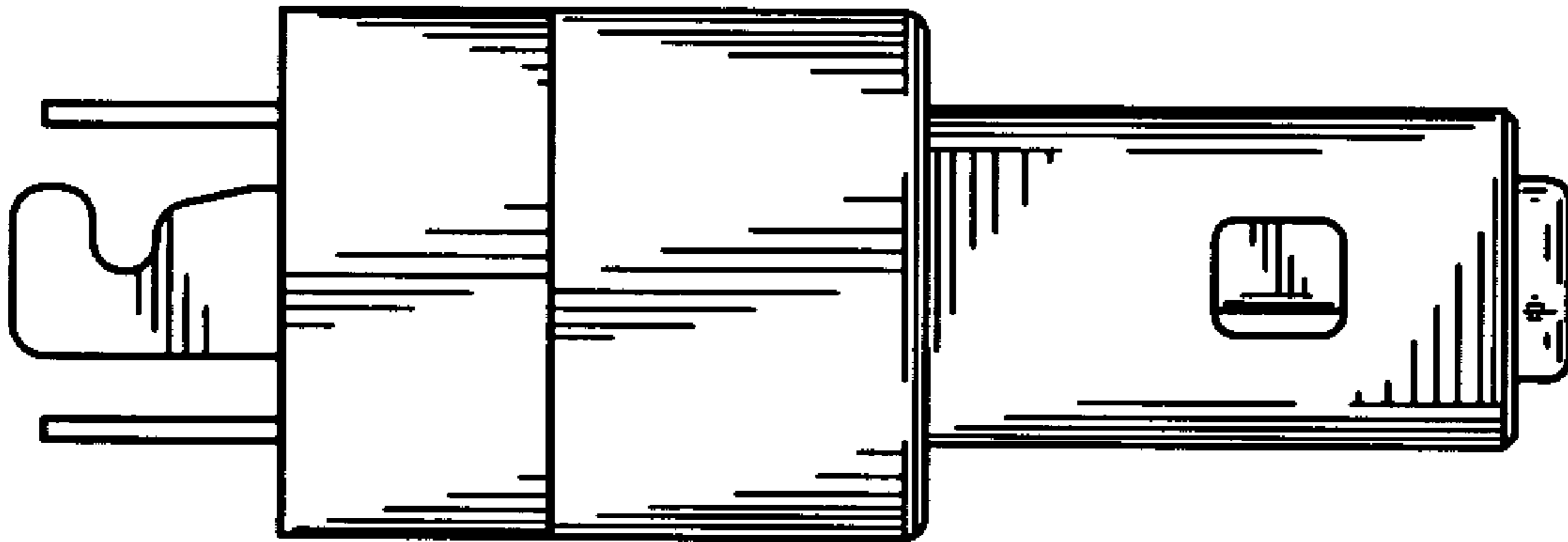


FIG. 4

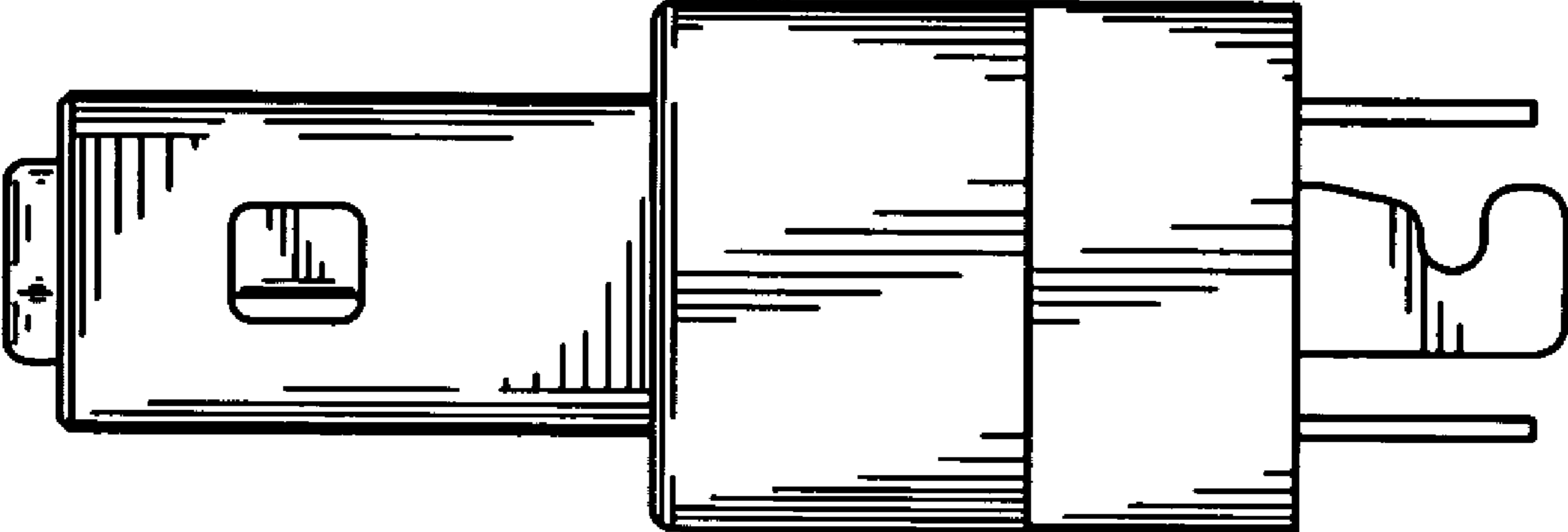


FIG. 5

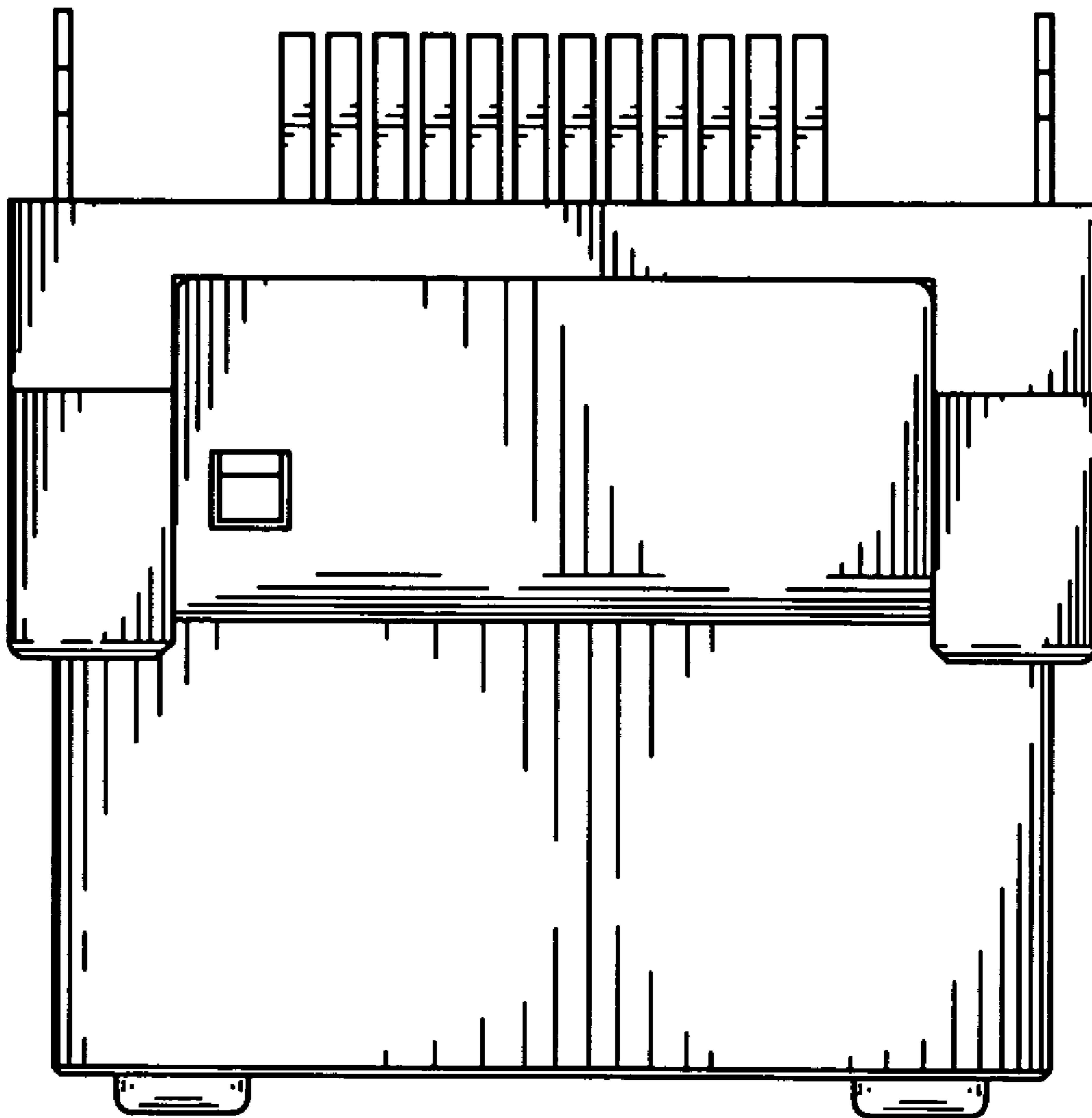


FIG. 6

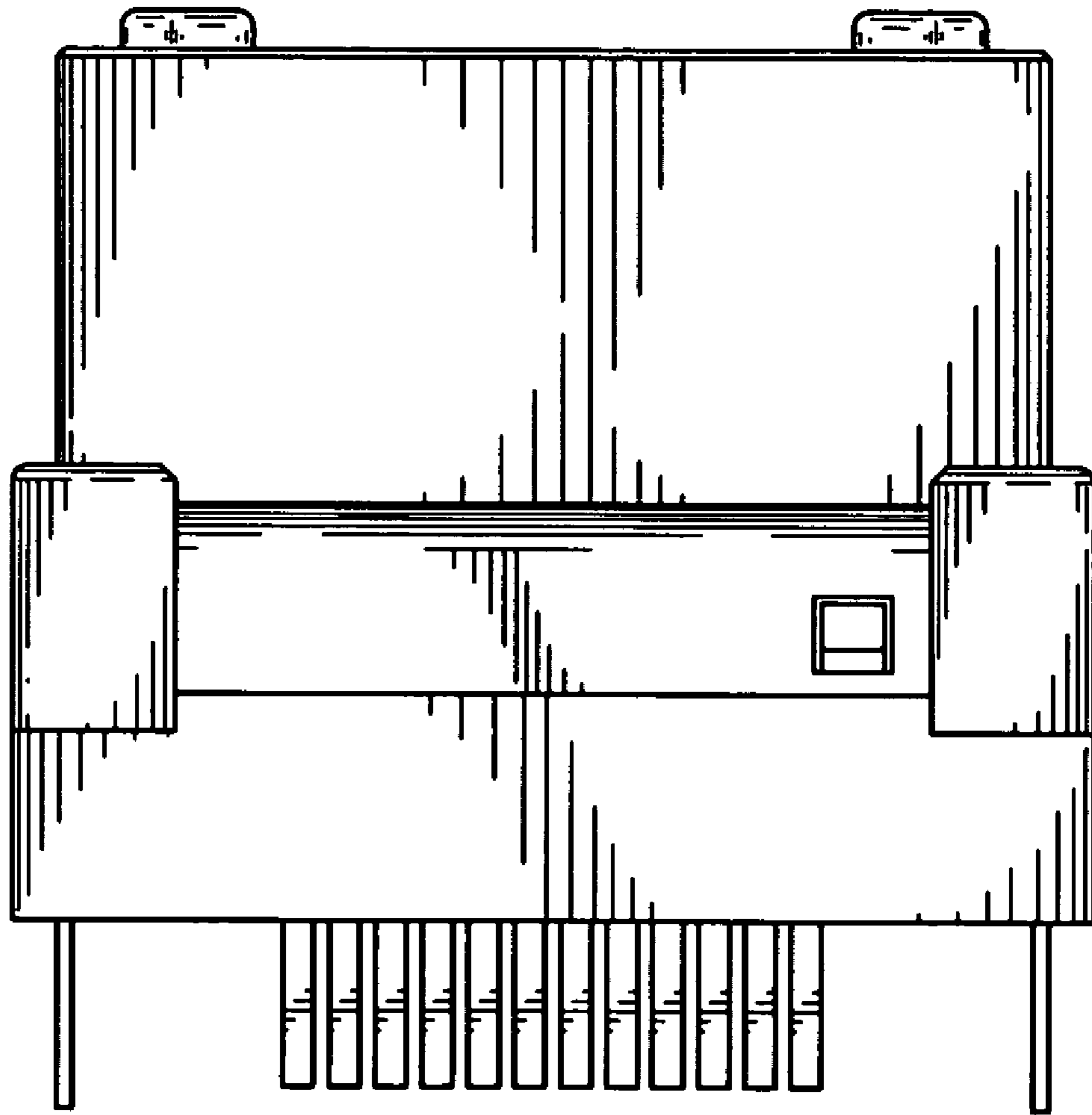


FIG. 7