



US00D591924S

(12) **United States Design Patent**  
**Abe**

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(45) **Date of Patent:** **\*\* May 5, 2009**

(54) **ARM FOR WAFER TRANSPORTATION FOR  
MANUFACTURING SEMICONDUCTOR  
WAFERS**

2006/0169674 A1\* 8/2006 Mao et al. .... 216/88  
2006/0210387 A1\* 9/2006 Saeki et al. .... 414/744.5

\* cited by examiner

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(\*\*) Term: **14 Years**

(57) **CLAIM**

(21) Appl. No.: **29/300,495**

The ornamental design for an arm for wafer transportation for manufacturing semiconductor wafers, as shown and described.

(22) Filed: **Apr. 2, 2008**

(30) **Foreign Application Priority Data**

**DESCRIPTION**

Oct. 3, 2007 (JP) ..... 2007-026880

FIG. 1 is a top view of the design for a arm for wafer transportation for manufacturing semiconductor wafers in accordance with the invention;

(51) **LOC (9) Cl.** ..... **12-05**

(52) **U.S. Cl.** ..... **D34/35**

FIG. 2 is a bottom view thereof;

(58) **Field of Classification Search** ..... D34/29,

FIG. 3 is a right side view thereof;

D34/35; 198/771, 866, 890, 367, 890.1,

FIG. 4 is a left side view thereof;

198/370.1, 370.4, 782, 722; 193/35 A, 35 C;

FIG. 5 is a front view thereof;

294/1.1; 414/744.5, 754; D8/366, 371, 499

FIG. 6 is a rear view thereof;

See application file for complete search history.

FIG. 7 is a magnified, rear, sectional view between lines A—A' of FIG. 2, along line D—D of FIG. 2; and,

(56) **References Cited**

**U.S. PATENT DOCUMENTS**

6,116,848 A \* 9/2000 Thomas et al. .... 414/754

D525,408 S \* 7/2006 Nitadori ..... D34/35

7,290,813 B2 \* 11/2007 Bonora et al. .... 294/1.1

D562,524 S \* 2/2008 Hiroki et al. .... 34/28

2003/0035711 A1 \* 2/2003 Gilchrist ..... 414/744.5

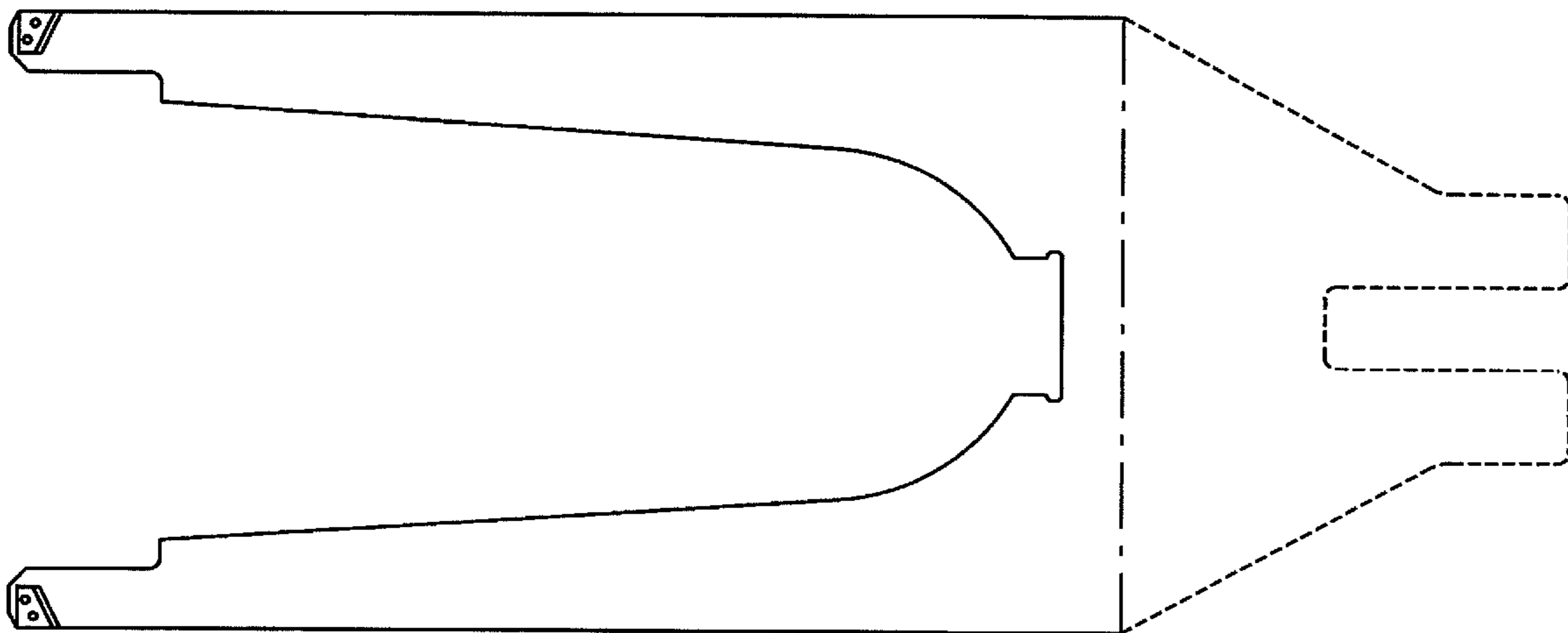
2004/0048474 A1 \* 3/2004 Asano ..... 438/689

2006/0166503 A1 \* 7/2006 Sasaki et al. .... 438/692

FIG. 8 is a magnified, rear, sectional view between lines B—B' of FIG. 2, along line D—D of FIG. 2.

The broken line showing in the figures is included for the purpose of illustrating environment and forms no part of the claimed design.

**1 Claim, 8 Drawing Sheets**



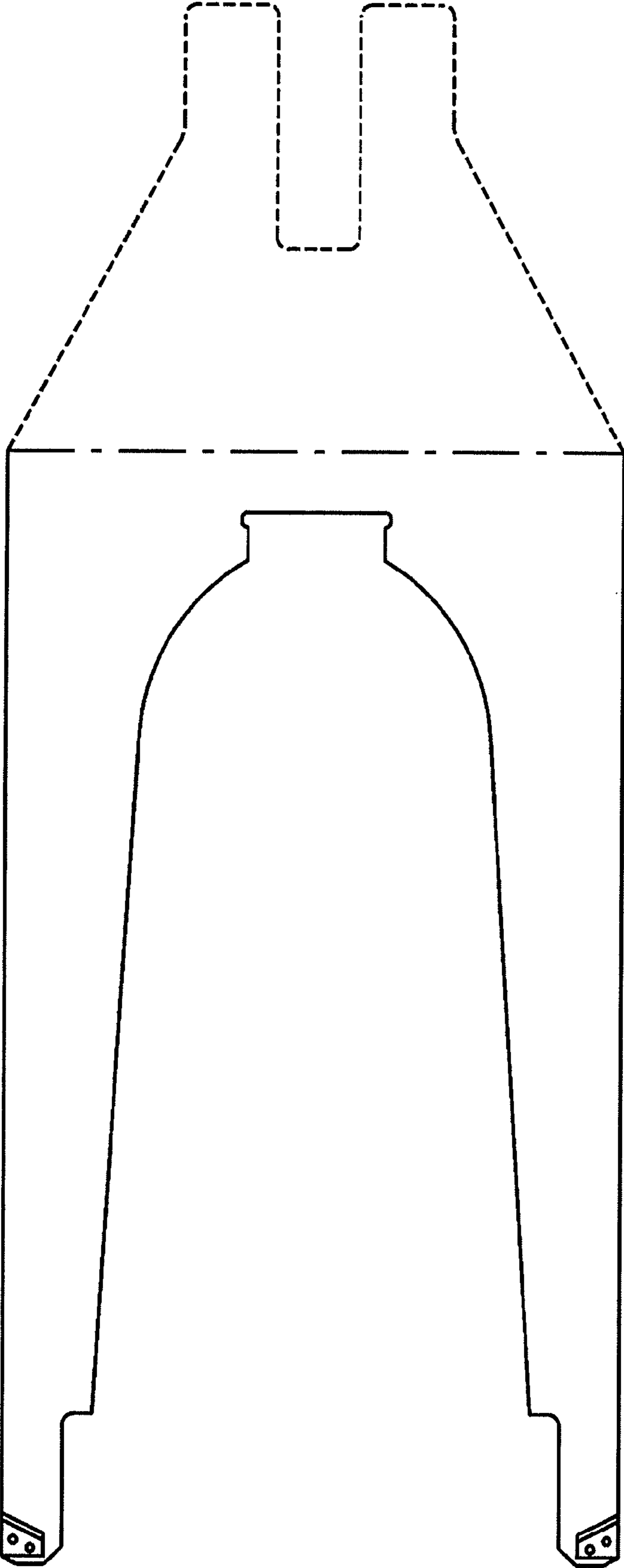


Fig.1

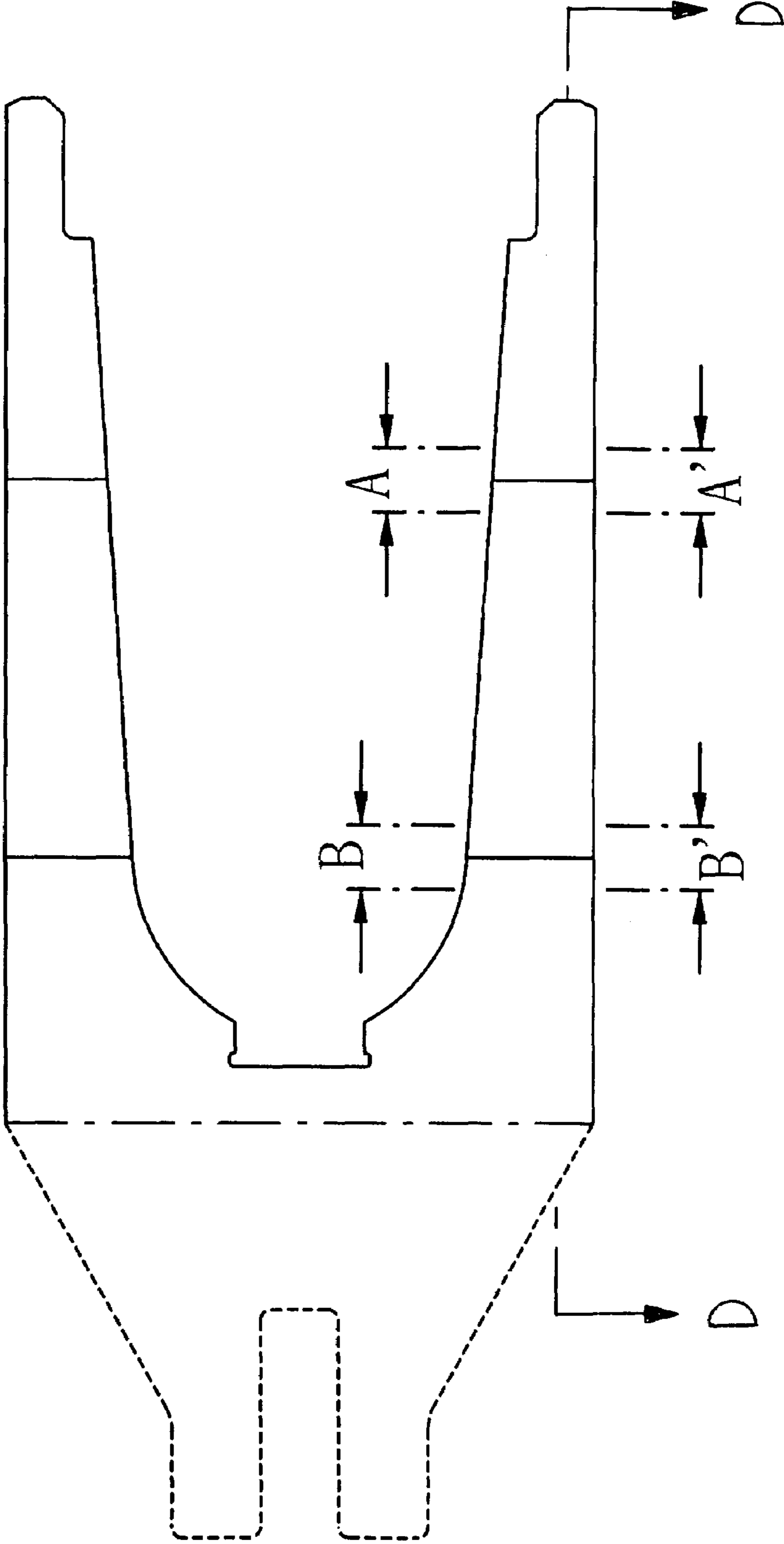


Fig. 2

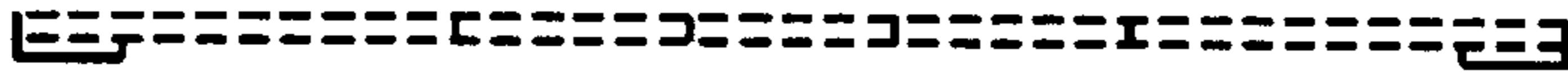


Fig. 3

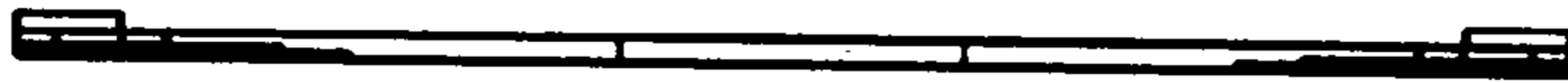


Fig. 4



Fig. 5

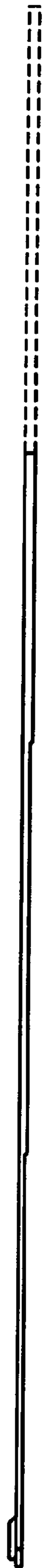


Fig. 6

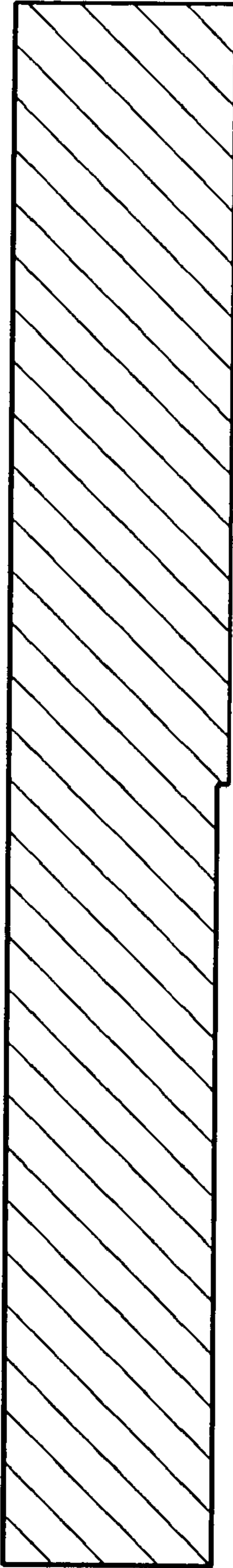


Fig. 7



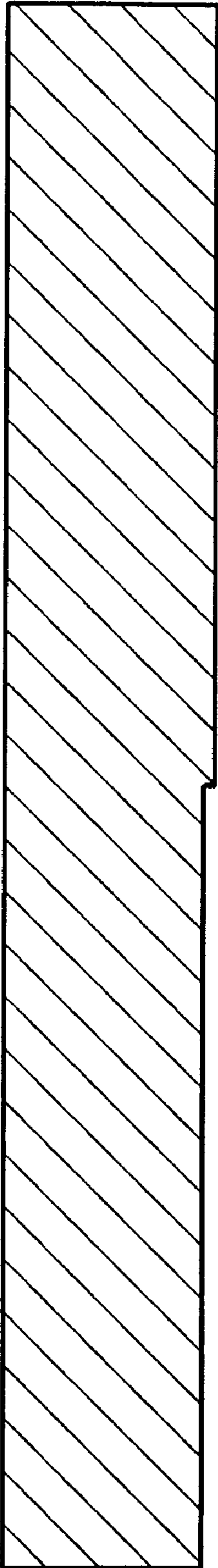


Fig. 8