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(12) **United States Design Patent**
Soyano et al.

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- (54) **SEMICONDUCTOR DEVICE**
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- (73) Assignee: **Fuji Electric Device Technology Co., Ltd.**, Tokyo (JP)
- (**) Term: **14 Years**
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- (22) Filed: **Jun. 18, 2008**
- (30) **Foreign Application Priority Data**
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- (51) **LOC (9) Cl.** **13-03**
- (52) **U.S. Cl.** **D13/182**
- (58) **Field of Classification Search** D13/110,
D13/182; 257/690; 324/754, 755, 765; 361/713,
361/728, 736, 775
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for semiconductor device, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of the front, right and bottom of semiconductor device showing our new design;

FIG. 2 is a front view thereof;

FIG. 3 is a rear view thereof;

FIG. 4 is a left side view thereof;

FIG. 5 is a right side view thereof;

FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom view thereof.

1 Claim, 3 Drawing Sheets

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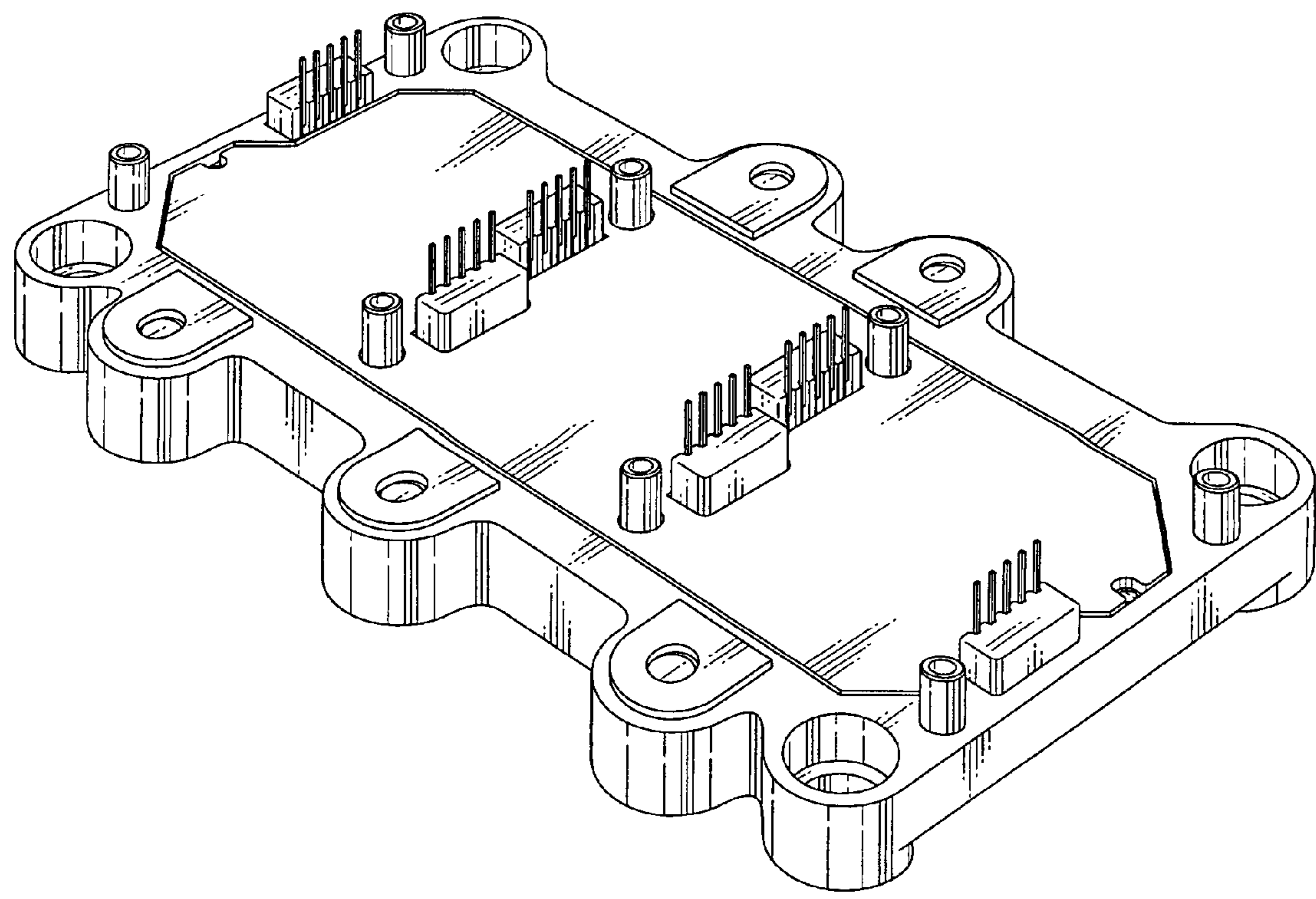


FIG.1

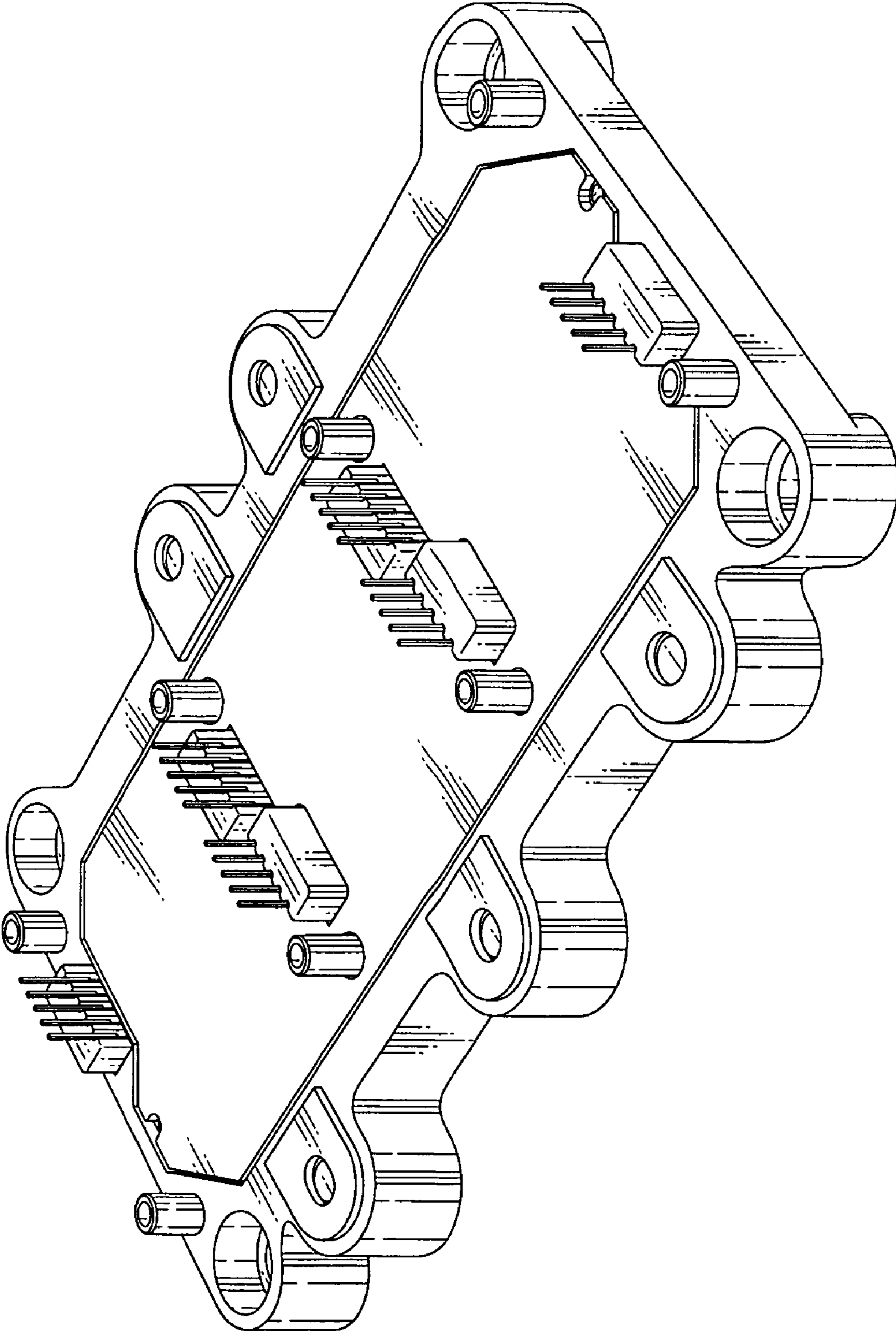


FIG.2

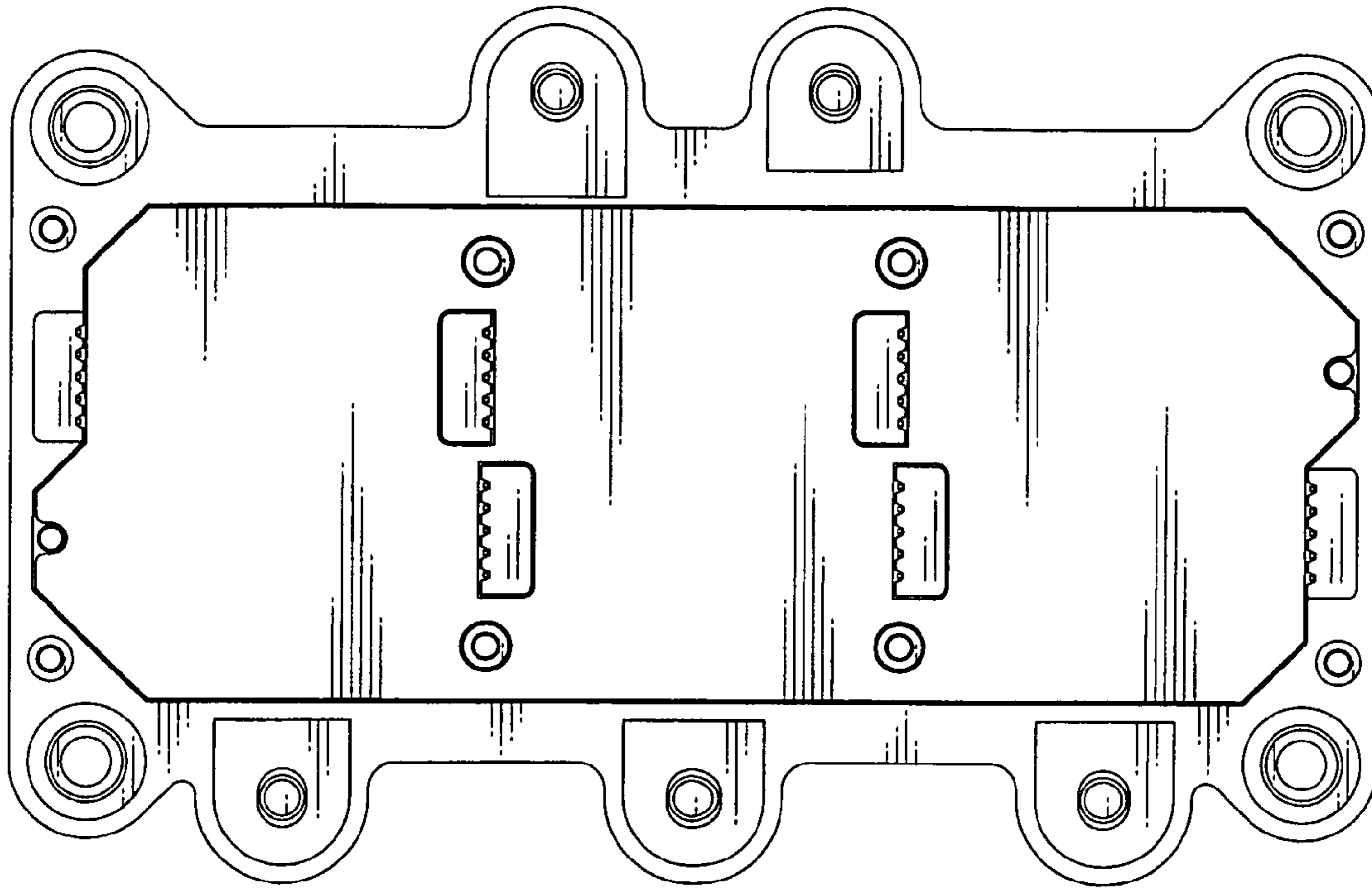


FIG.3

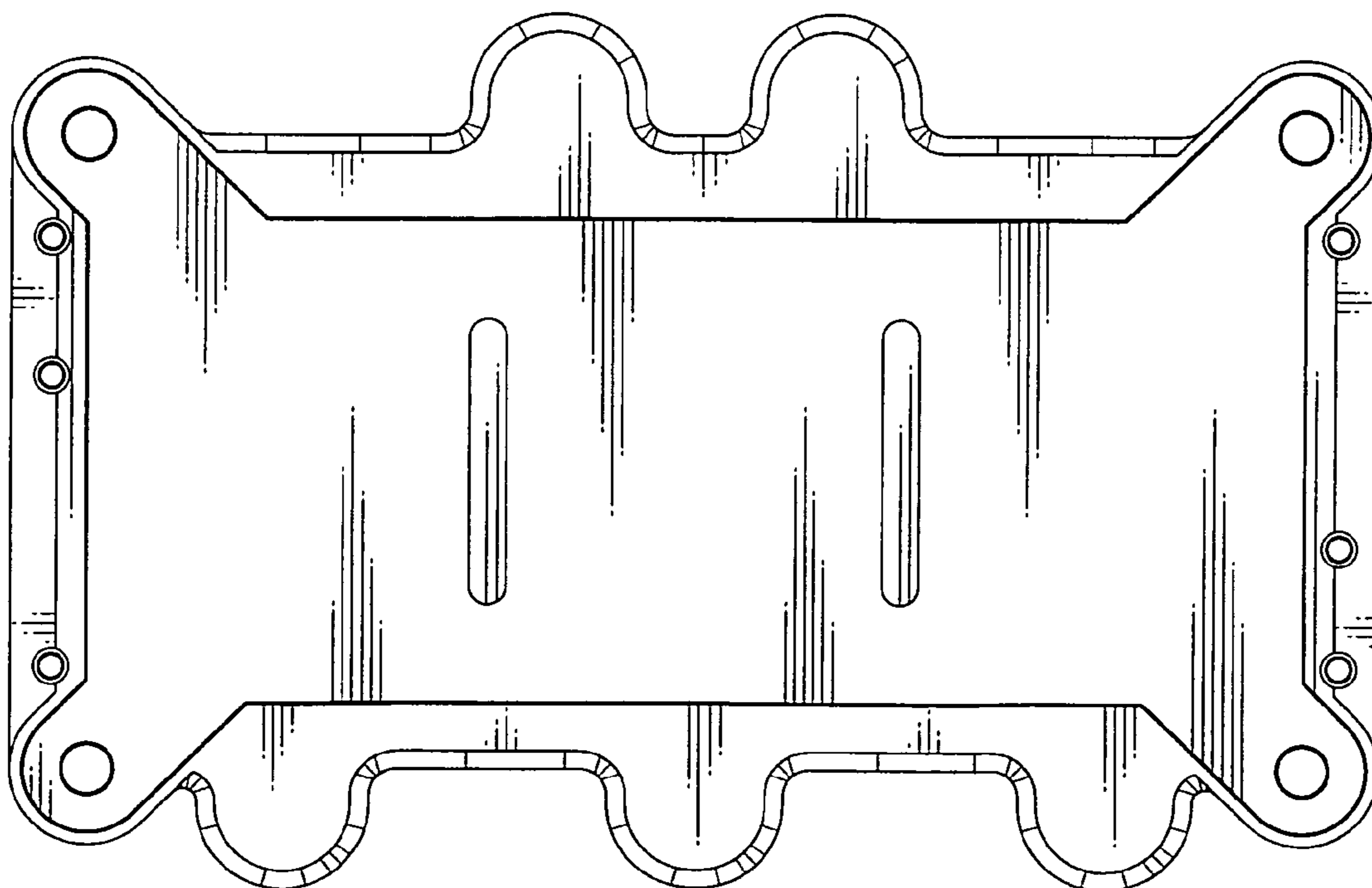


FIG.4

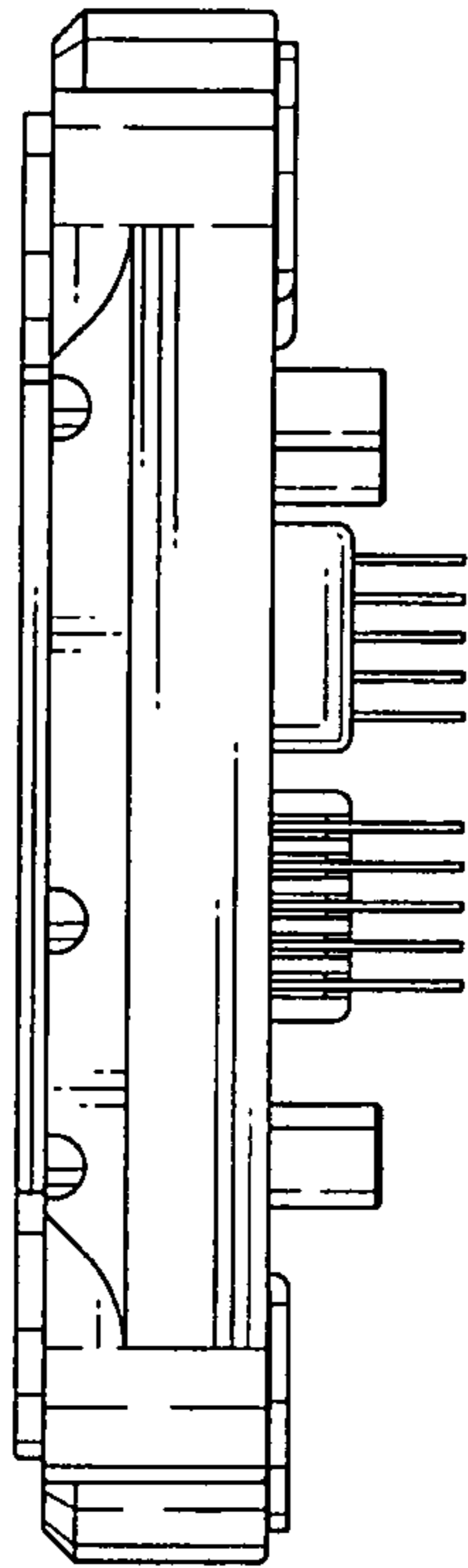


FIG.5

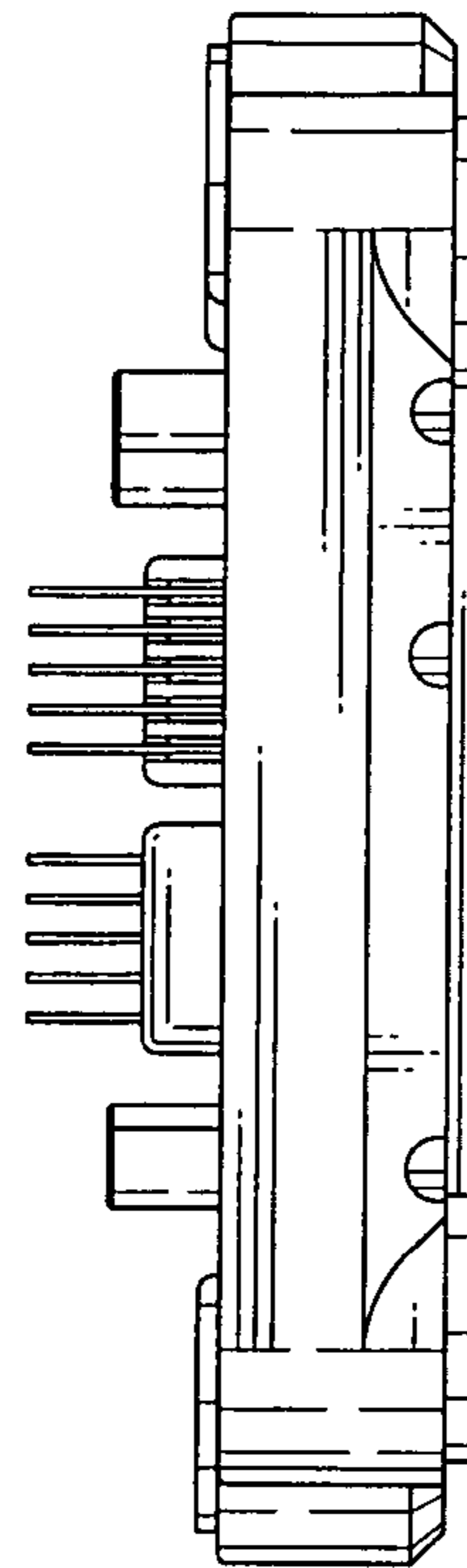


FIG.6

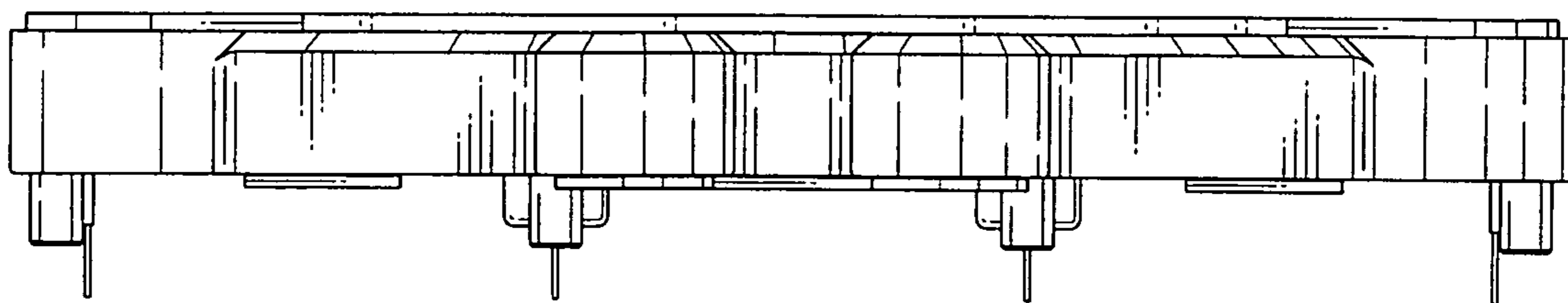


FIG.7

