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Horikoshi et al.

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(54) **ELECTRICAL DOUBLE LAYER CAPACITOR**

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(**) Term: **14 Years**

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(30) **Foreign Application Priority Data**

Oct. 19, 2007 (JP) 2007-028804

(51) **LOC (9) Cl.** **13-03**

(52) **U.S. Cl.** **D13/125**

(58) **Field of Classification Search** D13/118,
D13/123–125, 133, 184, 199; 320/2; 361/523,
361/535, 536, 538, 539, 540, 271, 277, 301.1,
361/328–330, 763; 429/178

See application file for complete search history.

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(57) **CLAIM**

We claim the ornamental design for an electrical double layer capacitor, as shown and described.

DESCRIPTION

FIG. 1 is a front upper right perspective view of an electrical double layer capacitor showing our new design;

FIG. 2 is a front lower left perspective view thereof;

FIG. 3 is a front elevation view thereof; the rear elevation view being a mirror image thereto;

FIG. 4 is a right side elevation view thereof;

FIG. 5 is a left side elevation view thereof;

FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom plan view thereof.

1 Claim, 5 Drawing Sheets

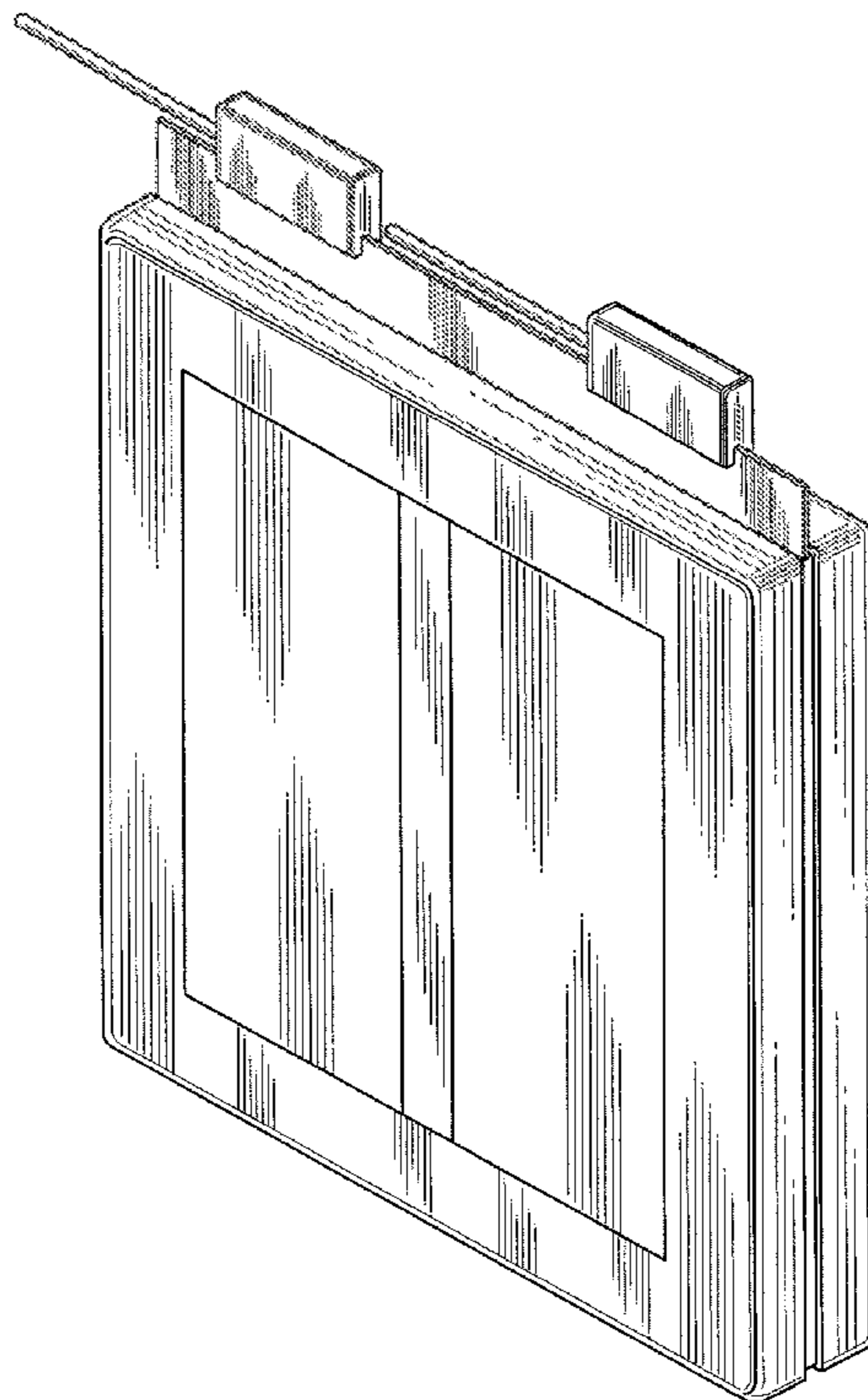


FIG. 1

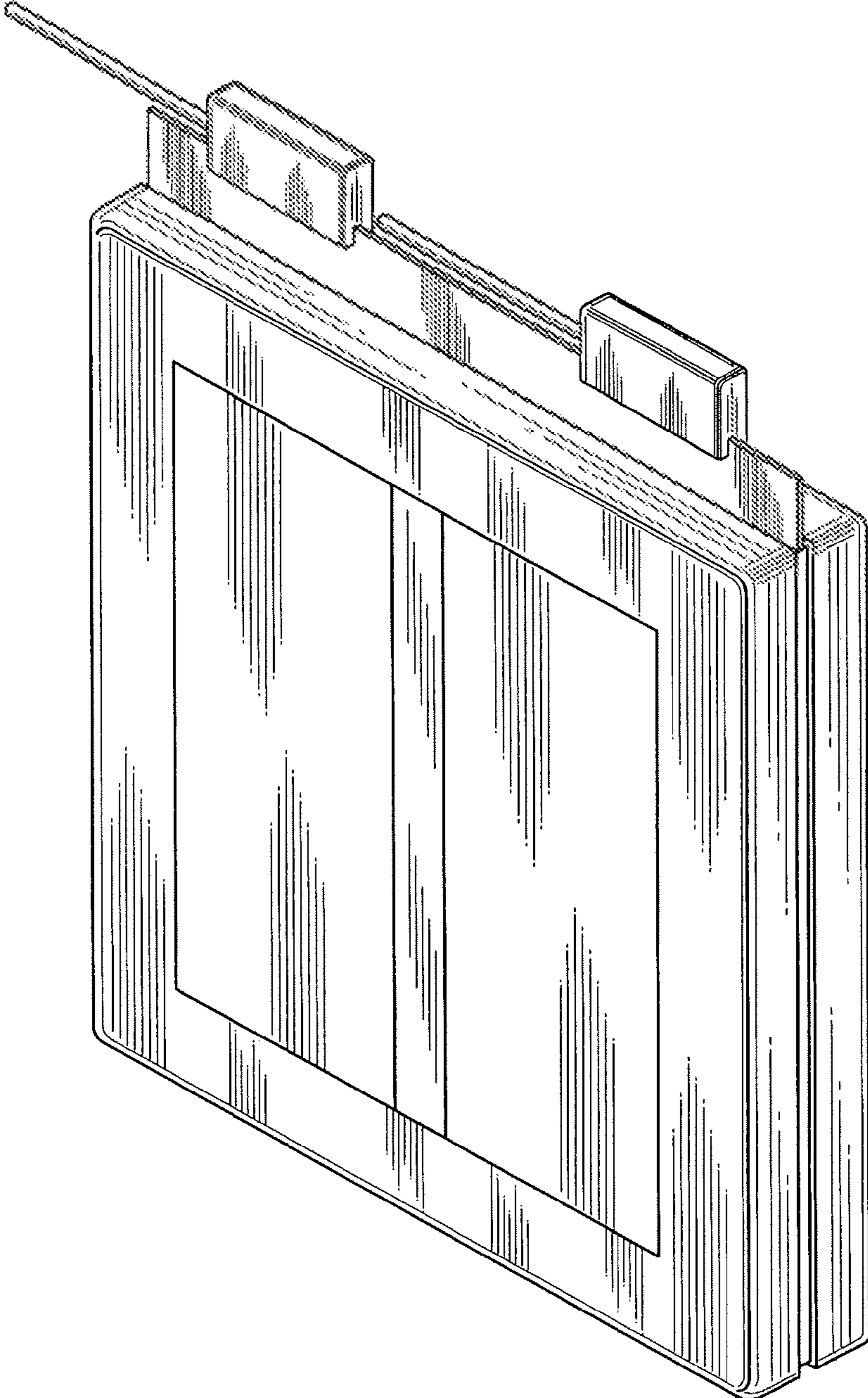


FIG.2

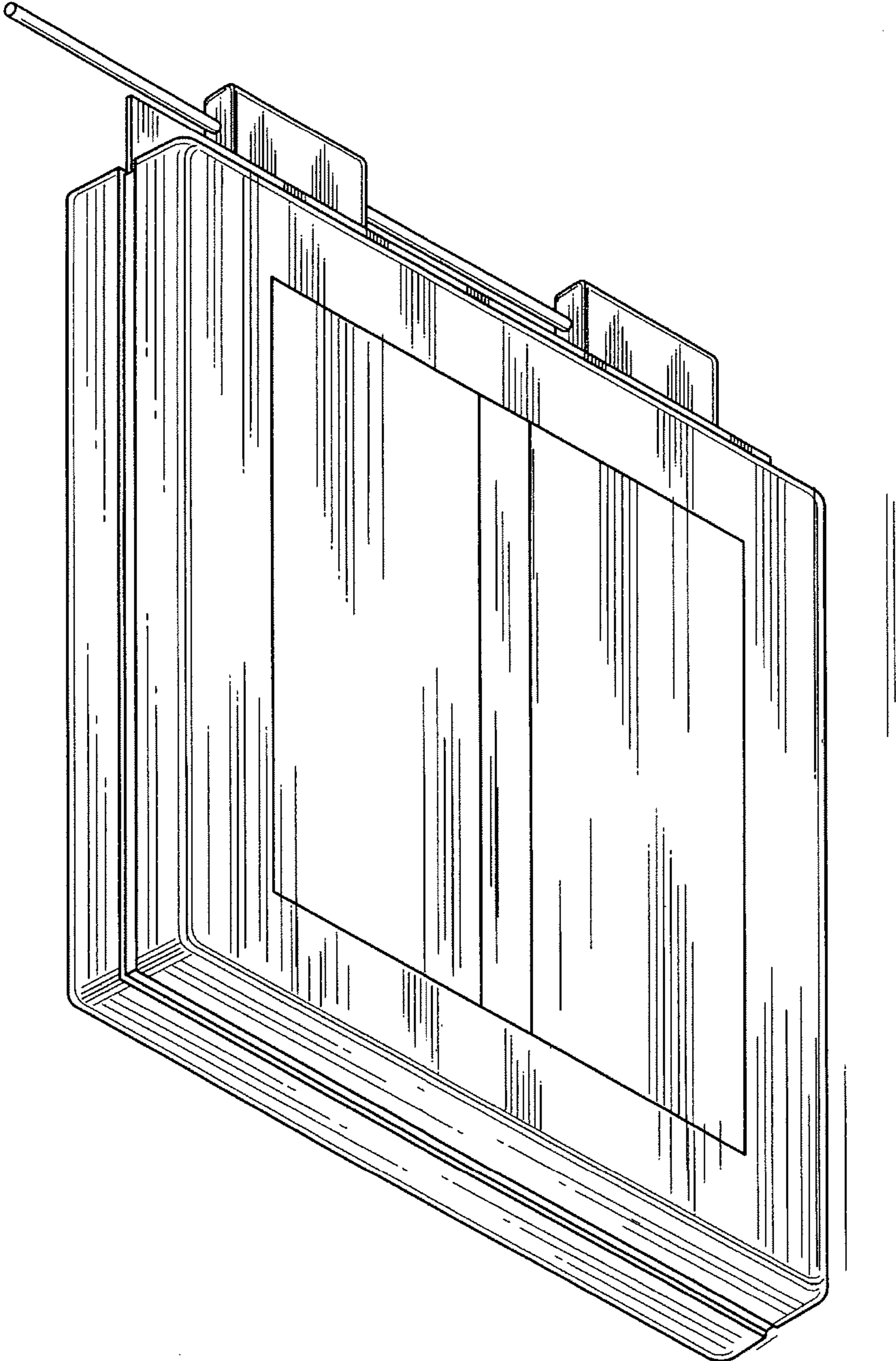


FIG.3

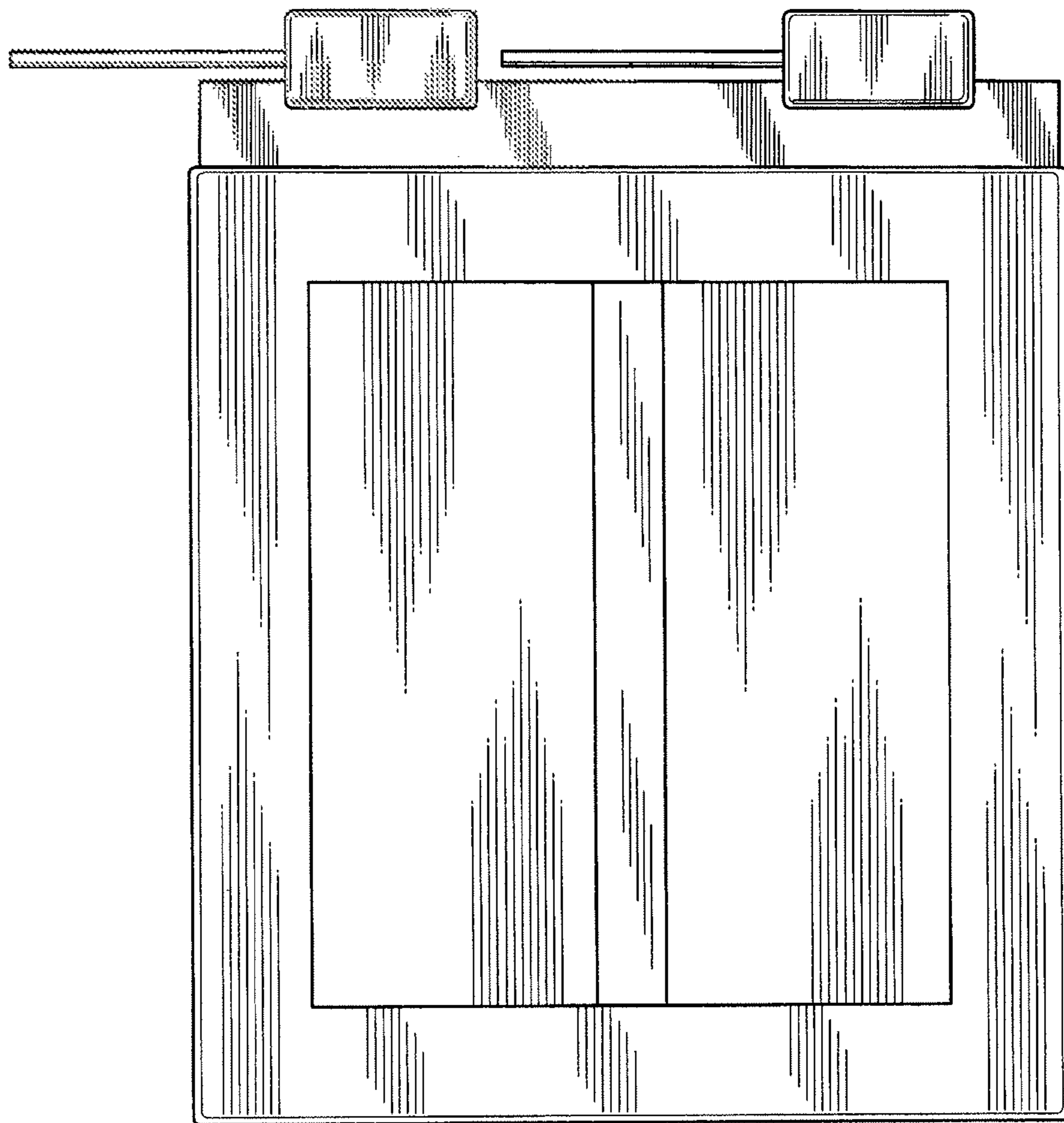


FIG.5

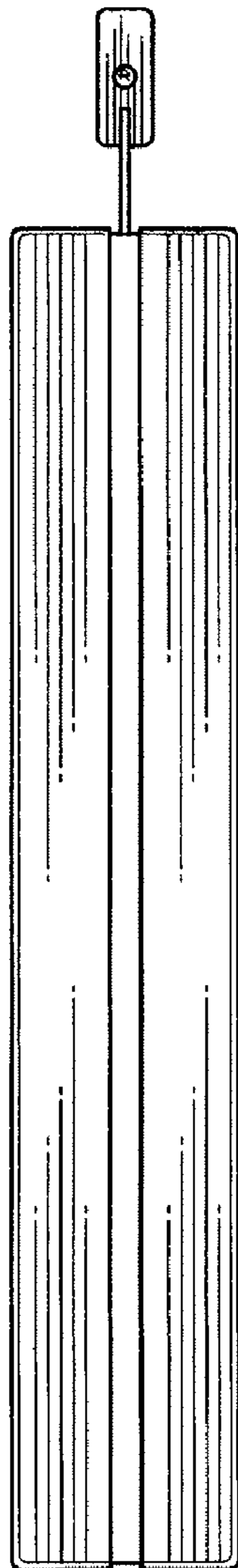


FIG.4

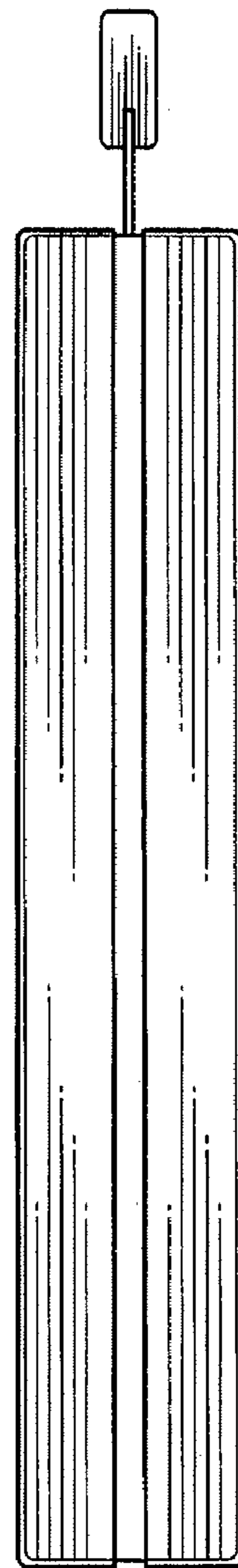


FIG. 6

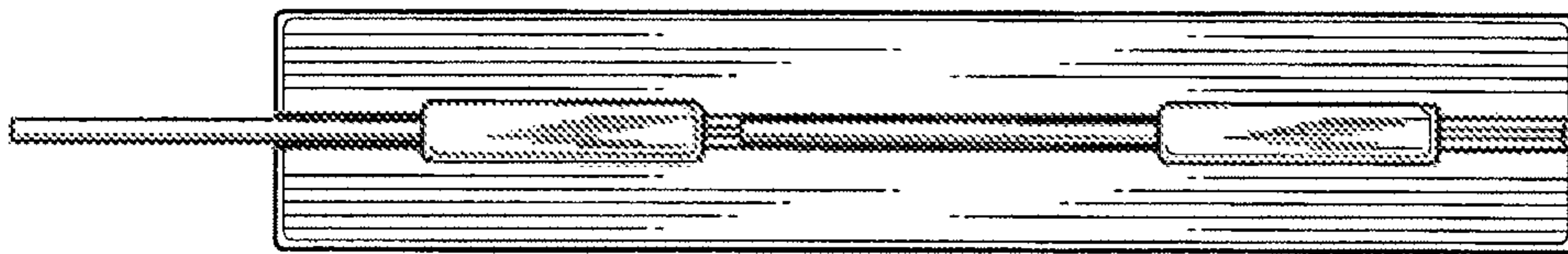


FIG. 7

