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(12) **United States Design Patent**  
**Mine et al.**

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(45) **Date of Patent:** **\*\* Sep. 2, 2008**

(54) **ARITHMETIC AND CONTROL UNIT FOR  
COMPUTER**

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(73) Assignee: **NEC Corporation**, Tokyo (JP)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/291,954**

(22) Filed: **Sep. 21, 2007**

(30) **Foreign Application Priority Data**

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(51) **LOC (8) Cl.** ..... **14-02**

(52) **U.S. Cl.** ..... **D14/356**

(58) **Field of Classification Search** ..... D14/351-370,  
D14/137, 363, 230-231, 233, 235, 237, 240-242,  
D14/432, 299, 155, 496, 300-301, 313-314,  
D14/214, 348-349, 435-436, 167-168; D13/149,  
D13/162, 184, 199; D10/65, 75, 78; D3/201,  
D3/273; 312/223.1, 223.2; 369/34.01, 36.01,  
369/272.1; 379/93.01; 720/613; 711/100,  
711/115; 361/685; 360/132-133, 135  
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for the arithmetic and control unit for computer, as shown and described.

**DESCRIPTION**

FIG. 1 is a top perspective view of the arithmetic and control unit for computer showing our new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a rear elevational view thereof;

FIG. 4 is a side elevational view of one side thereof;

FIG. 5 is a side elevational view of the other side thereof;

FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom plan view thereof.

The broken lines showing of ports in FIGS. 1-3 are included for the purpose of illustrating unclaimed portions and form no part of the claimed design.

**1 Claim, 3 Drawing Sheets**

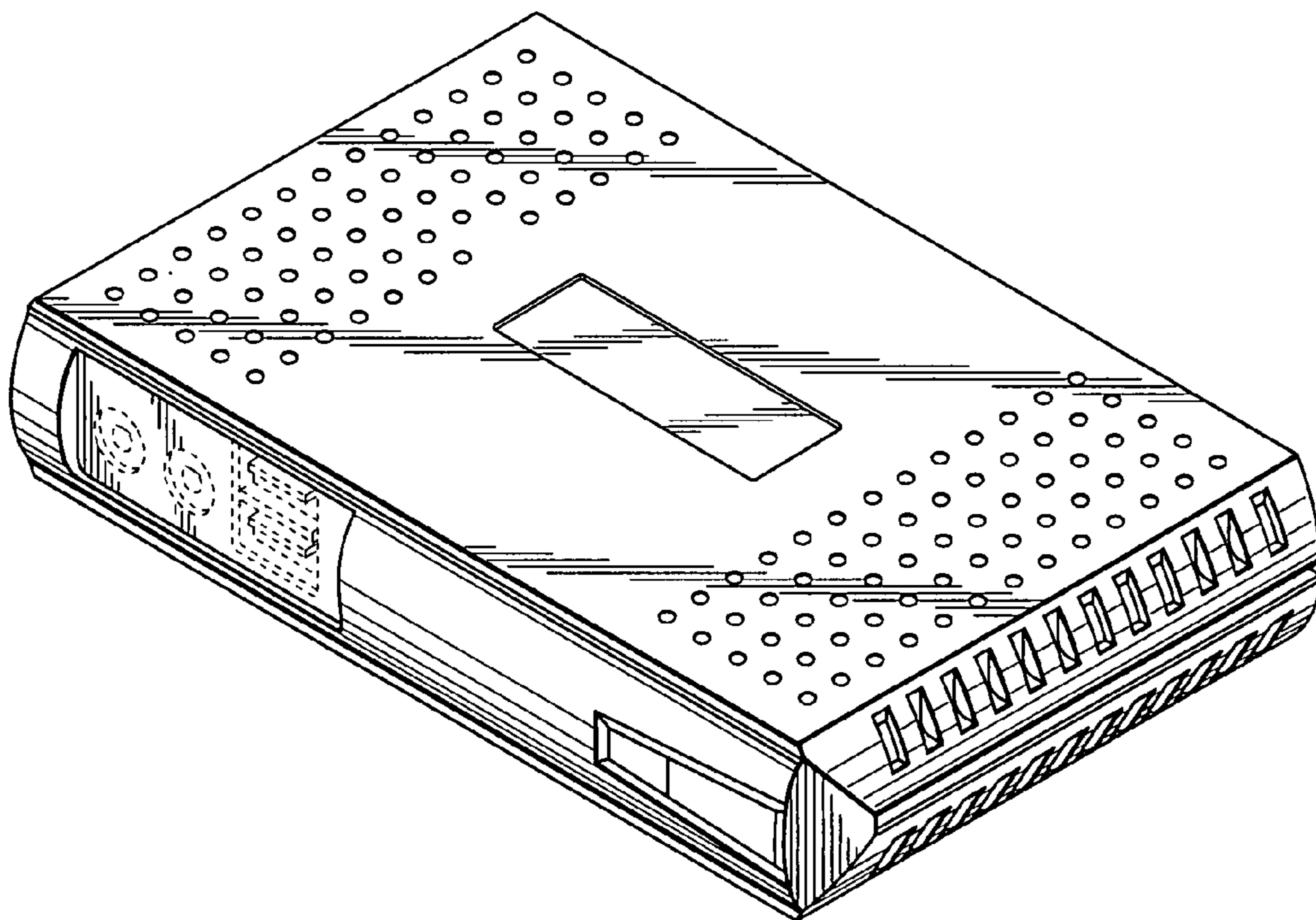


Fig. 1

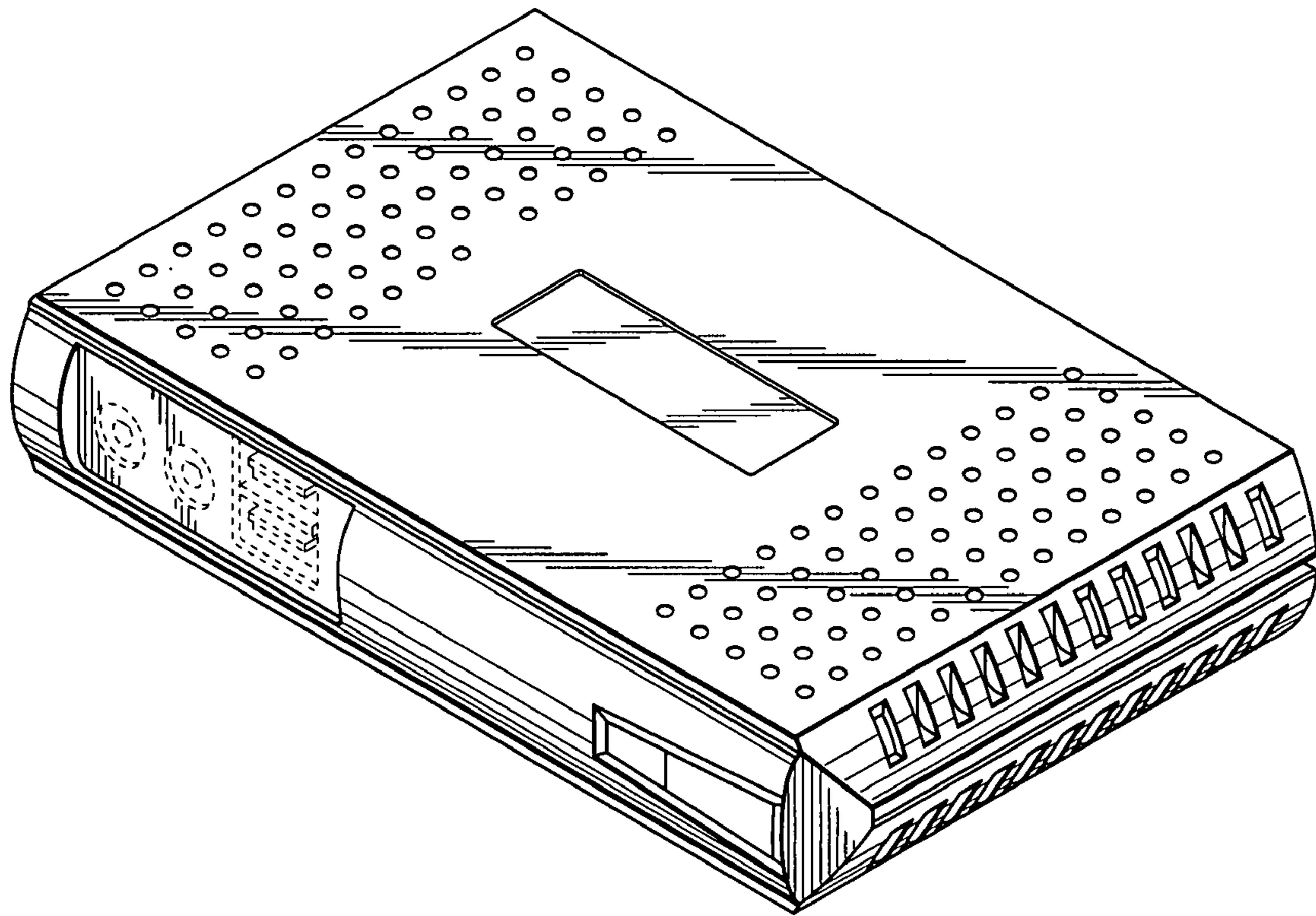


Fig. 2

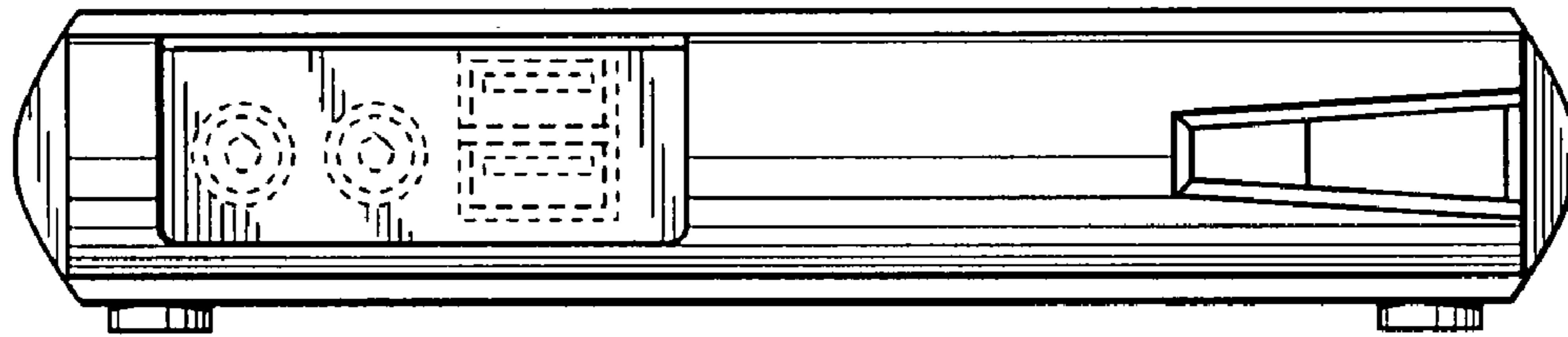


Fig. 3

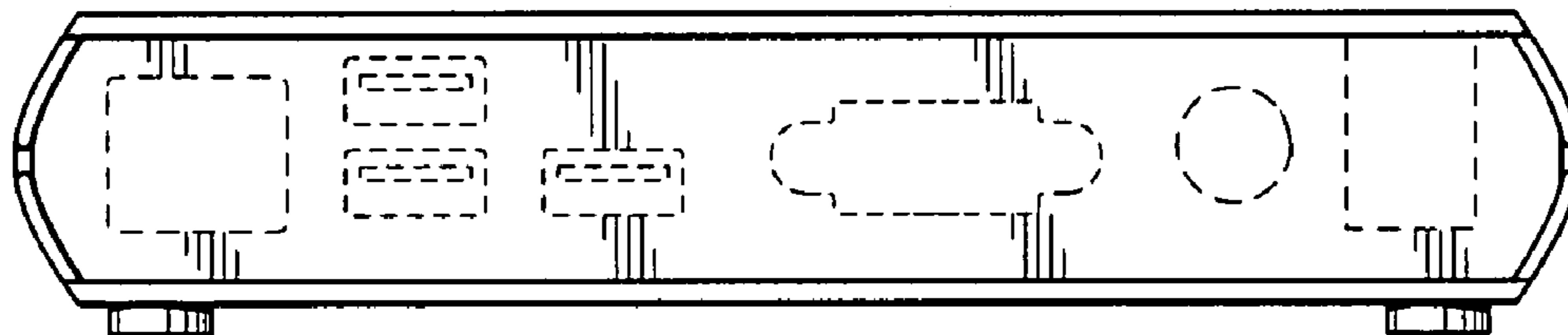


Fig. 4

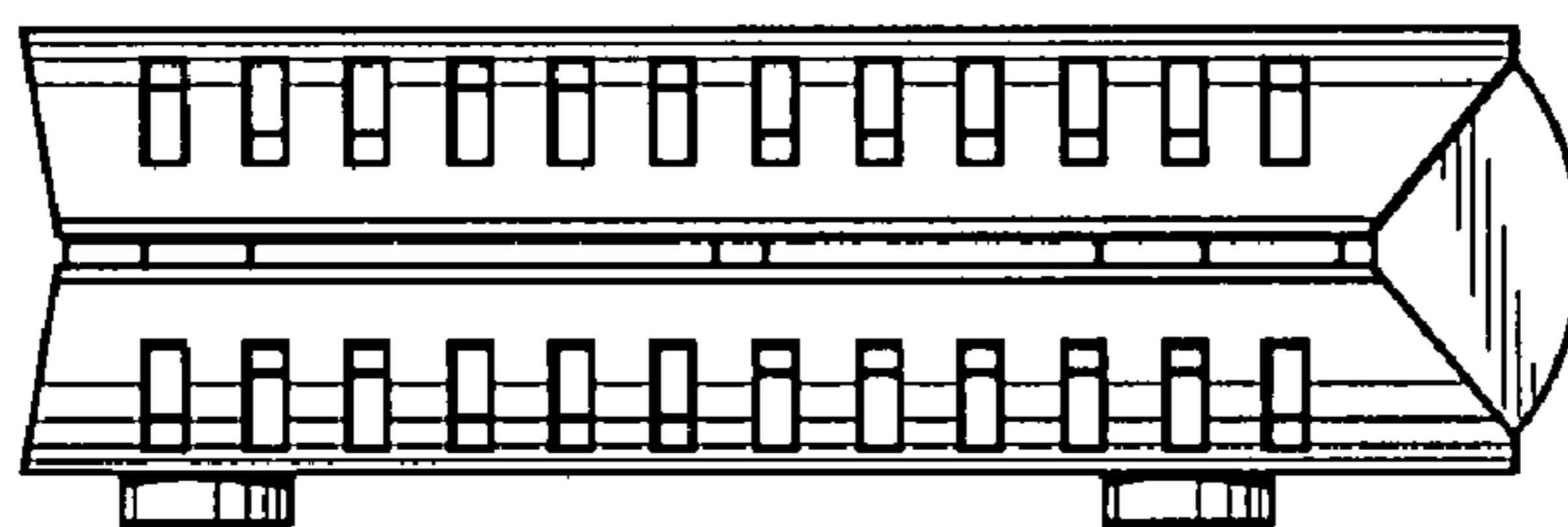


Fig. 5

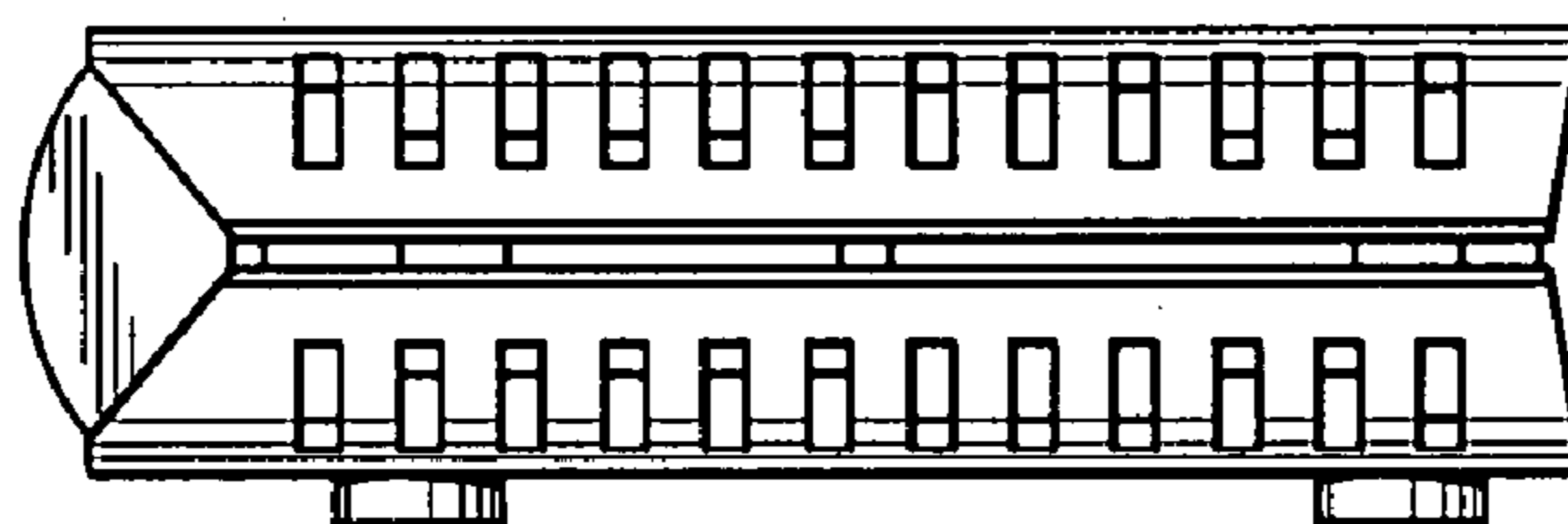


Fig. 6

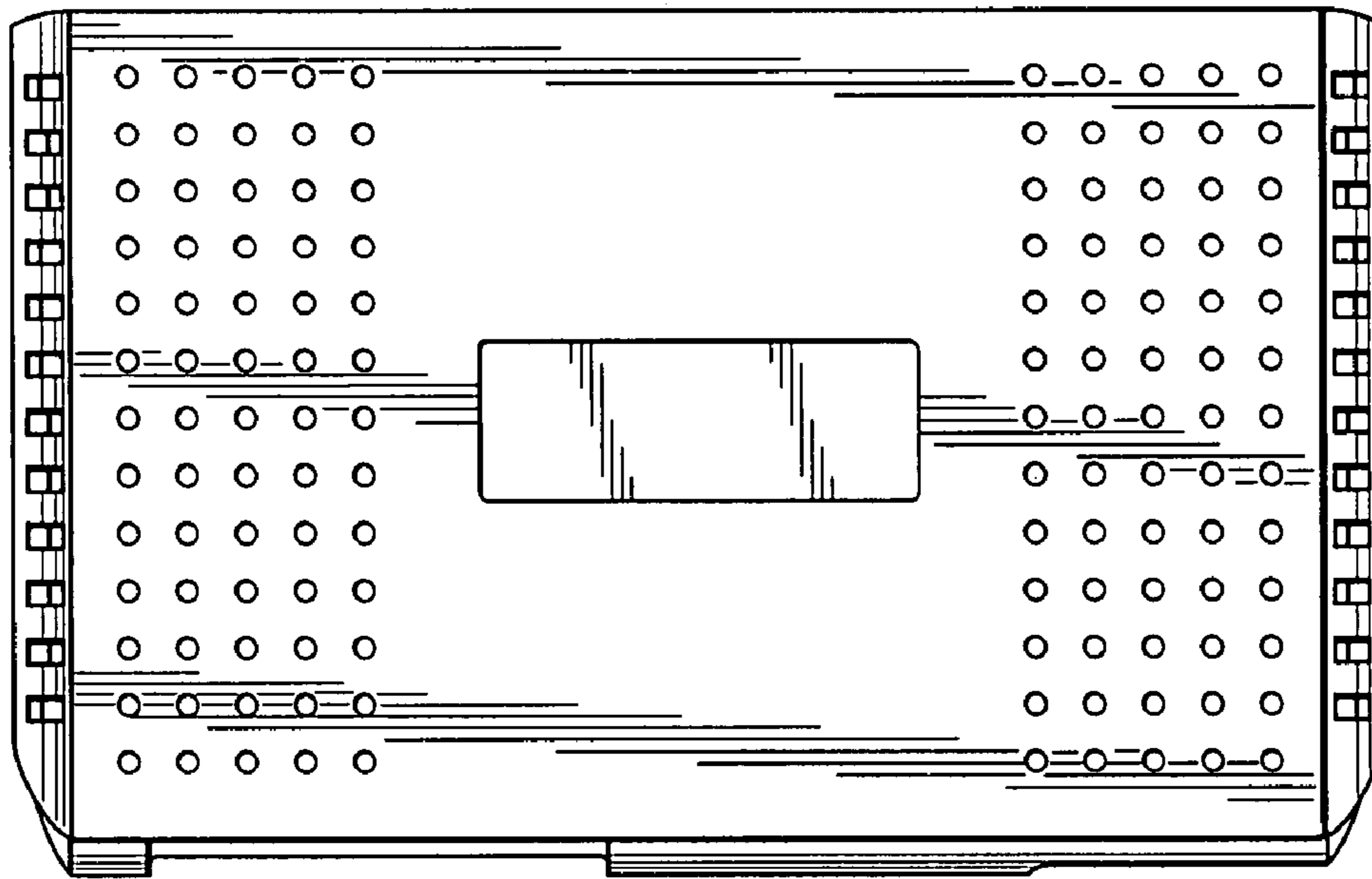


Fig. 7

