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(12) **United States Design Patent**  
**Kutaragi et al.**

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(54) **CONTROLLER FOR ARITHMETIC AND CONTROL UNIT**

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(73) Assignee: **Sony Computer Entertainment Inc.**, Tokyo (JP)

(\*\*) Term: **14 Years**

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(22) Filed: **Nov. 16, 2005**

(30) **Foreign Application Priority Data**

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(51) **LOC (8) Cl.** ..... **14-02**

(52) **U.S. Cl.** ..... **D14/401; D21/333**

(58) **Field of Classification Search** ..... D14/217, D14/218, 356, 388, 399, 400, 401, 412-416, D14/432, 434, 496, 454-457; D21/324, D21/332, 333, 436, 437; 463/1, 30, 36-38, 463/46, 47; 273/148 B, 148 R; 345/156-161, 345/905

See application file for complete search history.

(56) **References Cited**

**U.S. PATENT DOCUMENTS**

- D382,603 S 8/1997 Goto
- D386,544 S \* 11/1997 Kajikawa et al. .... D14/401
- D391,998 S \* 3/1998 Jwo ..... D14/401
- D410,643 S \* 6/1999 Tonino ..... D14/401
- D417,663 S \* 12/1999 Peng ..... D14/401
- D417,664 S 12/1999 Goto
- D420,660 S \* 2/2000 Chen ..... D14/401
- D421,433 S \* 3/2000 Alviar et al. .... D14/401
- D424,047 S \* 5/2000 Chan ..... D14/401
- D425,047 S \* 5/2000 Tyler et al. .... D14/401

- D430,222 S 8/2000 Goto
- D440,567 S \* 4/2001 Wahlgren et al. .... D14/401
- D447,480 S \* 9/2001 Sheehan et al. .... D14/401
- D453,758 S \* 2/2002 Han et al. .... D14/401
- D456,410 S \* 4/2002 Ashida ..... D14/401
- D467,922 S \* 12/2002 Lee ..... D14/401
- D469,436 S \* 1/2003 Hussaini et al. .... D14/401
- D491,183 S \* 6/2004 Swan et al. .... D14/401

**OTHER PUBLICATIONS**

CNet Networks, Inc., <http://crave.cnet.co.uk/gamesgear/0,39029441,39189264,00.htm>, "Sony PS3: 'Reality Synthesiser'", May 17, 2005, 1 pg.\*

\* cited by examiner

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(57) **CLAIM**

The ornamental design for a controller for arithmetic and control unit, as shown and described.

**DESCRIPTION**

FIG. 1 is a front elevational view of a controller for arithmetic and control unit showing our new design;

FIG. 2 is a rear elevational view thereof;

FIG. 3 is a left side elevational view thereof;

FIG. 4 is a right side elevational view thereof;

FIG. 5 is a top plan view thereof;

FIG. 6 is a bottom plan view thereof; and,

FIG. 7 is a perspective view thereof.

Portions of FIGS. 1-7 that are shown as broken lines are for illustrative purpose only and form no part of the claimed design.

**1 Claim, 3 Drawing Sheets**

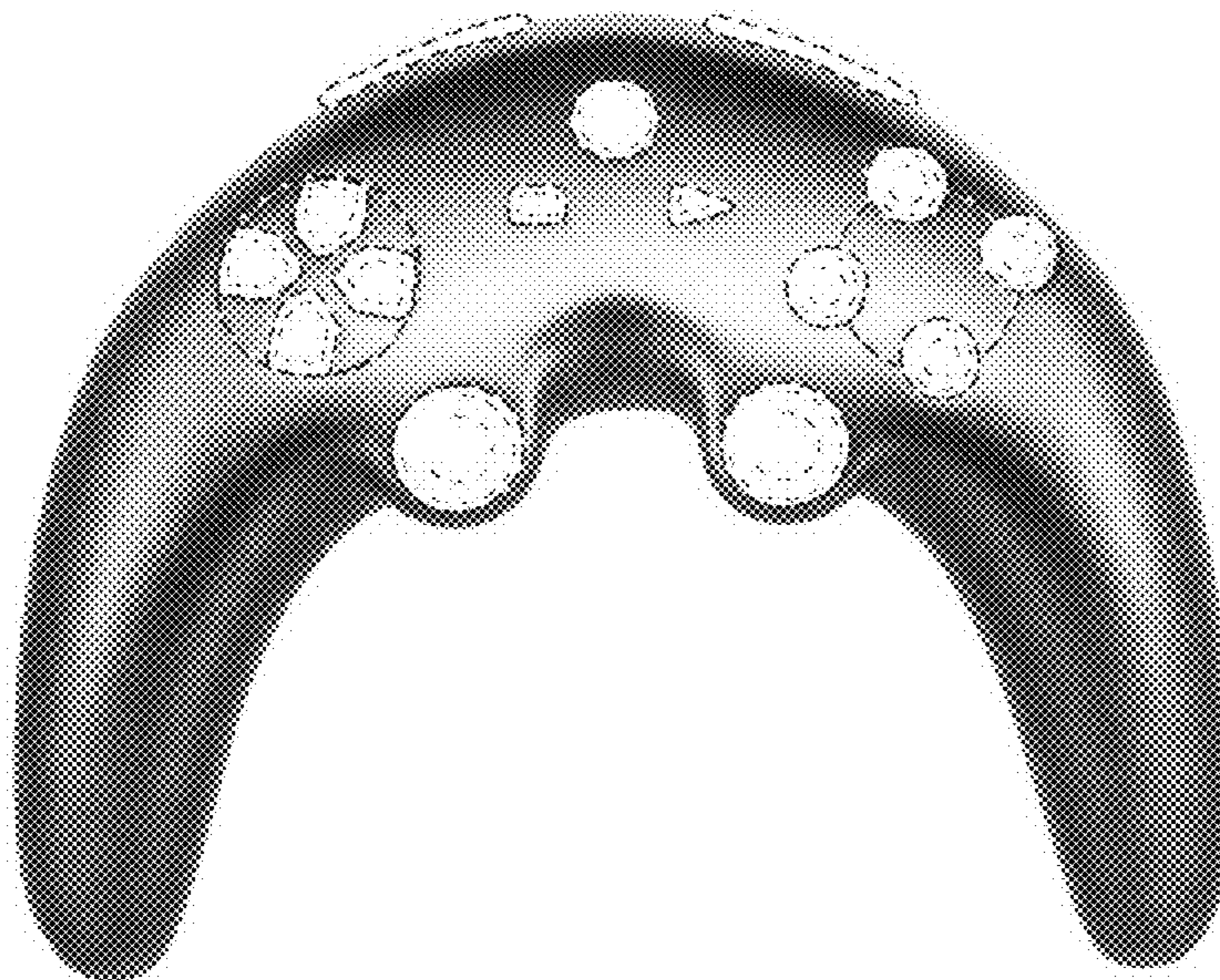


FIG. 1



FIG. 2



FIG. 3

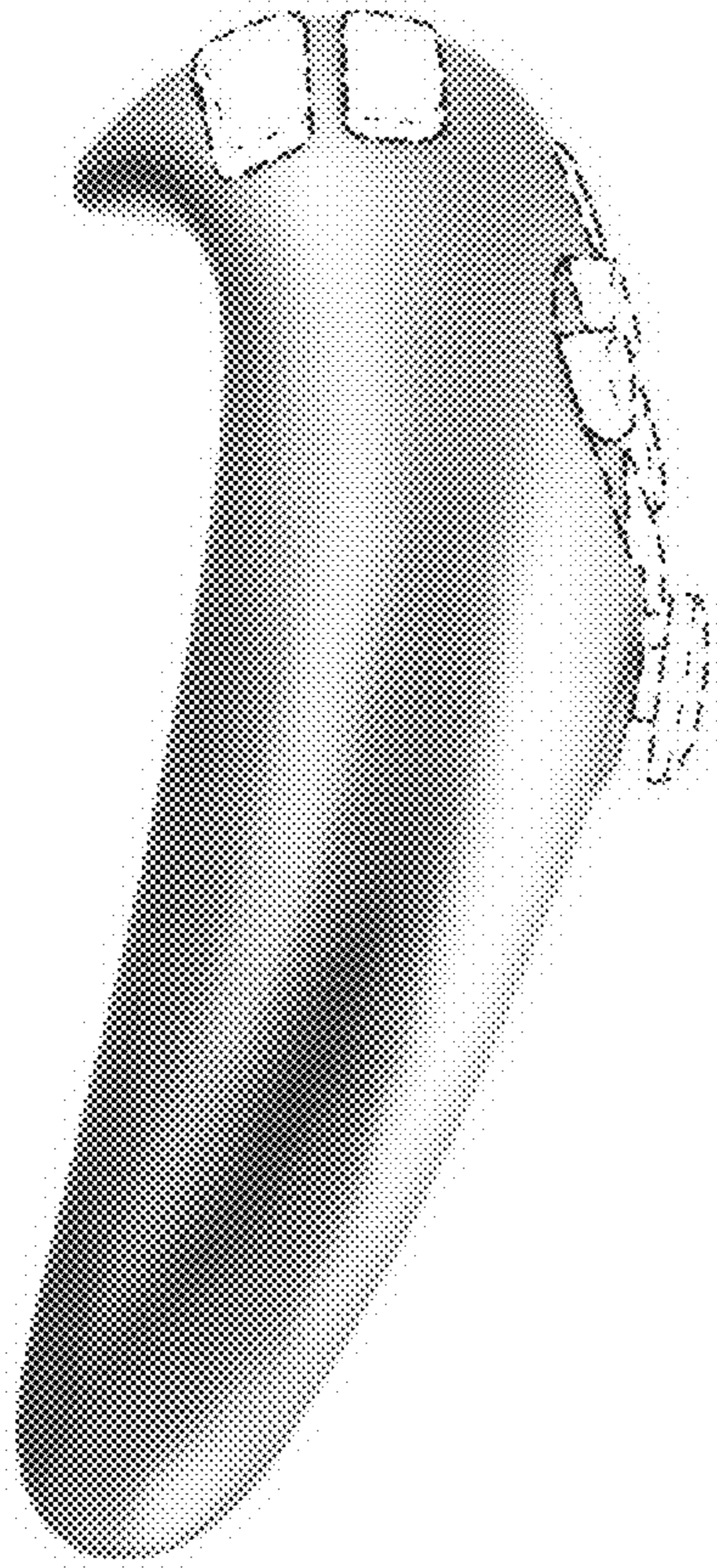


FIG. 4

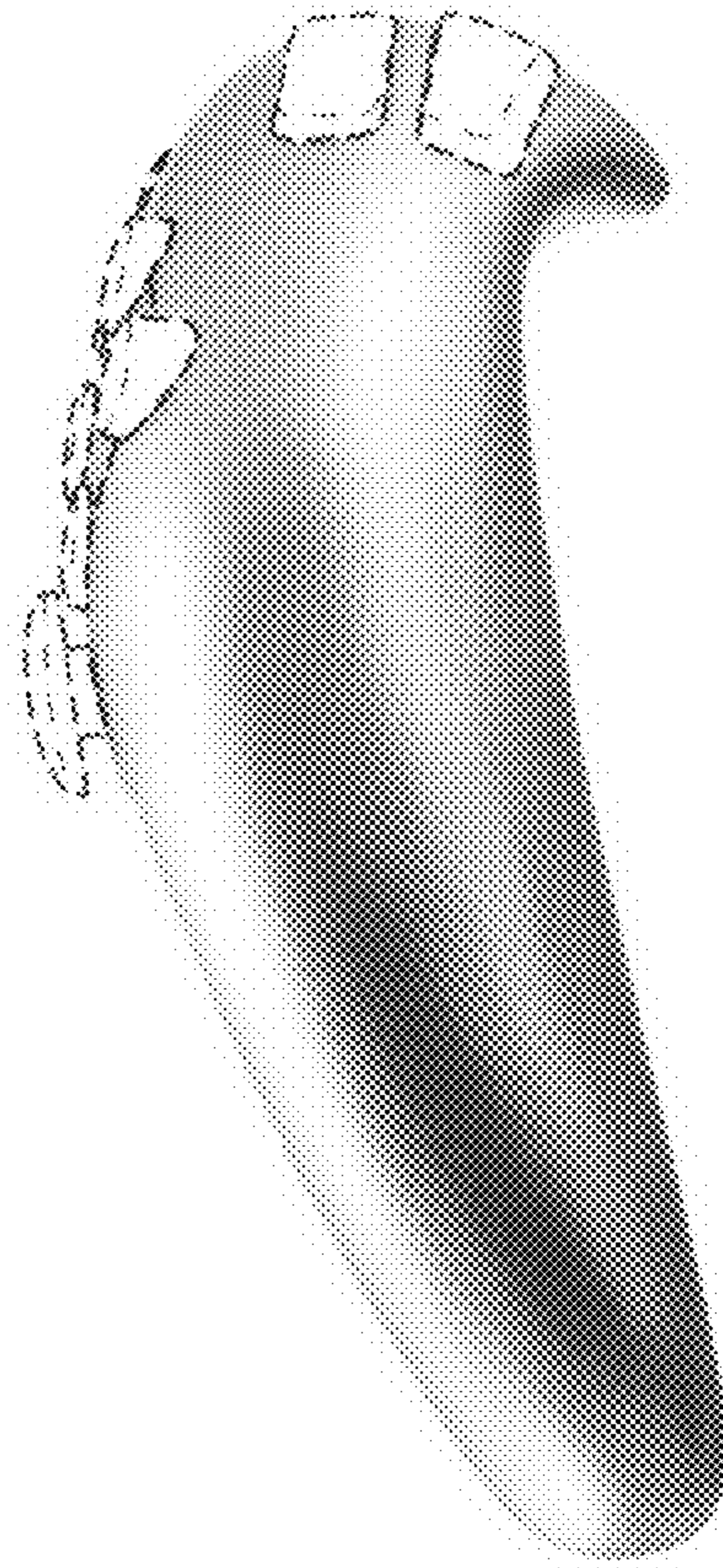


FIG. 5

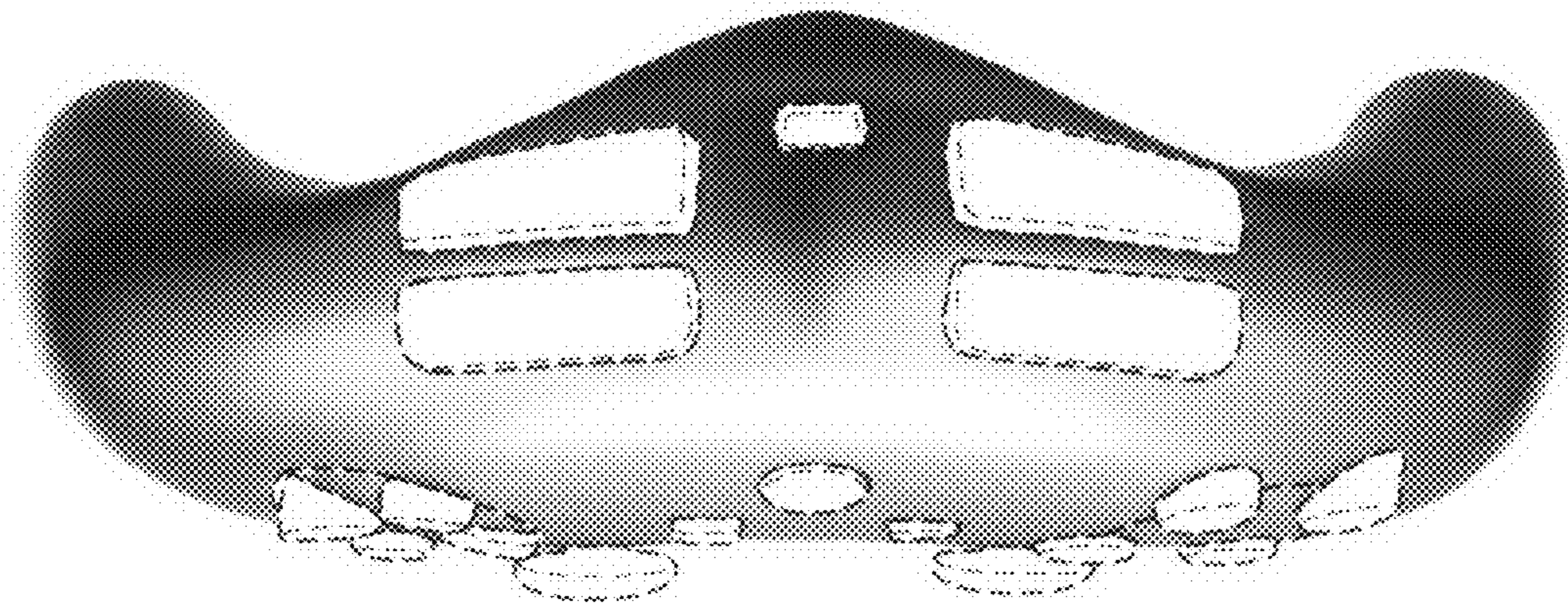


FIG. 6

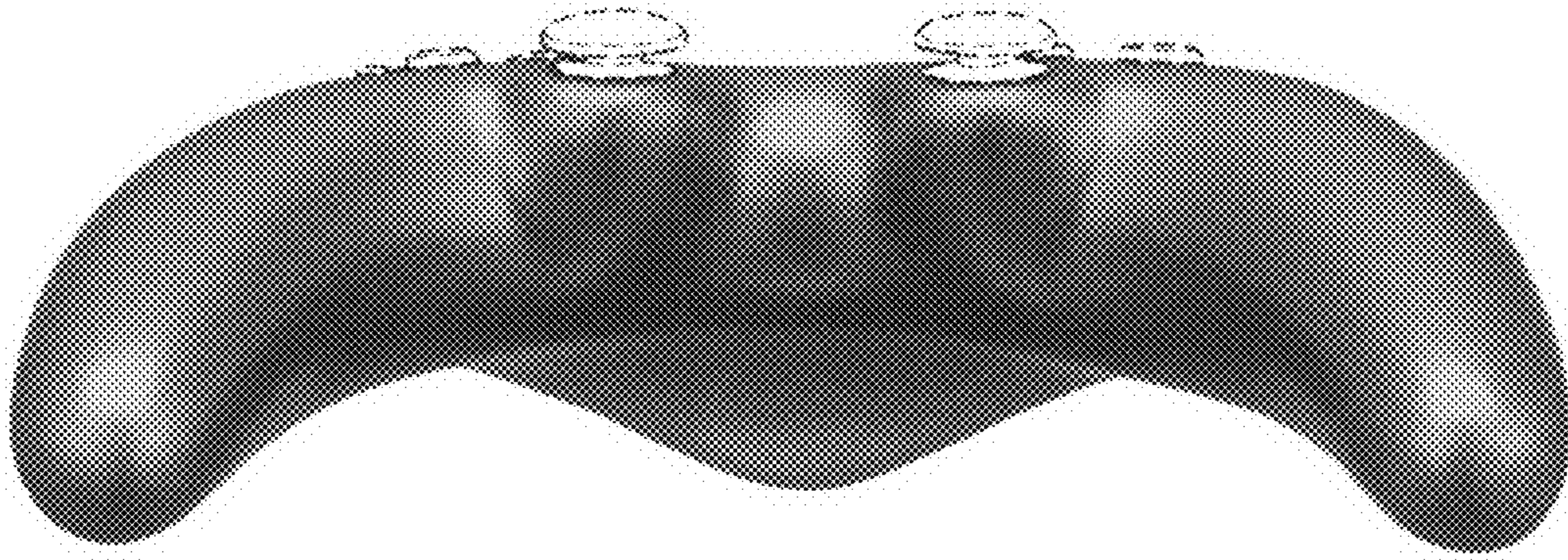


FIG. 7

