



US00D561561S

(12) **United States Design Patent**  
**Wohlford et al.**

(10) **Patent No.:** **US D561,561 S**  
(45) **Date of Patent:** **\*\* Feb. 12, 2008**

(54) **IN-WALL DEVICE MOUNT INTERFACE**

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**Dominic Grey**, Blaine, MN (US)

(73) Assignee: **CSAV, Inc.**, Savage, MN (US)

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/265,252**

(22) Filed: **Aug. 28, 2006**

(51) **LOC (8) Cl.** ..... **08-05**

(52) **U.S. Cl.** ..... **D8/354**

(58) **Field of Classification Search** ..... D8/363,  
D8/354, 349; 248/274.1, 284.1, 279.1  
See application file for complete search history.

(56) **References Cited**

**U.S. PATENT DOCUMENTS**

- 5,145,136 A \* 9/1992 McConnell ..... 248/284.1
- D437,772 S \* 2/2001 Erwin ..... D8/354
- 6,186,460 B1 \* 2/2001 Lin ..... 248/284.1
- D523,734 S \* 6/2006 Calacone ..... D8/354

**OTHER PUBLICATIONS**

CHIEF, Large Flat Panel In-Wall Accessory, *Chief Full Line Catalog*, Jun. 2006, pp. 43.

\* cited by examiner

*Primary Examiner*—Holly H. Baynham

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(57) **CLAIM**

We claim the ornamental design for in-wall device mount interface, as shown.

**DESCRIPTION**

FIG. 1 is a front perspective view of a in-wall device mount interface according to an embodiment of the invention;

FIG. 2 is a front elevation view of a in-wall device mount interface according to an embodiment of the invention;

FIG. 3 is a side elevation view of the in-wall device mount interface depicted in FIG. 1;

FIG. 4 is a bottom plan view of the in-wall device mount interface depicted in FIG. 1;

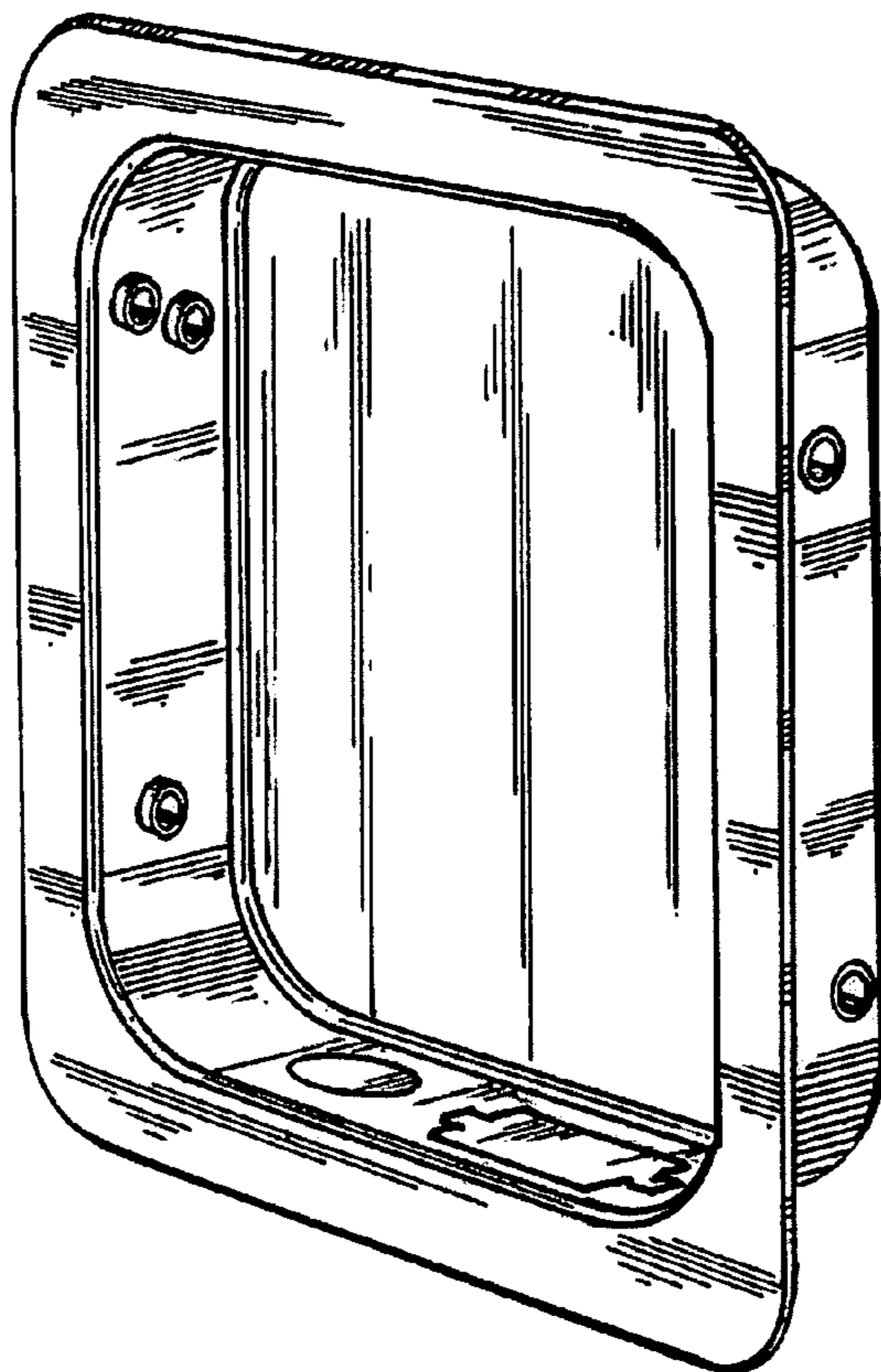
FIG. 5 is a top plan view of the in-wall device mount interface depicted in FIG. 1;

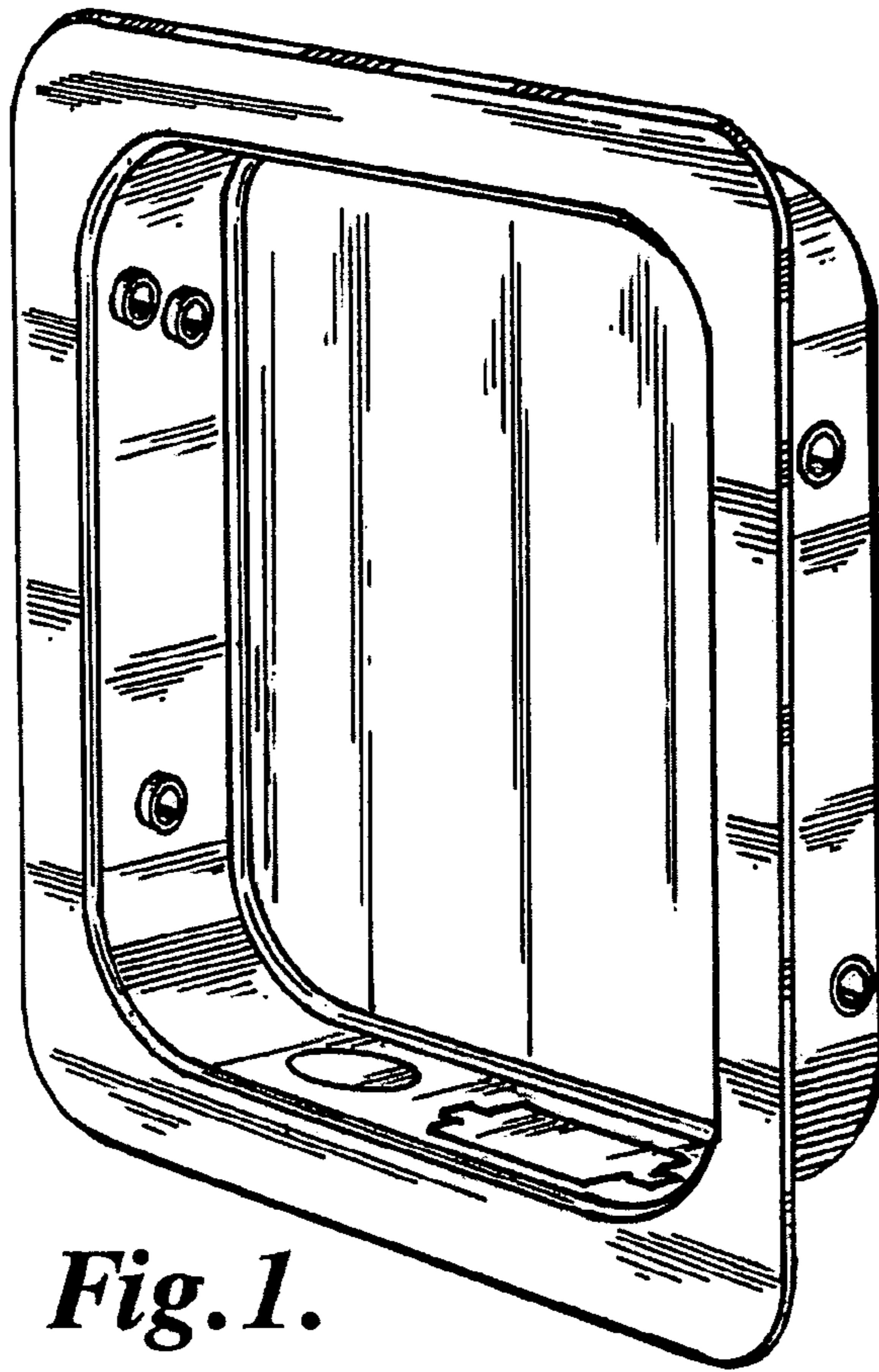
FIG. 6 is a rear elevation view of the in-wall device mount interface depicted in FIG. 1; and,

FIG. 7 is an elevation view of the in-wall device mount interface of the side opposite that depicted in FIG. 3.

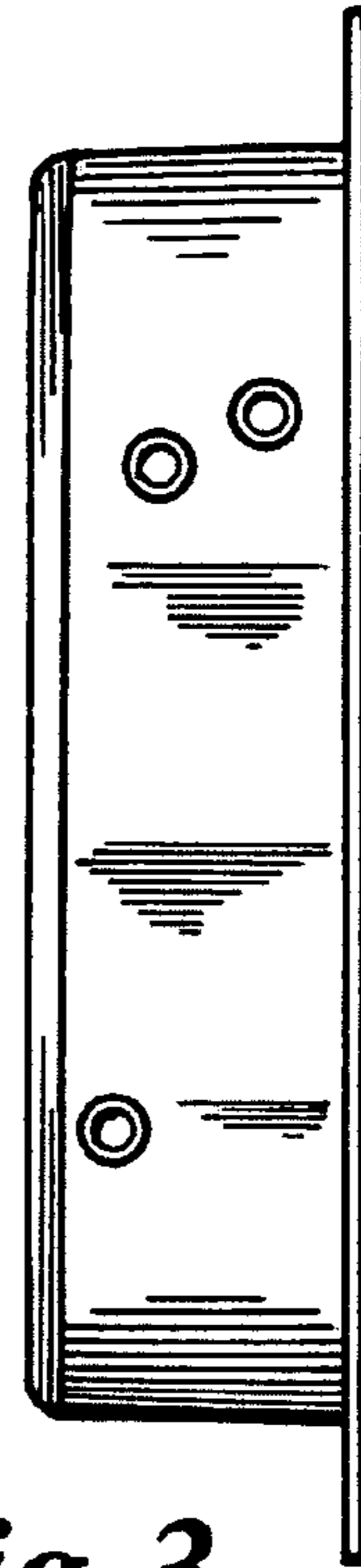
The broken-line disclosure in the views is for illustrative purposes only and forms no part of the claimed design.

**1 Claim, 2 Drawing Sheets**

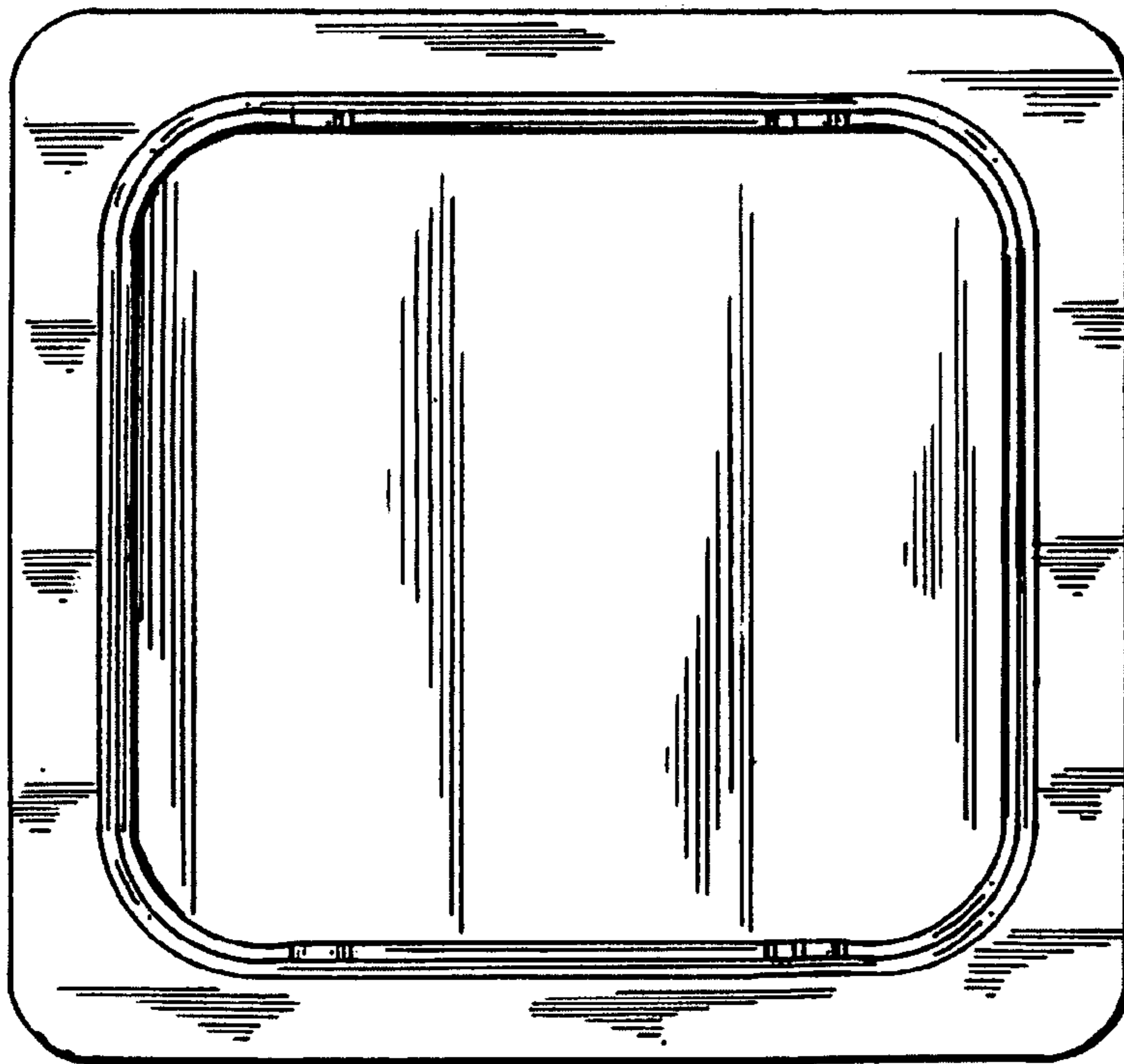




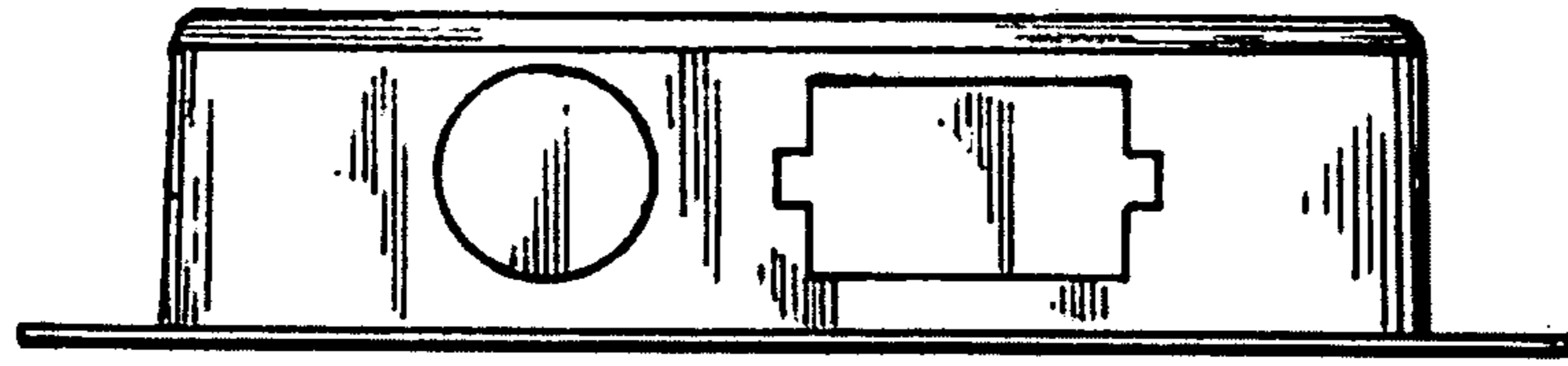
*Fig. 1.*



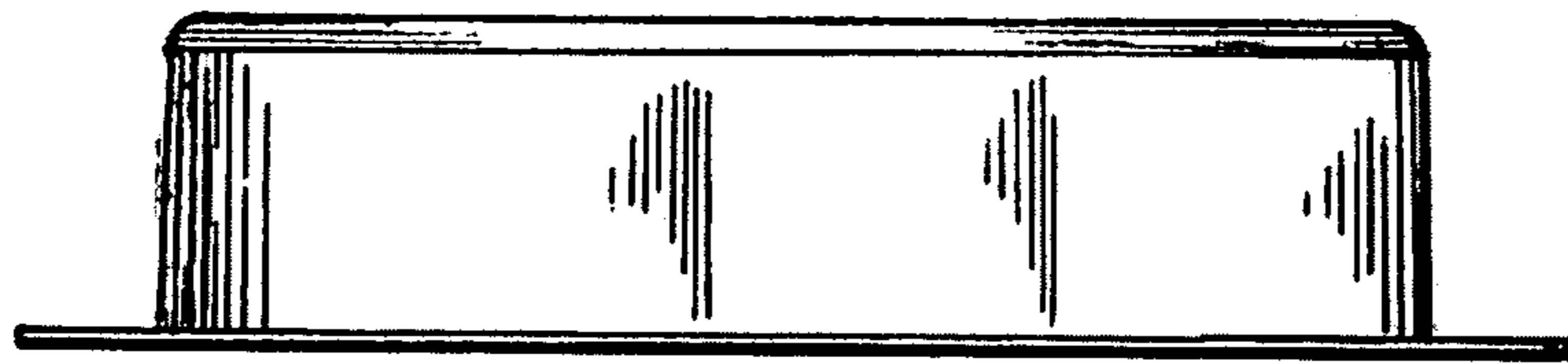
*Fig. 3.*



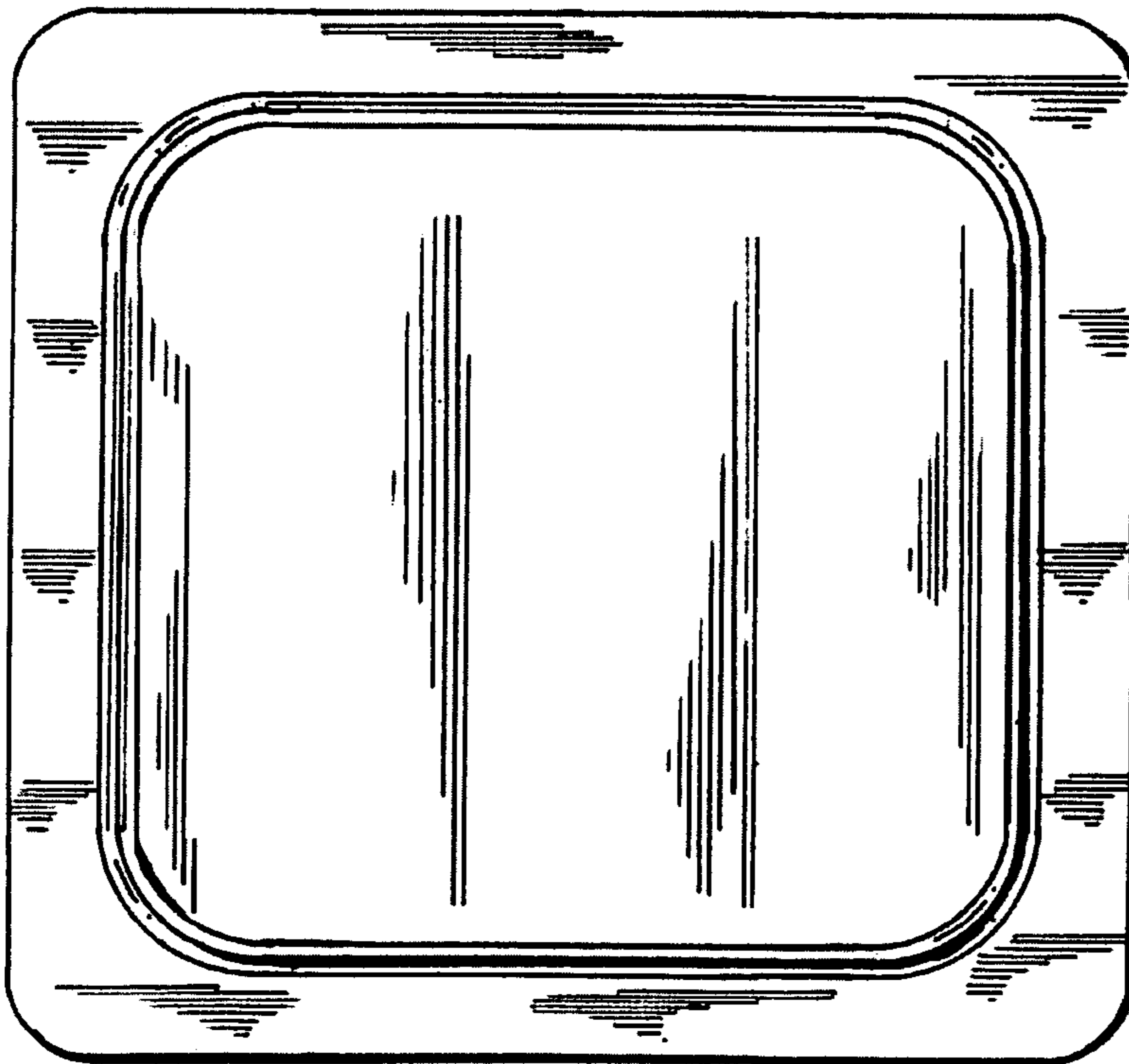
*Fig. 2.*



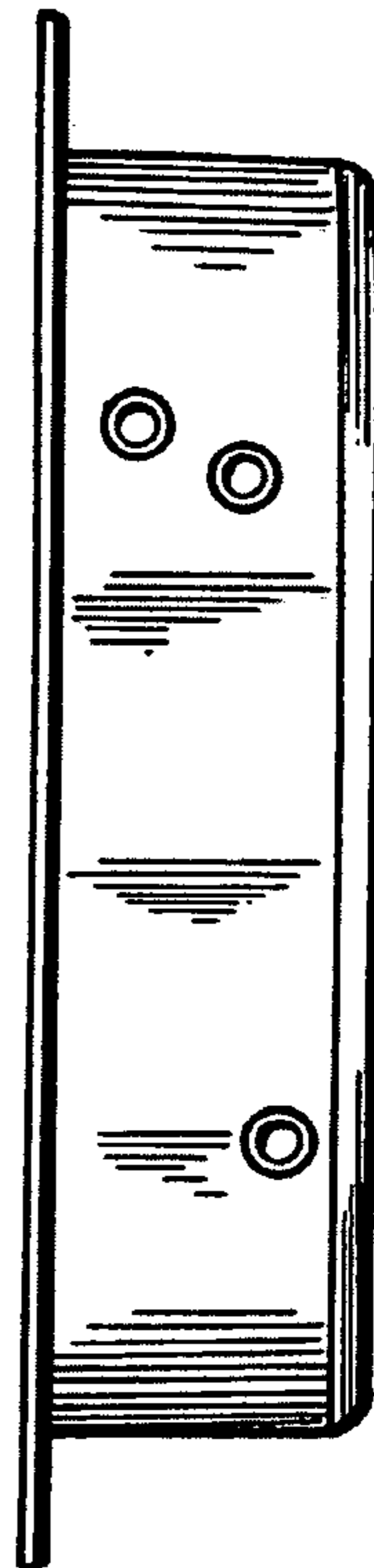
*Fig. 4.*



*Fig. 5.*



*Fig. 6.*



*Fig. 7.*

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : D561,561 S  
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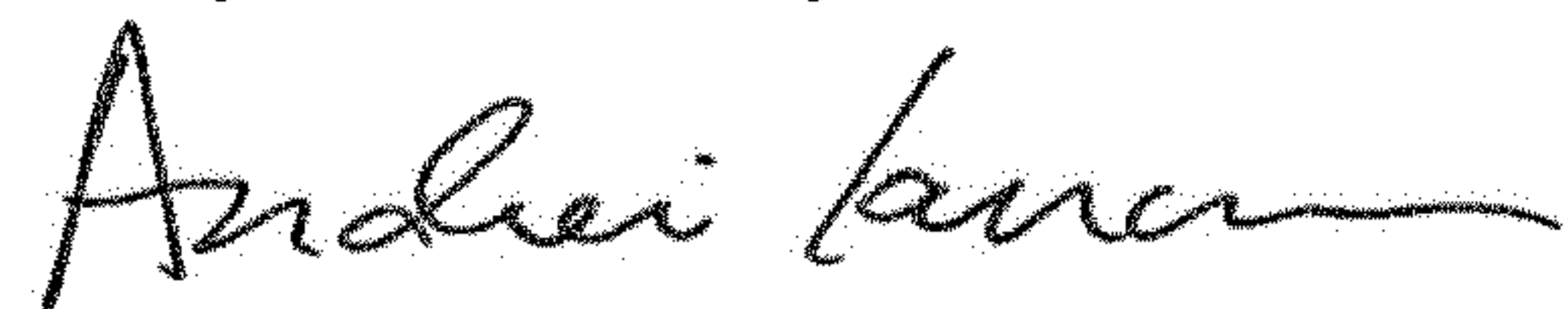
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Title Page

Item (75) Inventor is corrected to read:

-- Jim Wohlford, Edina, (MN);  
Dominic D. Grey, Blaine, (MN);  
Peter Tribuno, Roseville, (MN) --.

Signed and Sealed this  
Twenty-second Day of October, 2019



Andrei Iancu  
*Director of the United States Patent and Trademark Office*