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(12) **United States Design Patent** (10) **Patent No.:** **US D554,119 S**  
**Otsuka et al.** (45) **Date of Patent:** **\*\* Oct. 30, 2007**

(54) **USB FLASH MEMORY**  
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(73) Assignee: **Kabushiki Kaisha Toshiba** (JP)

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(\*\*) Term: **14 Years**

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(21) Appl. No.: **29/236,835**

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(22) Filed: **Aug. 24, 2005**

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(51) **LOC (8) Cl.** ..... **14-02**

(52) **U.S. Cl.** ..... **D14/356; D14/435**

(58) **Field of Classification Search** ..... D14/137,  
D14/156, 167, 240, 242, 299, 348, 356, 384-5,  
D14/402, 432-435, 496; 361/685-6; 365/63,  
365/131; 439/139-41, 144, 518, 638; 710/62,  
710/74

(57) **CLAIM**

The ornamental design for a USB flash memory, as shown and described.

See application file for complete search history.

**DESCRIPTION**

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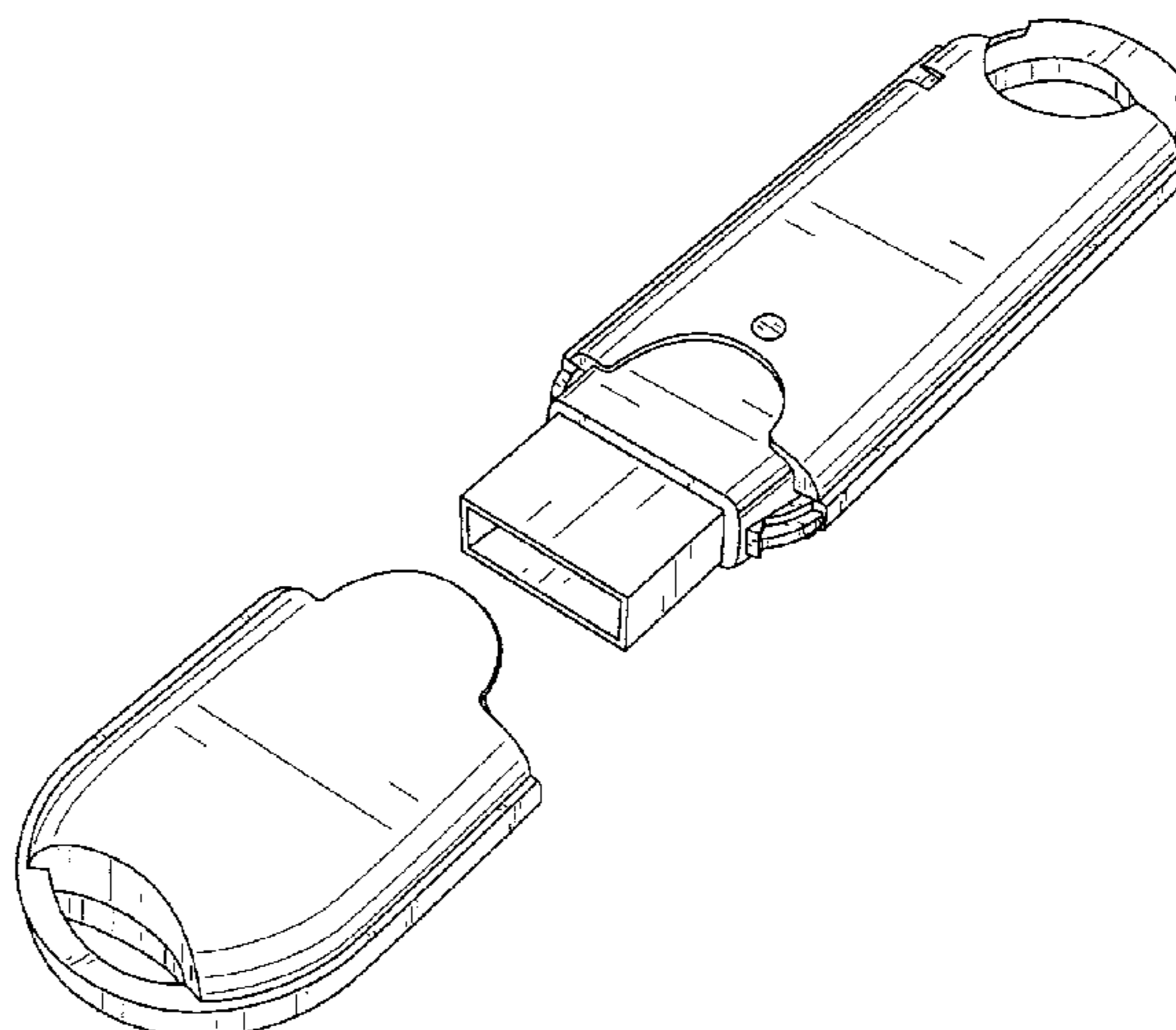
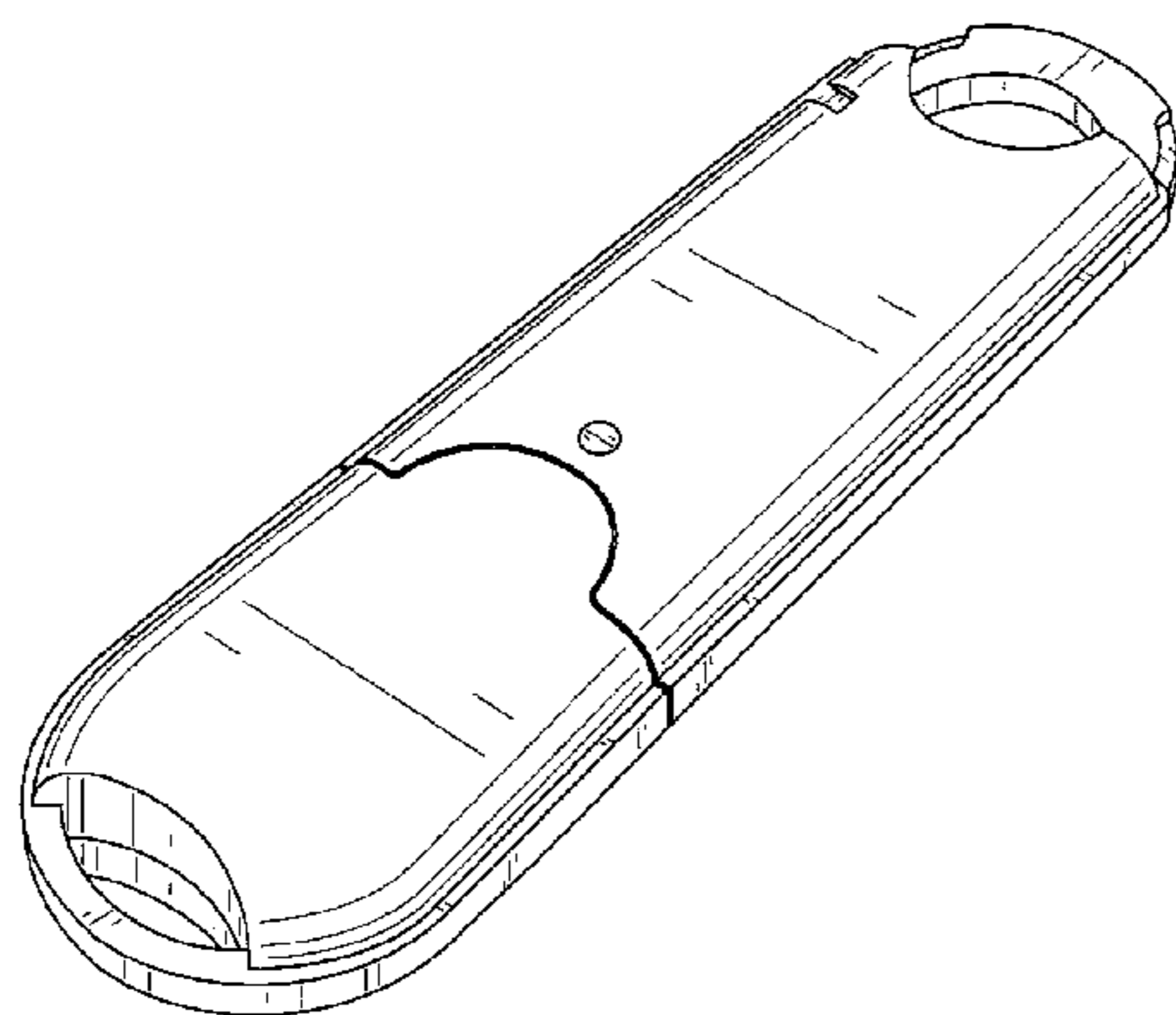
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FIG. 1 is a front, bottom and left side perspective view of a USB flash memory, showing our new design;  
FIG. 2 is a front, bottom and left side perspective view thereof with a cap in a removed state;  
FIG. 3 is a front elevational view thereof;  
FIG. 4 is a rear elevational view thereof;  
FIG. 5 is a top plan view thereof;  
FIG. 6 is a bottom plan view thereof;  
FIG. 7 is a right side elevational view thereof;  
FIG. 8 is a left side elevational view thereof;  
FIG. 9 is a top plan view thereof with the cap in a removed state;  
FIG. 10 is a front elevational view thereof with the cap in a removed state; and,  
FIG. 11 is a left side elevational view thereof with the cap in a removed state.

**1 Claim, 5 Drawing Sheets**



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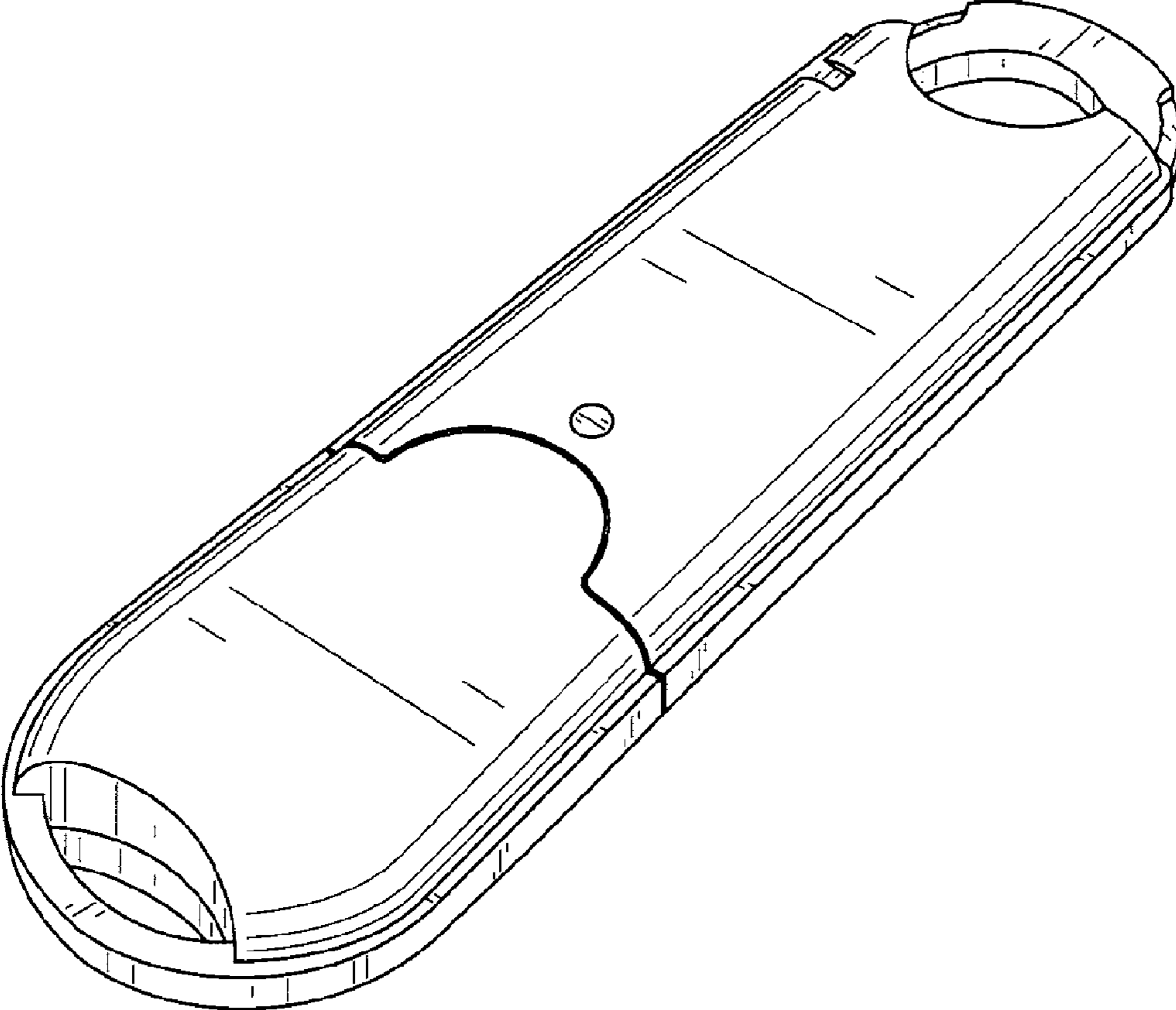


FIG. 1

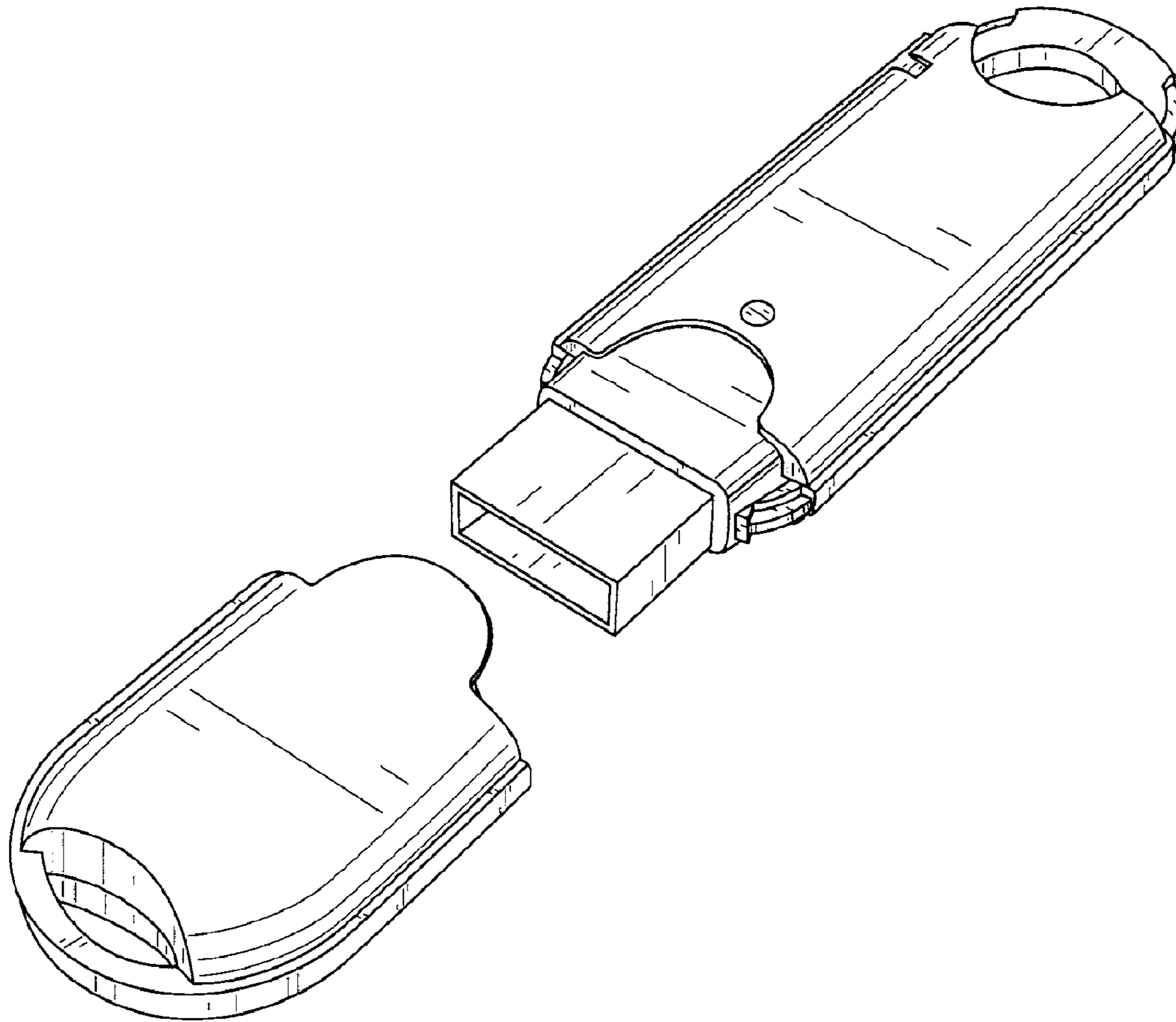


FIG. 2

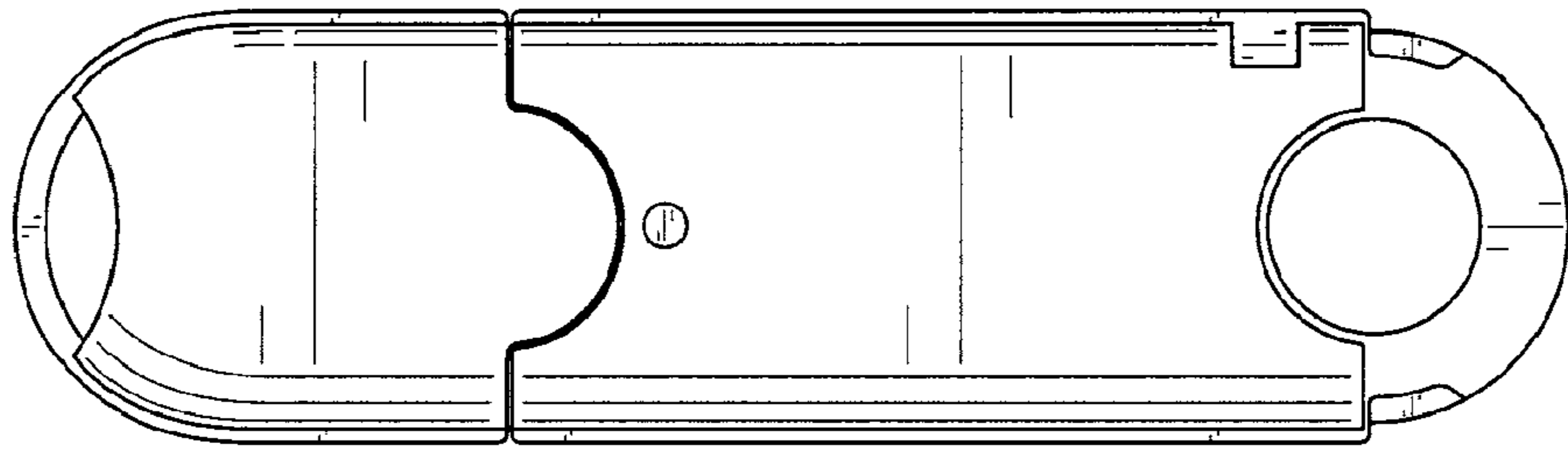


FIG. 3

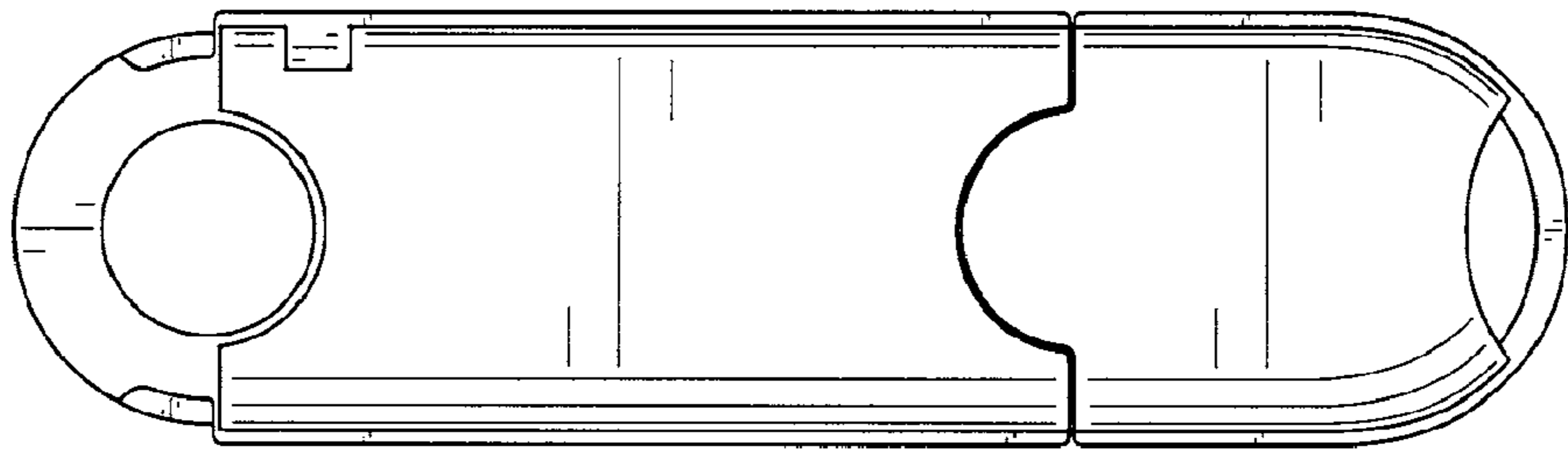


FIG. 4

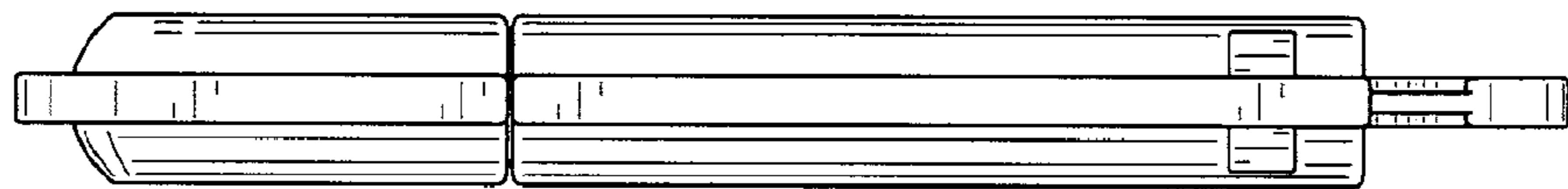


FIG. 5

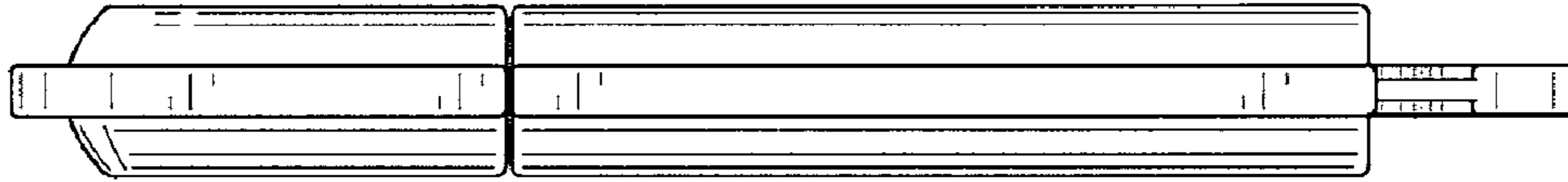


FIG. 6

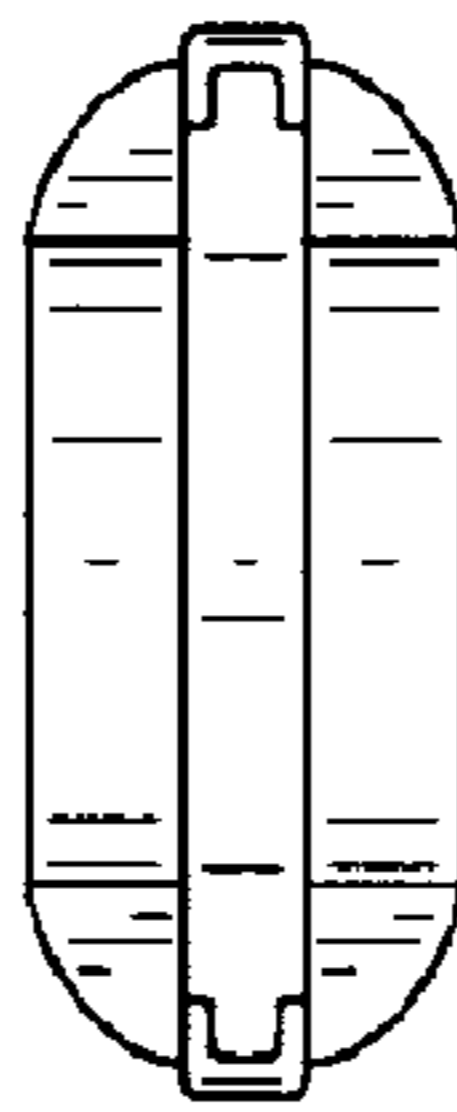


FIG. 7

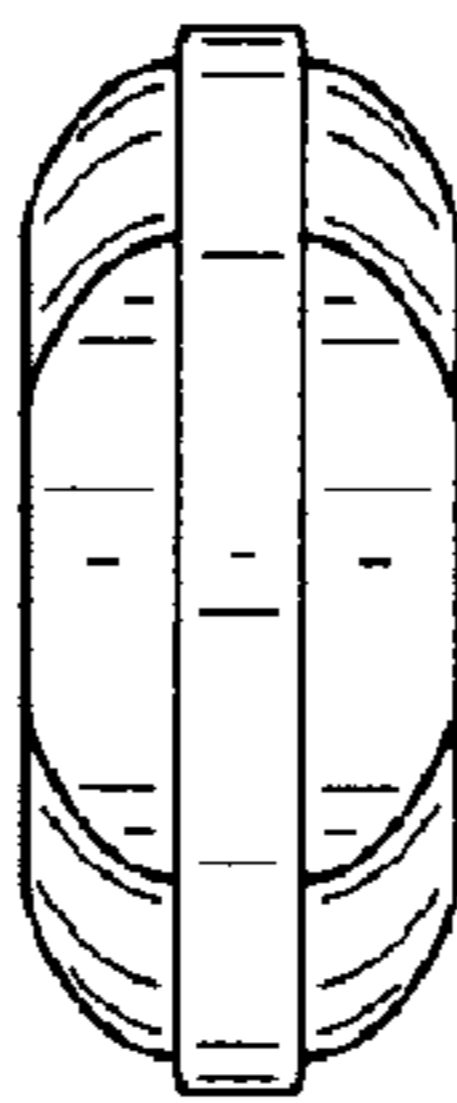


FIG. 8



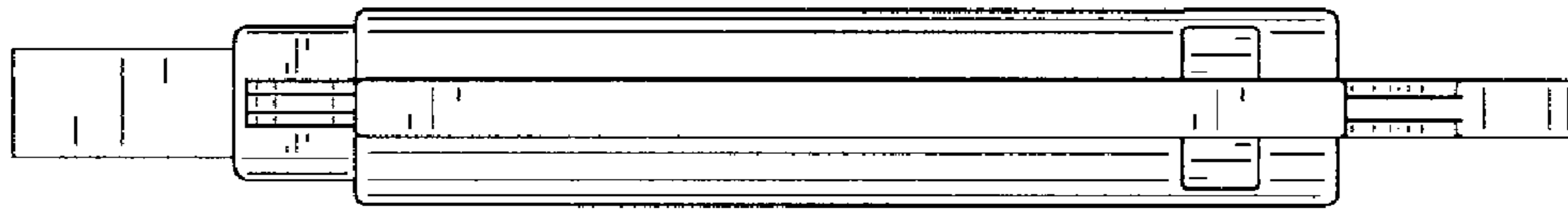


FIG. 9

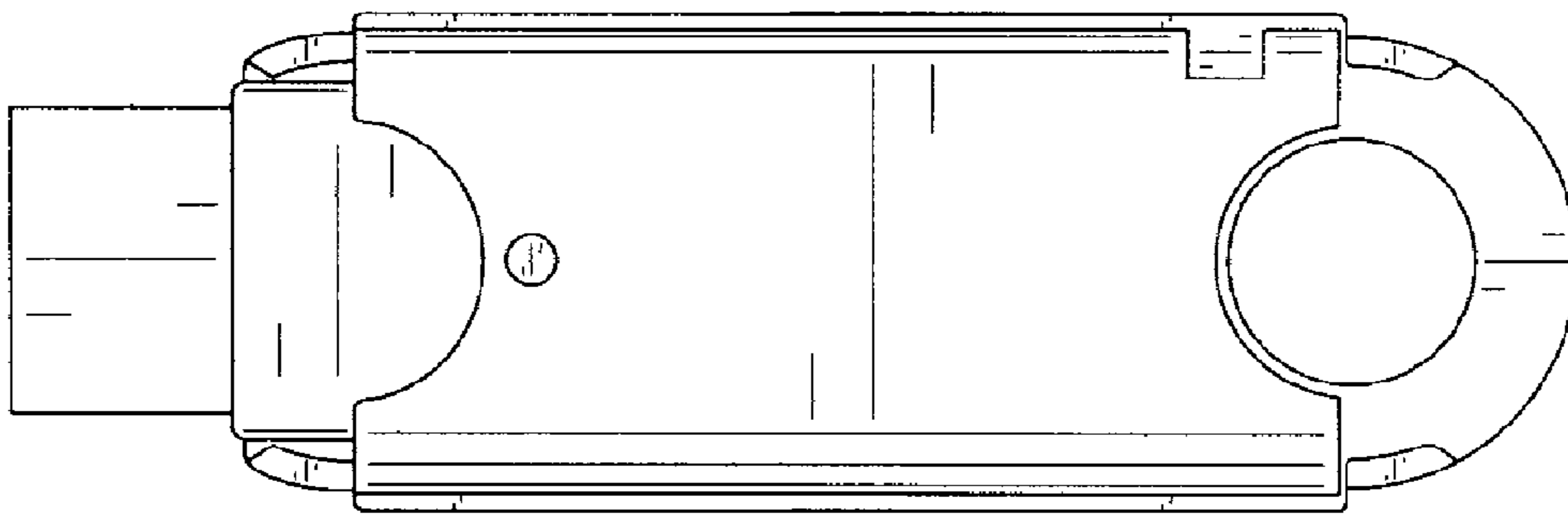


FIG. 10

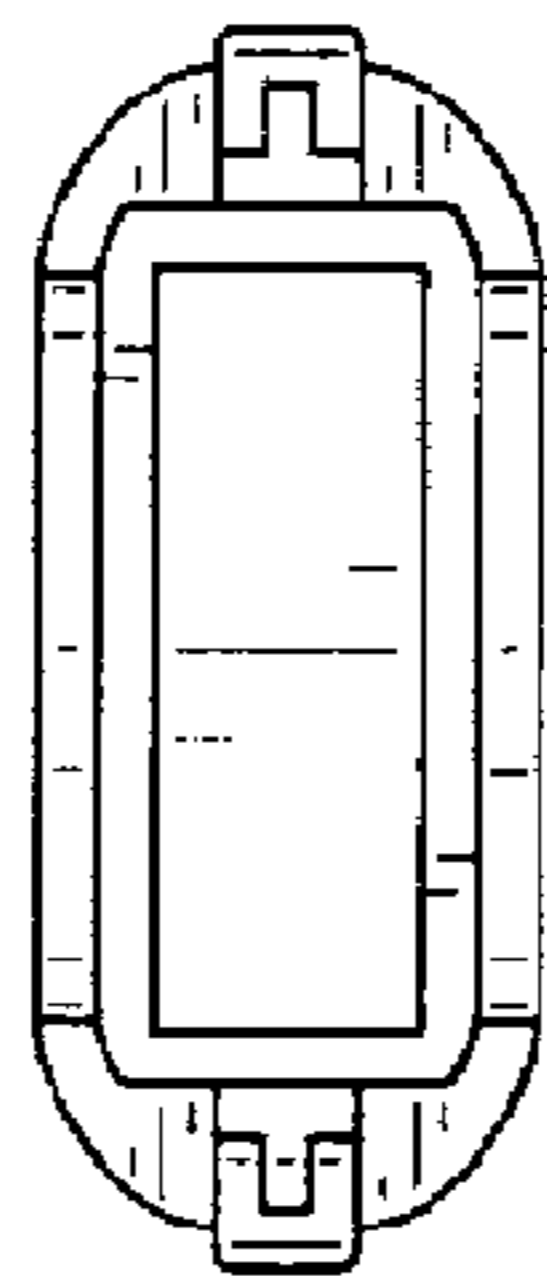


FIG. 11