

US00D554084S

(12) **United States Design Patent** (10) **Patent No.:** **US D554,084 S**
Iizuka (45) **Date of Patent:** **** Oct. 30, 2007**

(54) **PORTION OF A SEMICONDUCTOR
APPARATUS MOUNTING-POSITION
ACCURACY MEASUREMENT JIG**

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(**) **Term:** **14 Years**

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(51) **LOC (8) Cl.** **13-03**

(52) **U.S. Cl.** **D13/182**

(58) **Field of Classification Search** D13/182;
174/260; 250/214 R; 257/290, 458; 356/3,
356/401; 438/401; 716/19

See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a portion of a semiconductor apparatus mounting-position accuracy measurement jig, as shown and described.

DESCRIPTION

FIG. 1 is a front, top and right side perspective view of a portion of a semiconductor apparatus mounting-position accuracy measurement jig, showing my new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a rear elevational view thereof;

FIG. 4 is a right side elevational view thereof;

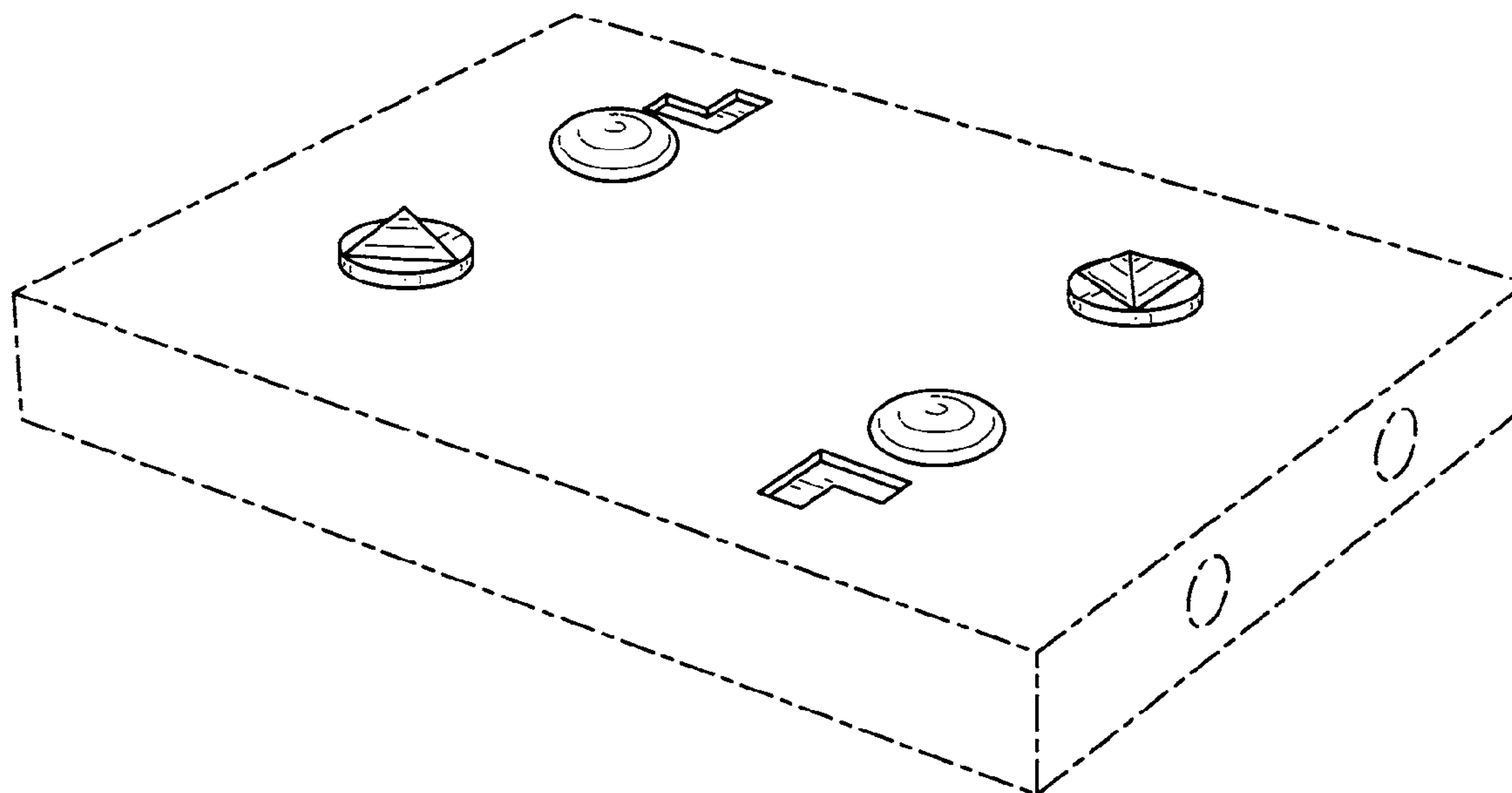
FIG. 5 is a left side elevational view thereof;

FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom plan view thereof.

The broken lines are for illustrative purposes only and form no part of the claimed design.

1 Claim, 5 Drawing Sheets



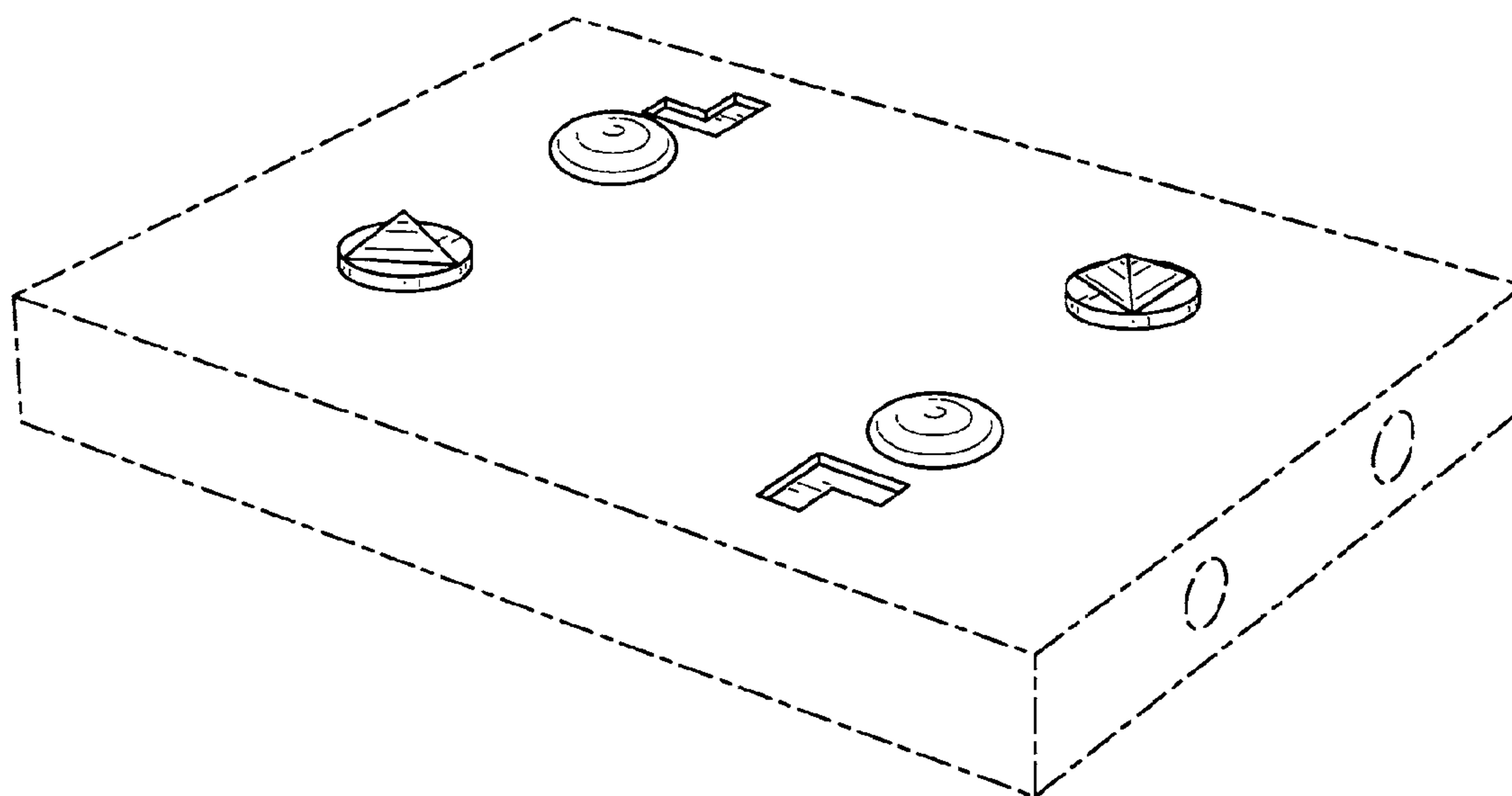


FIG. 1

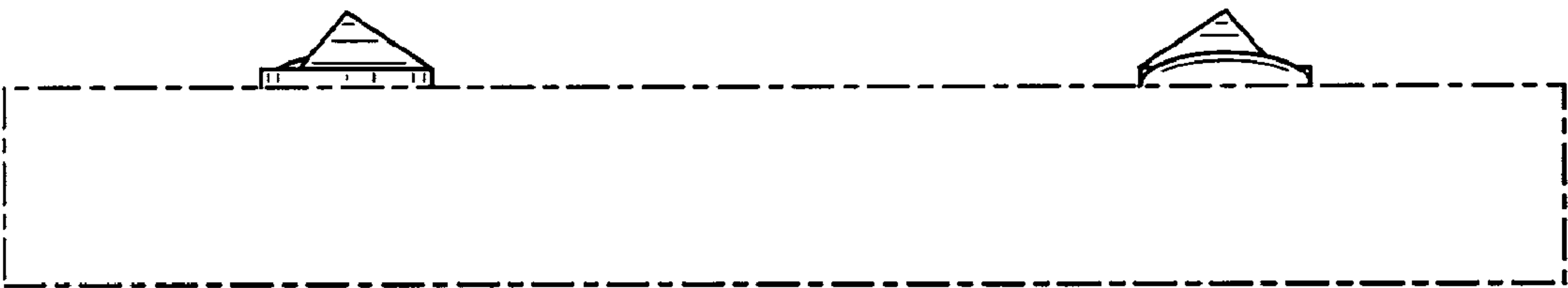


FIG. 2

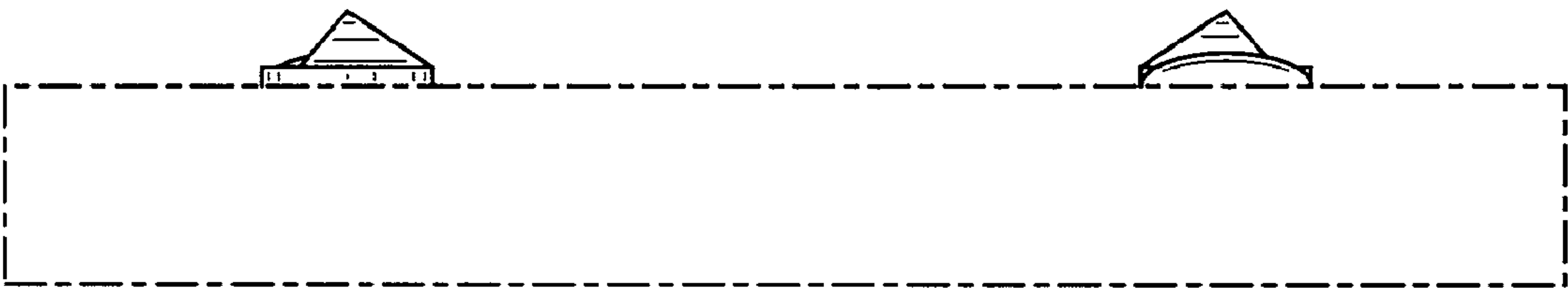


FIG. 3



FIG. 4

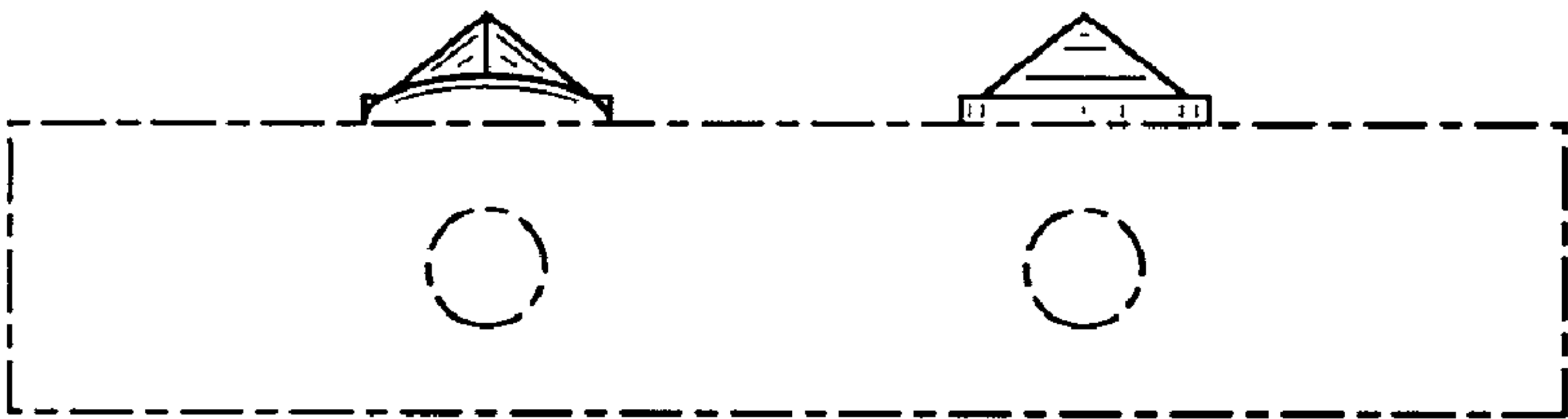


FIG. 5

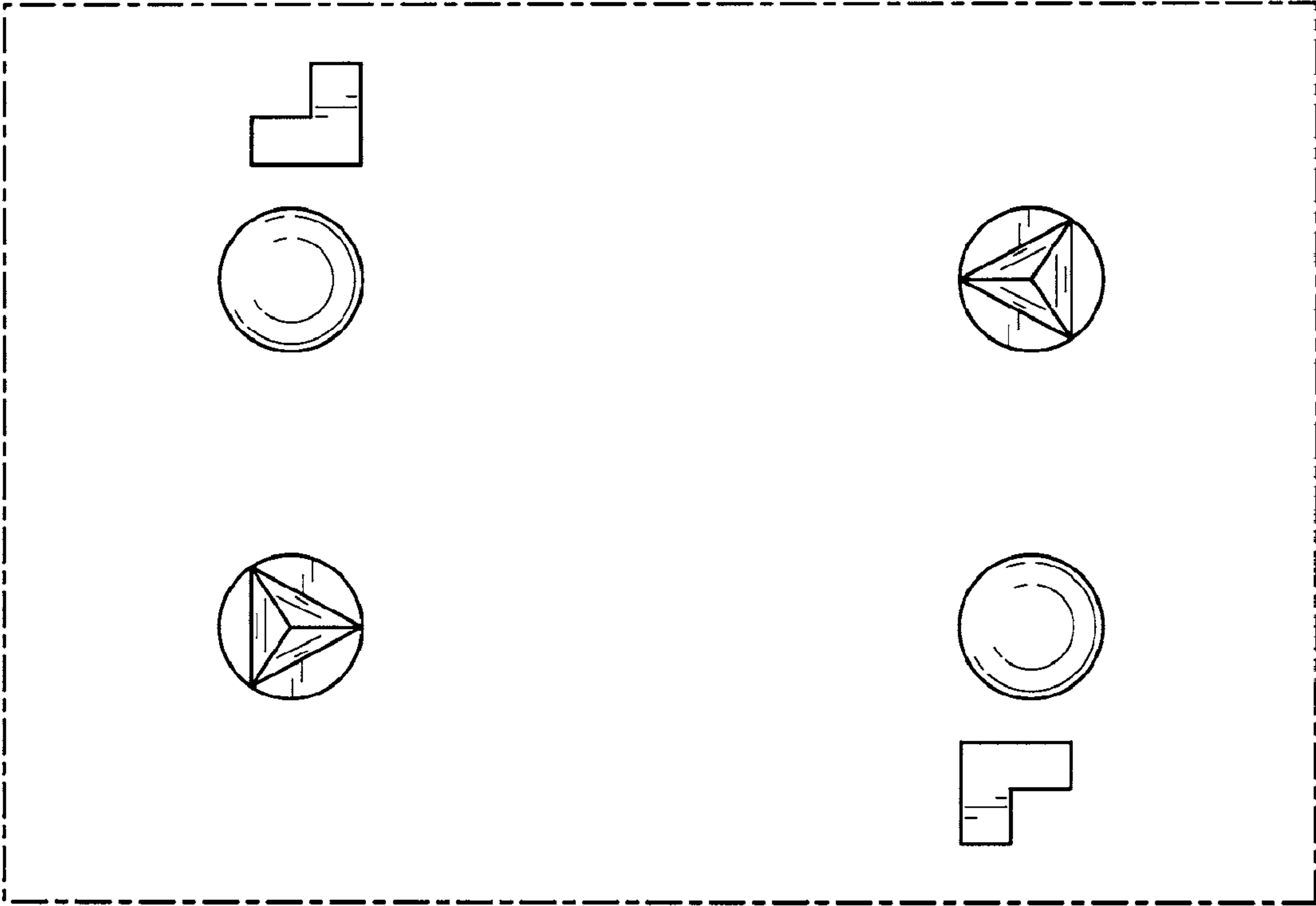


FIG. 6

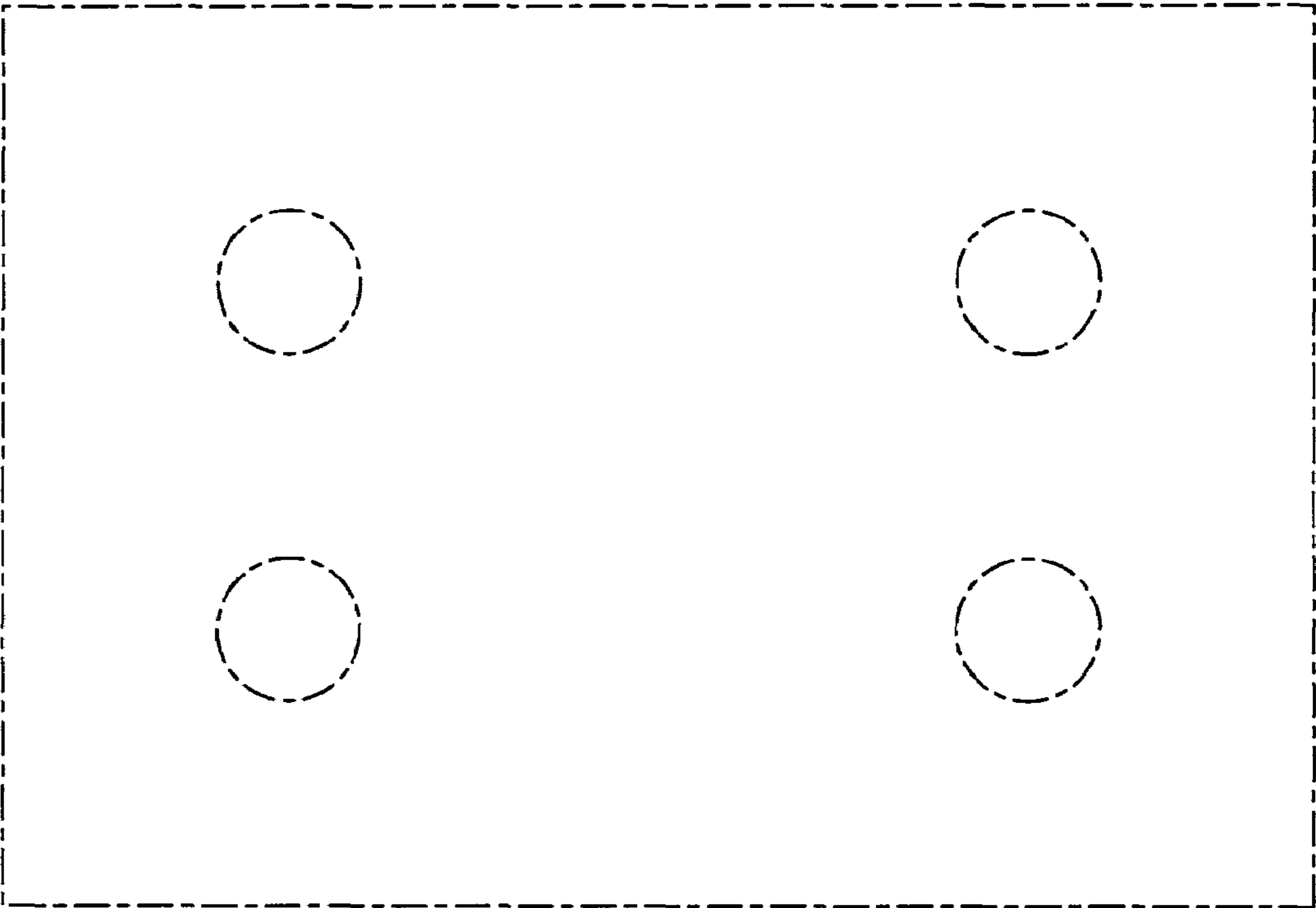


FIG. 7