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(12) **United States Design Patent**
Farmer

(10) **Patent No.:** **US D553,777 S**

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- (54) **LAMP WITH INTEGRAL CLOCK**
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- (73) Assignee: **Verilux, Inc.**, Stamford, CT (US)
- (**) Term: **14 Years**
- (21) Appl. No.: **29/229,884**
- (22) Filed: **May 13, 2005**

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Primary Examiner—Marcus A. Jackson

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- (51) **LOC (8) Cl.** **26-05**
 - (52) **U.S. Cl.** **D26/54**
 - (58) **Field of Classification Search** D26/51–54;
368/10–11, 276–277, 285
- See application file for complete search history.

(57) **CLAIM**

The ornamental design for a lamp with integral clock, as shown and described.

DESCRIPTION

- FIG. 1 is a front perspective view of a lamp with integral clock in accordance with the new design;
- FIG. 2 is a front perspective view thereof;
- FIG. 3 is a front view thereof;
- FIG. 4 is a left side view thereof;
- FIG. 5 is a right side view thereof; and,
- FIG. 6 is a rear view thereof.

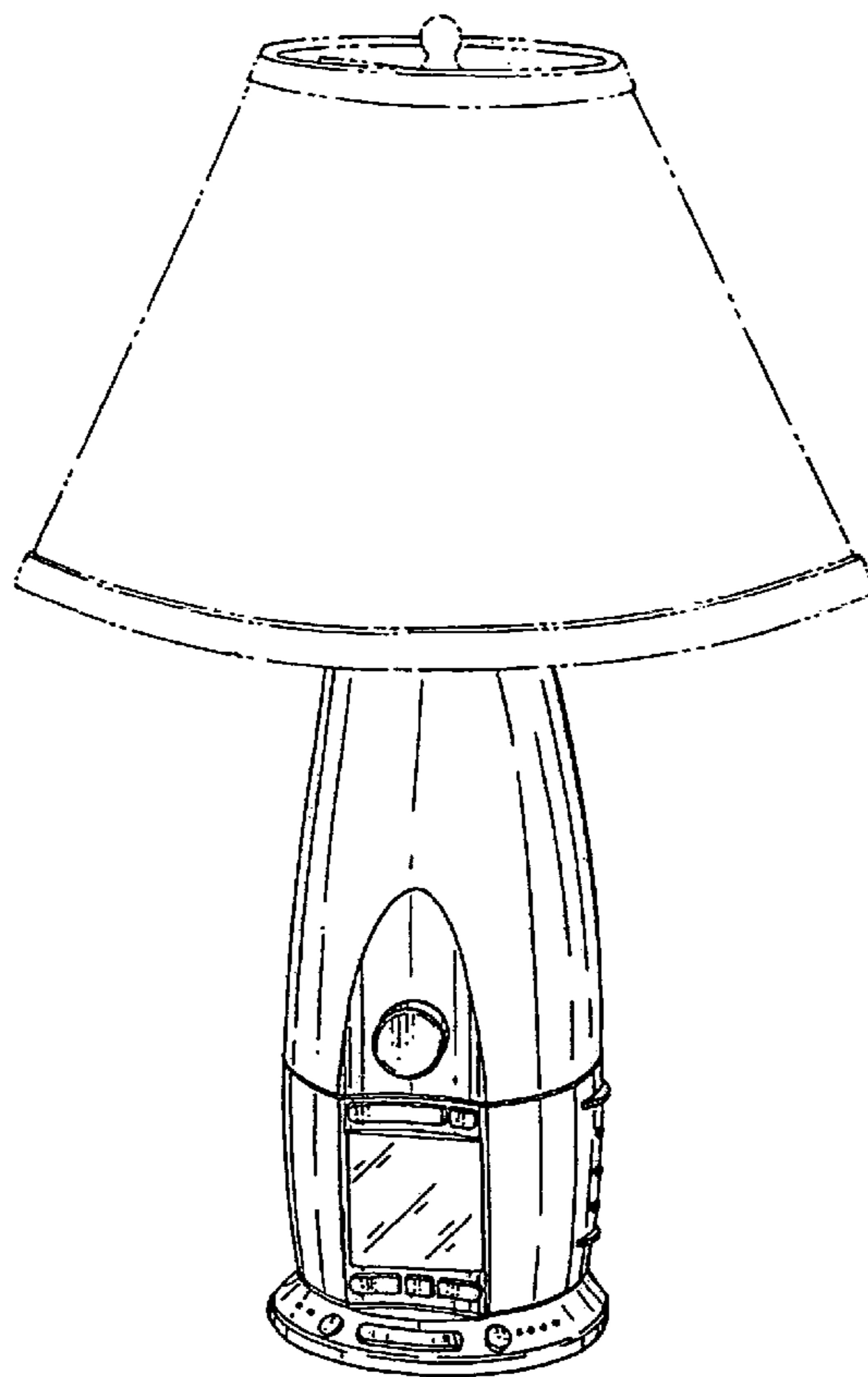
The broken line showing of the lamp shade is included for the purpose of illustrating the lamp with integral clock in use and forms no part of the claimed design.

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1 Claim, 6 Drawing Sheets



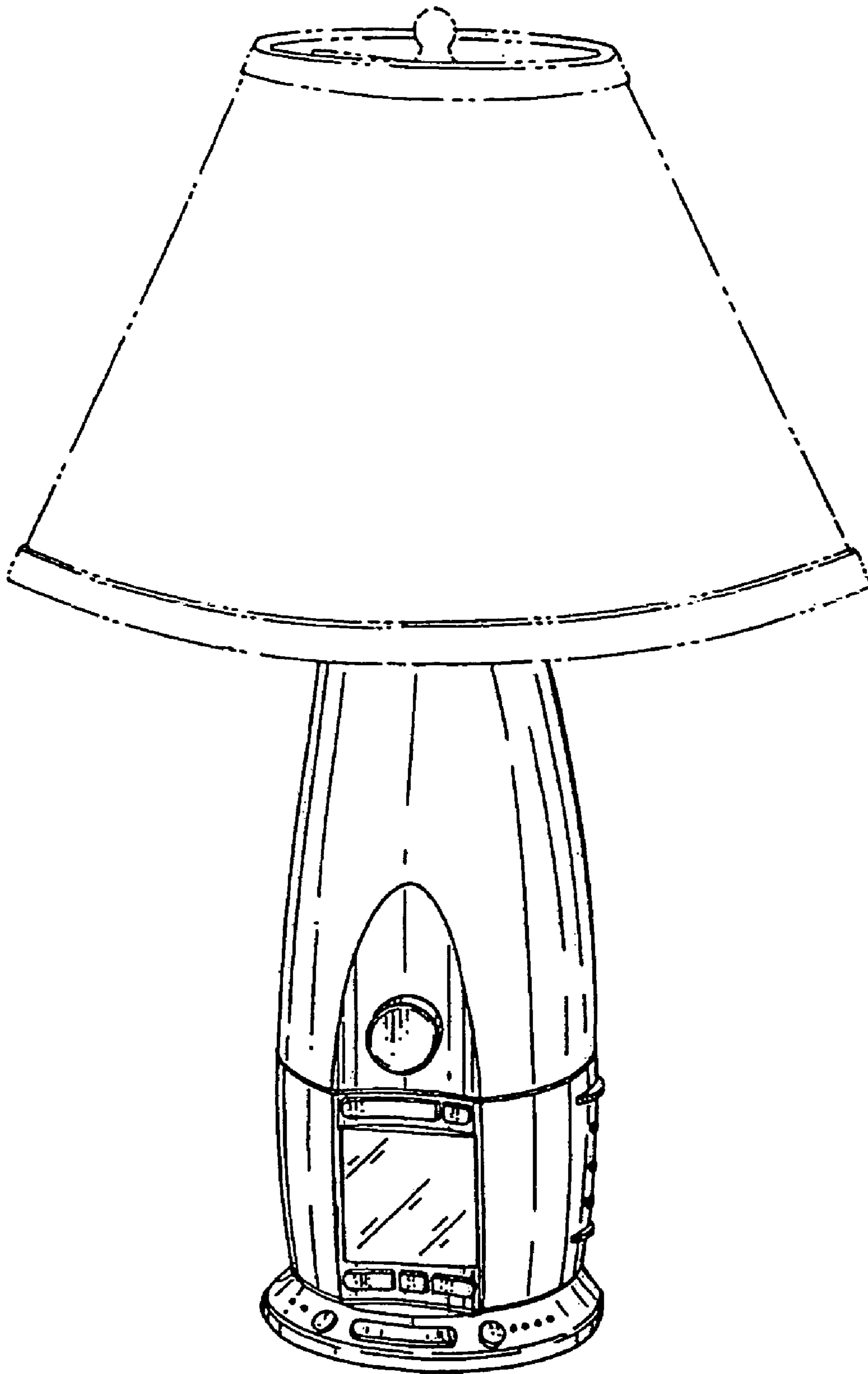


FIG. 1

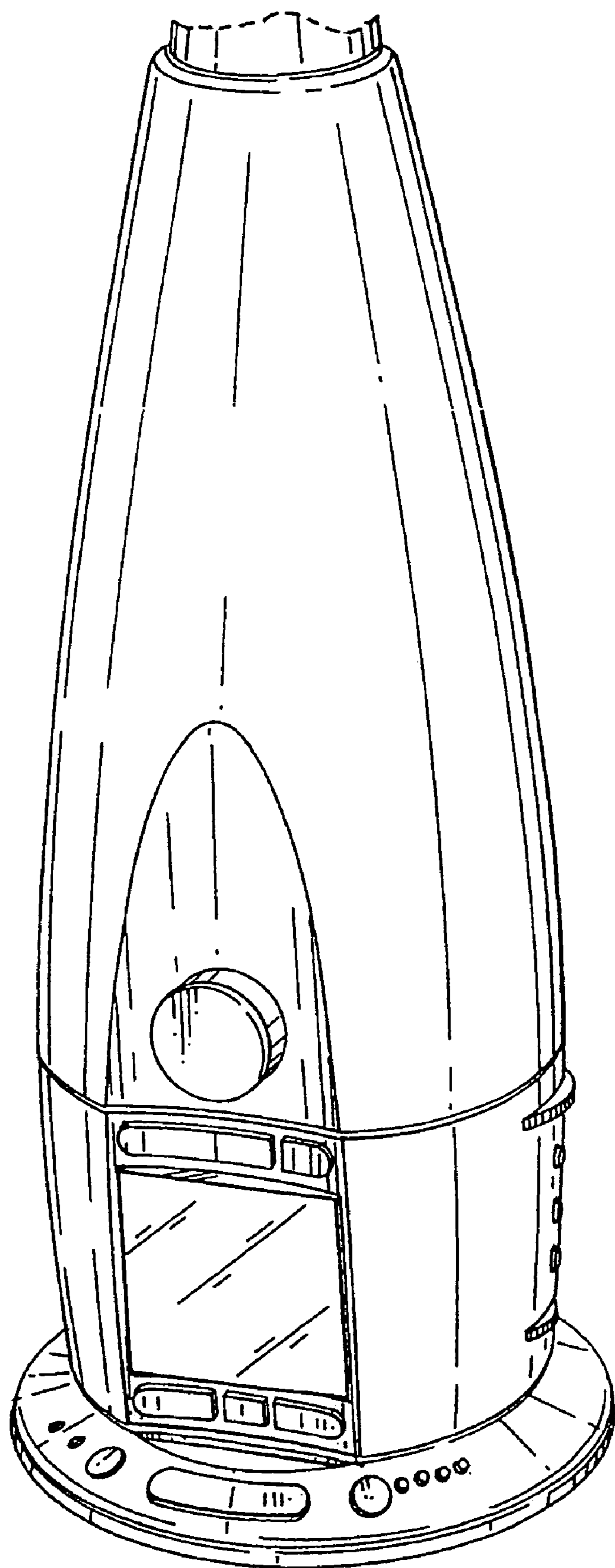


FIG. 2

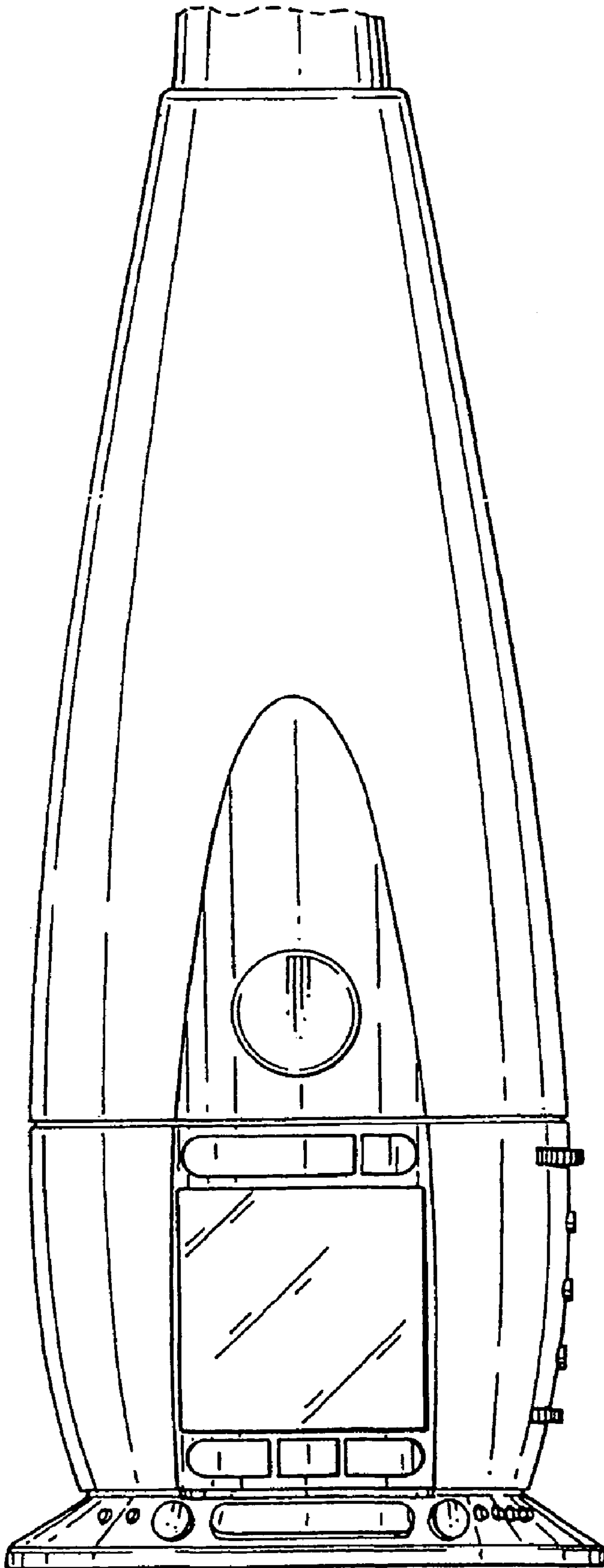


FIG. 3

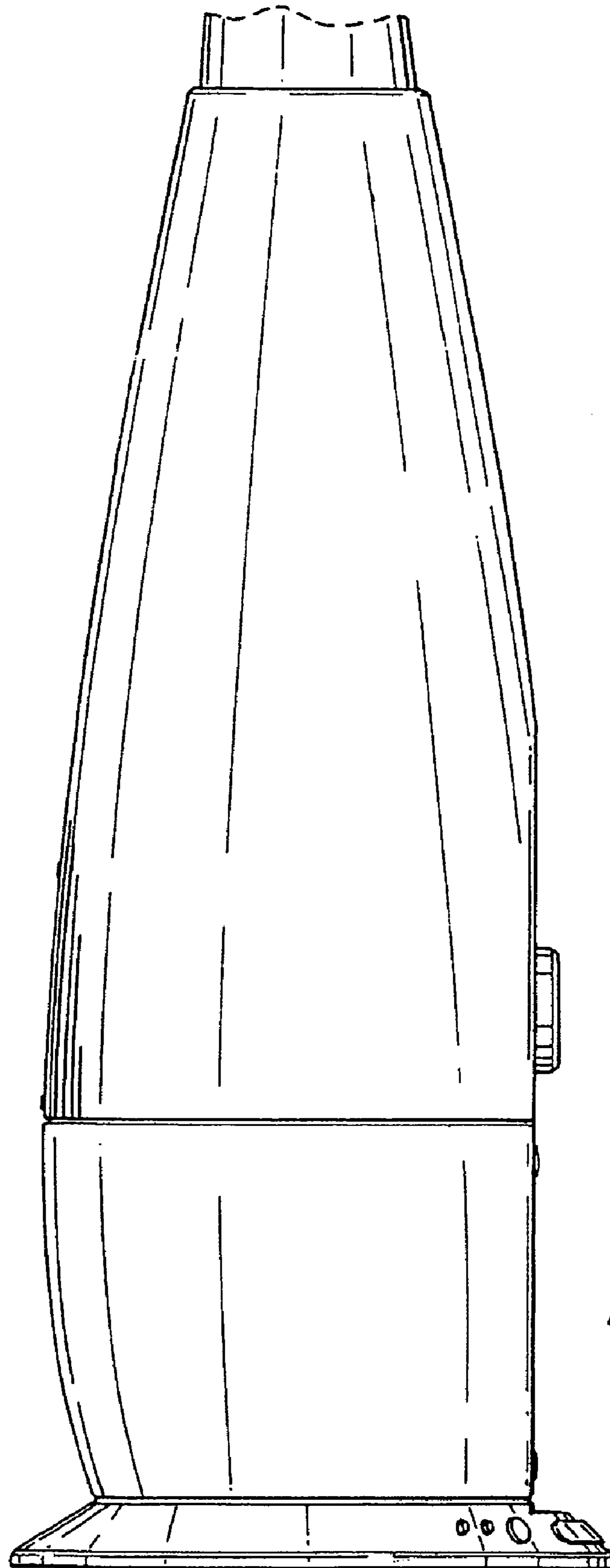


FIG. 4

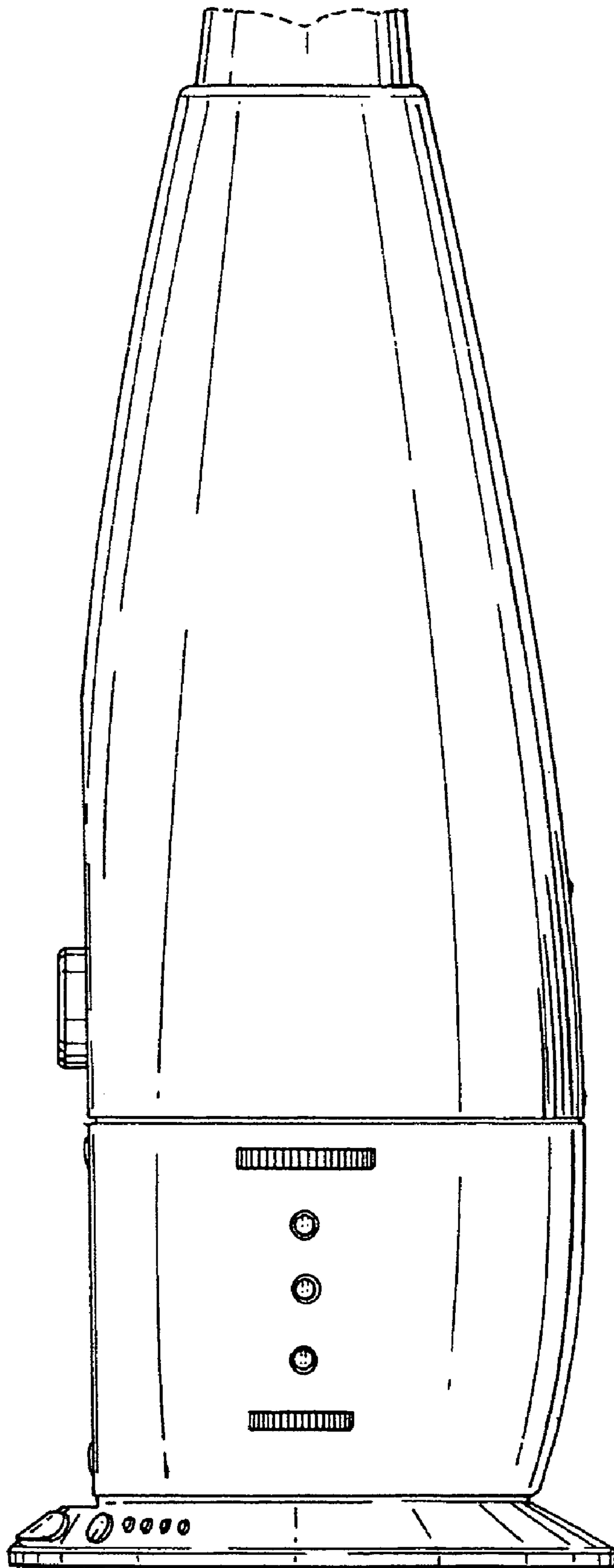


FIG. 5

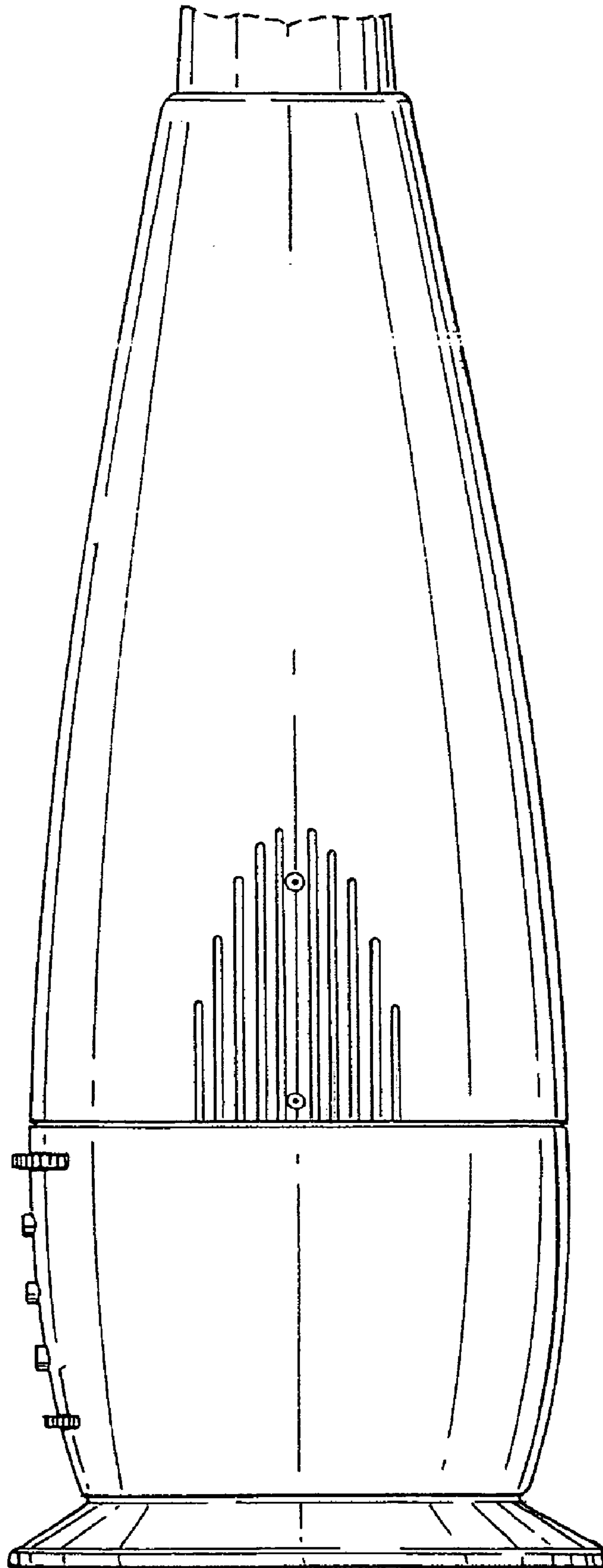


FIG. 6