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(12) **United States Design Patent** (10) **Patent No.:** **US D553,103 S**
Sumitani (45) **Date of Patent:** **** Oct. 16, 2007**

(54) **LIGHT EMITTING DIODE**

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(73) Assignee: **Nichia Corporation**, Anan-shi (JP)

(**) Term: **14 Years**

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Related U.S. Application Data

(62) Division of application No. 29/265,890, filed on Sep. 12, 2006, now Pat. No. Des. 539,760, which is a division of application No. 29/258,944, filed on May 1, 2006, now Pat. No. Des. 535,263, which is a division of application No. 29/244,841, filed on Dec. 15, 2005, now Pat. No. Des. 528,515, which is a division of application No. 29/226,509, filed on Mar. 30, 2005, now Pat. No. Des. 522,466, which is a division of application No. 29/200,831, filed on Mar. 5, 2004, now Pat. No. Des. 510,913.

(30) **Foreign Application Priority Data**

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(51) **LOC (8) Cl.** **13-03**

(52) **U.S. Cl.** **D13/180**

(58) **Field of Classification Search** D13/180;
D26/2; 257/79, 80, 81, 88, 89, 95-100, 433,
257/434, 676, 678, 680, 690; 313/483, 498,
313/500; 361/820; 362/555, 800; 372/45.01
See application file for complete search history.

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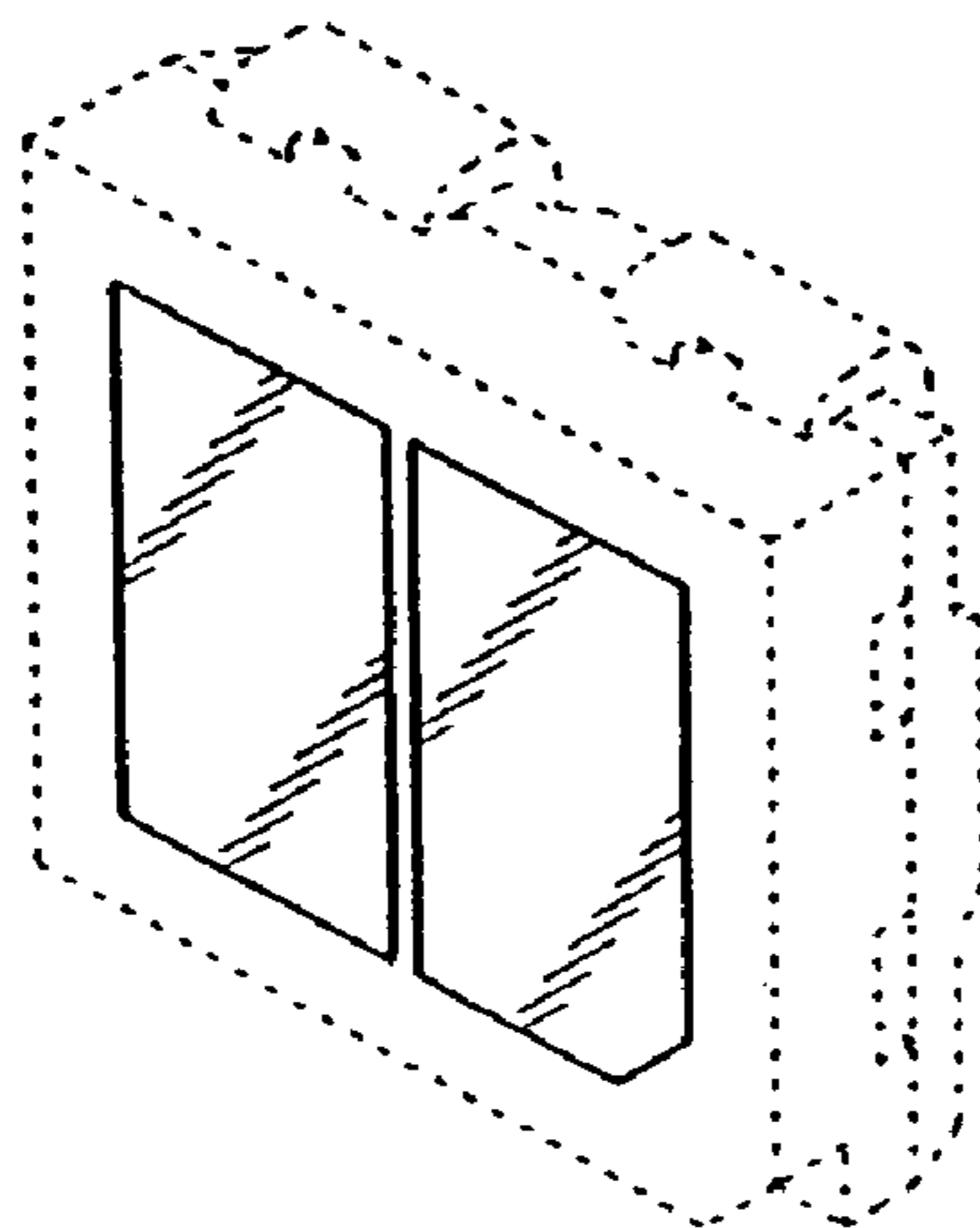
(57) **CLAIM**

I claim the ornamental design for a light emitting diode, as shown and described.

DESCRIPTION

FIG. 1 is a front perspective view of a light emitting diode showing my new design;
FIG. 2 is a rear perspective view thereof;
FIG. 3 is a top plan view thereof;
FIG. 4 is a left side elevation view thereof;
FIG. 5 is a front elevation view thereof;
FIG. 6 is a right side elevation view thereof;
FIG. 7 is a bottom plan view thereof; and,
FIG. 8 is a rear elevation view thereof.
The broken line showing of the environment is for illustrative purpose only and forms no part of the claimed design.

1 Claim, 2 Drawing Sheets



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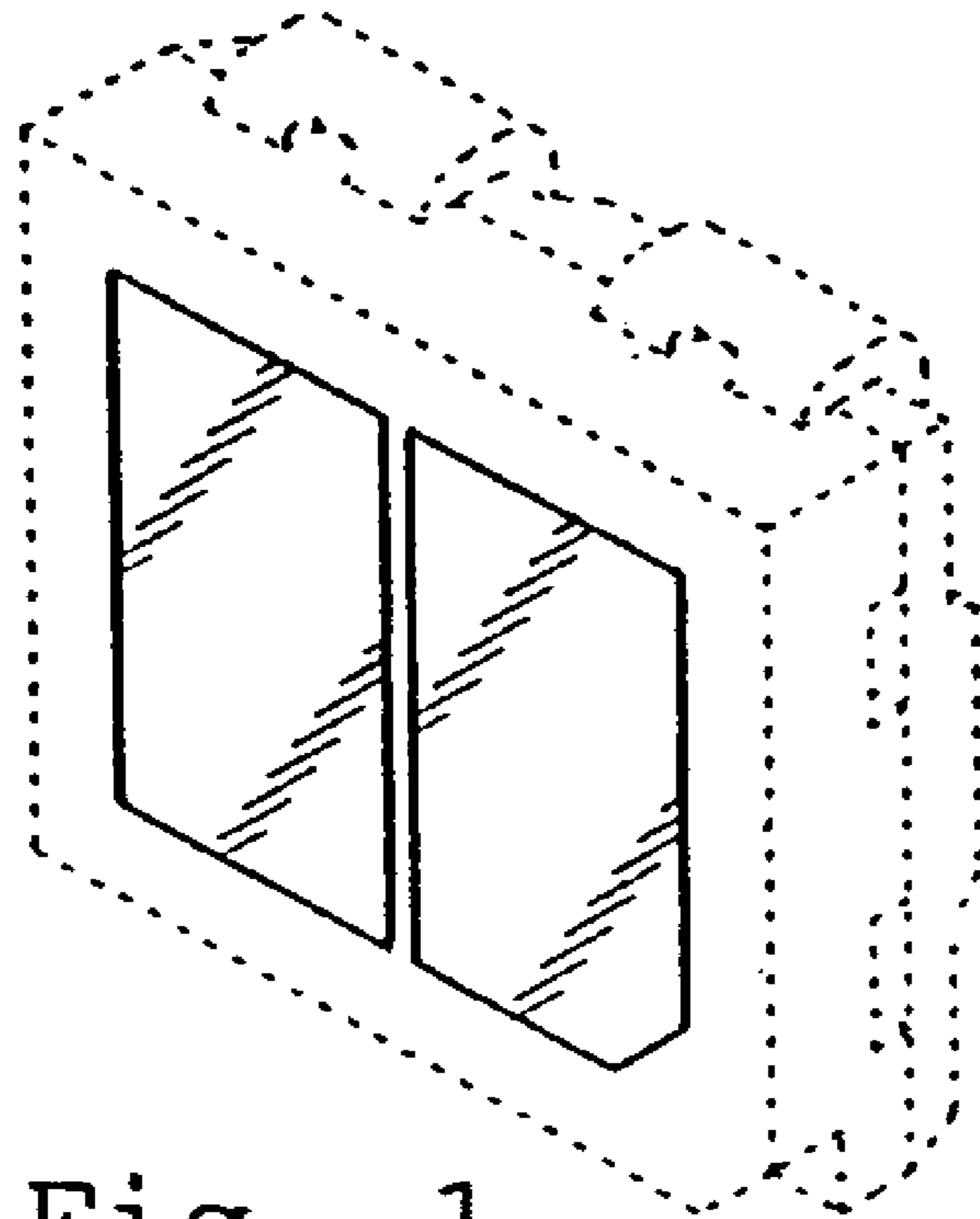


Fig. 1

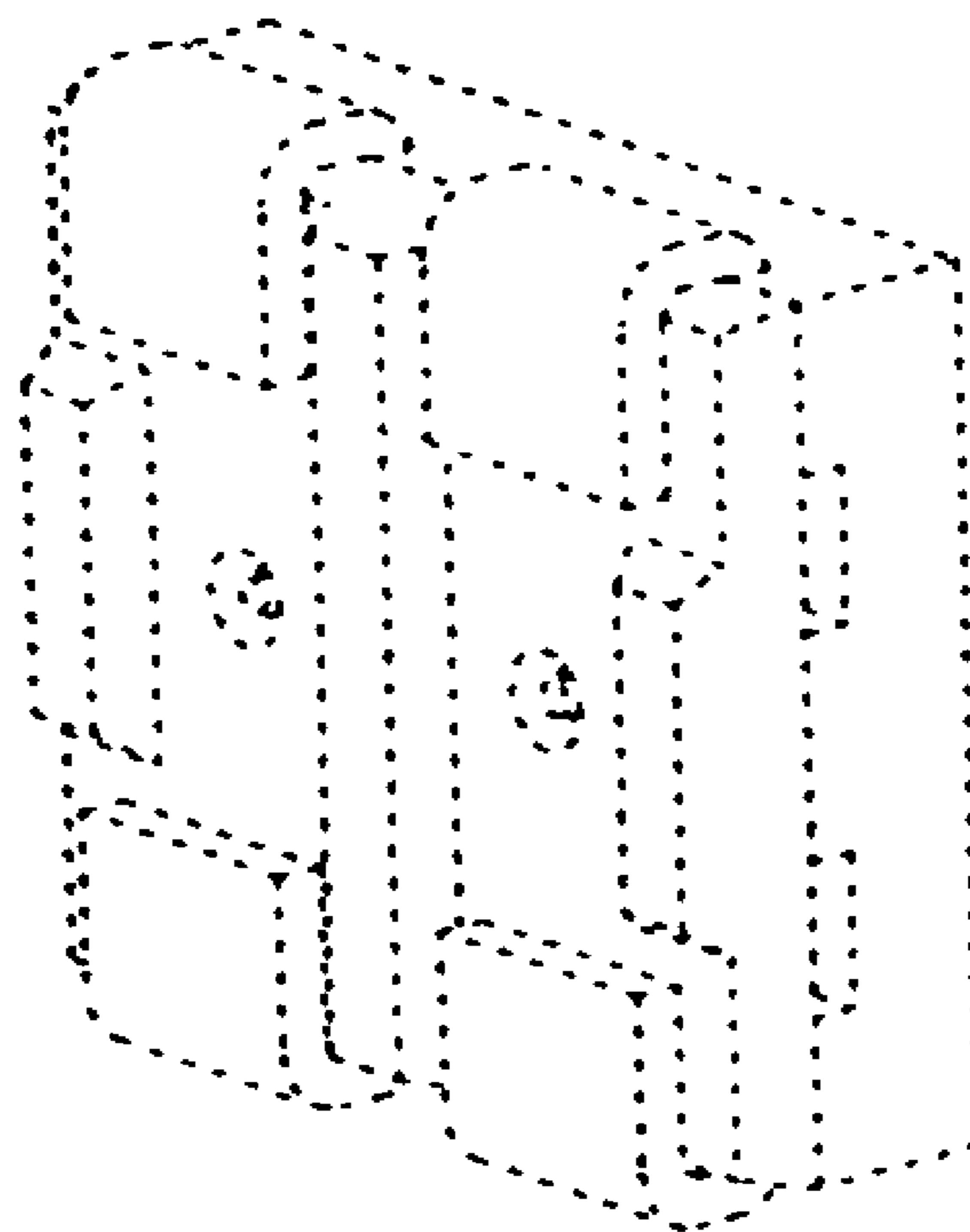


Fig. 2

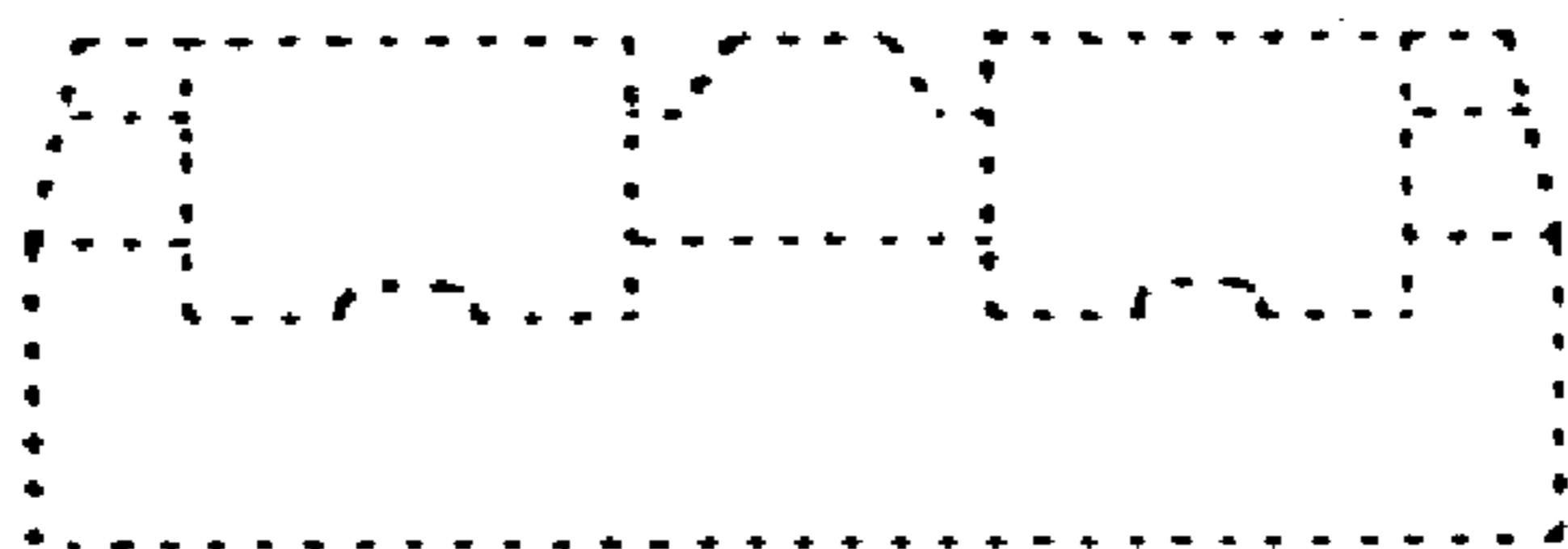


Fig. 3

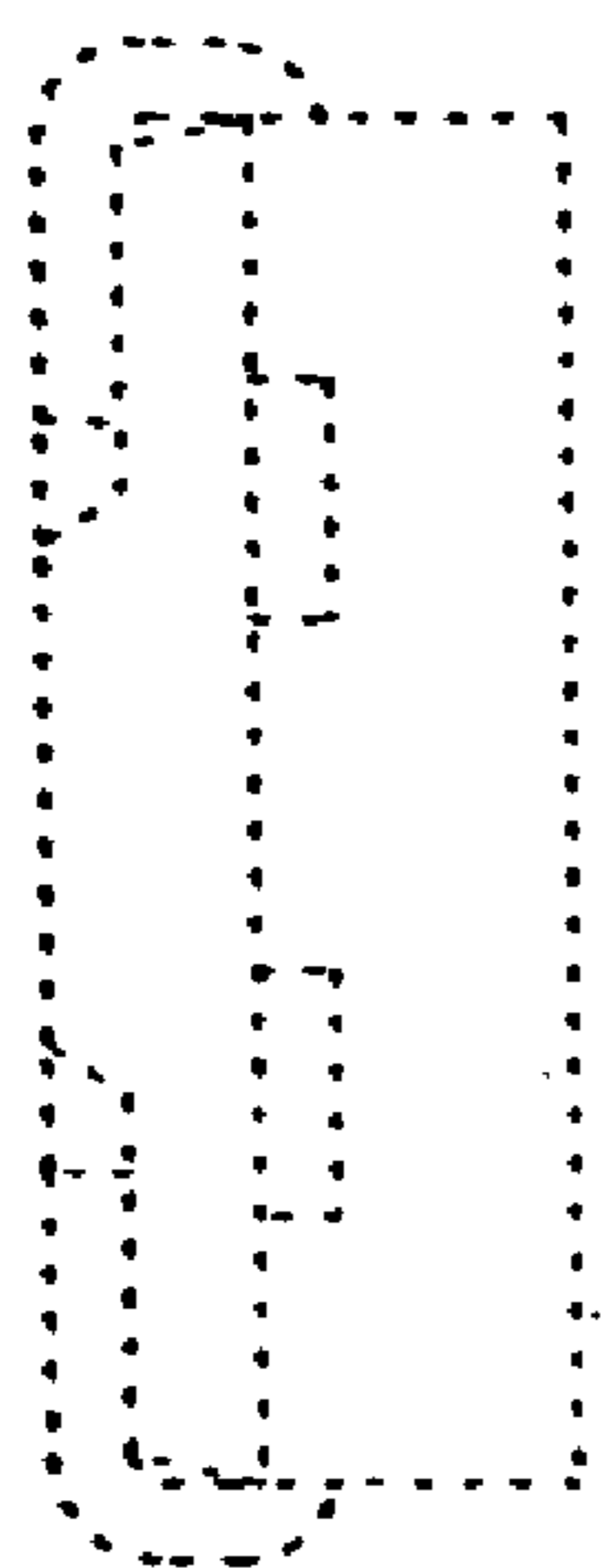


Fig. 4

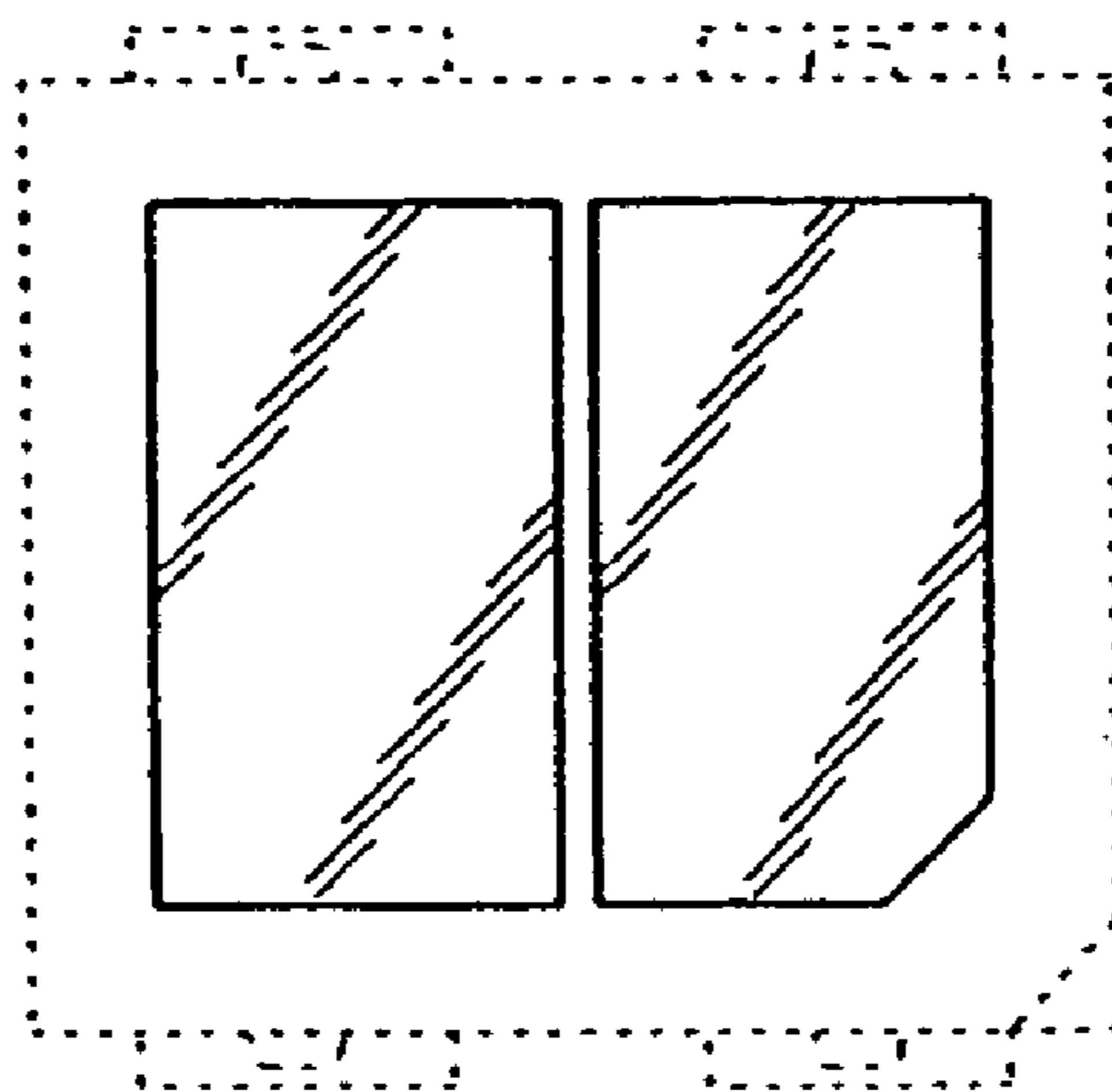


Fig. 5

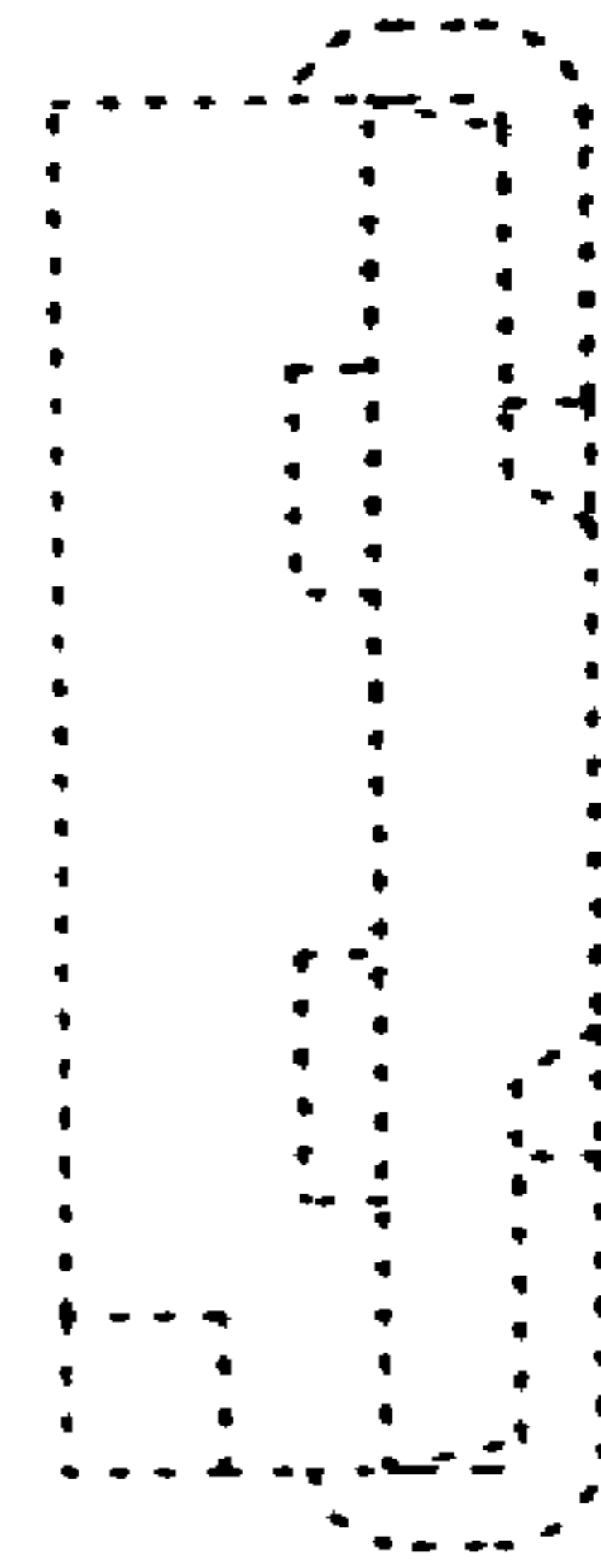


Fig. 6

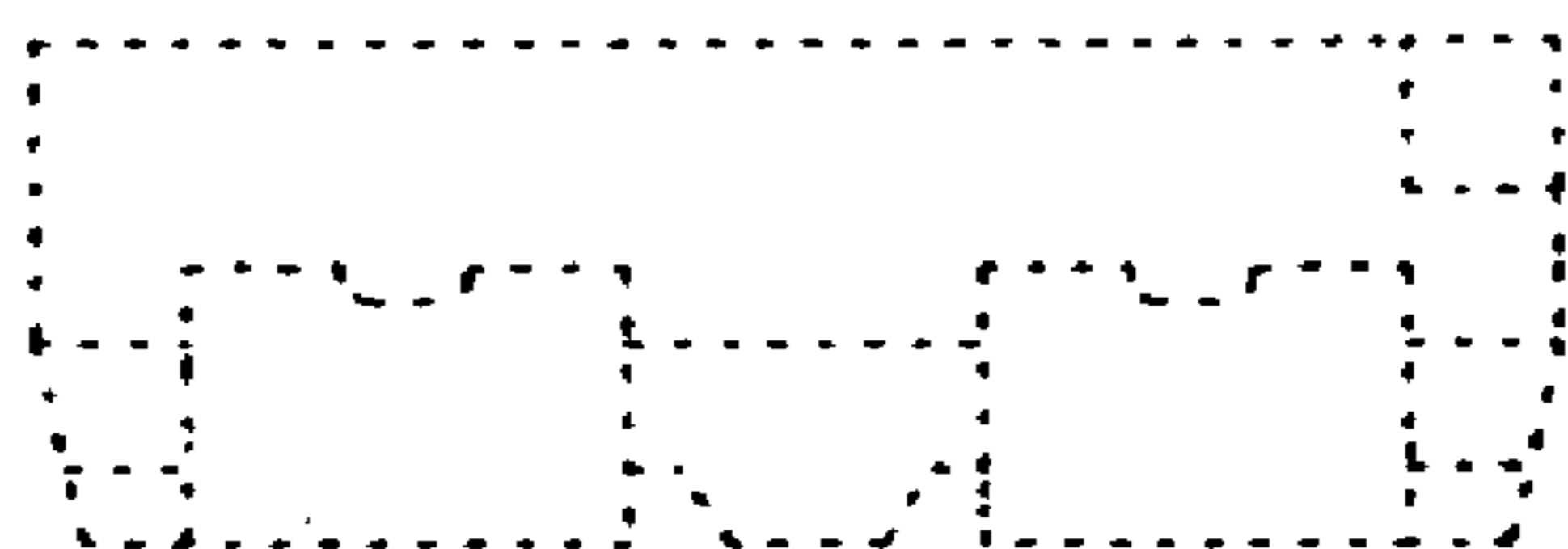


Fig. 7

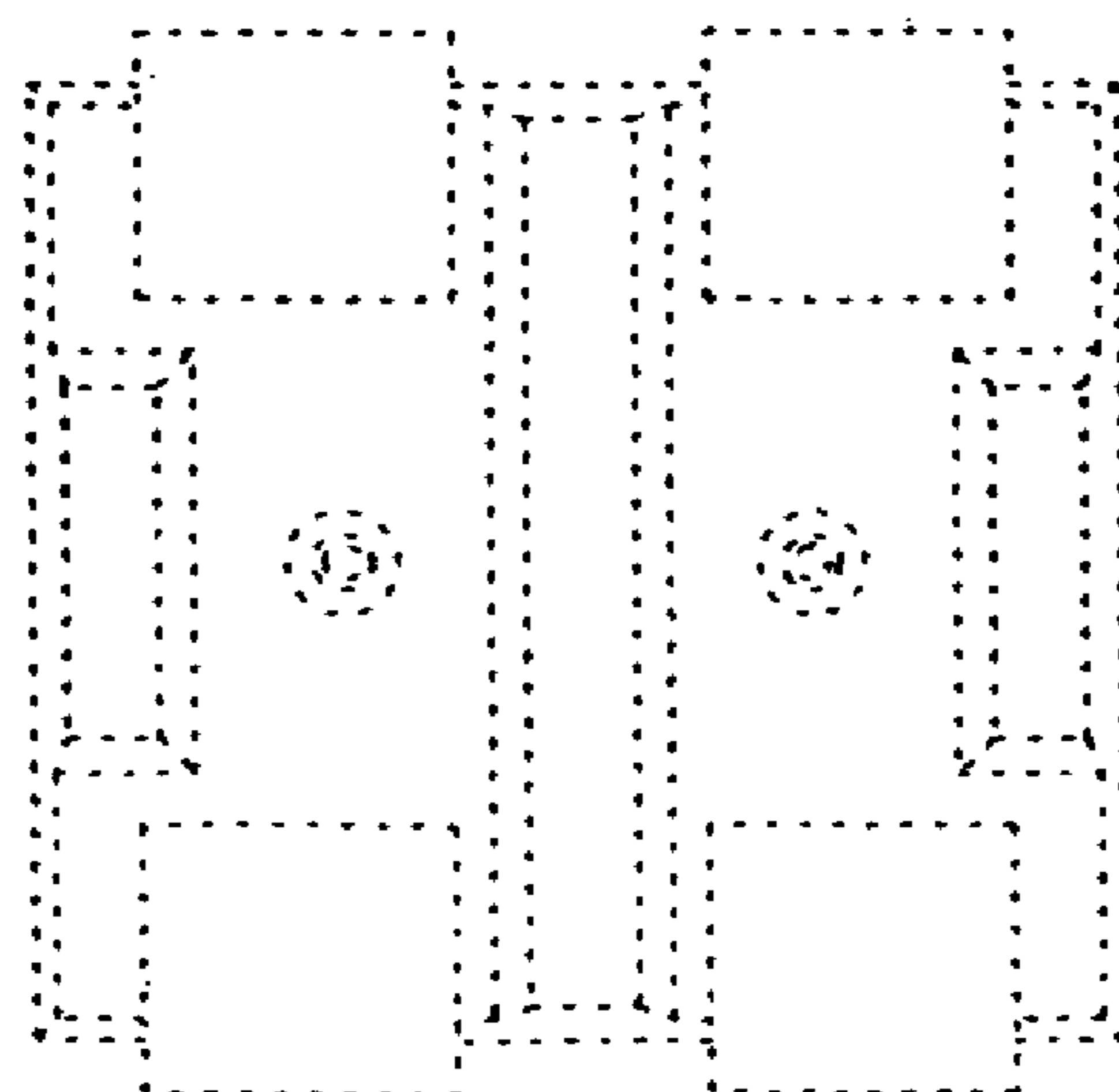


Fig. 8