



US00D552048S

(12) **United States Design Patent** (10) **Patent No.:** **US D552,048 S**
He (45) **Date of Patent:** **** Oct. 2, 2007**

(54) **PRINTED CIRCUIT BOARD**

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(**) Term: **14 Years**

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(30) **Foreign Application Priority Data**

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(51) **LOC (8) Cl.** **13-03**

(52) **U.S. Cl.** **D13/182**

(58) **Field of Classification Search** D13/182;
174/250, 251, 253-256, 260, 261, 265; 361/720,
361/748, 760; 428/901

See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a printed circuit board, as shown and described.

DESCRIPTION

FIG. 1 is a top, front, and right side perspective view of a printed circuit board showing my new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a rear elevational view thereof;

FIG. 4 is a right-side, elevational view thereof;

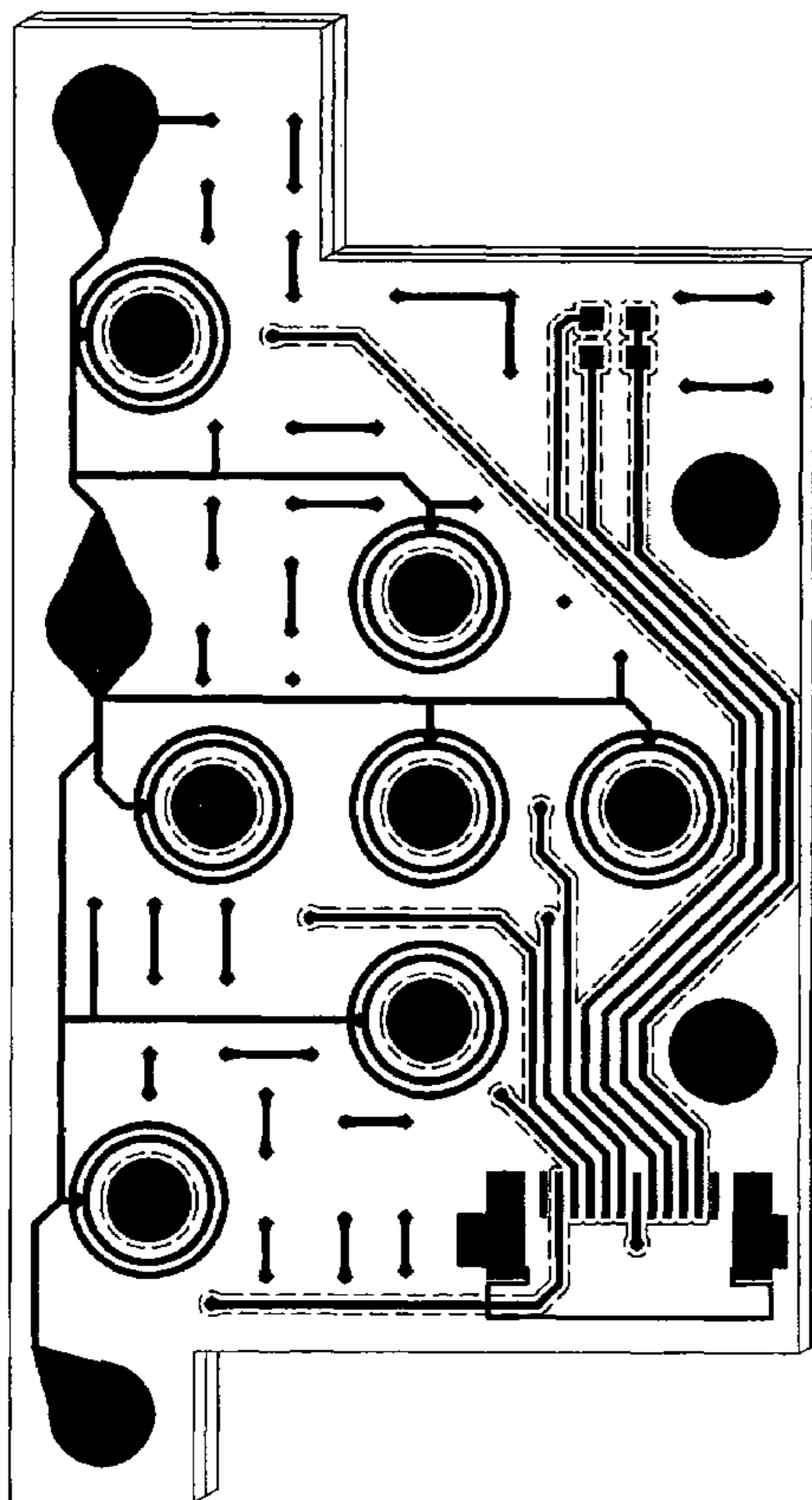
FIG. 5 is a left-side, elevational view thereof;

FIG. 6 is a bottom plan view thereof; and,

FIG. 7 is a top plan view thereof.

(Portions of the printed circuit board shown in broken lines form no part of the claimed design.)

1 Claim, 5 Drawing Sheets



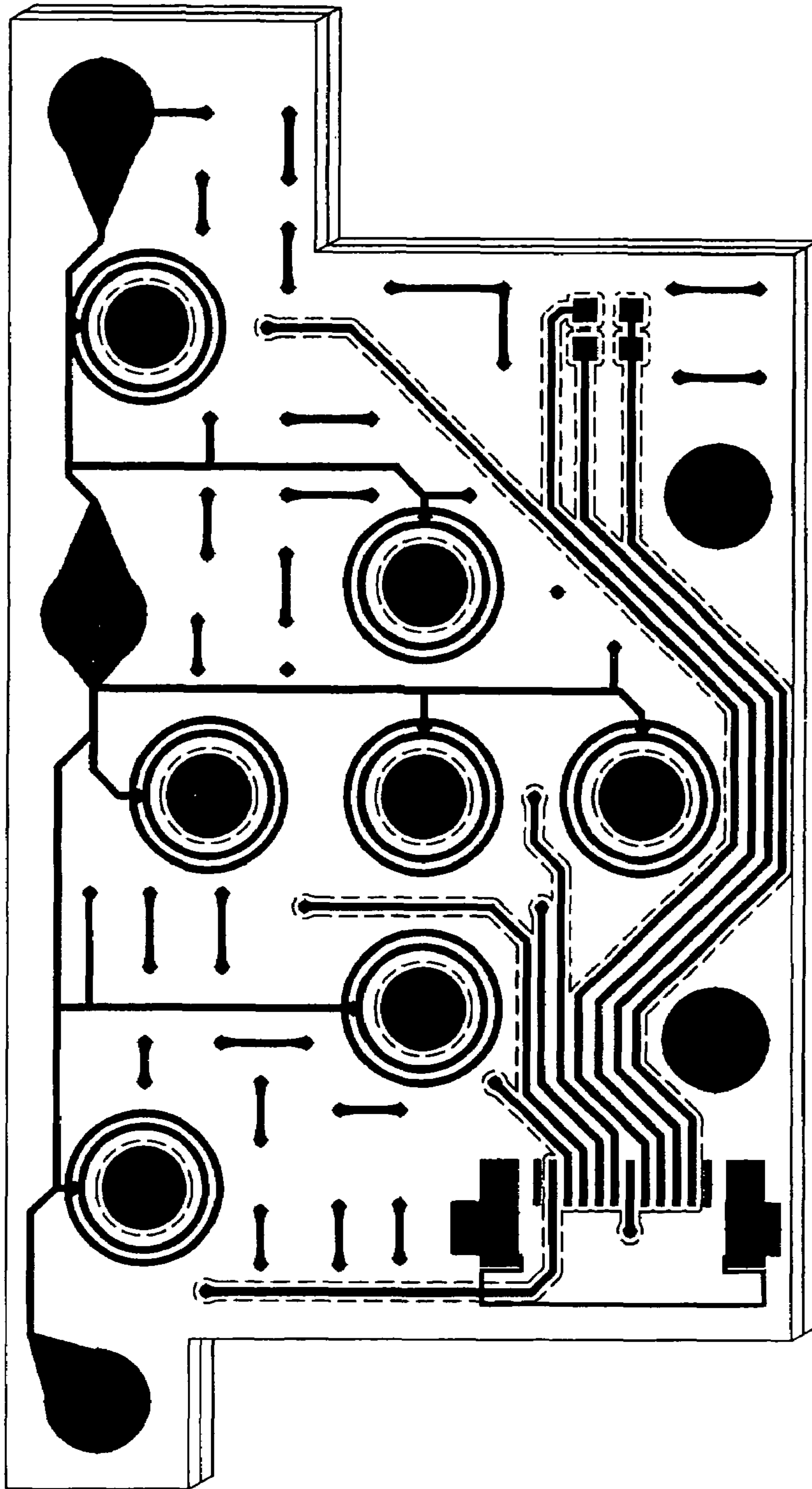


FIG. 1

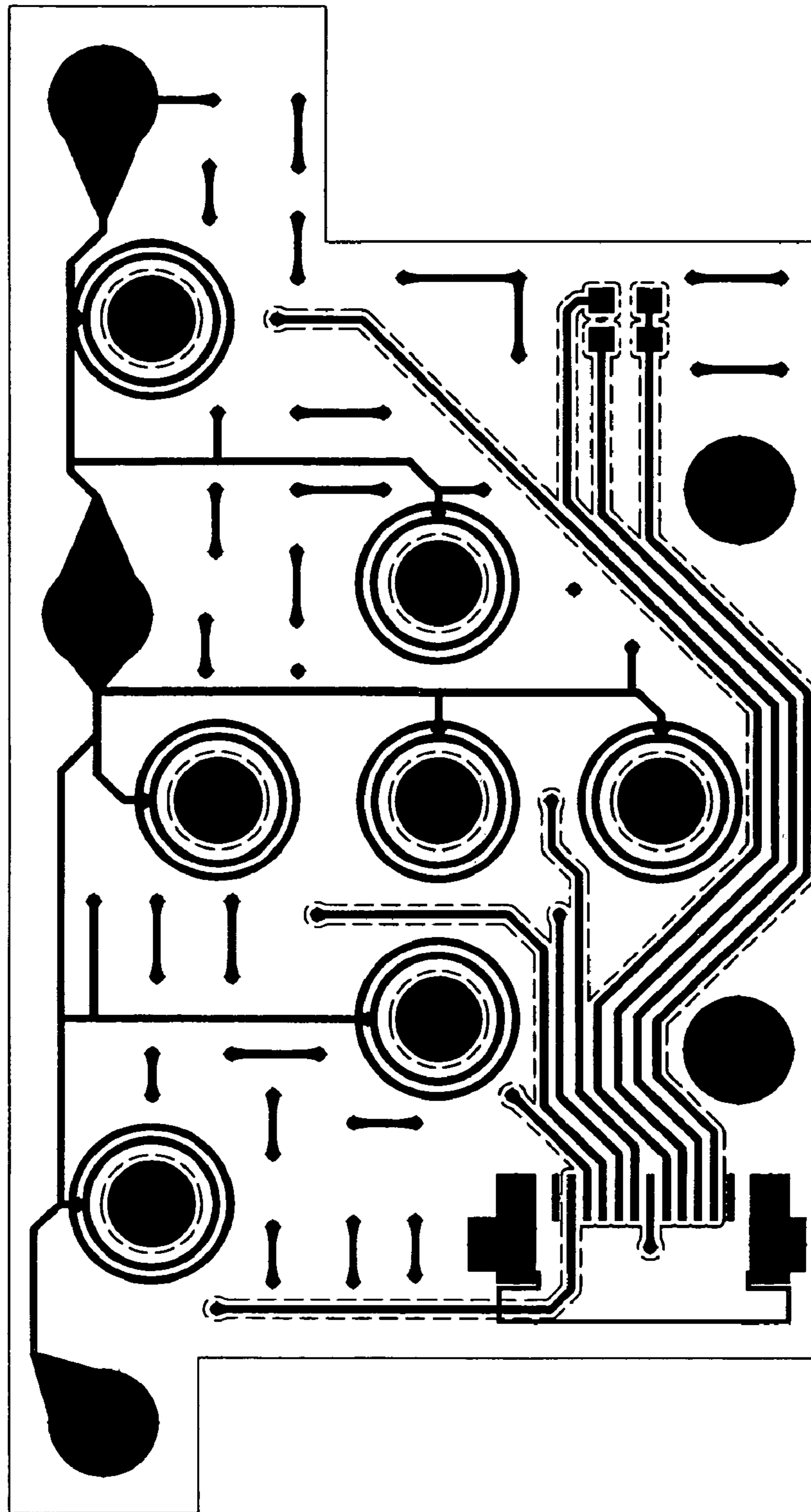


FIG. 2

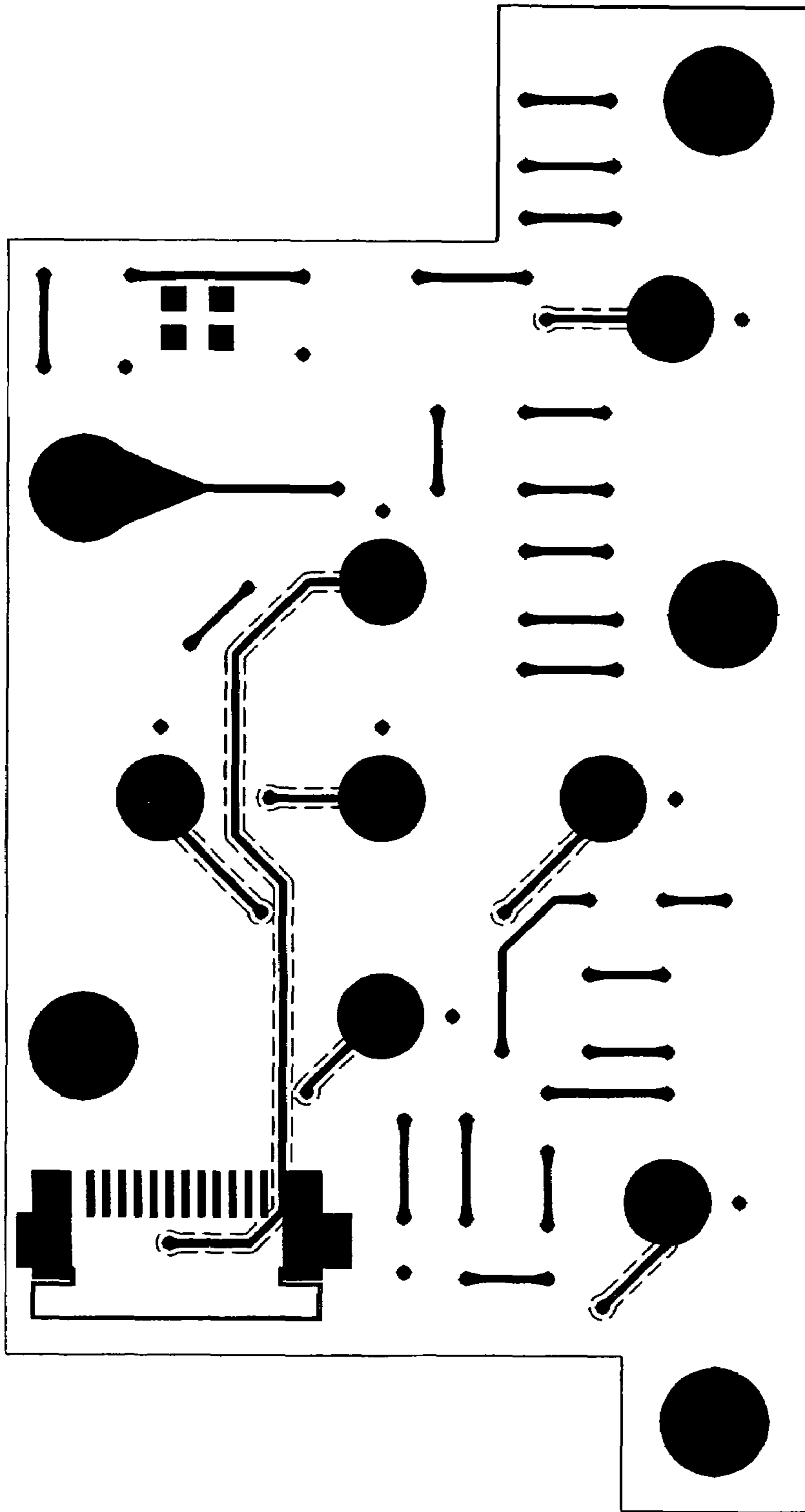


FIG. 3



FIG. 4



FIG. 5

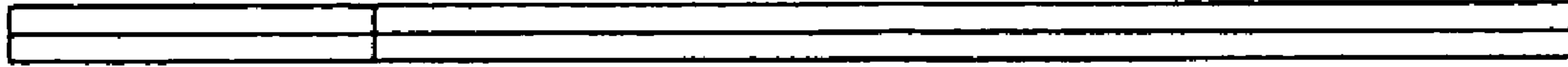


FIG. 6

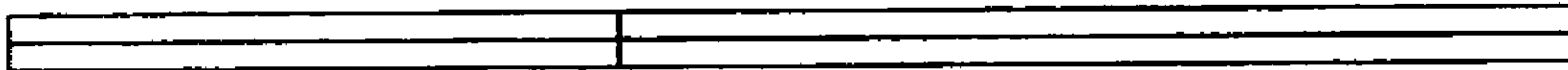


FIG. 7