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(12) **United States Design Patent**
Dayan

(10) **Patent No.:** **US D551,166 S**

(45) **Date of Patent:** **** Sep. 18, 2007**

(54) **COMBINED MODULATOR AND POWER INVERTER**

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(73) Assignee: **Intellectual Solutions, Inc.**

(**) Term: **14 Years**

(21) Appl. No.: **29/277,216**

(22) Filed: **Feb. 19, 2007**

Related U.S. Application Data

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(51) **LOC (8) Cl.** **13-02**

(52) **U.S. Cl.** **D13/110; D14/155**

(58) **Field of Classification Search** D13/110, D13/107, 108, 144, 199; D14/155, 240; 307/150, 151; 361/600; 439/668, 669
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a combined modulator and power inverter, as shown and described.

DESCRIPTION

FIG. 1 is a top perspective view of a combined modulator and power inverter showing my new design;

FIG. 2 is a front view thereof;

FIG. 3 is a rear view thereof;

FIG. 4 is a left side view thereof;

FIG. 5 is a right side view thereof;

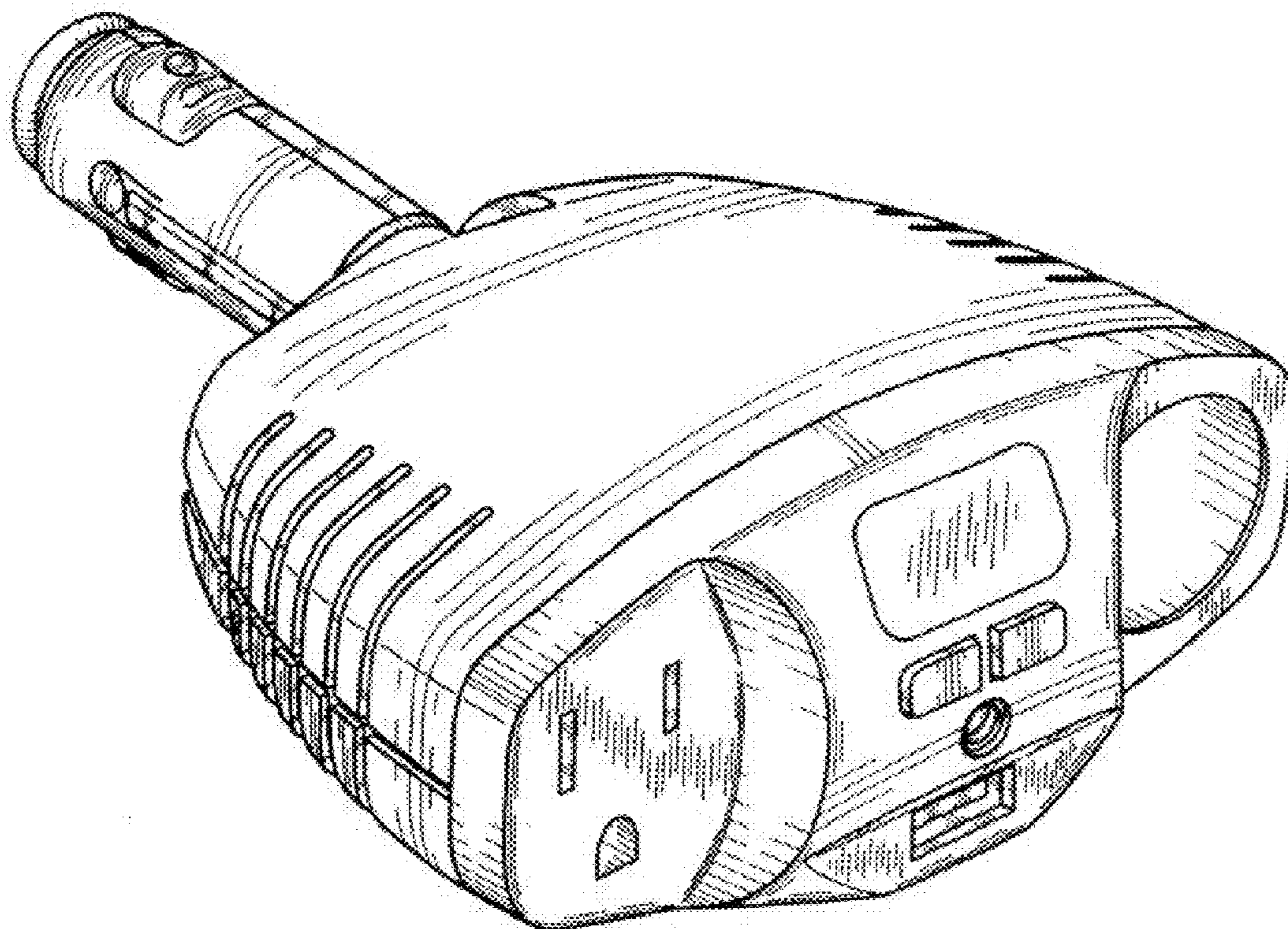
FIG. 6 is a top plan view thereof;

FIG. 7 is a bottom plan view thereof;

FIG. 8 is a right side upward articulated view thereof; and,

FIG. 9 is a right side downward articulated view thereof.

1 Claim, 6 Drawing Sheets



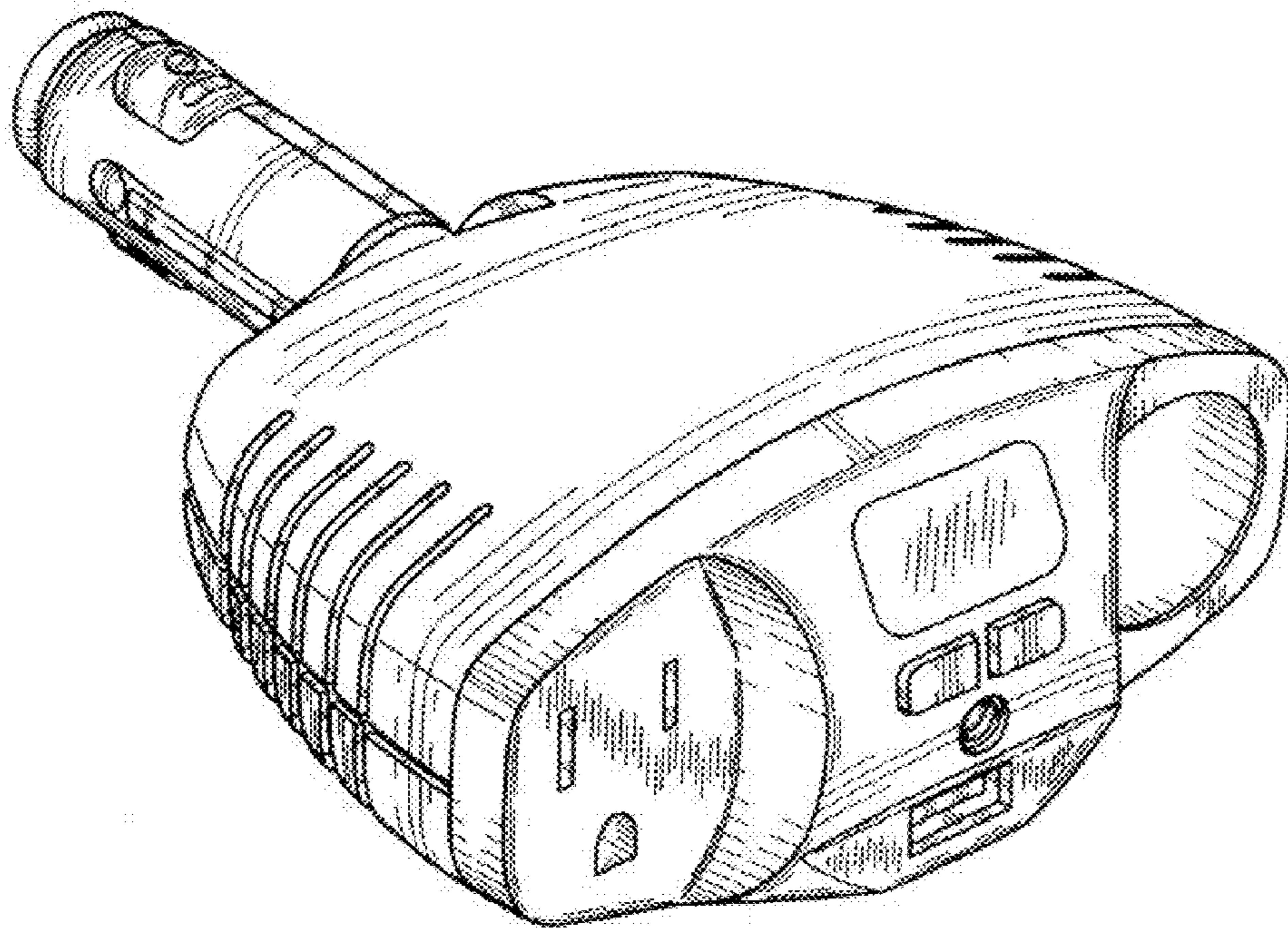


FIG. 1

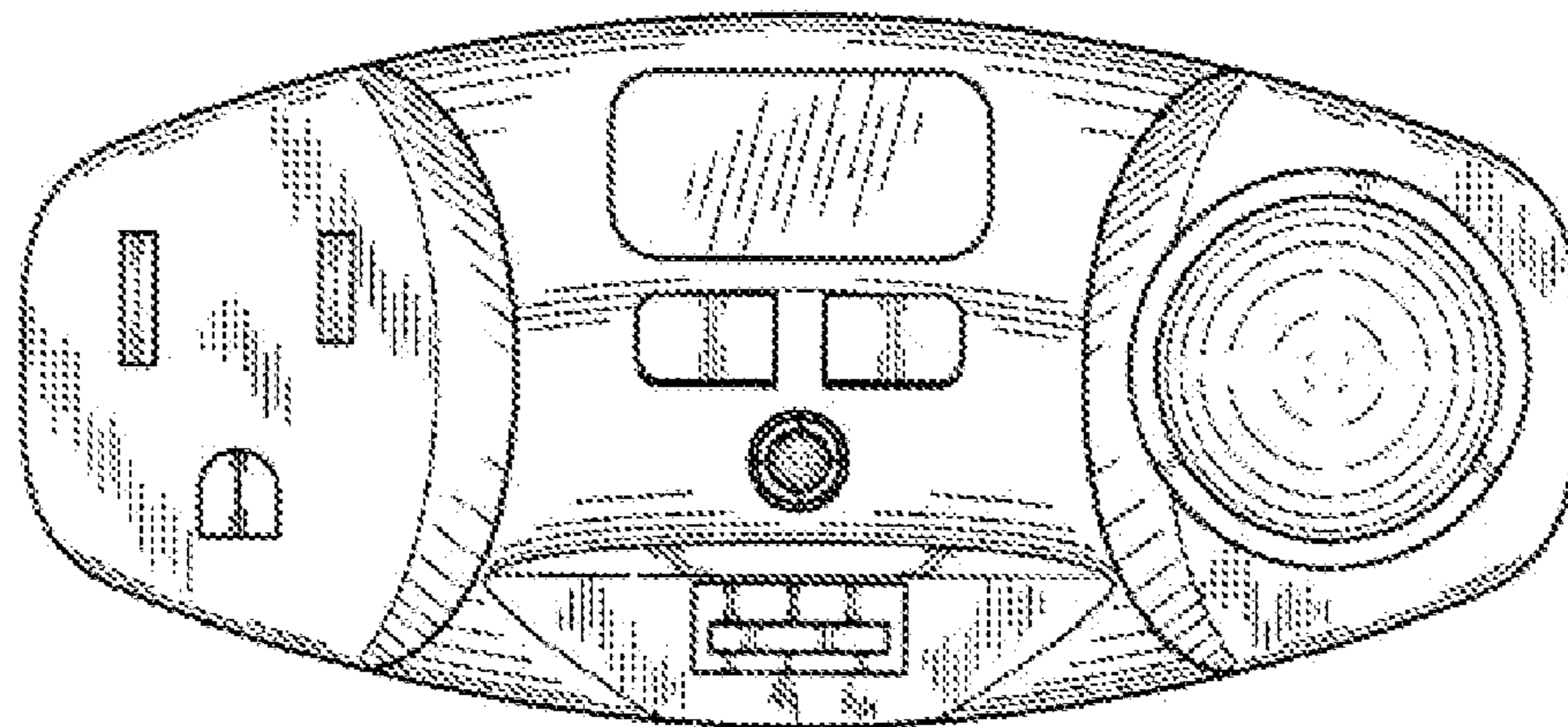


FIG. 2

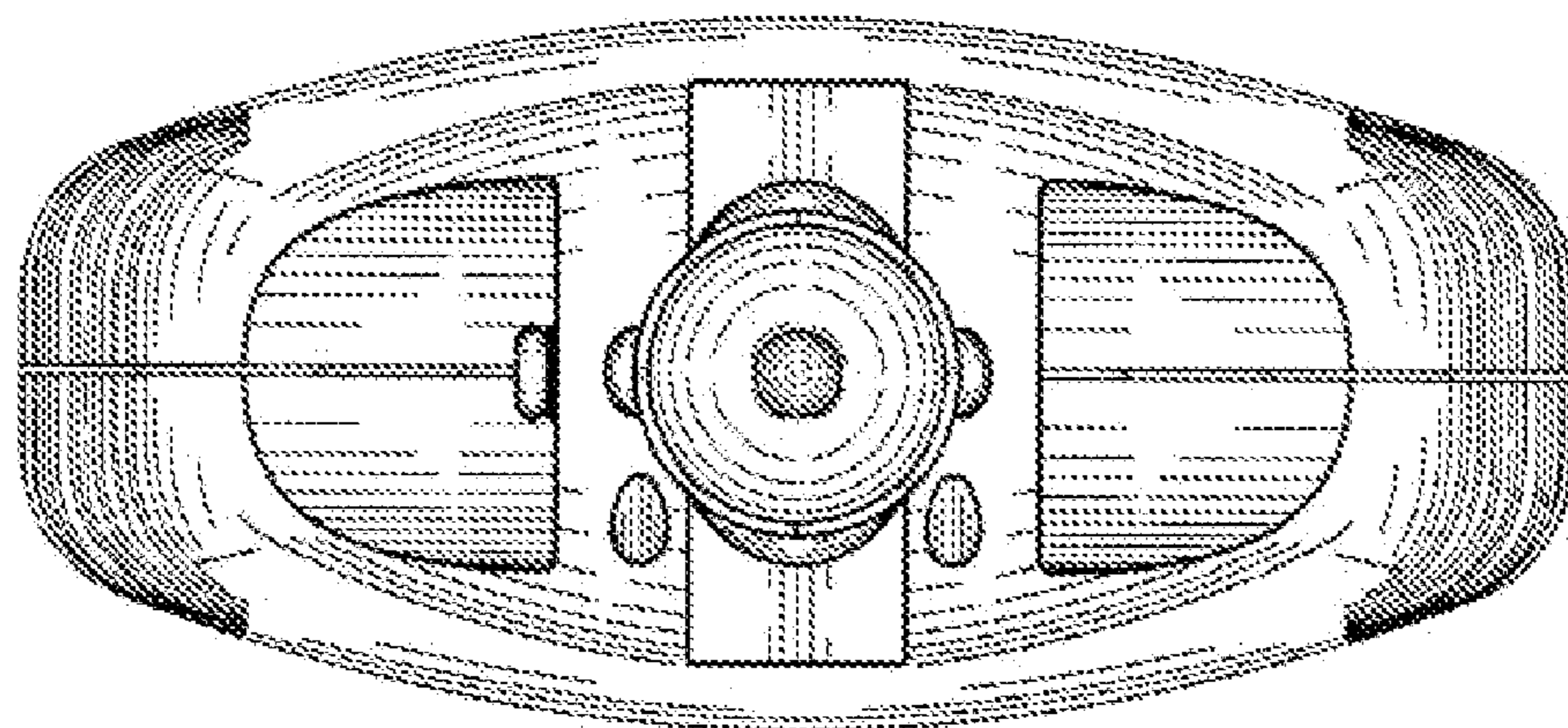


FIG. 3

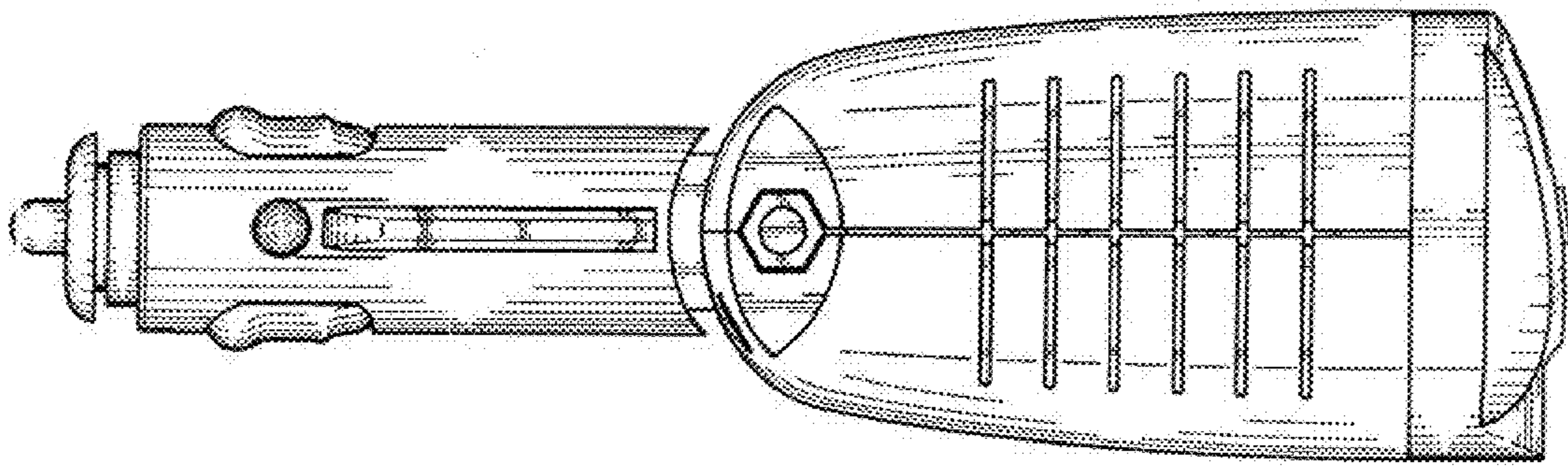


FIG. 4

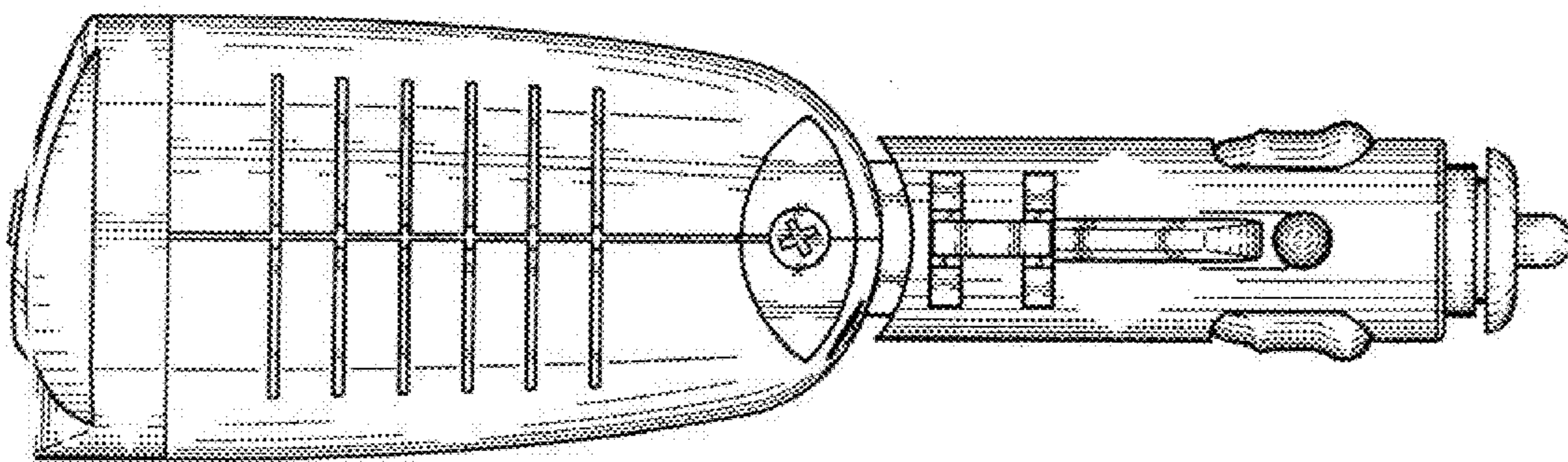


FIG. 5

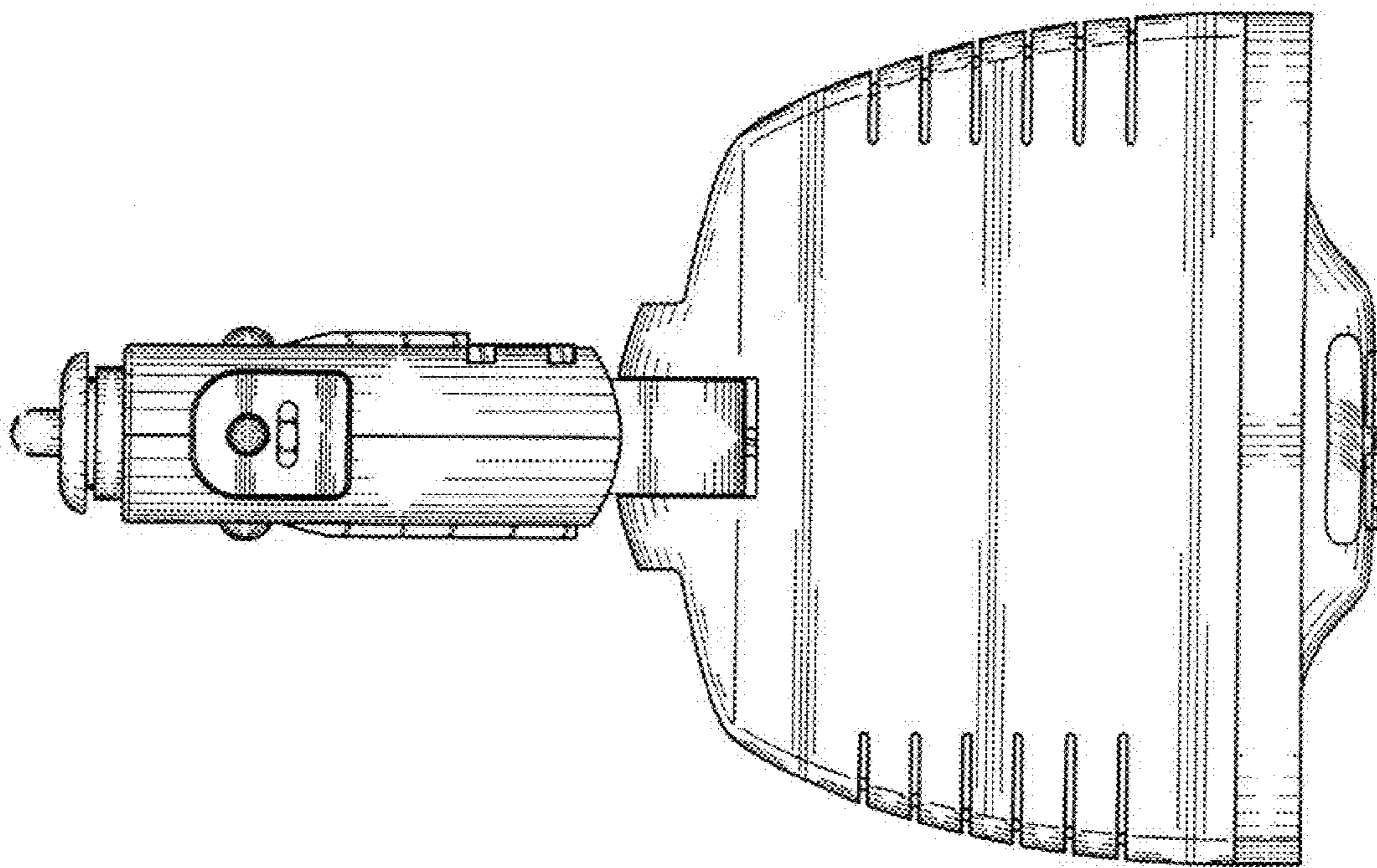


FIG. 6

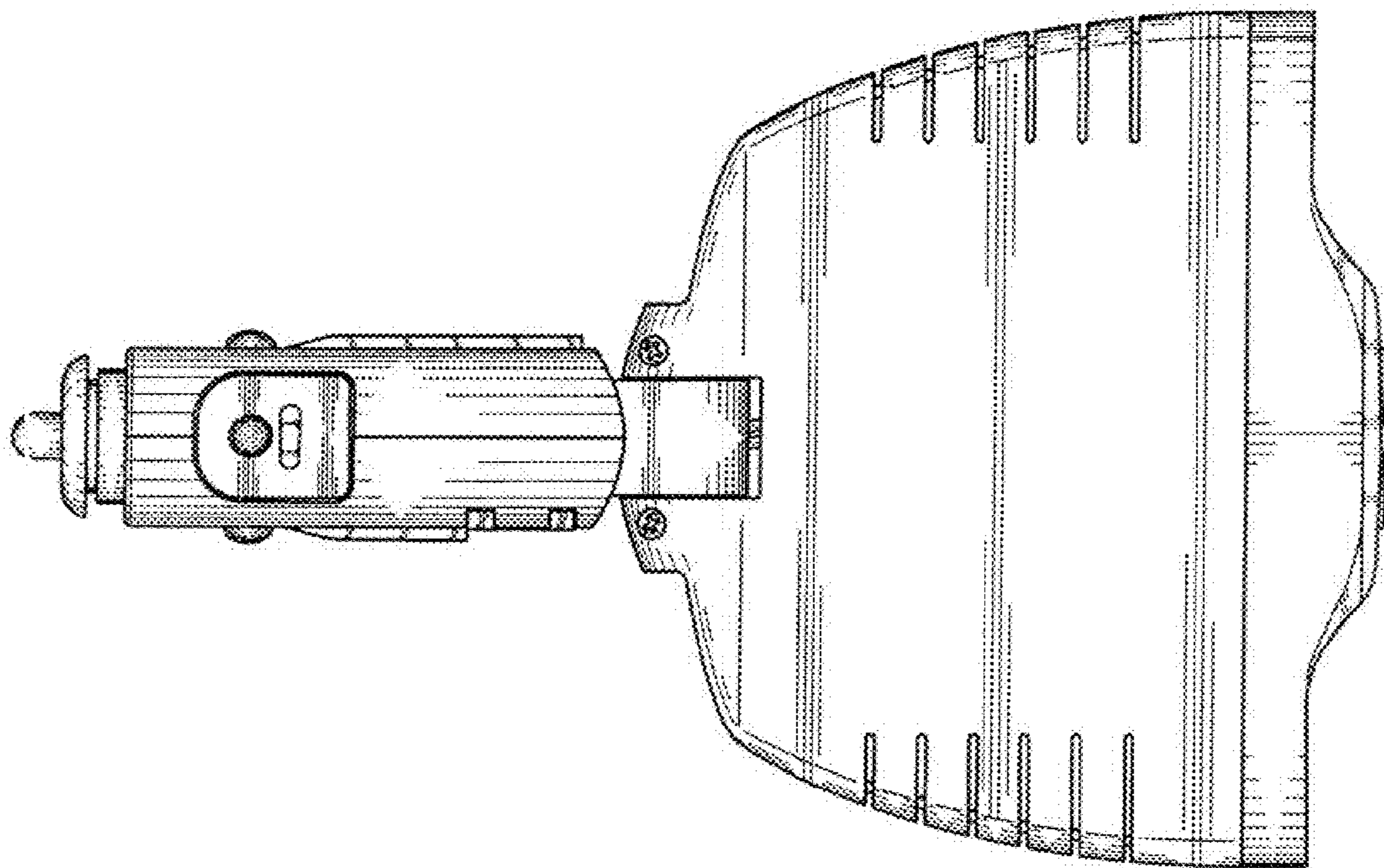


FIG. 7

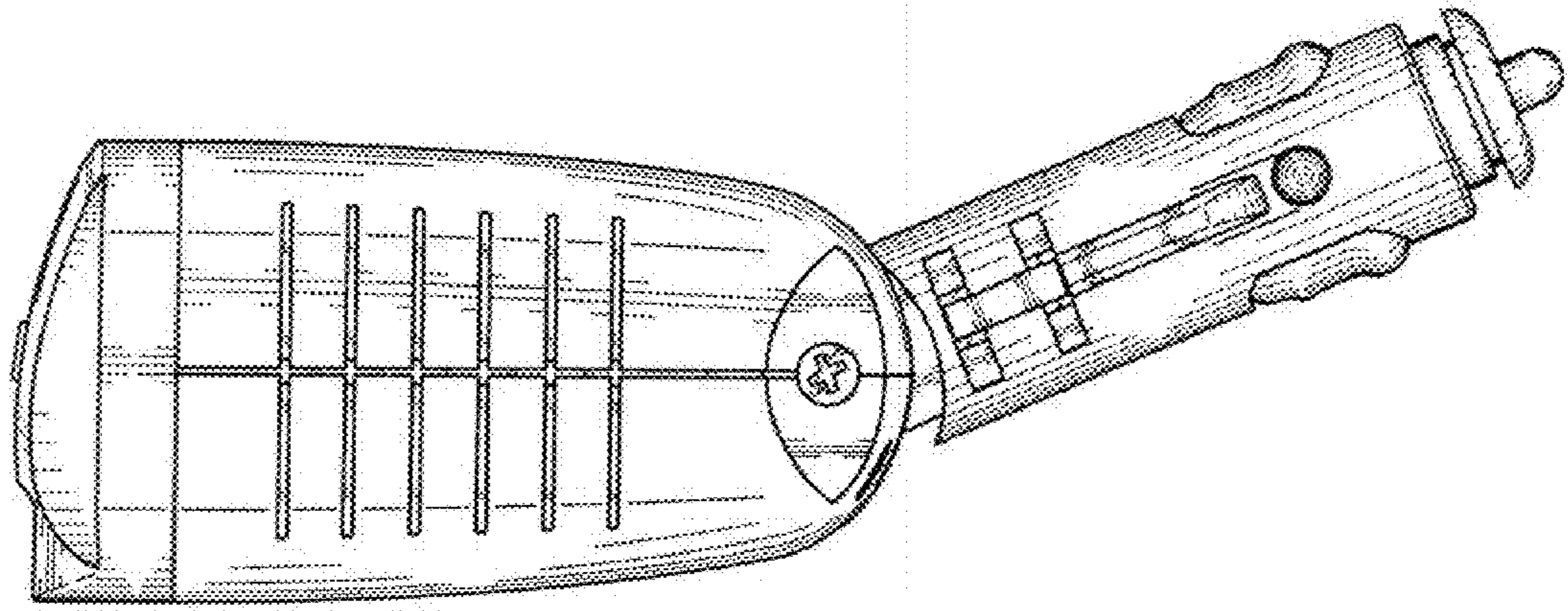


FIG. 8

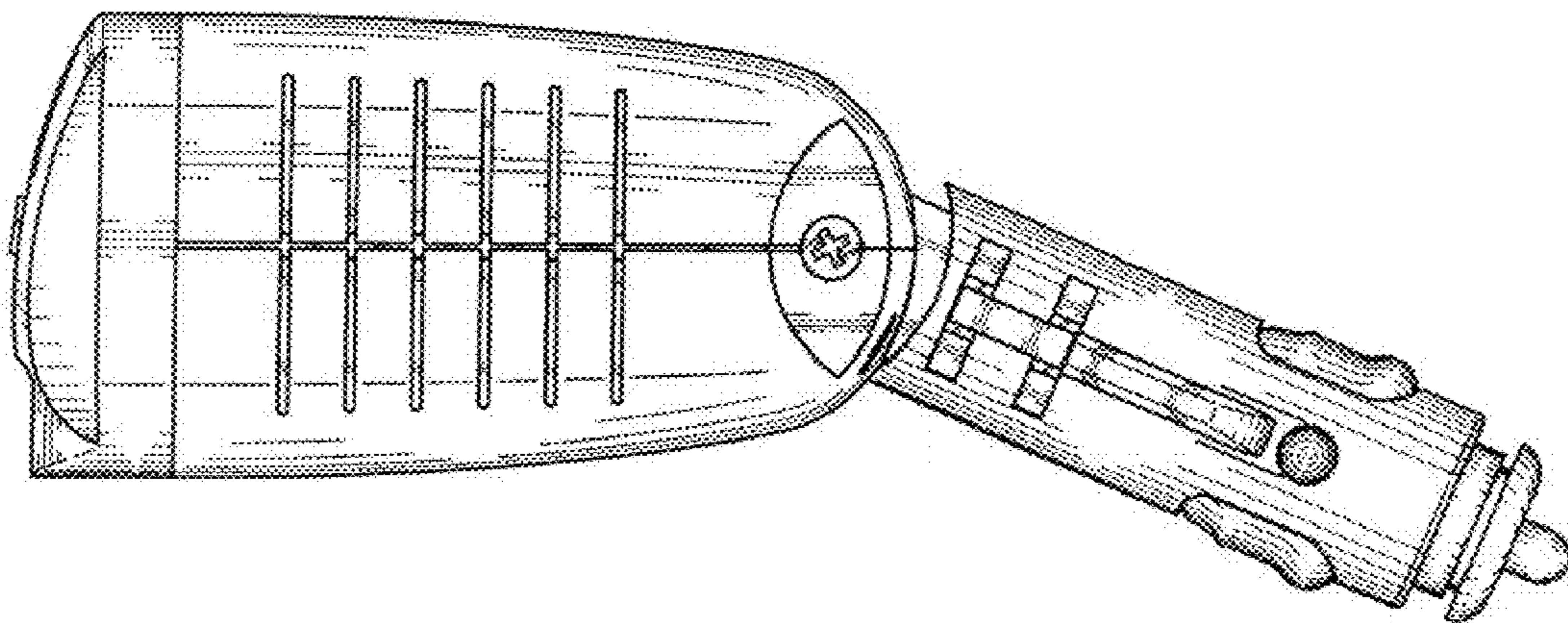


FIG. 9