



US00D550629S

(12) **United States Design Patent**
Kagami

(10) **Patent No.:** **US D550,629 S**

(45) **Date of Patent:** **** Sep. 11, 2007**

(54) **MEMORY CARD CONNECTOR**

2004/0127079 A1* 7/2004 Nagata 439/159
2006/0063422 A1* 3/2006 Lu et al. 439/489

(75) Inventor: **Daisuke Kagami**, Tokyo (JP)

* cited by examiner

(73) Assignee: **Honda Tsushin Kogyo Co., Ltd.**,
Tokyo (JP)

Primary Examiner—Daniel Bui
(74) *Attorney, Agent, or Firm*—Wenderoth, Lind & Ponack,
L.L.P.

(**) Term: **14 Years**

(21) Appl. No.: **29/269,243**

(57) **CLAIM**

(22) Filed: **Nov. 27, 2006**

The original design for a memory card connector, as shown
and described.

(30) **Foreign Application Priority Data**

Jun. 27, 2006 (JP) 2006-016662

DESCRIPTION

(51) **LOC (8) Cl.** **13-03**

FIG. 1 is a perspective view of a memory card connector
showing my new design;

(52) **U.S. Cl.** **D13/147**

FIG. 2 is a front elevational view thereof;

(58) **Field of Classification Search** D13/133,
D13/146-147, 149, 154, 184, 199; D14/432-433,
D14/435; 361/684, 754; 439/64, 79, 159-160,
439/108, 607-610, 629-630, 636, 638
See application file for complete search history.

FIG. 3 is a rear elevational view thereof;

FIG. 4 is a right side elevational view thereof;

FIG. 5 is a left side elevational view thereof;

FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom plan view thereof.

(56) **References Cited**

U.S. PATENT DOCUMENTS

6,835,100 B1* 12/2004 Chen 439/630
D520,453 S * 5/2006 Yoneyama et al. D13/147
D520,957 S * 5/2006 Chang et al. D13/147
D535,258 S * 1/2007 Sasaki D13/147

Broken line representations are shown in FIGS. 2, 3, 6 and
7 for illustrative purposes only and form no part of the
claimed design.

1 Claim, 3 Drawing Sheets

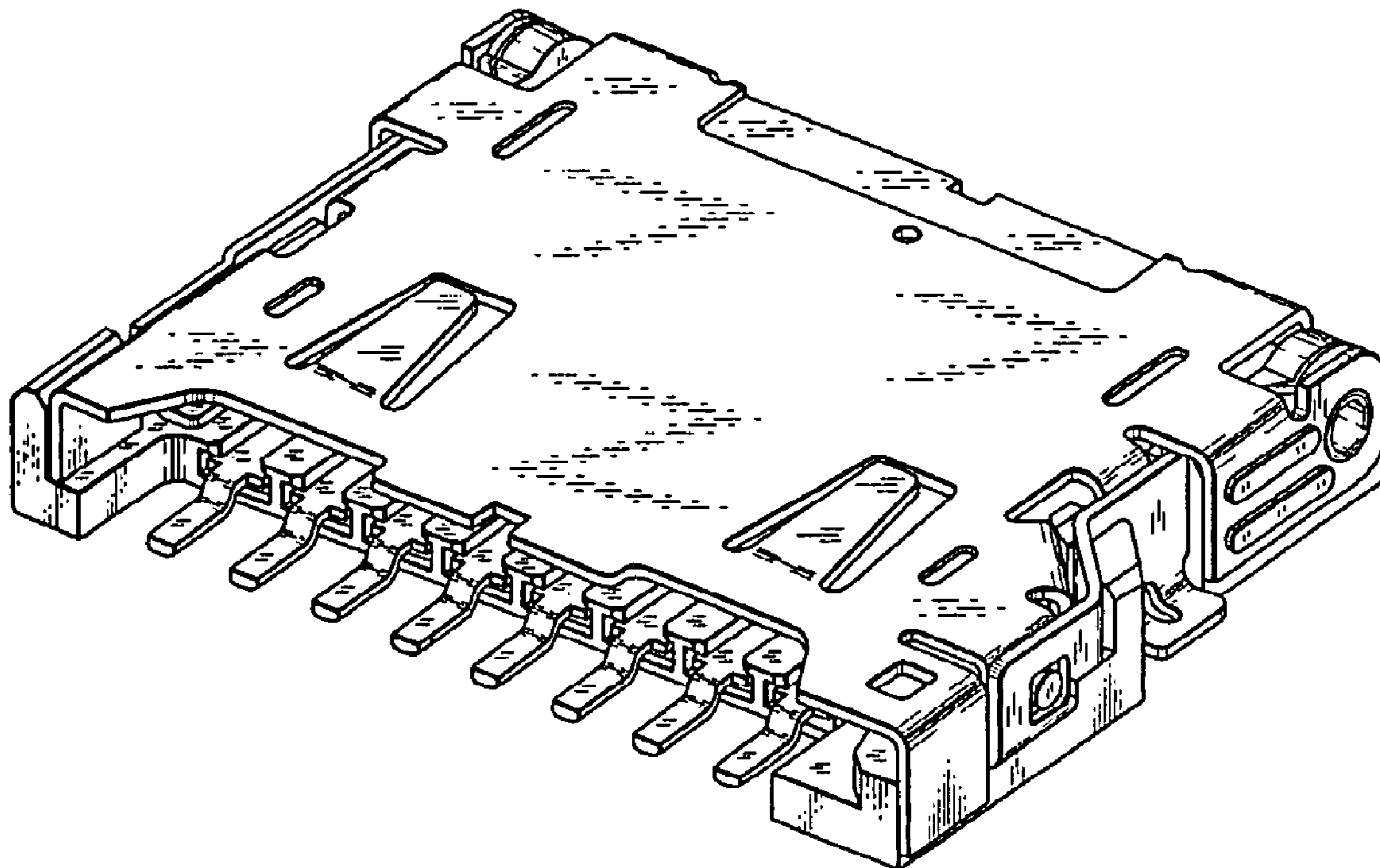


Fig. 1

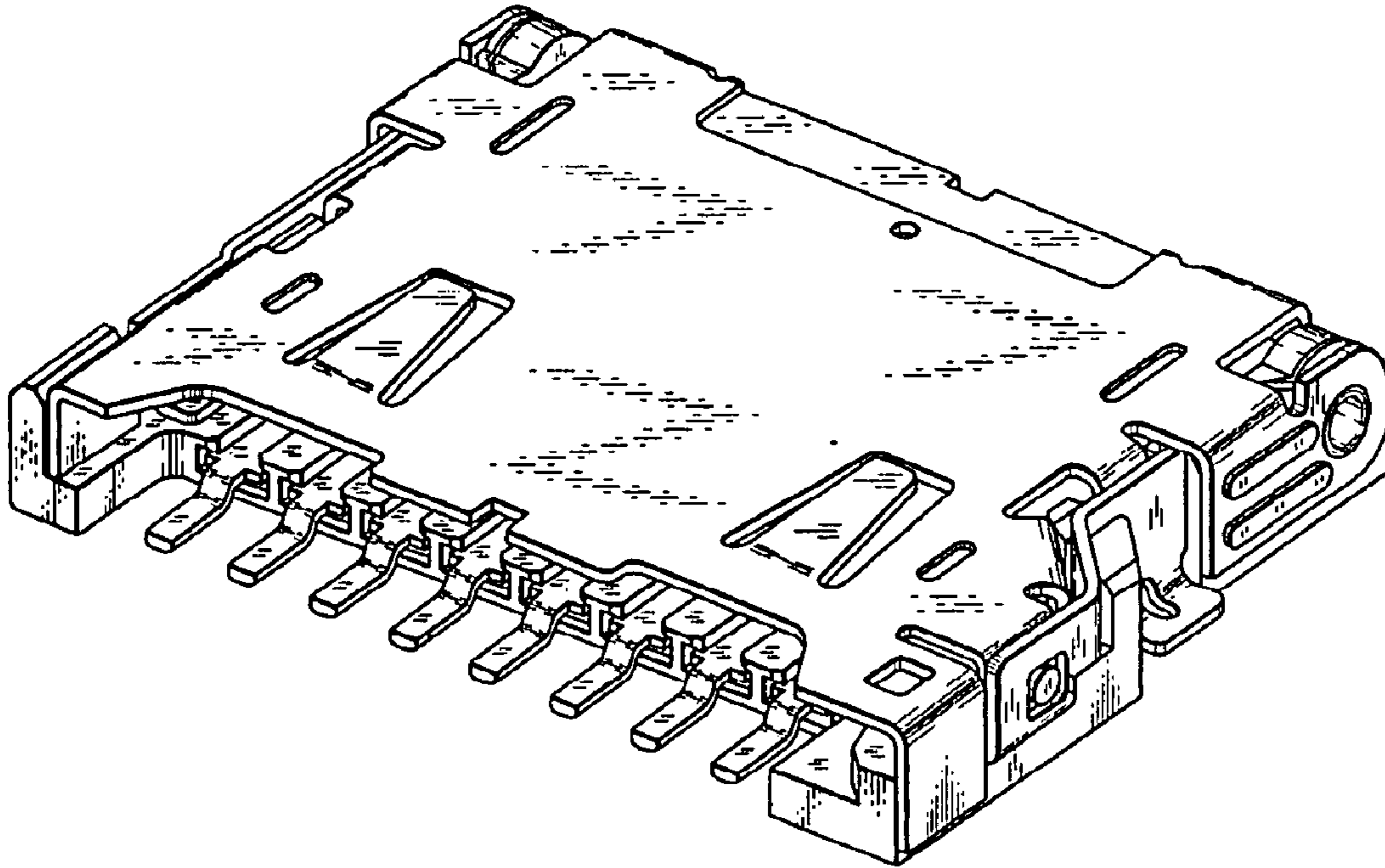


Fig. 2

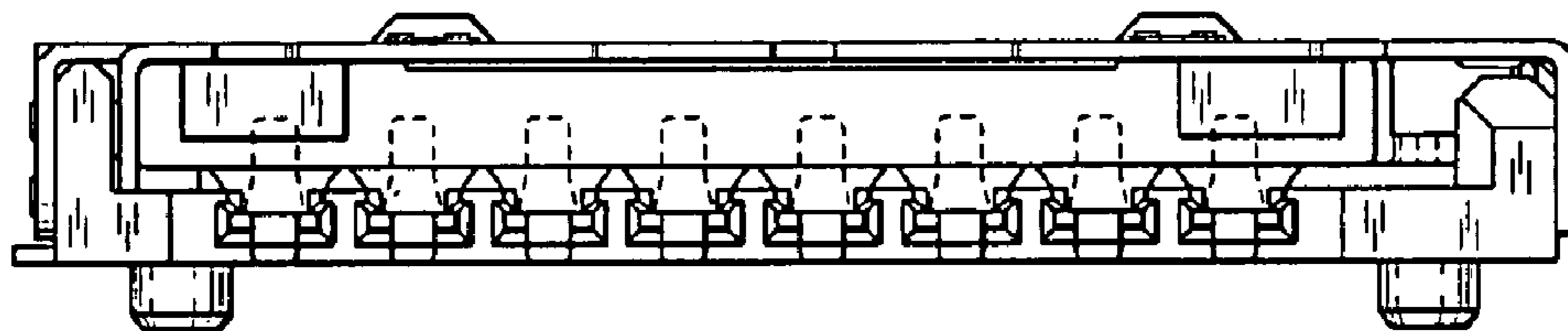


Fig. 3

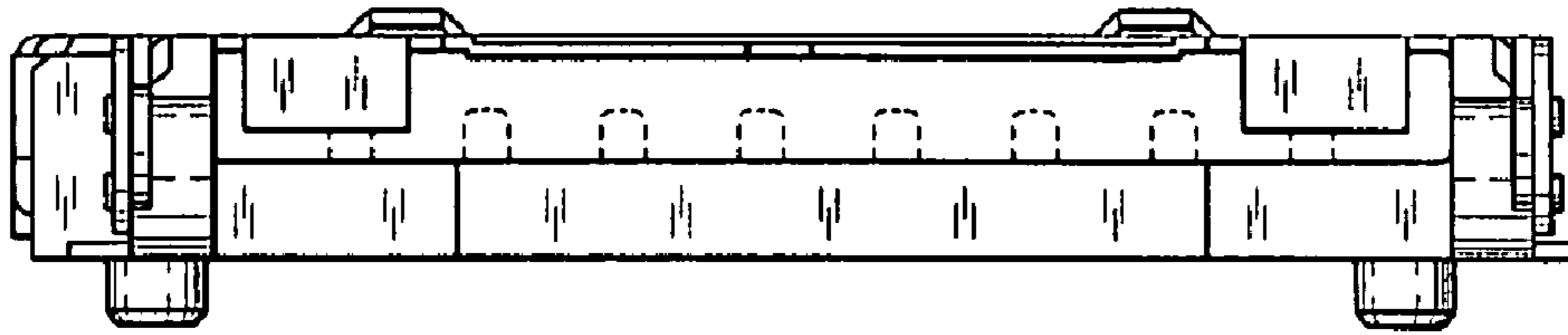


Fig. 4

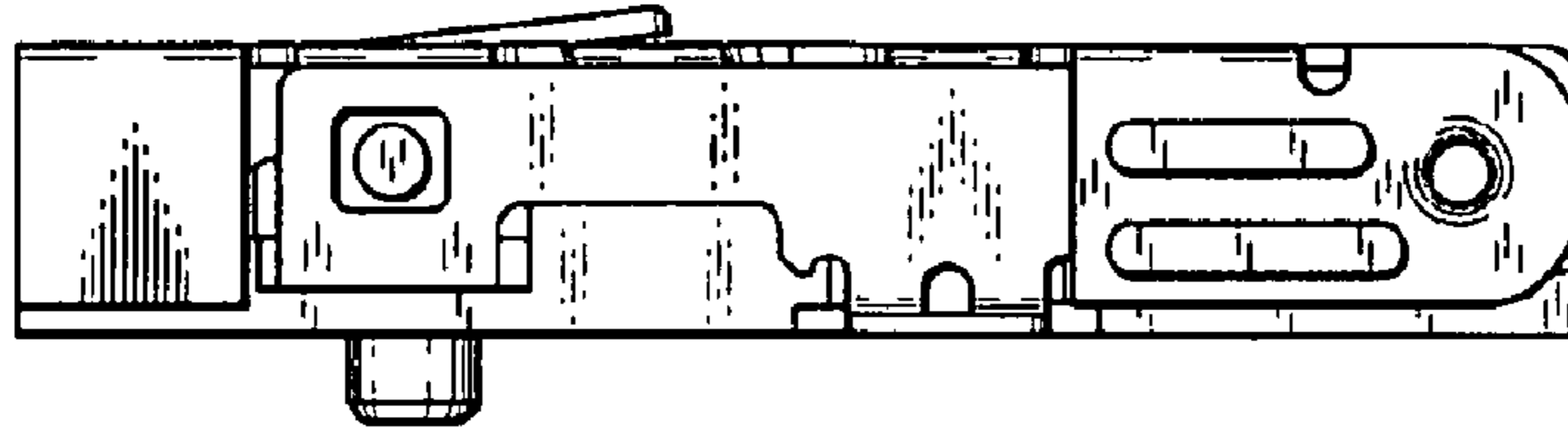


Fig. 5

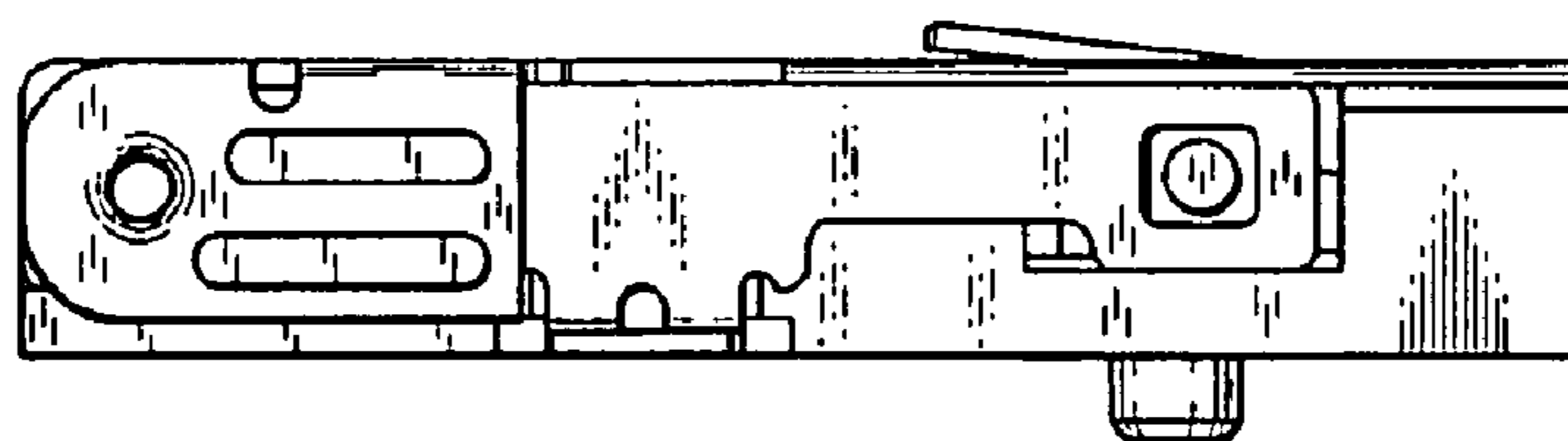


Fig. 6

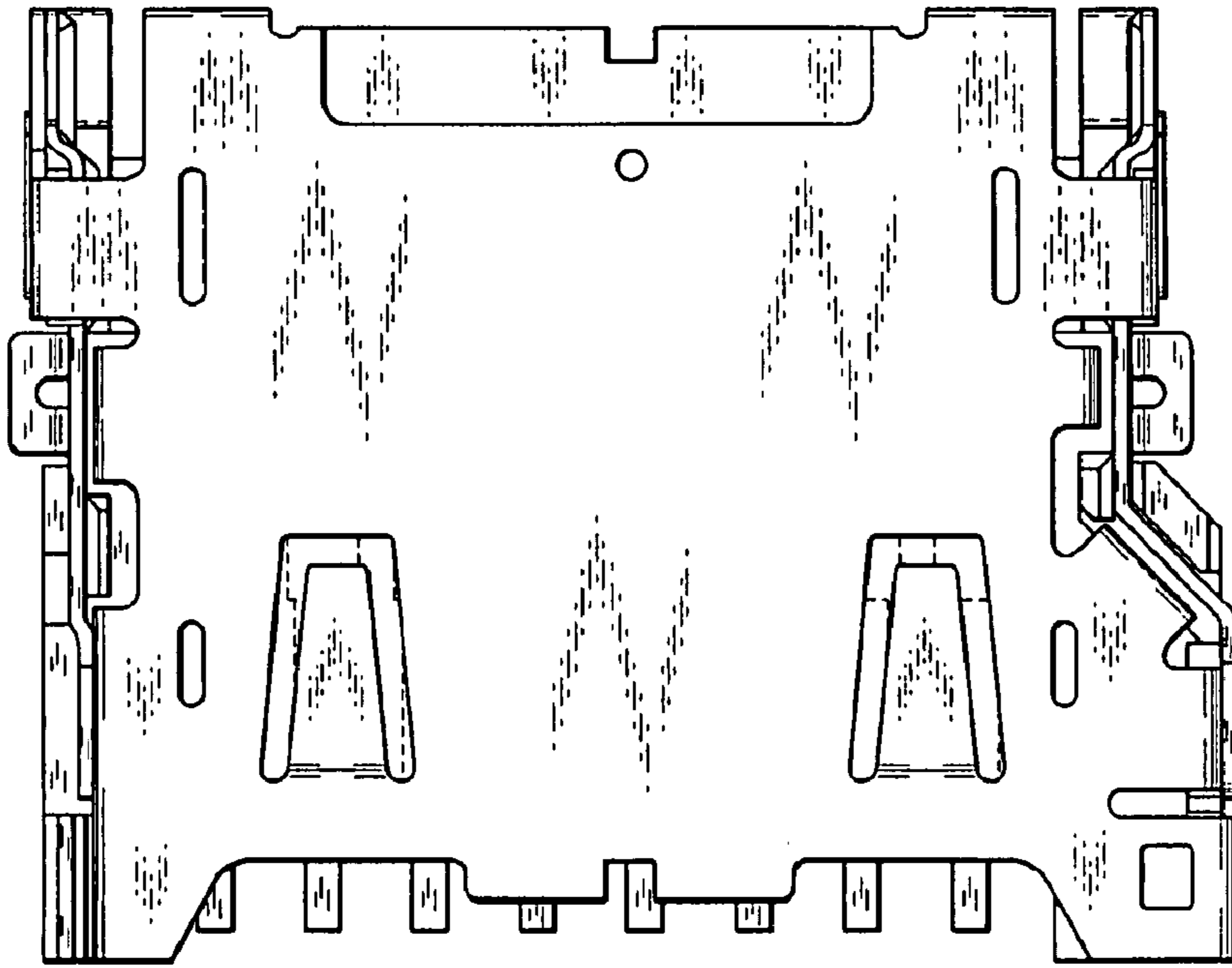


Fig. 7

