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(12) **United States Design Patent**
Yagi

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(45) **Date of Patent:** **** Aug. 21, 2007**

(54) **REMOTE CONTROLLER FOR SEMICONDUCTOR MEMORY CARD PLAYER**

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(73) Assignee: **Matsushita Electric Industrial Co., Ltd.**, Osaka (JP)

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JP D 1182589 8/2003
JP D 1239938 5/2005

(**) Term: **14 Years**

* cited by examiner

(21) Appl. No.: **29/268,941**

Primary Examiner—Louis S. Zarfes
Assistant Examiner—John Windmuller

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(74) *Attorney, Agent, or Firm*—Brinks Hofer Gilson & Lione

(30) **Foreign Application Priority Data**

(57) **CLAIM**

May 19, 2006 (JP) 2006-012692

I claim the ornamental design for a remote controller for semiconductor memory card player, as shown and described.

(51) **LOC (8) Cl.** **14-03**

(52) **U.S. Cl.** **D14/218; D11/4**

(58) **Field of Classification Search** D14/174, D14/137-139, 151, 217, 218, 191-193, 167, D14/299, 168, 247, 455, 456; D13/168, D13/162-164; D21/512-517, 566; D10/104, D10/106; 345/169; 348/114, 734; 455/352-355, 455/151.1-151.4, 128, 347, 90.3; D11/4
See application file for complete search history.

DESCRIPTION

FIG. 1 is a top perspective view of a remote controller for semiconductor memory card player showing my new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a rear elevational view thereof;

FIG. 4 is a left side elevational view thereof;

FIG. 5 is a right side elevational view thereof;

FIG. 6 is a top plan view thereof; and,

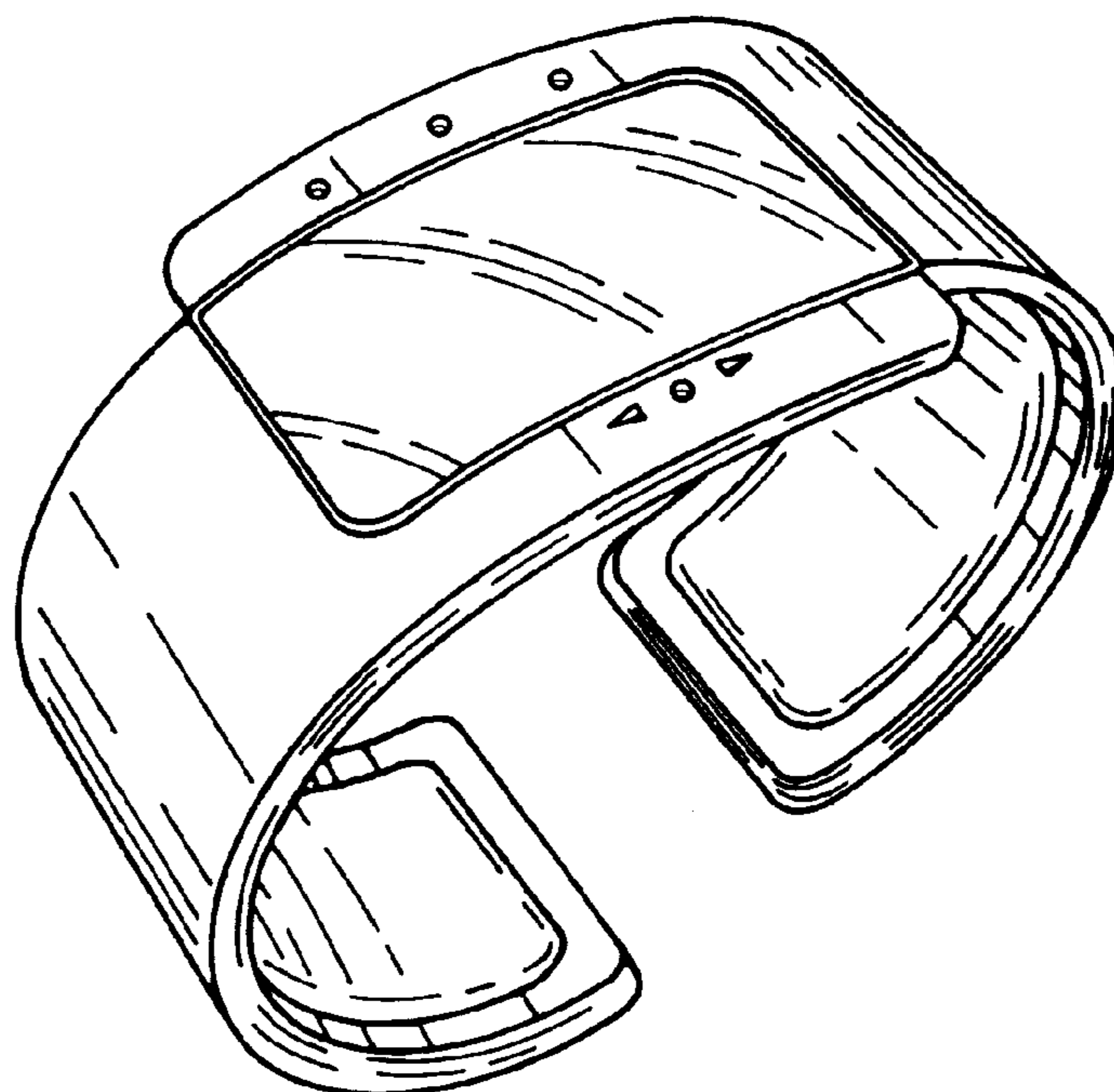
FIG. 7 is a bottom plan view thereof.

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1 Claim, 4 Drawing Sheets



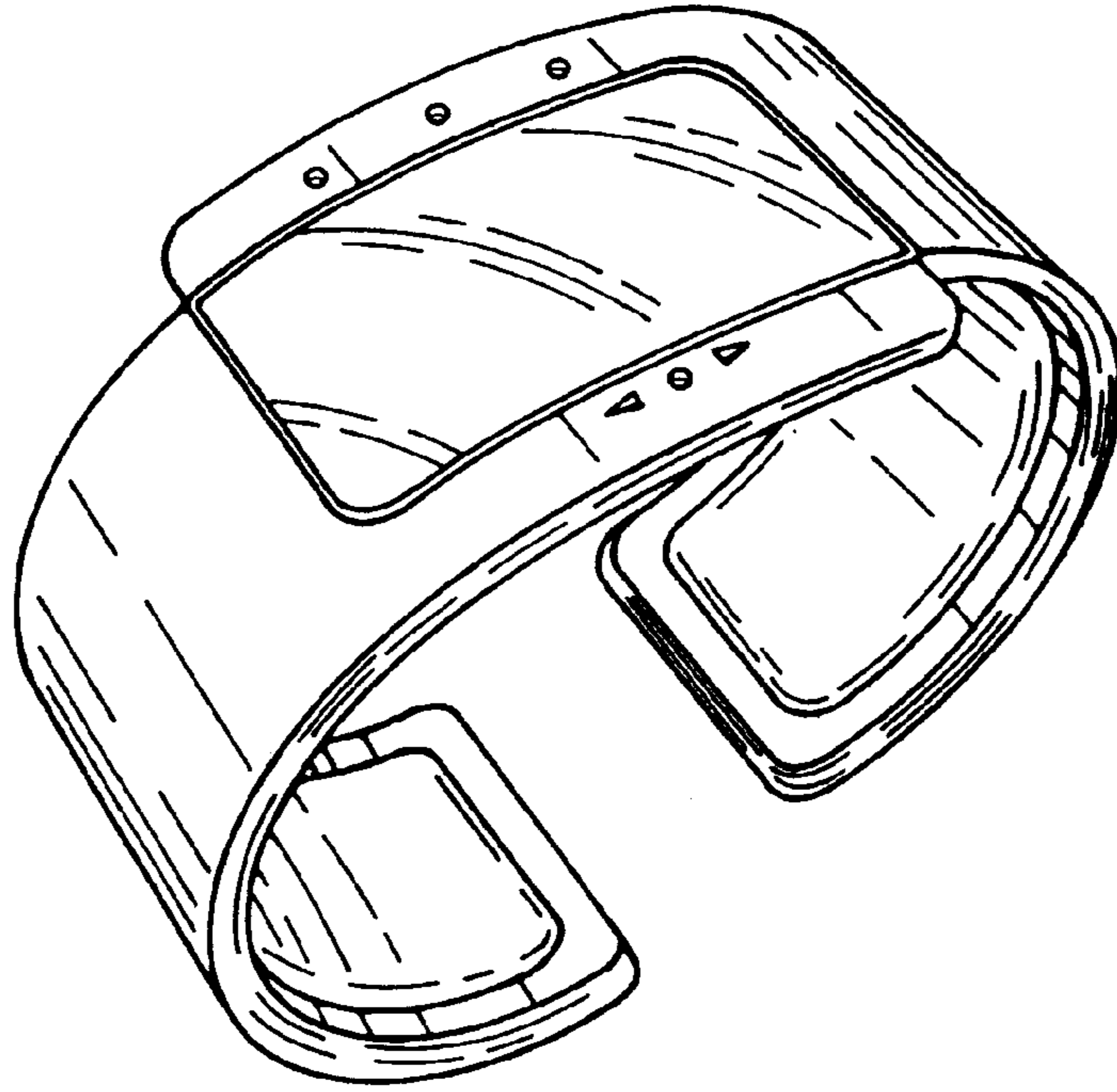


FIG. 1

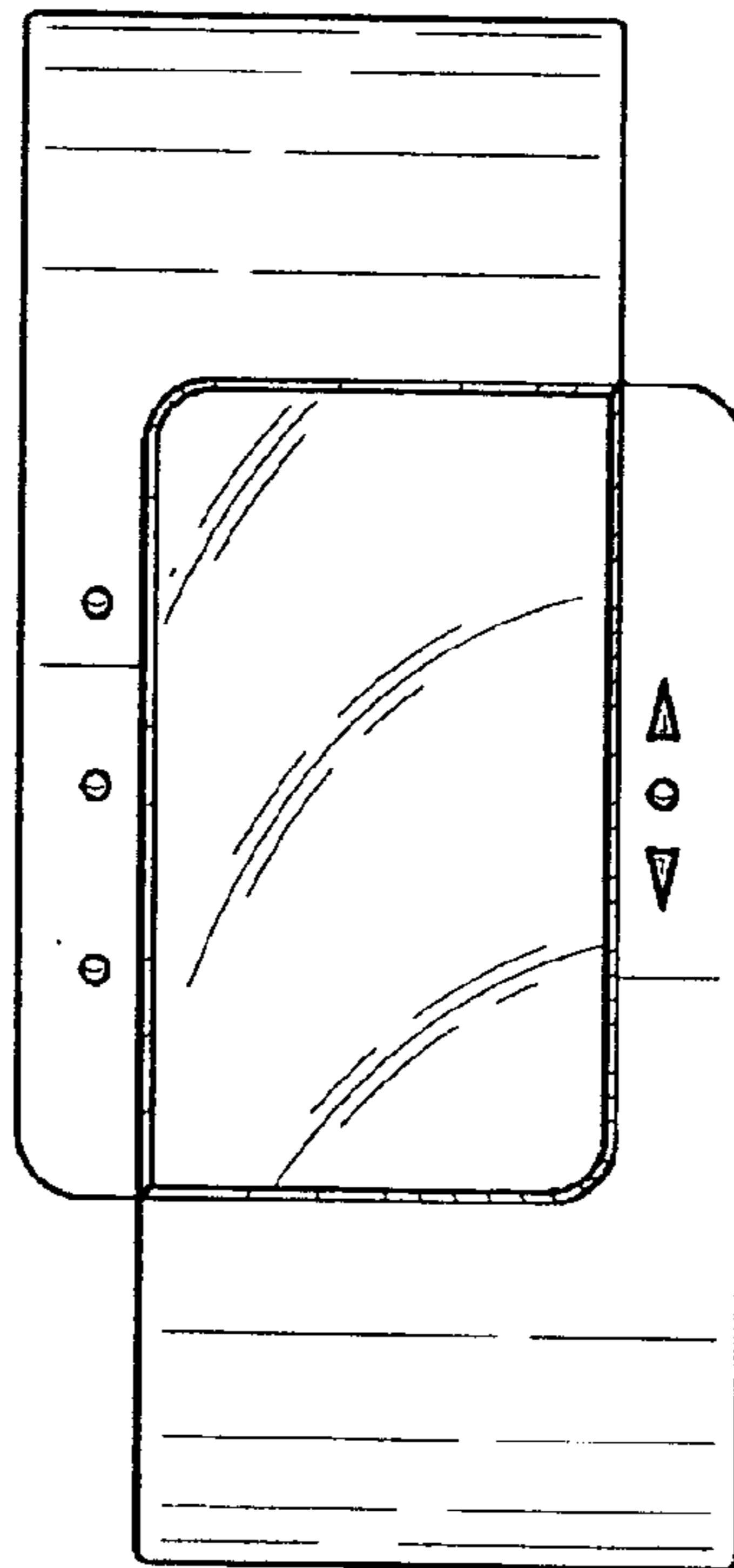


FIG. 2

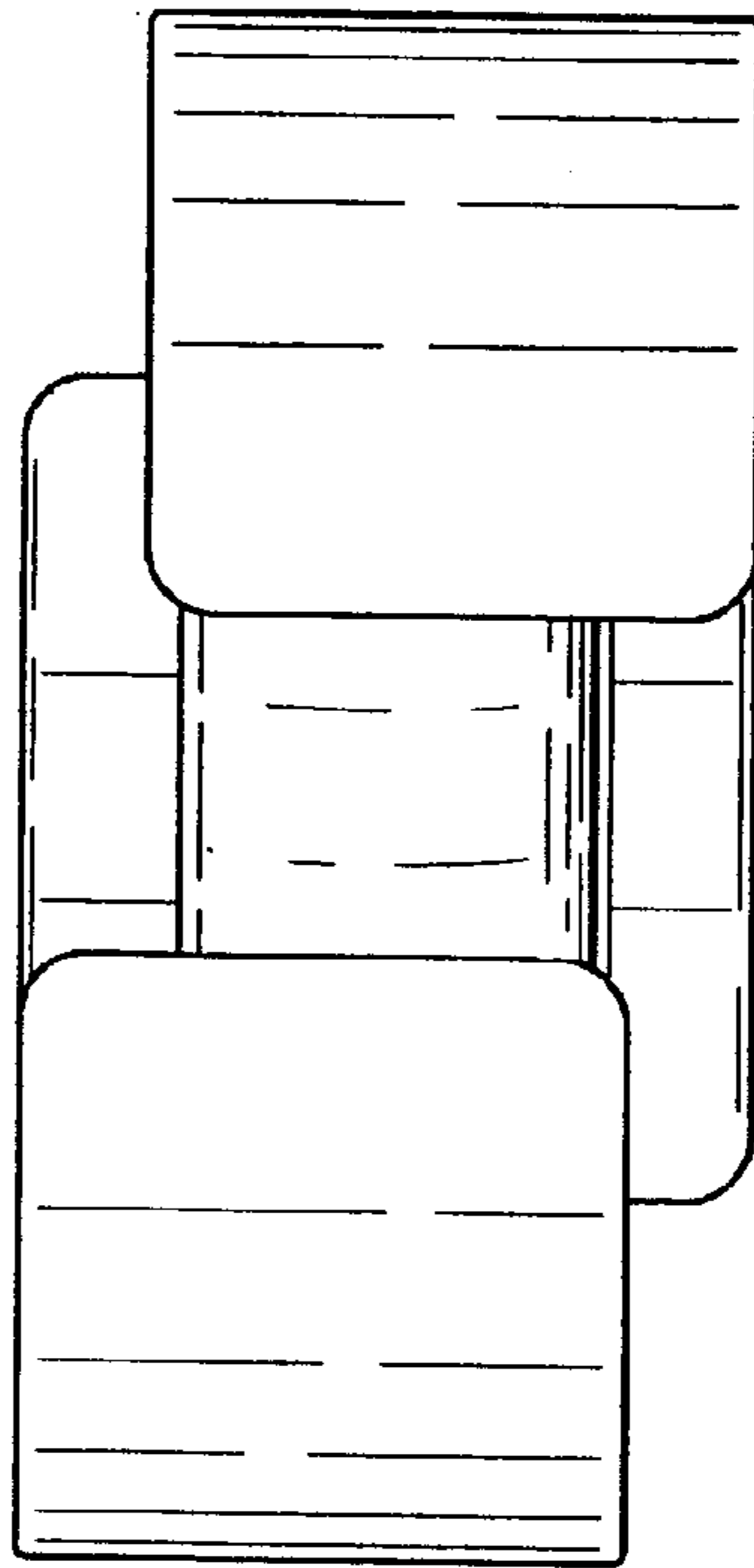


FIG. 3

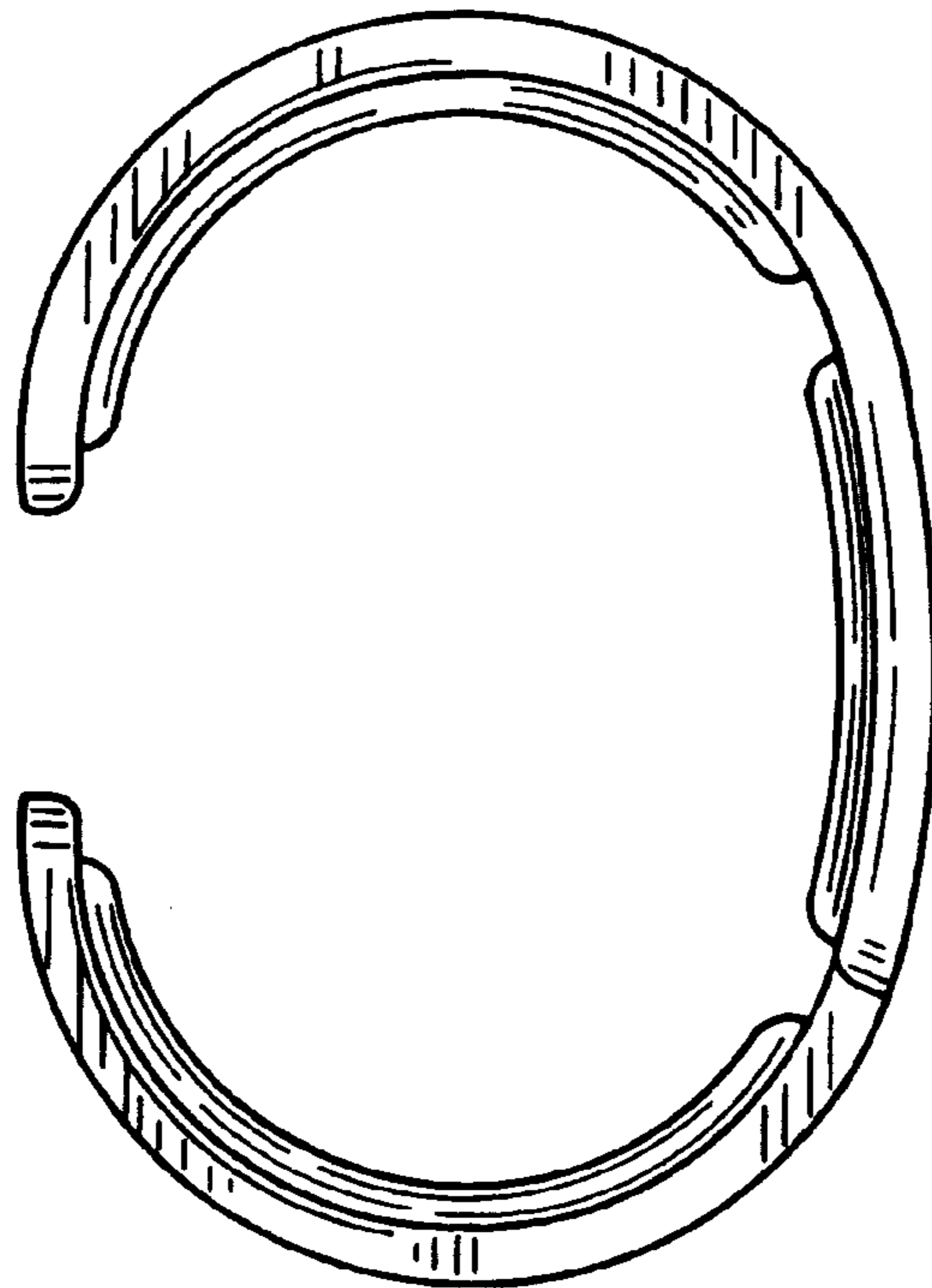


FIG. 4

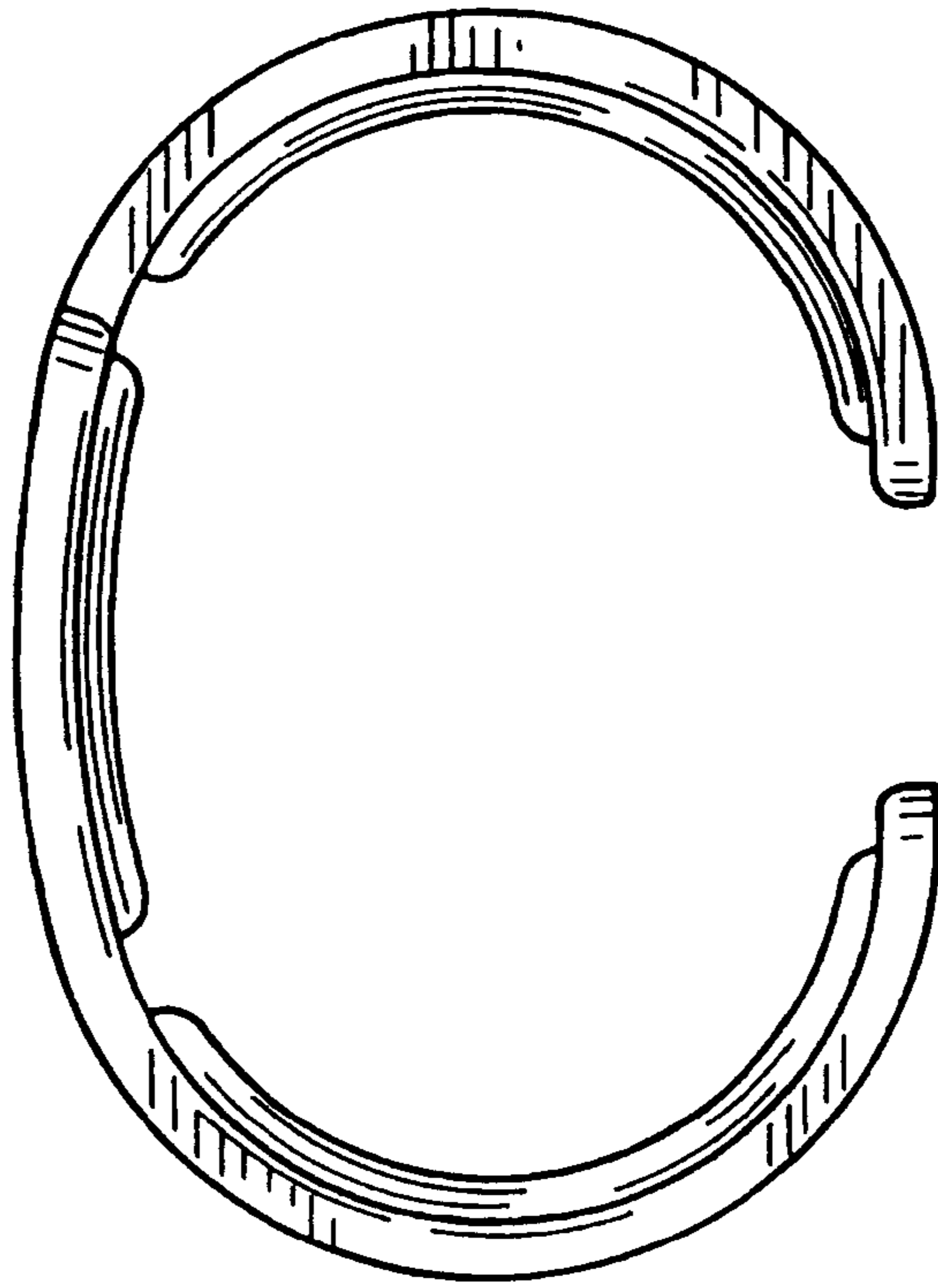


FIG. 5

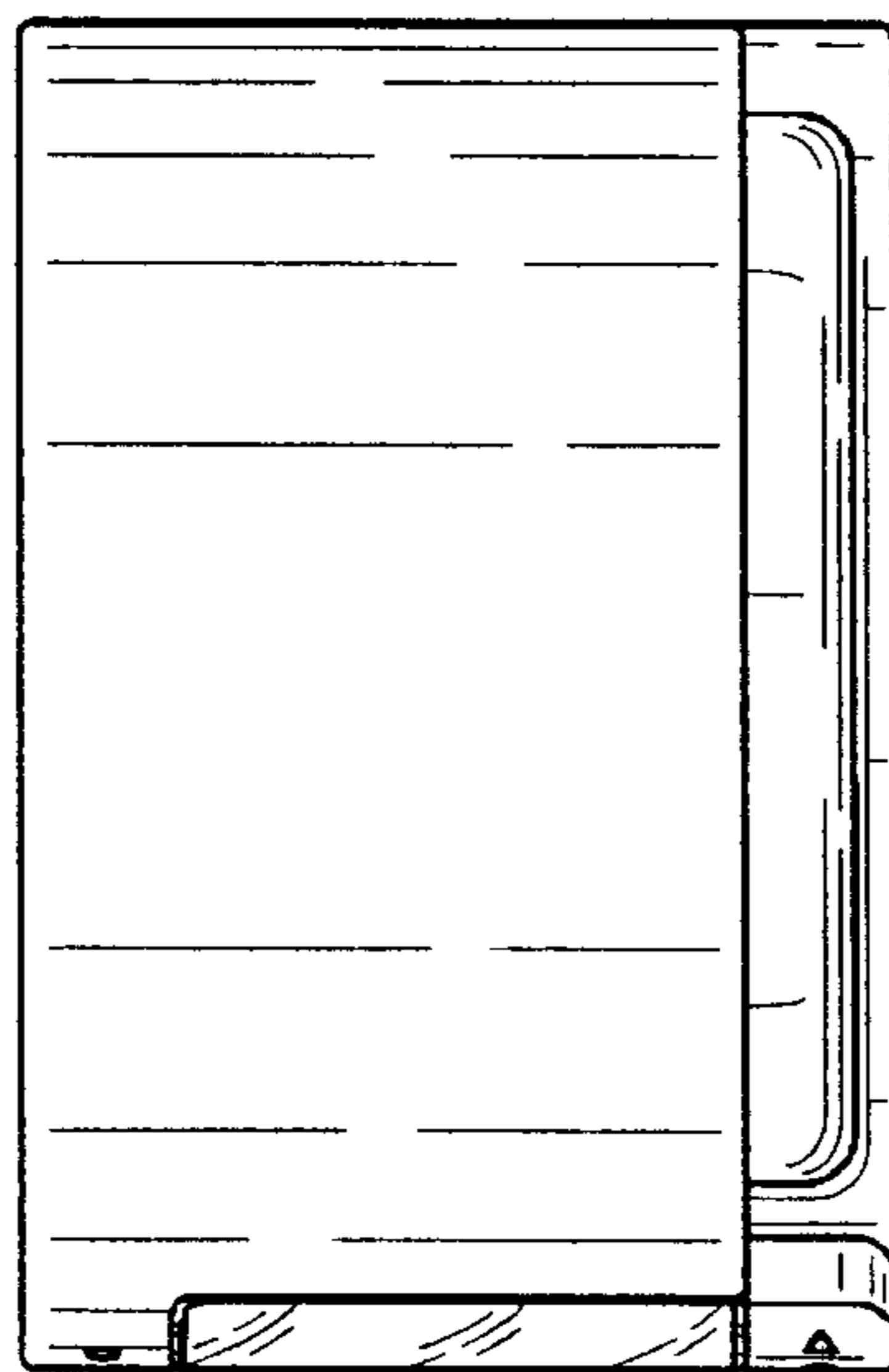


FIG. 6

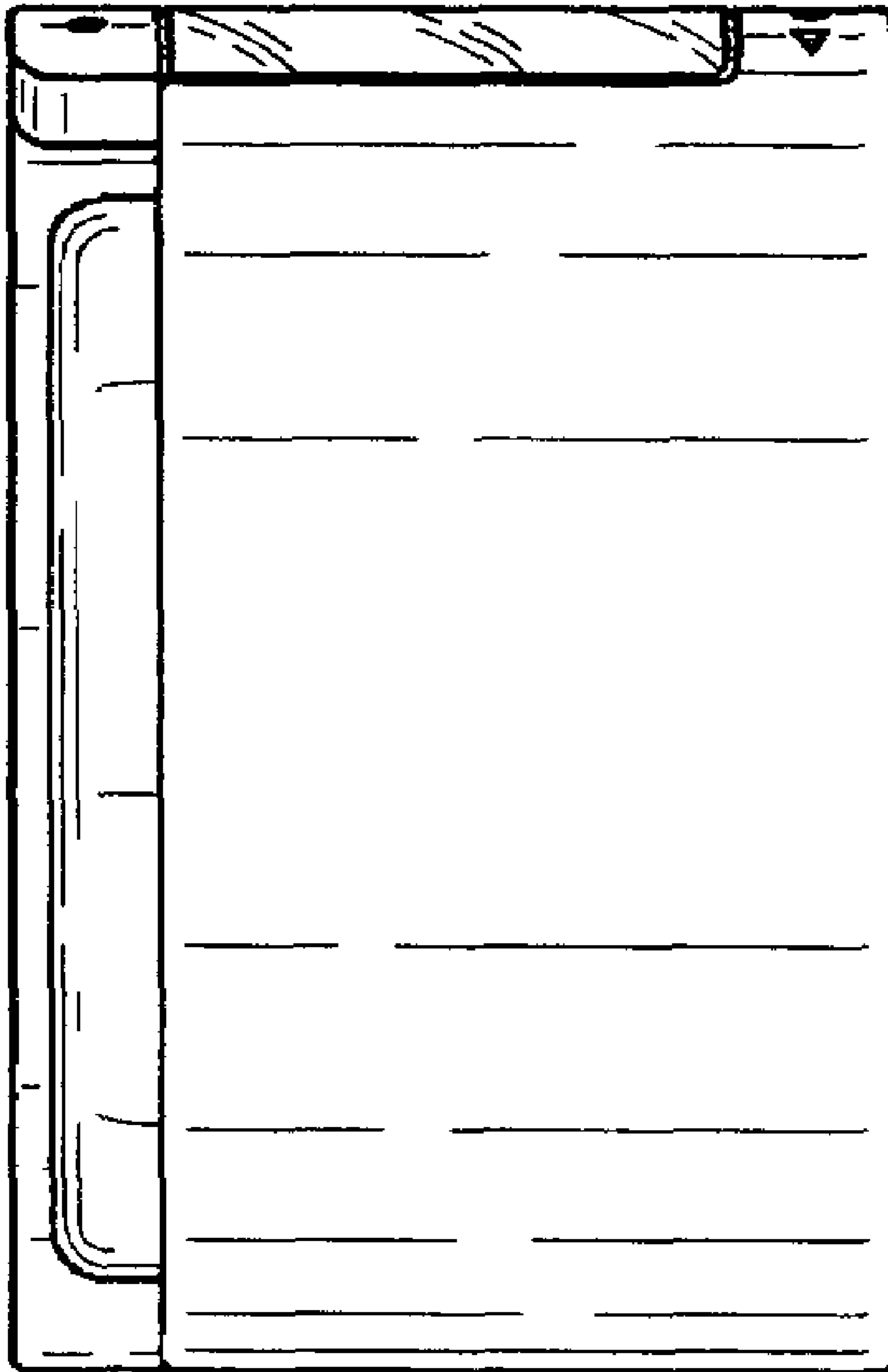


FIG. 7